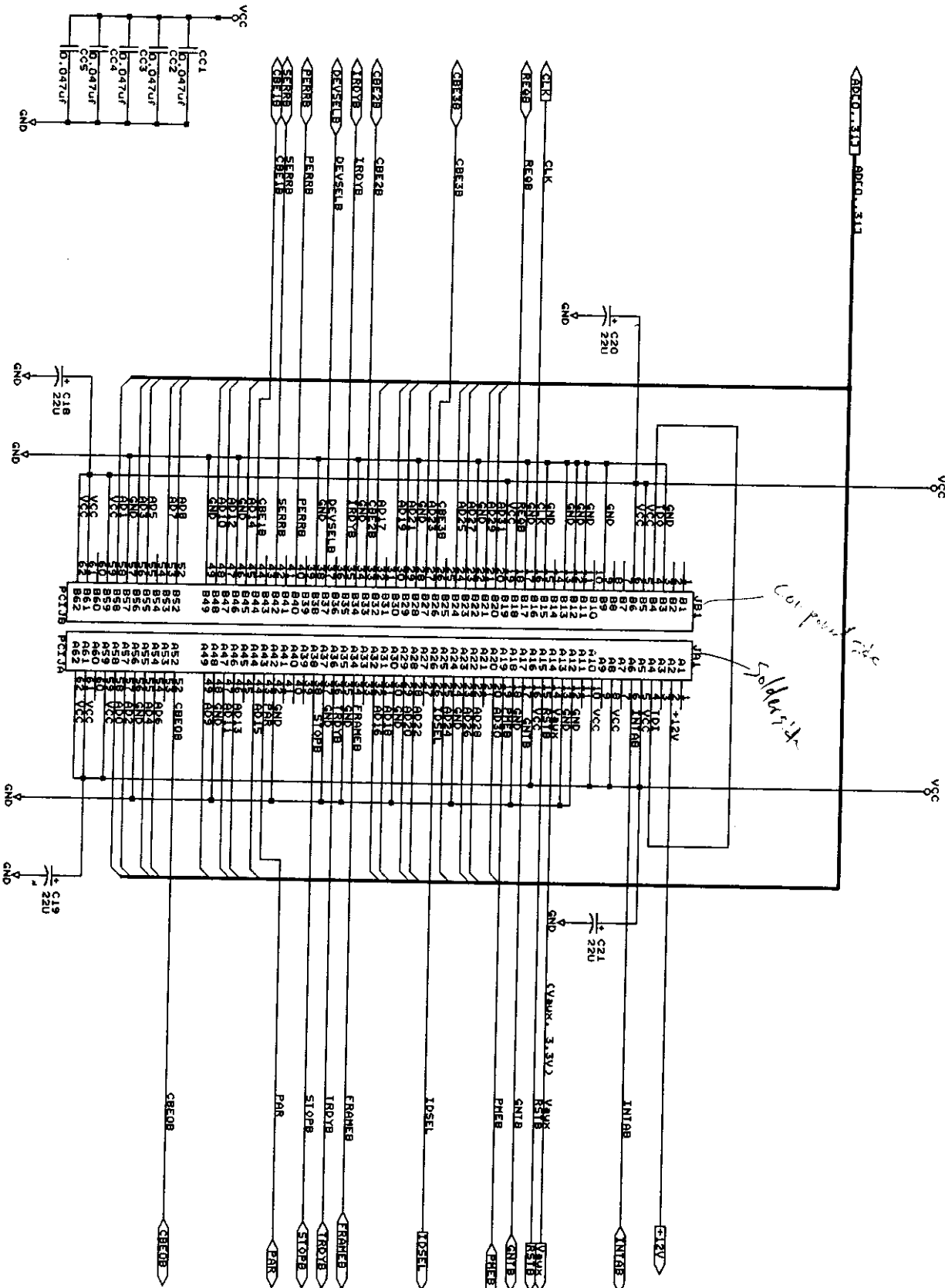


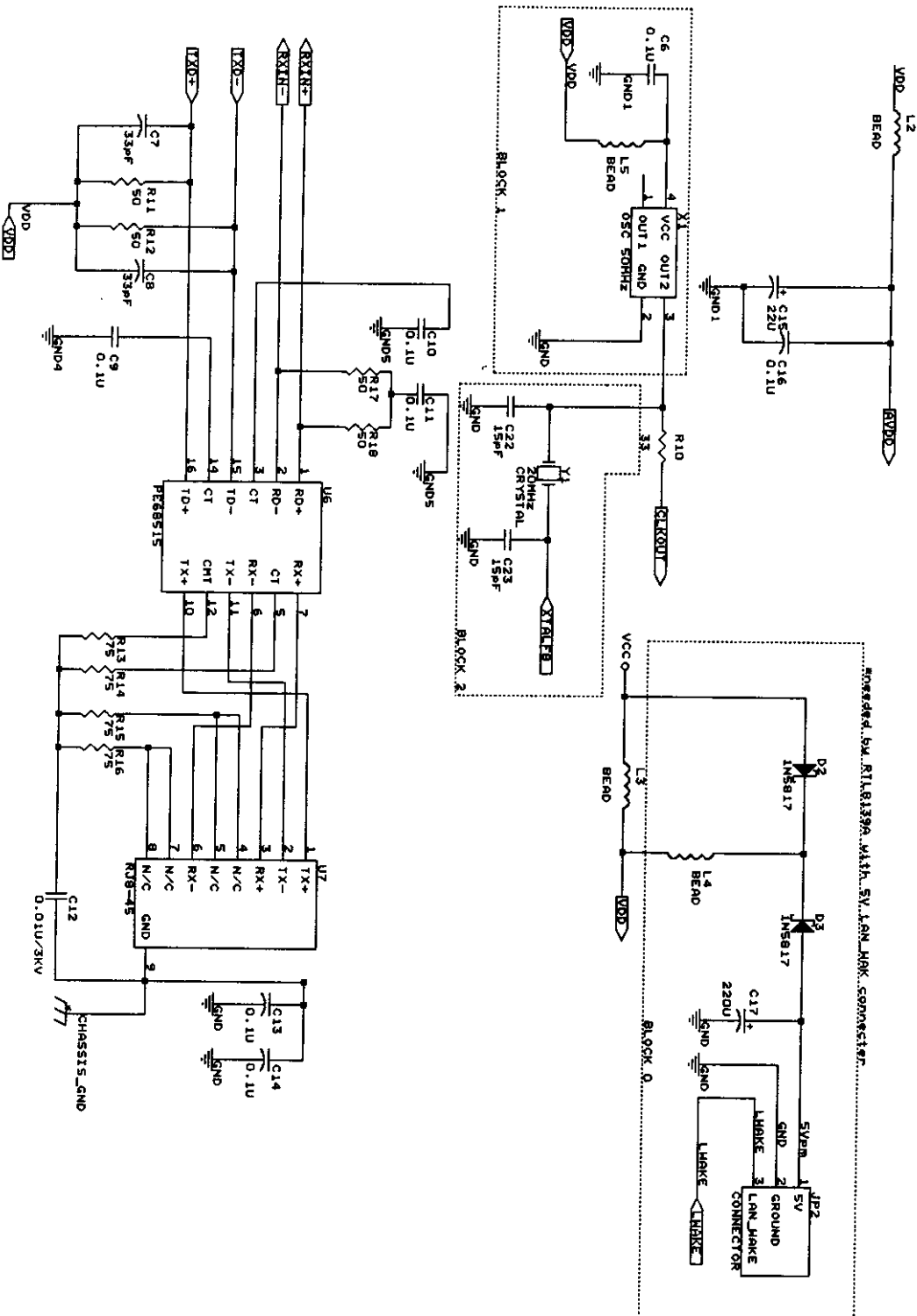
EXHIBIT D

Circuit Diagram



This circuit is not the final version, and is subject to change possibly in the future. It's for reference only.

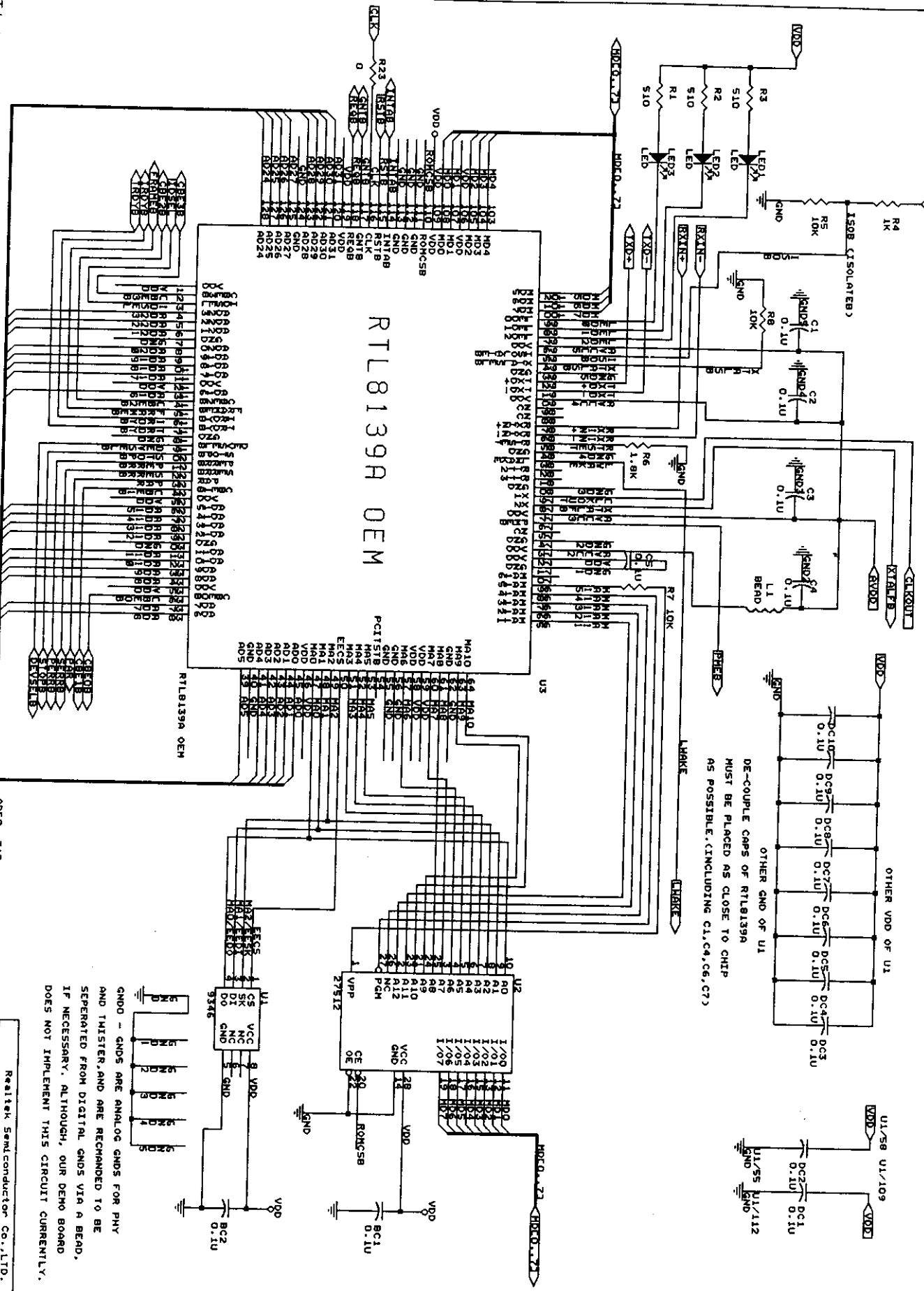
IF THE 20MHZ CRYSTAL IS USED AS THE CLOCK SOURCE, DISCARD BLOCK 1 AND APPLY R8 IN SHEET 1
 IF THE 50MHZ OSC IS USED AS THE CLOCK SOURCE, DISCARD BLOCK 2 AND R8 IN SHEET 1
 FOR RTL8139A WITH POWER MANAGEMENT APPLICATION, REMOVE L3
 FOR RTL8139A WITHOUT Vaux APPLICATION, REMOVE BLOCK 0
 FOR LUMP Vaux, C17 MUST BE LARGE ENOUGH TO AVOID POWER SUPPLY'S COMPATIBILITY
 FOR RTL8139 APPLICATION, L3 MUST HAVE RATED 300MA CURRENT/1000MHZ@100MHZ



This circuit is not the final version, and is subject to change possibly in the future. It's for reference only.

Realtek Semiconductor Co., Ltd.	
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REV	0.0
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RS: Apply this resistor to use 20MHz crystal as the clock source.
 Otherwise, discard this resistor to use 5MHz OSC as the clock source.



RTL8139A OEM

RTL8139A OEM

GND - GNDs ARE ANALOG GNDs FOR PHY
 AND TWISTER, AND ARE RECOMMENDED TO BE
 SEPERATED FROM DIGITAL GNDs VIA A BEAD.
 IF NECESSARY, OUR DEMO BOARD
 DOES NOT IMPLEMENT THIS CIRCUIT CURRENTLY.

This circuit is not the final version, and is subject to
 change possibly in the future. It's for reference only.

Realtek Semiconductor Co., Ltd.	
Title	RTL8139A OEM DEMO BOARD (LANCARD)
Size	Document Number
Rev	RTL8139A.D01.M03V1.000
Date	July 25, 1998 Sheet 1 of 3



PCI.SCH



RD39A001.SCH



RD39A002.SCH

KINSTON TECHNOLOGY INC.	
FILE : BLOCK.SCH	
ENGINEER : Bob Lee	
P/N : Main PCB Dlm.	
Title	
10/100 ENABLE PHY	
Size Document Number	
CIS-KNE1001X/E	
Date September 14, 1998	
Sheet	
of	
REV	VO