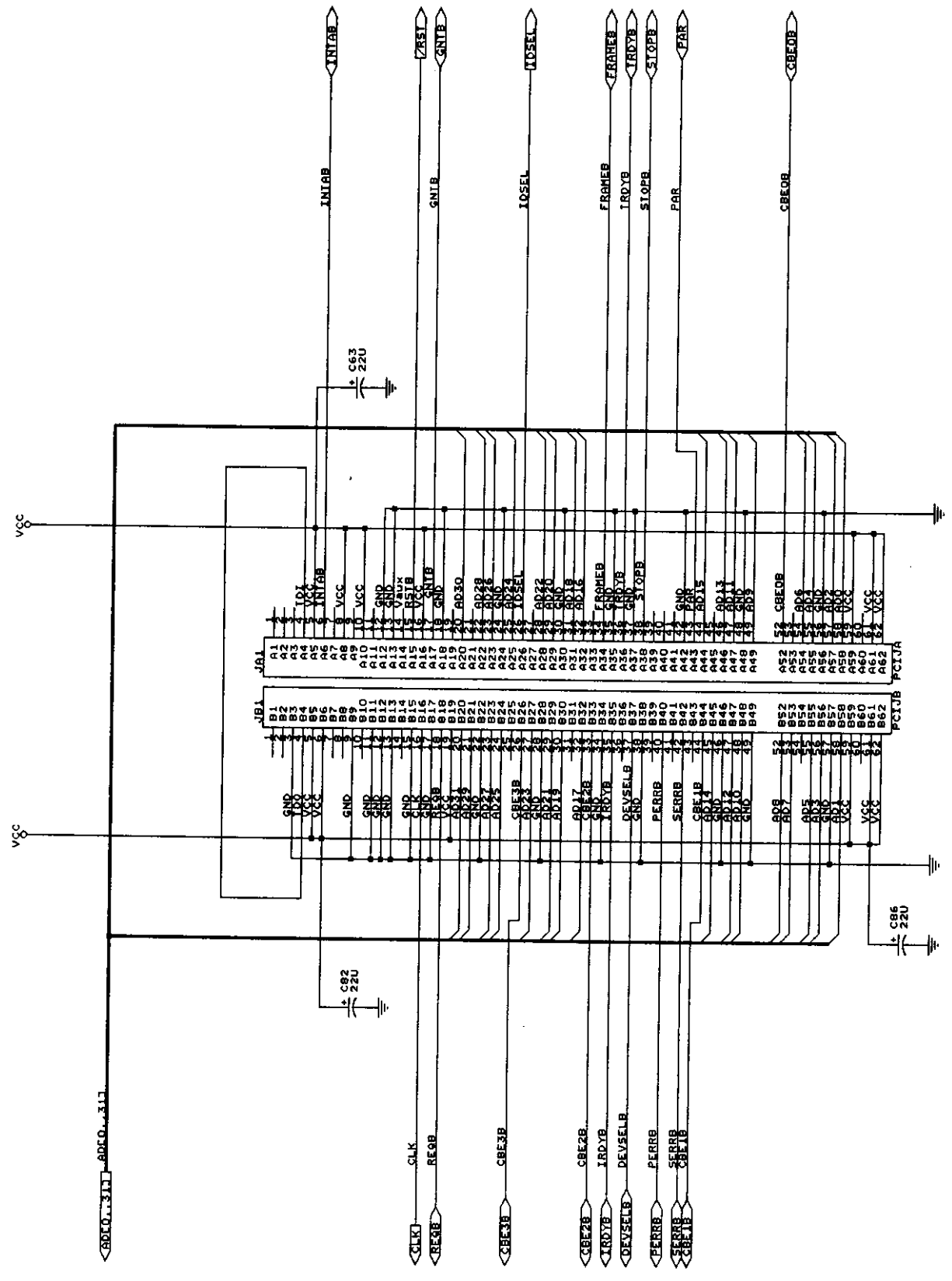
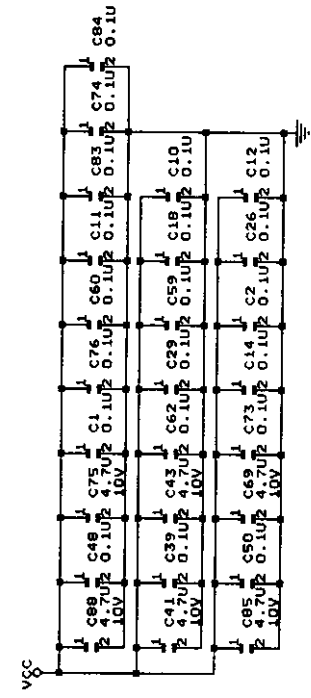
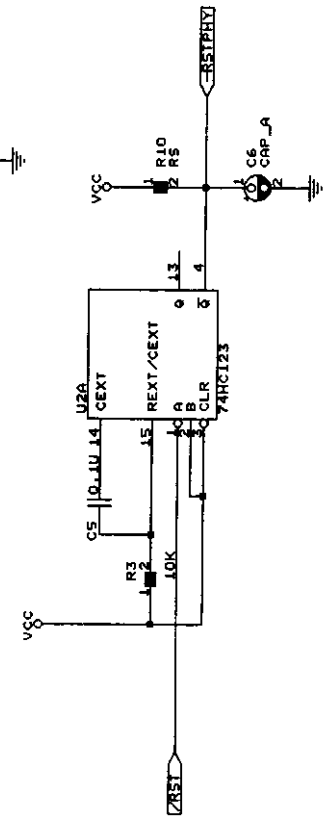
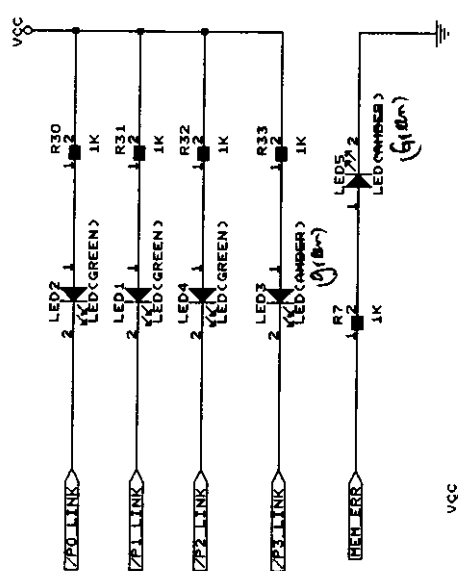
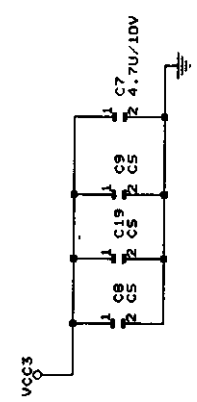
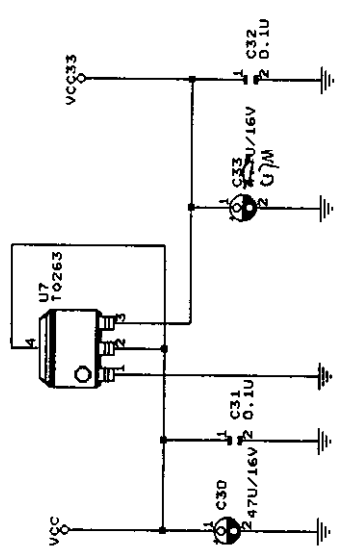
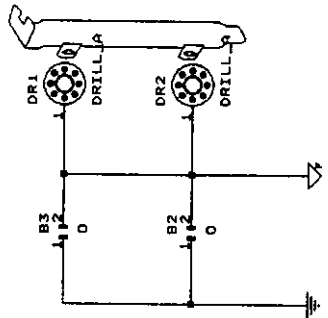
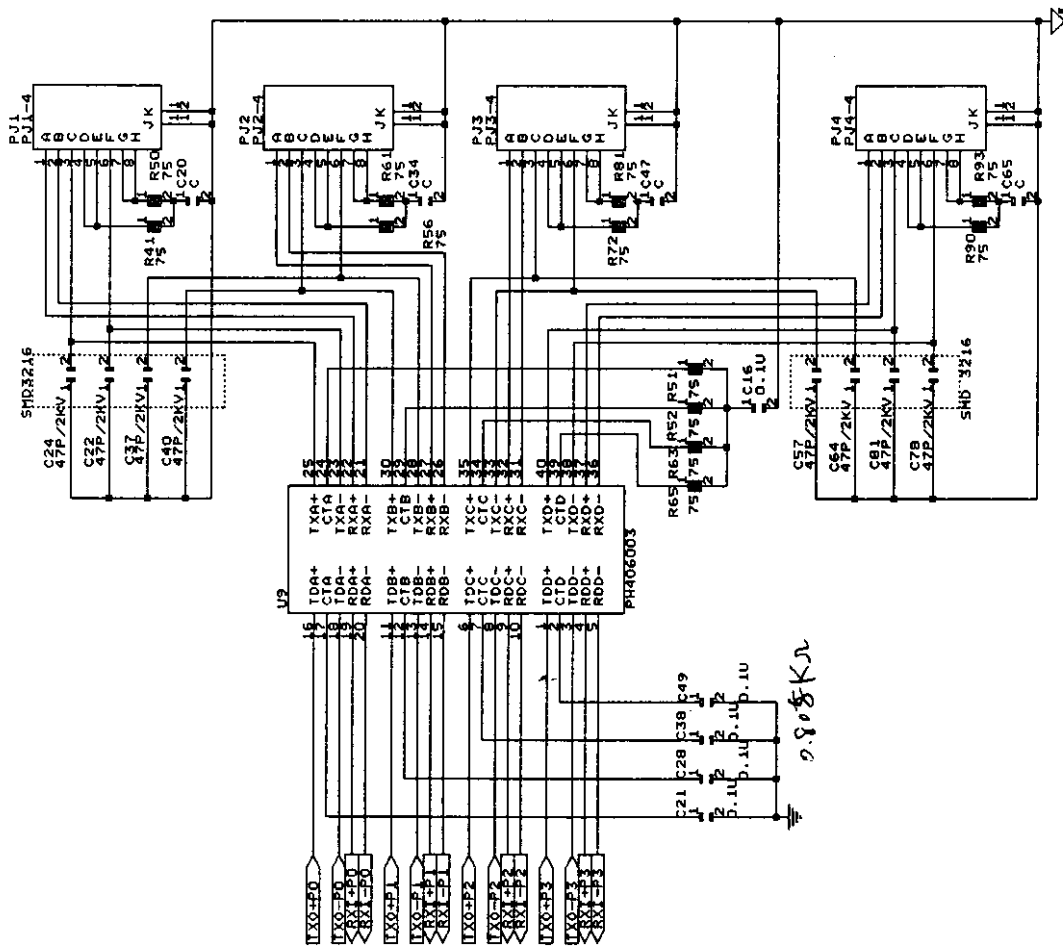


EXHIBIT D

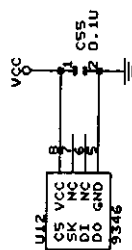
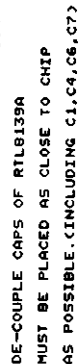
Circuit Diagram

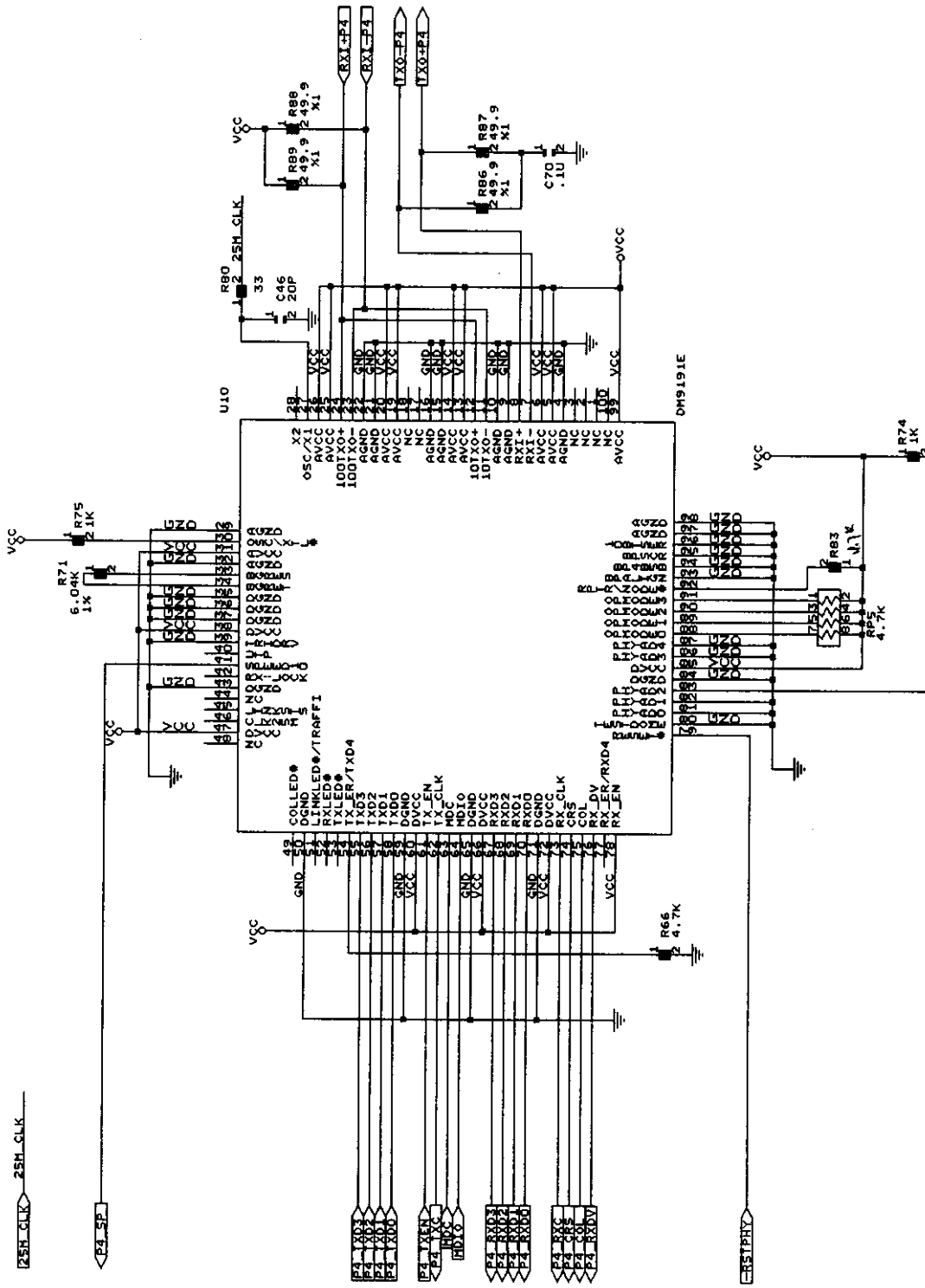




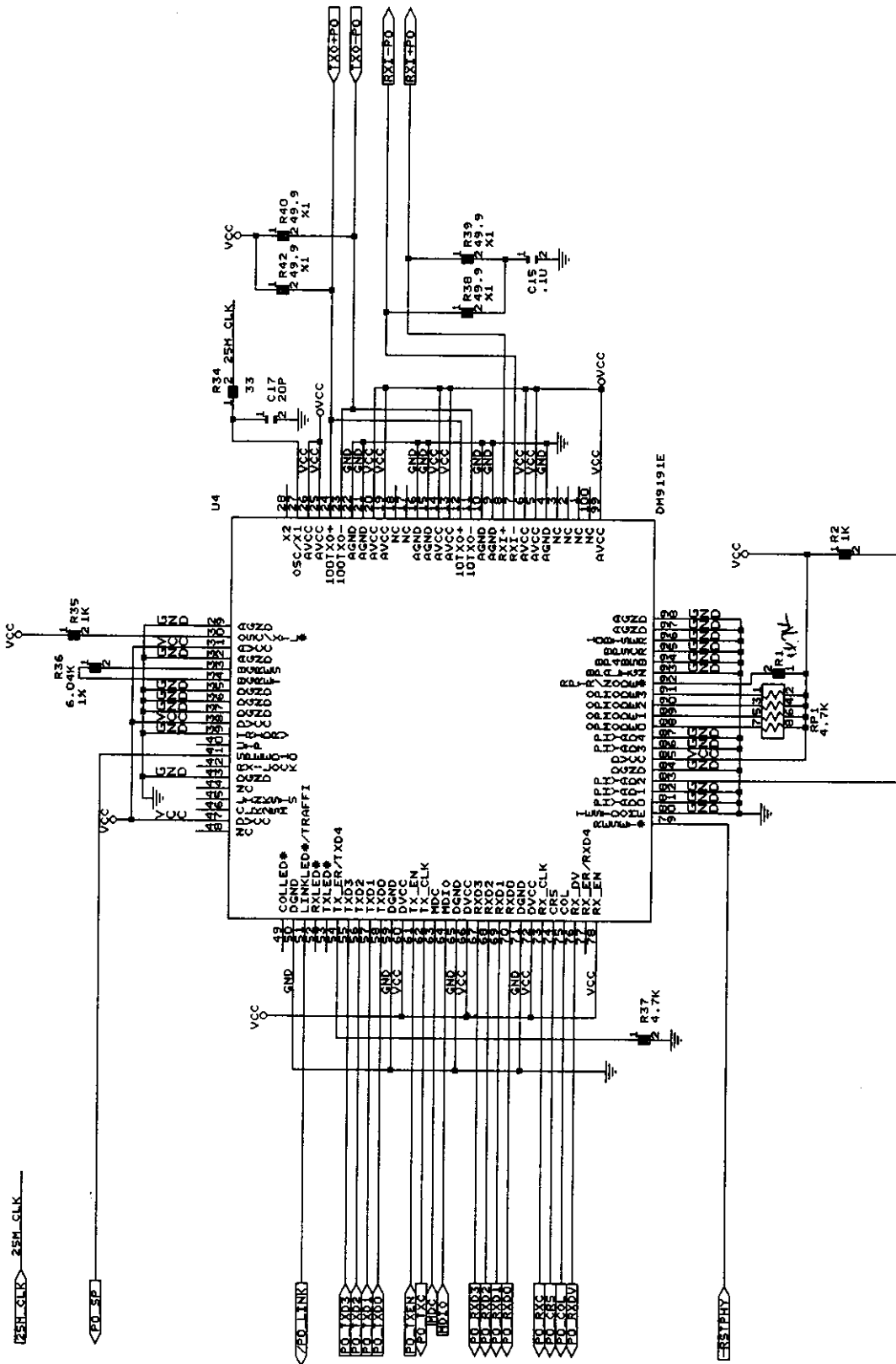


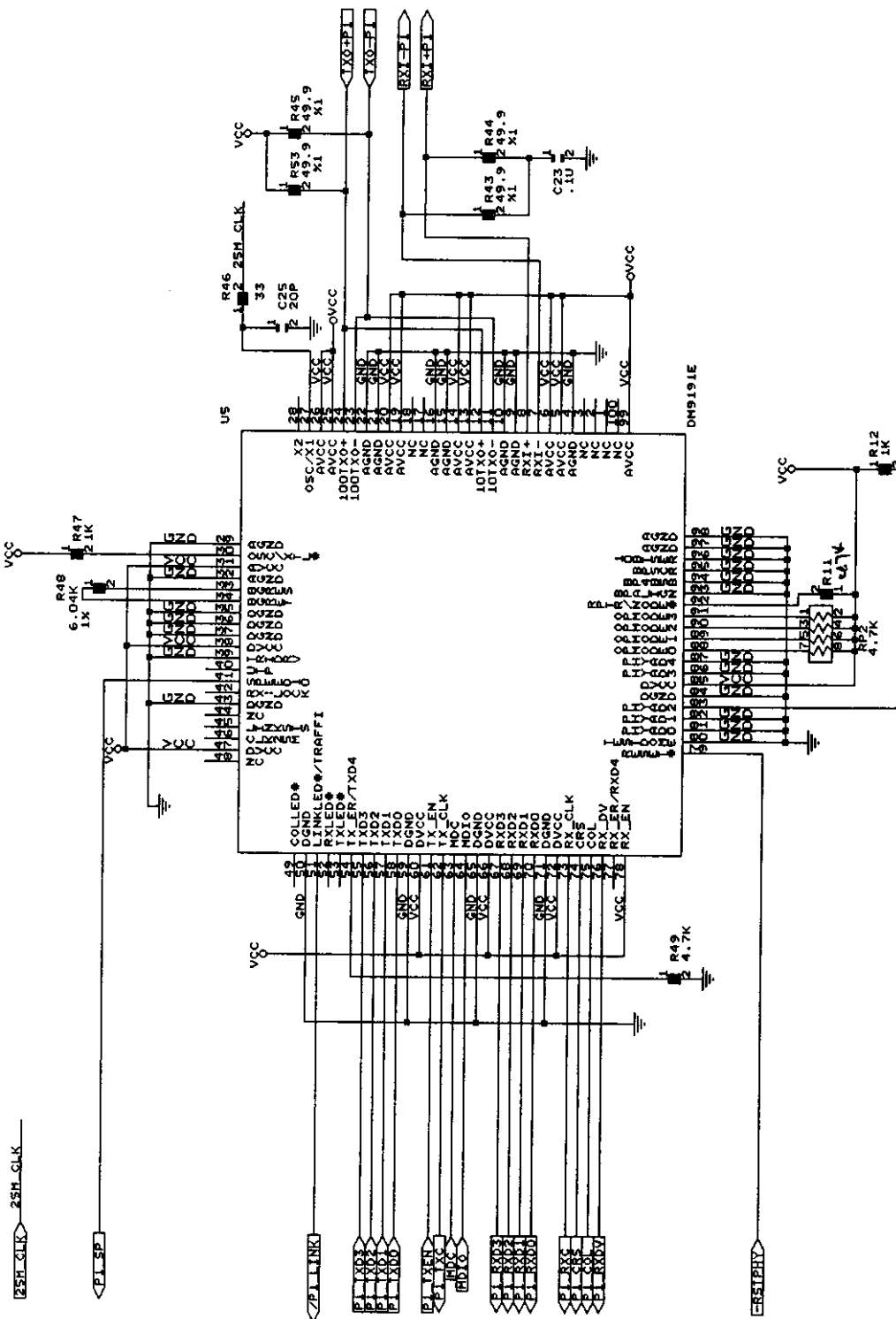
CIS TECHNOLOGY INC.	
FILE	: BLOCK.SCH
ENGINEER	: Bob Lee
P/N	: Main PCB Dim.
Title	
ETHERNET 10/100Mbps DUAL SPEED HUB	
Size	Document Number
B	CIS-DH05(1802K.SCH)
Date	: December 31, 1998
Sheet	11 of 11





CIS TECHNOLOGY INC.	
FILE : DM9101E.SCH	REV
ENGINEER : Bob Lee	B
P/N :	CIS-MS-DH05 FAST ETHERNET HUB(18021.00
Title	Date: December 3, 1998
DM9101E PHY	Sheet 9 of 11

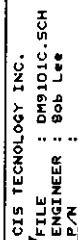




CIS TECHNOLOGY INC.
 FILE : DM9101B.SCH
 ENGINEER : Bob Lee
 P/N :

Title :

Size : 10000 Bytes
 Date : 31 Dec 1998
 Sheet : 6 of 11



DM9101E PHY

8 CIS-MS-PM05 FAST ETHERNET HIIRC1802G-00

DATE: December 2, 1965

