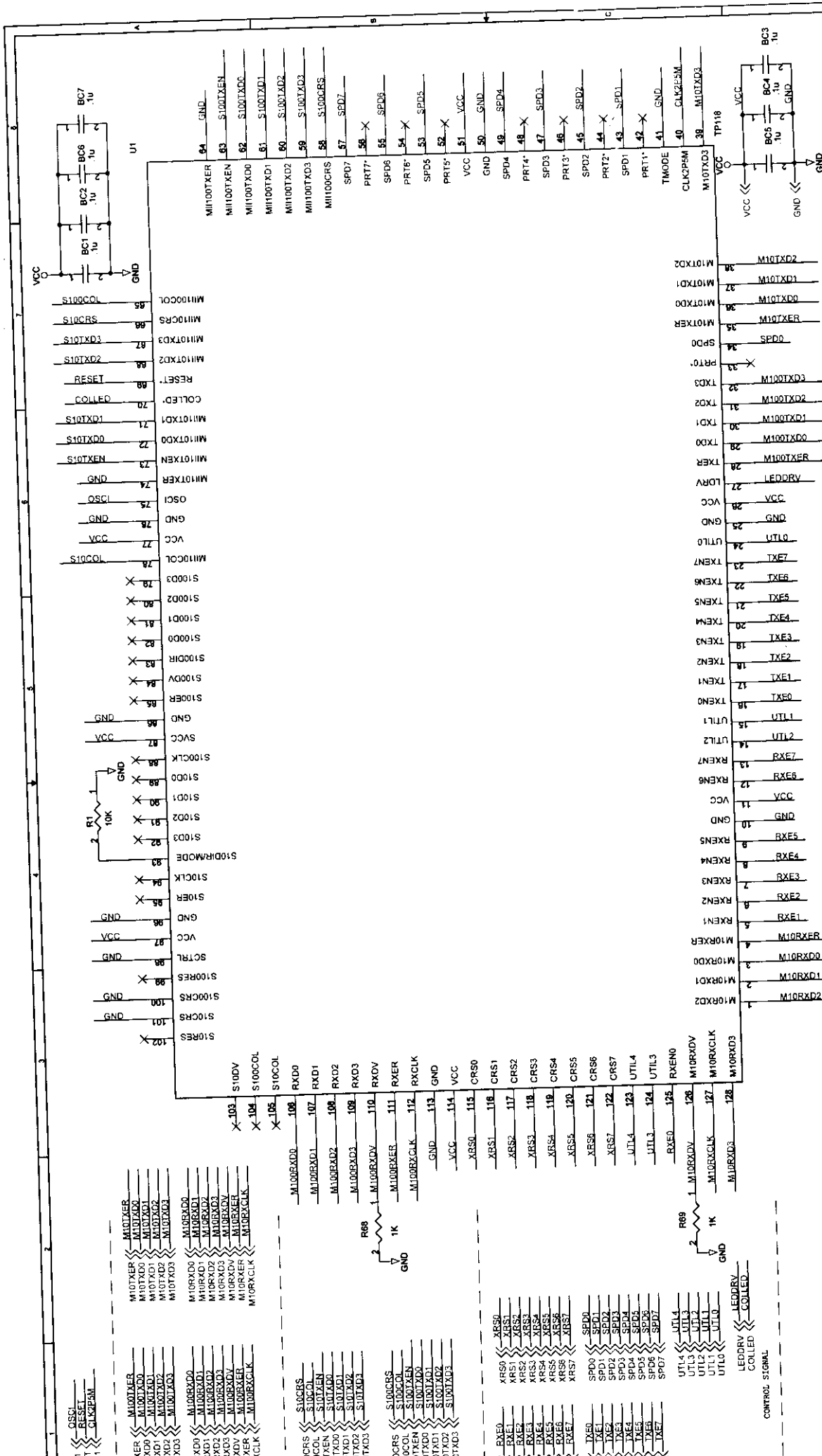


EXHIBIT D

Circuit Diagram

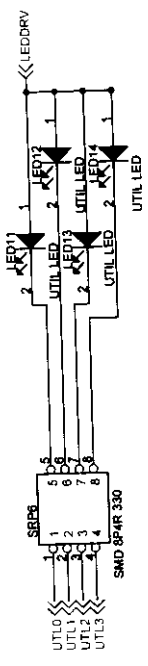
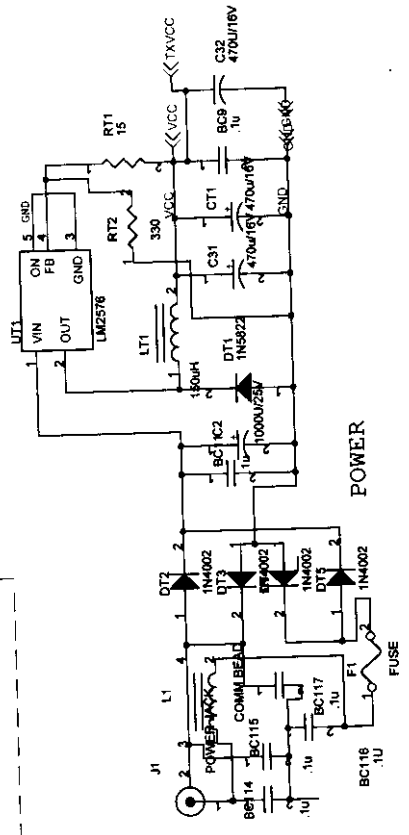
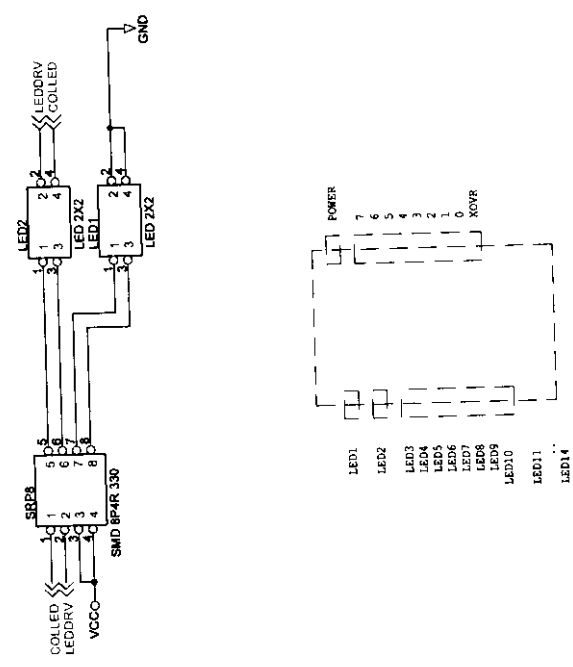
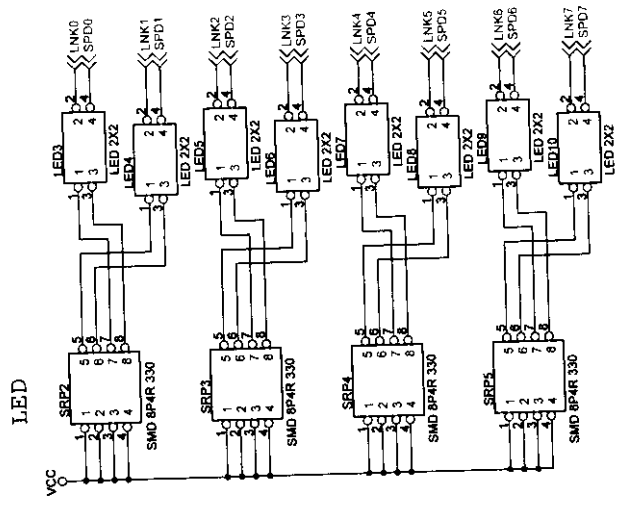
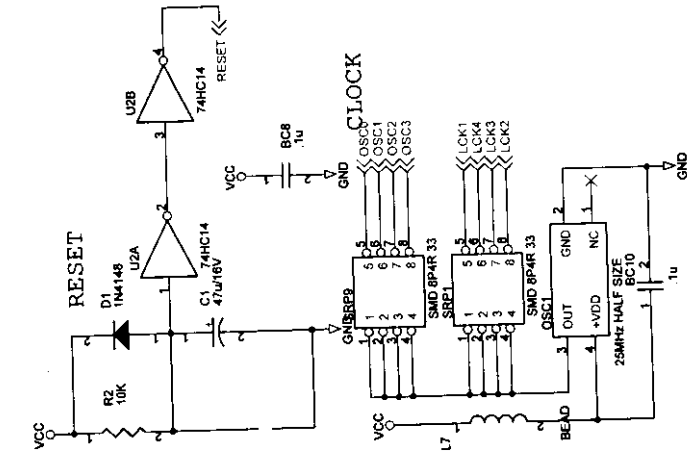


CIS TECHNOLOGY INC.
FILE: DH08-V2-1.SCH
ENGINEER: C. KANG
PIN: PL400807800

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Size	B	Document Number	CIS-DH08V2-DUMB-HUB-V1
Rev	1	Date	Wednesday, April 28, 1999



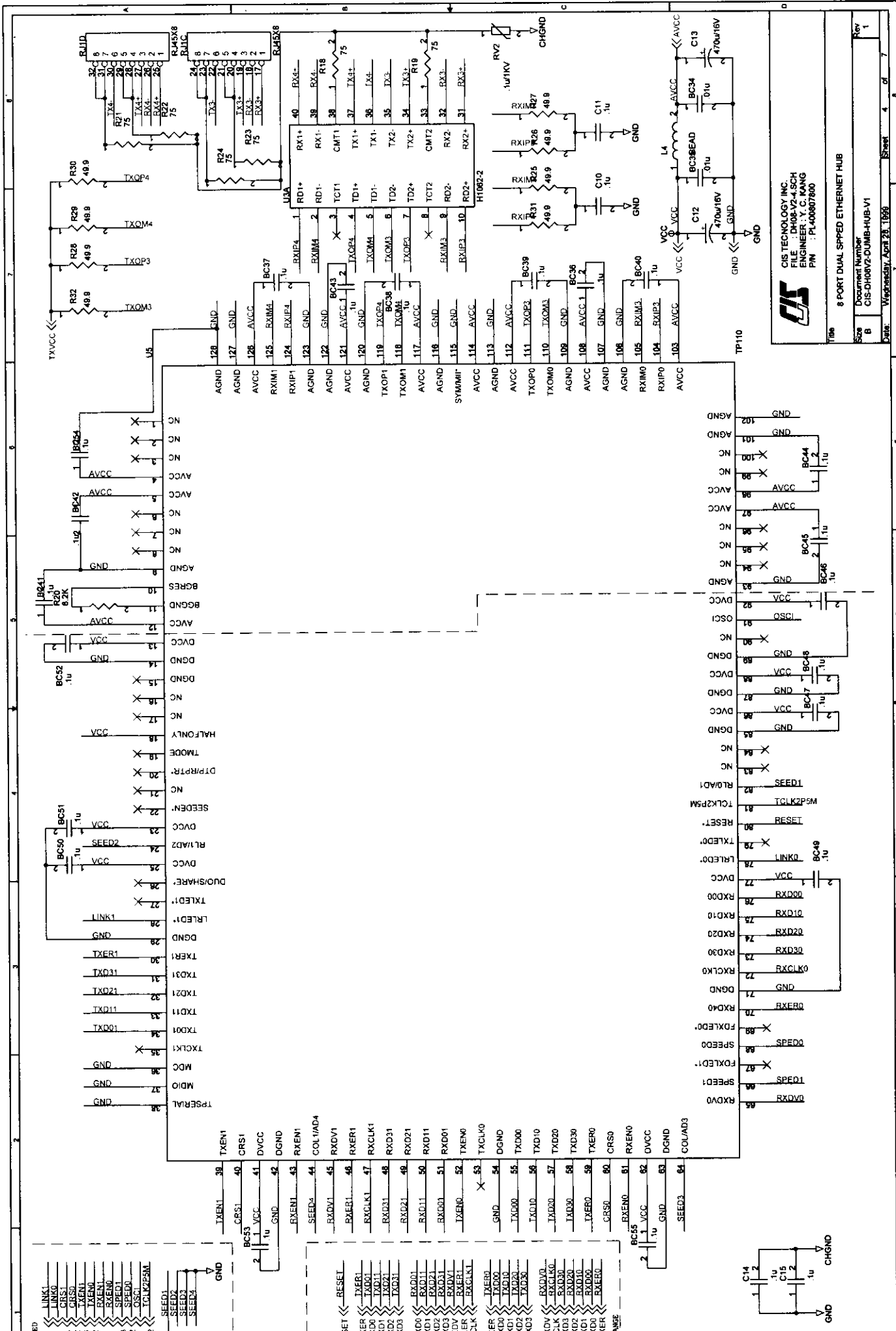
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1. DH08-V2-7. SCH



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 FILE : DH08V2-2.SCH
 ENGINEER : C. L. CHANG
 PIN : PL-00007/000

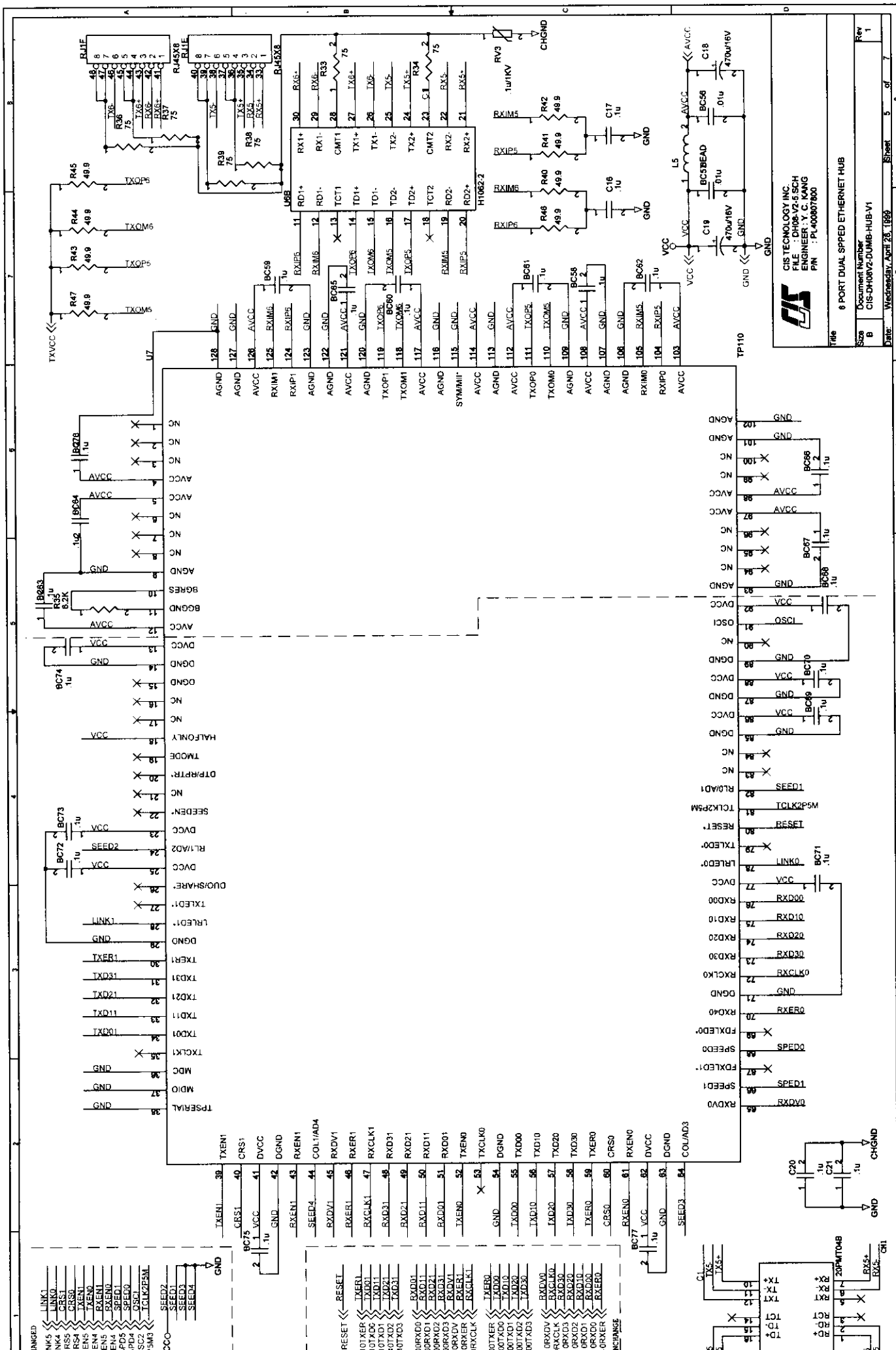
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 Date: Wednesday, April 28, 1999

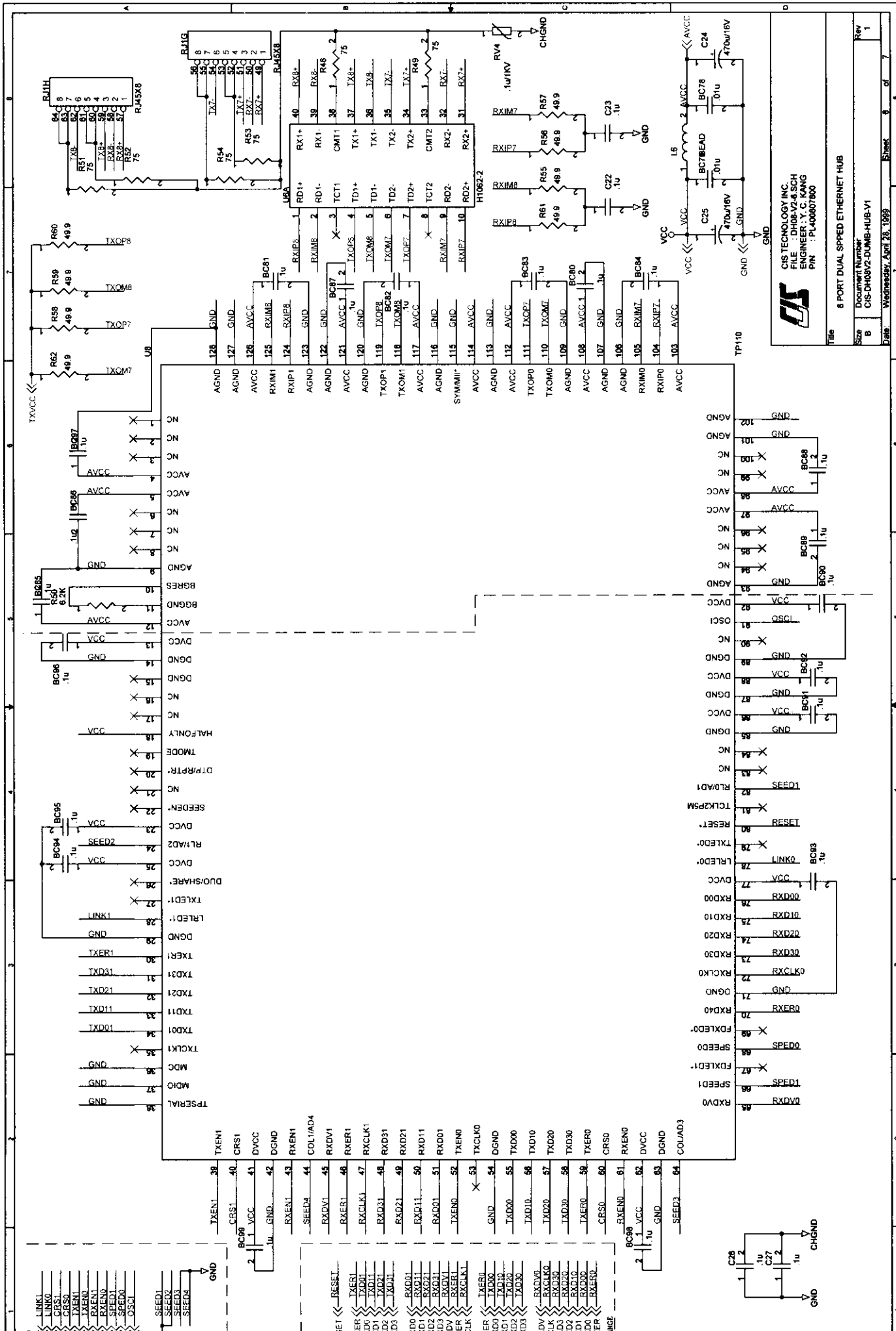
Size B
 Sheet 2 of 7
 Rev 1




CIS TECHNOLOGY INC.
 1000 N. 10TH AVE.
 ENGINEER - Y. C. KANG
 PIN : PL40007800

Title: **8 PORT DUAL SPEED ETHERNET HUB**
 Document Number: **CIS-DH06V2-DUMB-HUB-V1**
 Size: **8**
 Date: **Wednesday, April 28, 1999**





CIS TECHNOLOGY INC.
FILE : DH08-V2.8.SCH
ENGINEER : Y. C. KANG
PIN : PL40007800

Title		8 PORT DUAL SPEED ETHERNET HUB	
Size	B	Document Number	CIS-DH08V2-DUMB-HUB-V1
Rev	1	Date	Wednesday, April 28, 1999
Sheet	8	of	7

