

1. Description of Operation.

Super Heterodyne (315MHz) circuit :

RF signal is received by helical antenna, the signal goes to a 315 MHz SAW filter a LNA pramplifier and finally applied to Infineon Receiver IC TDA5211. This receiver IC has a LNA Stage one mixer, one IF filter at 10.7 MHZ a ASK detector , a low pass data filter of 5KHz and a data slicer circuit. Received digital Data is further processed by the master microcontroller IC. The LO signal which is also applied to the mixer stage is generated By a Frequency syntheziser which consist of a crystal controlled Reference oscillator running at 10.178 MHz a VCO running at 651.4 MHz which is divided by 2 and applied to the mixer to get the 10.7 MHz IF frequency. The VCO (651.4 MHz) is also divided by 64 in this process.

2. Owner/Operator manual (if available)

Not Available.

3. Product Information.

a) Designed by :

Delphi Delco Electronics

b) Manufactured by :

Delphi Delco Electronics

c) Person responsible for the product :

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