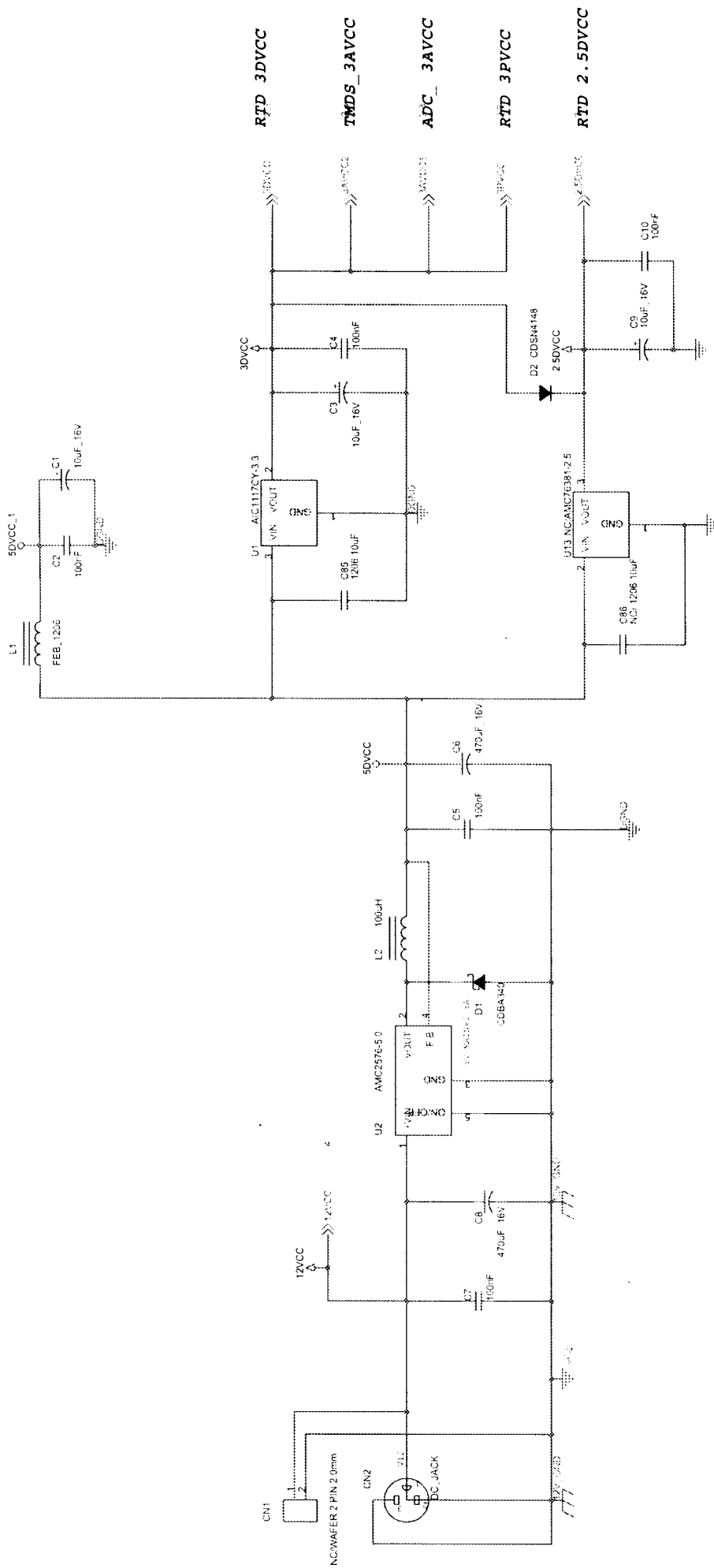
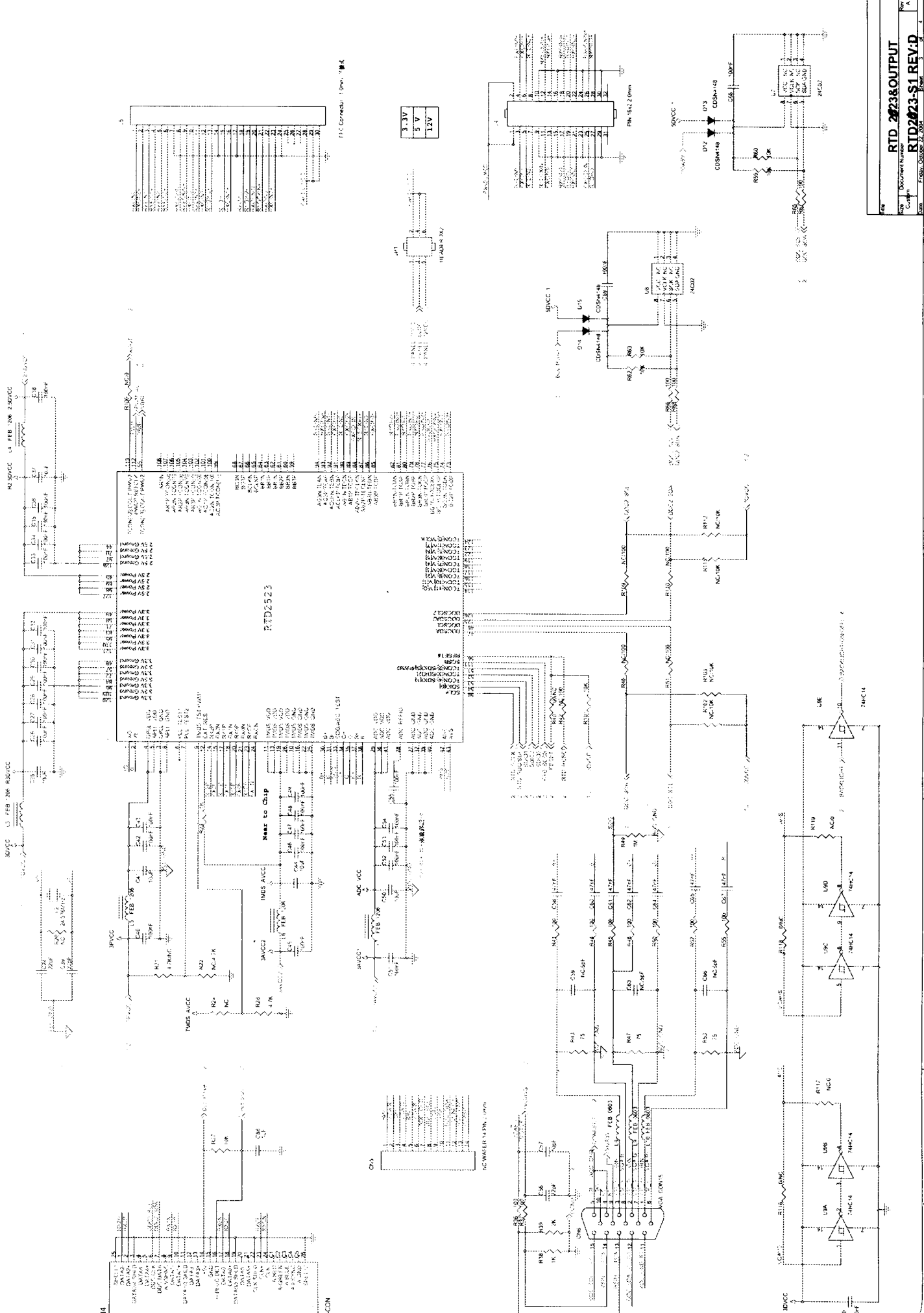
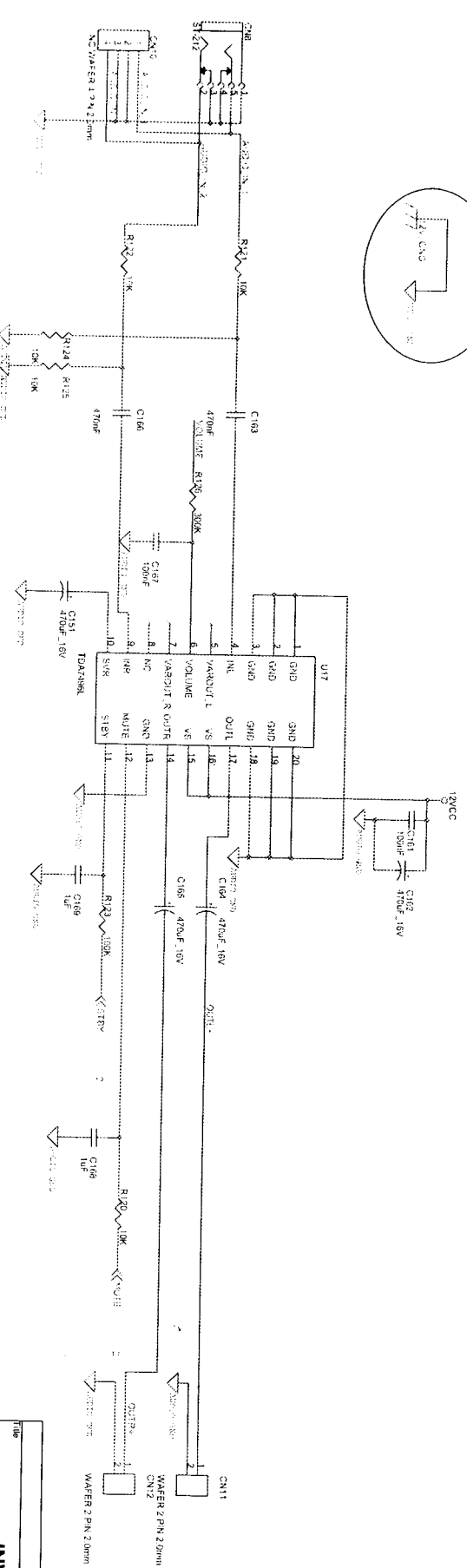
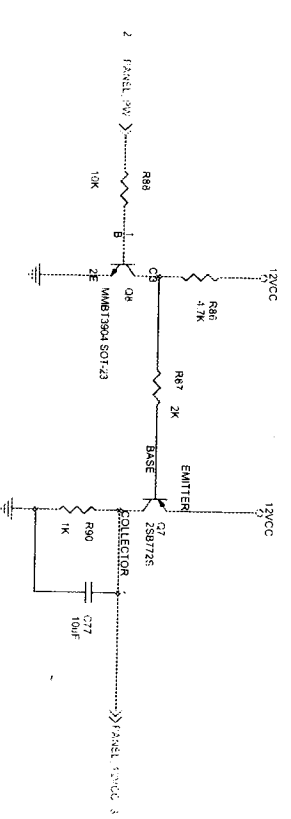
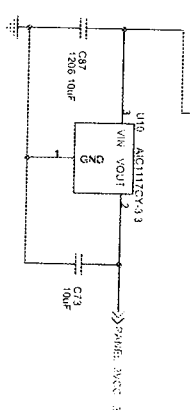
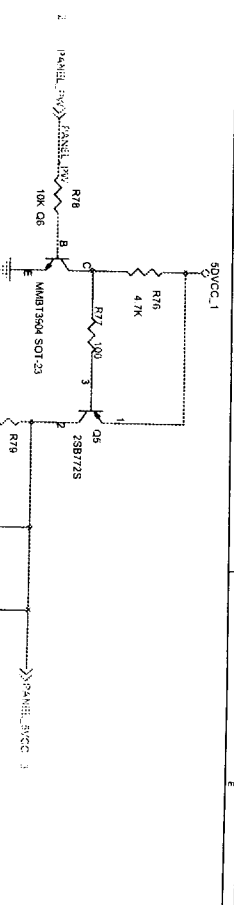
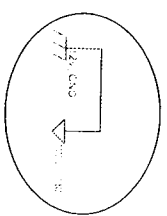
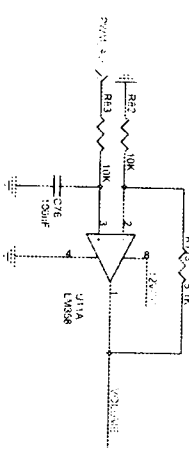
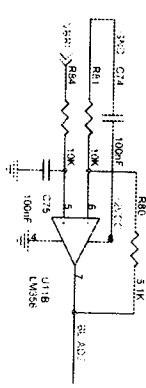
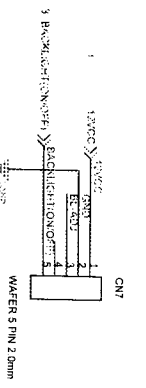
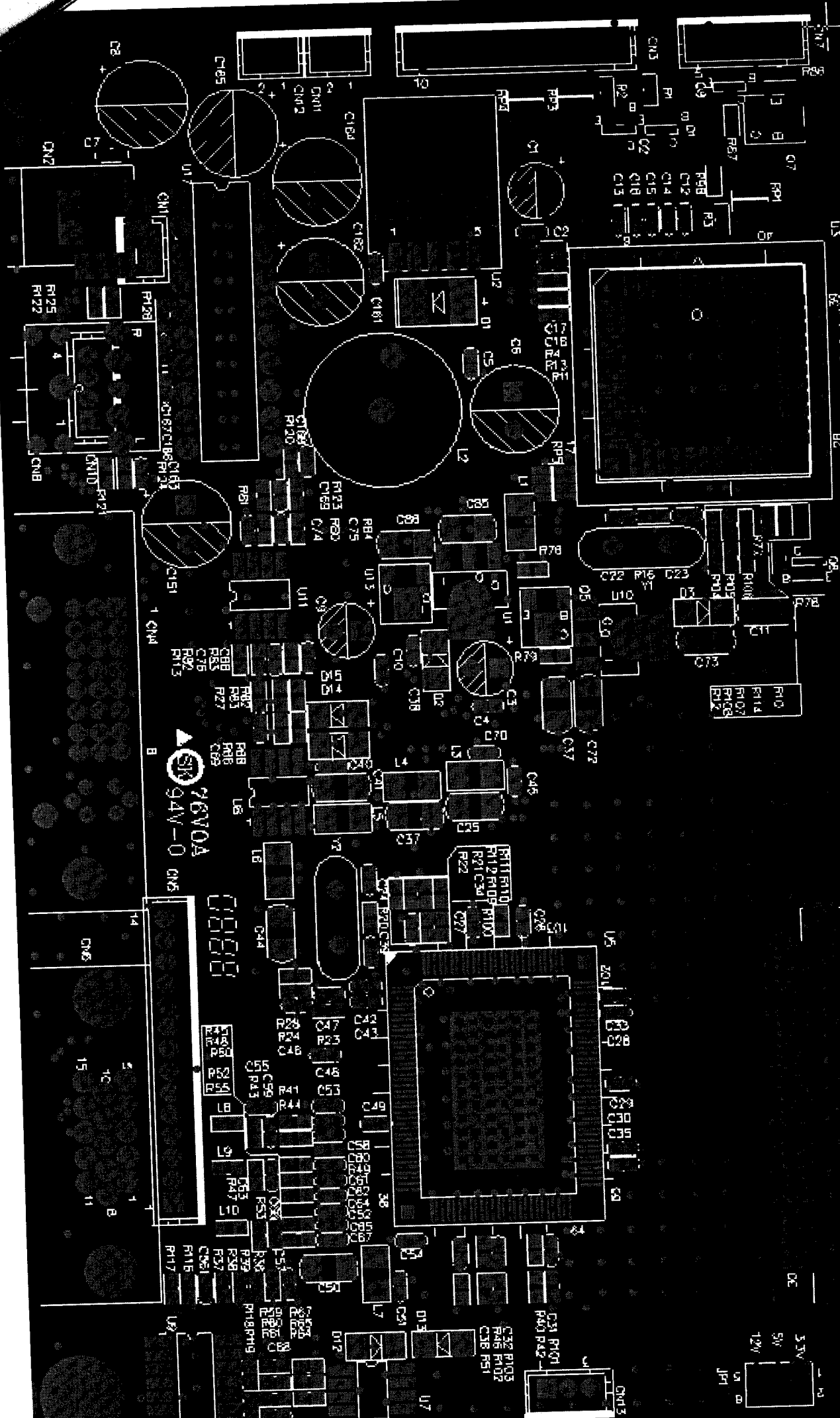
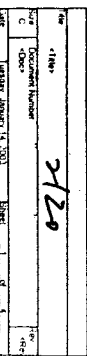


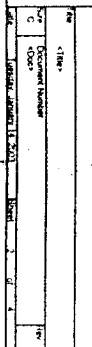
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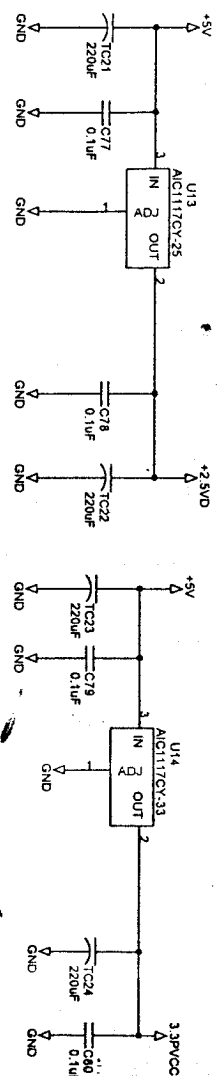
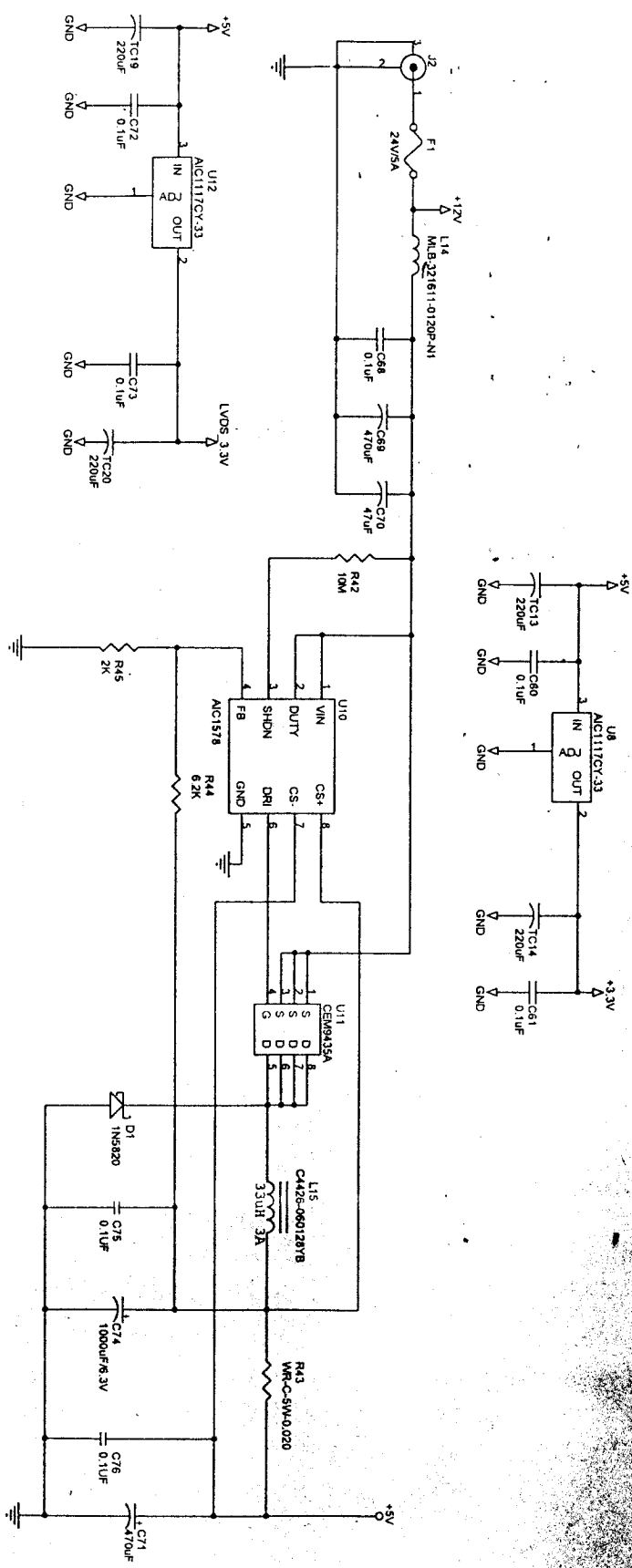




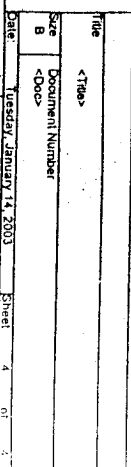


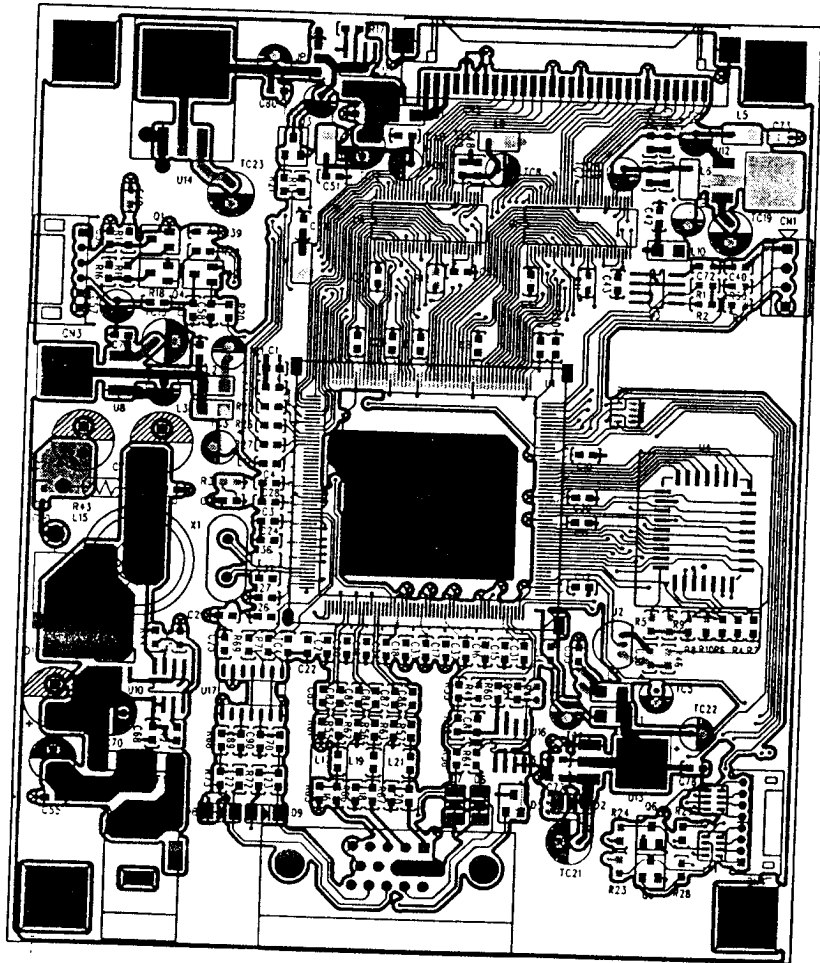


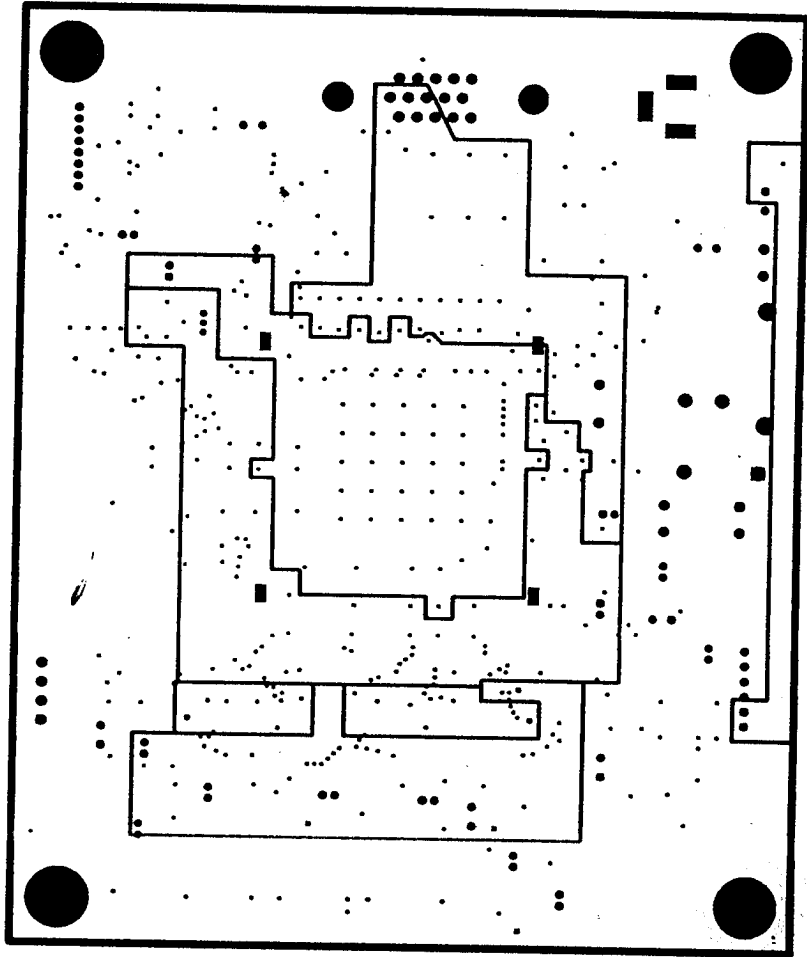


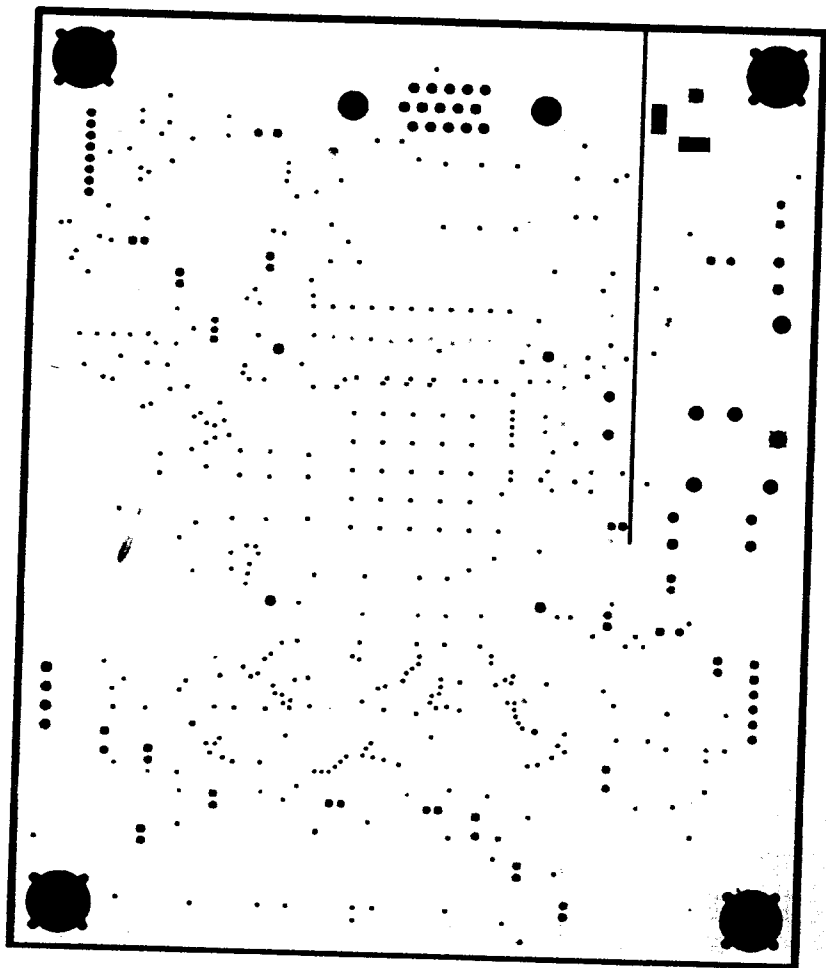


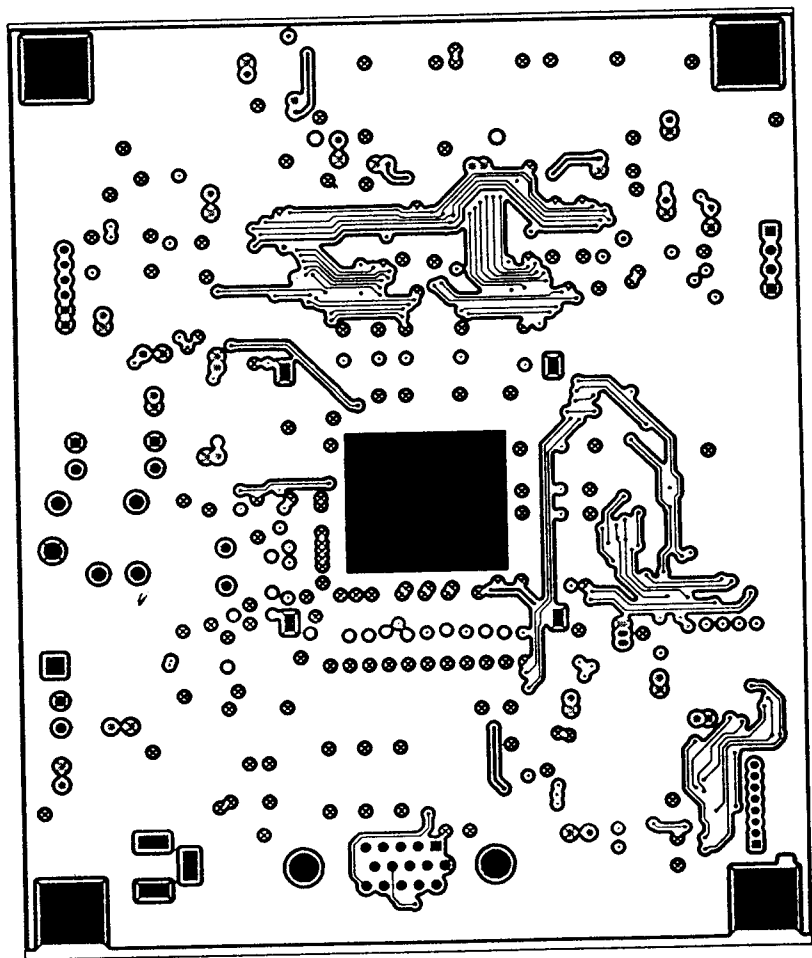
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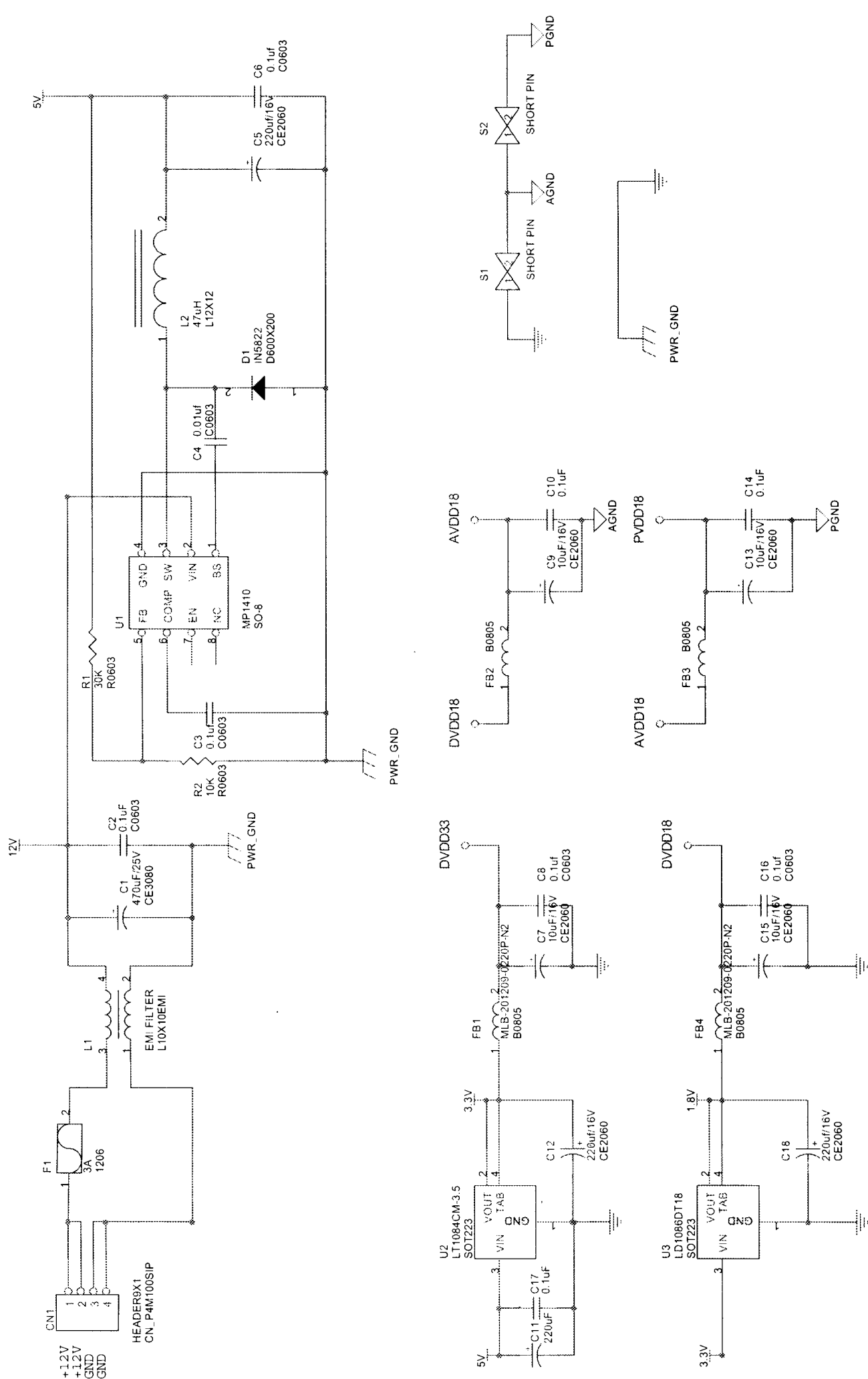










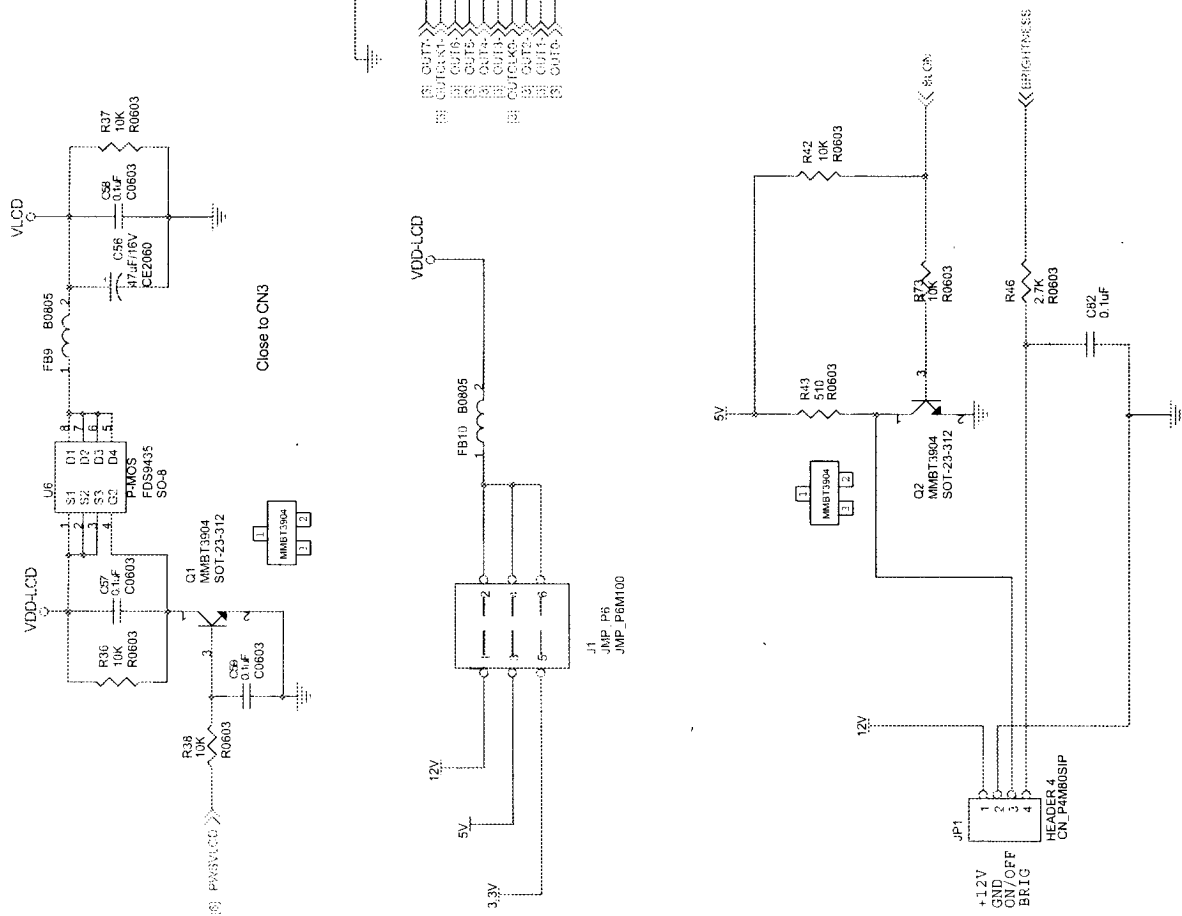


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Document:
KonAB 100-Pin 2-Layer Board With Dual LVDS Output

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Note:
 For Samsung SXGA 17"
 R47 and R50 both NP
 For AU L170E1-2 (Korean LVDS)
 R47=NP, R50=0ohm
 For AU L170E1-2 (Japanese LVDS)
 R47=4.7K, R50=0ohm

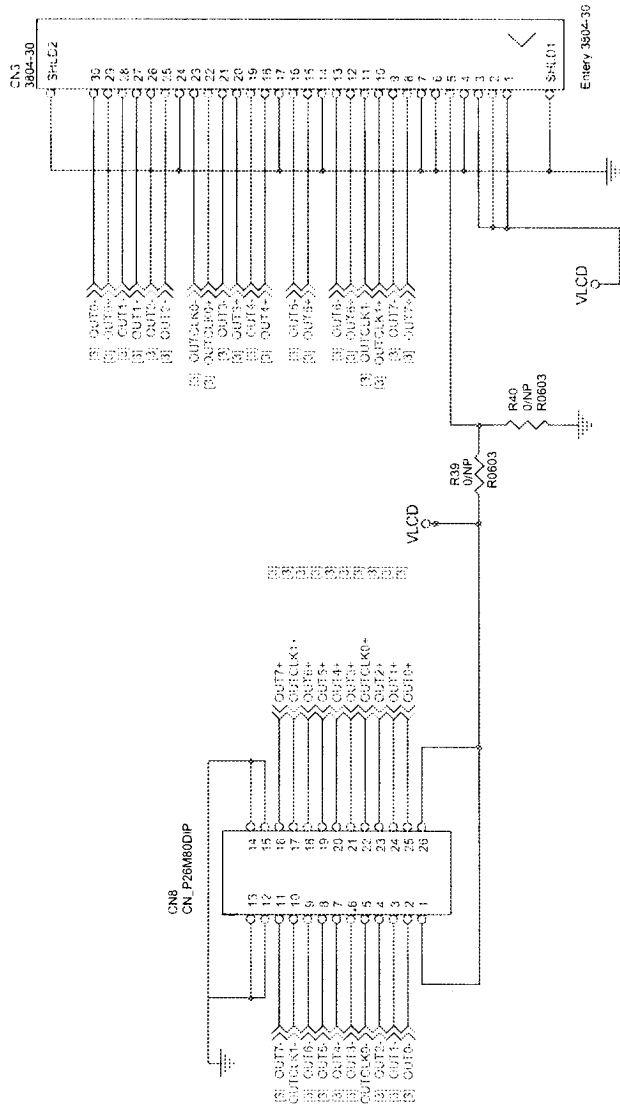
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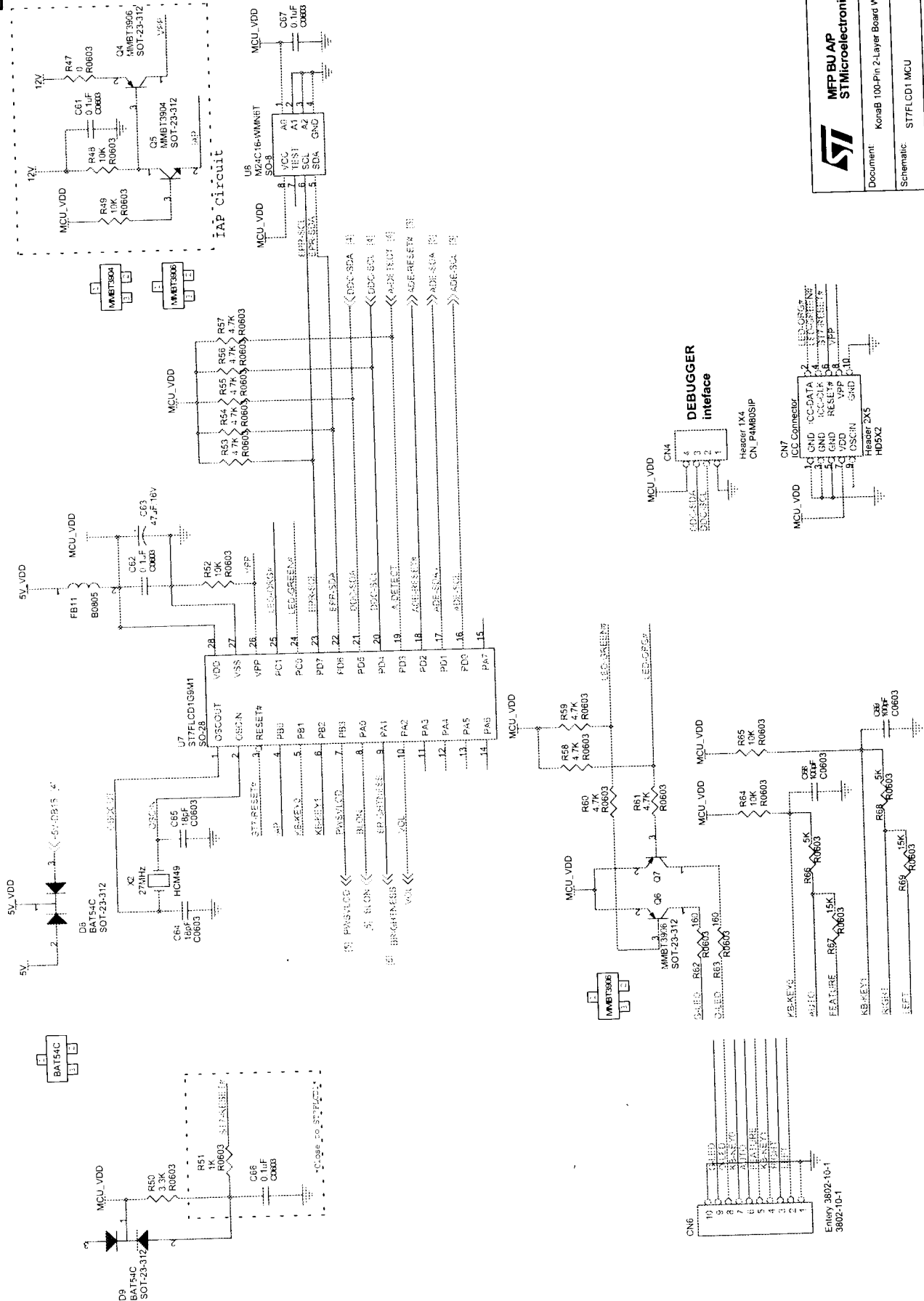
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Schematic: LVDS Output Interface

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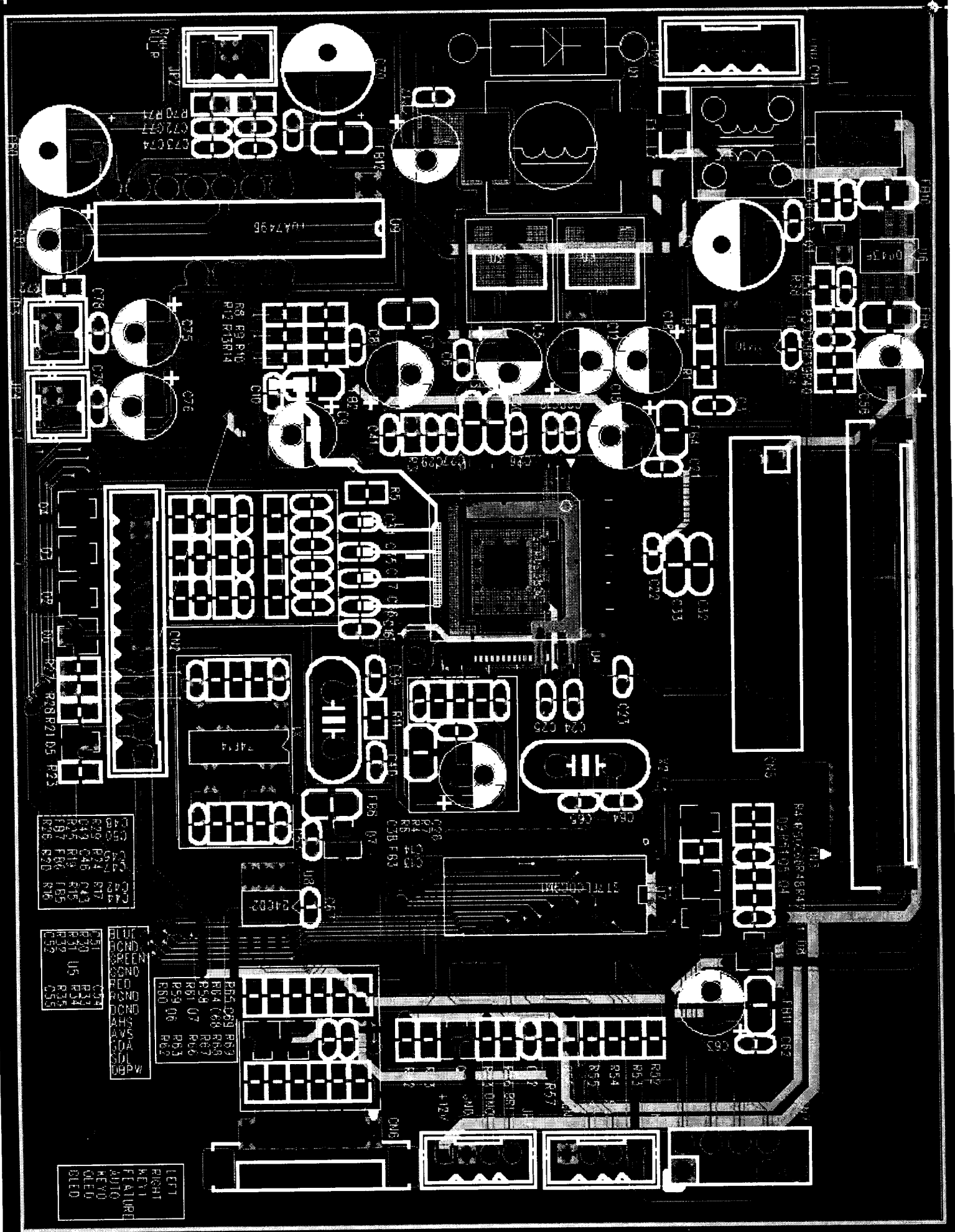
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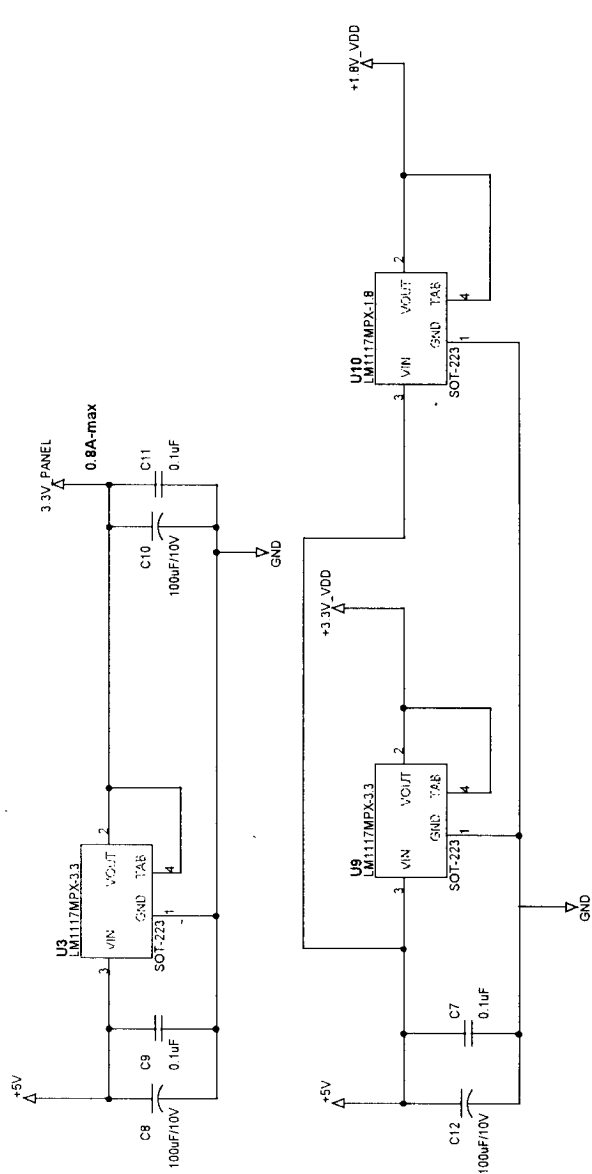
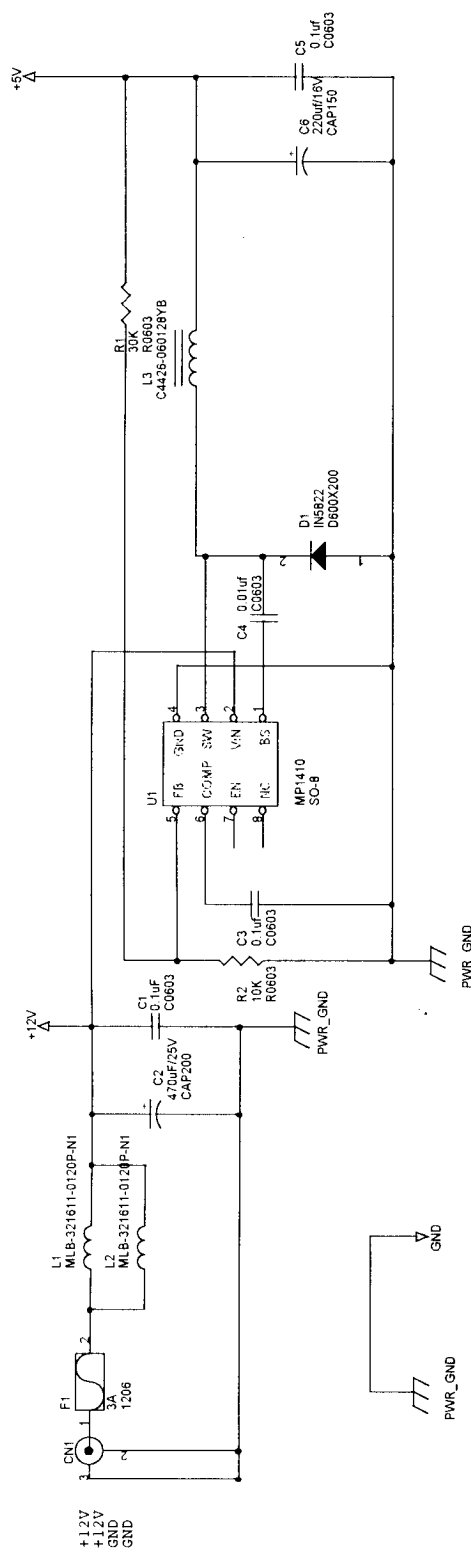
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Schematic: ST7FLCD1 MCU

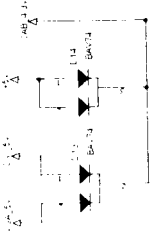
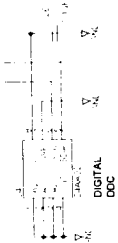
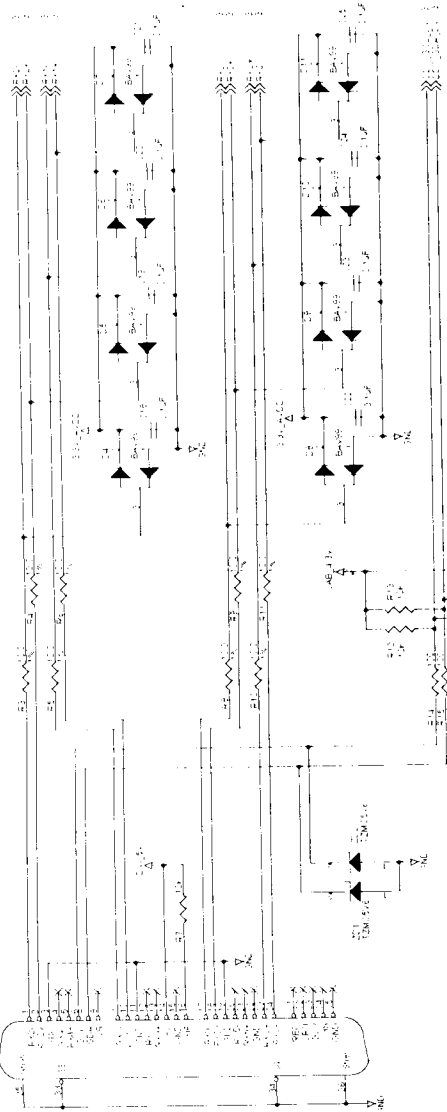
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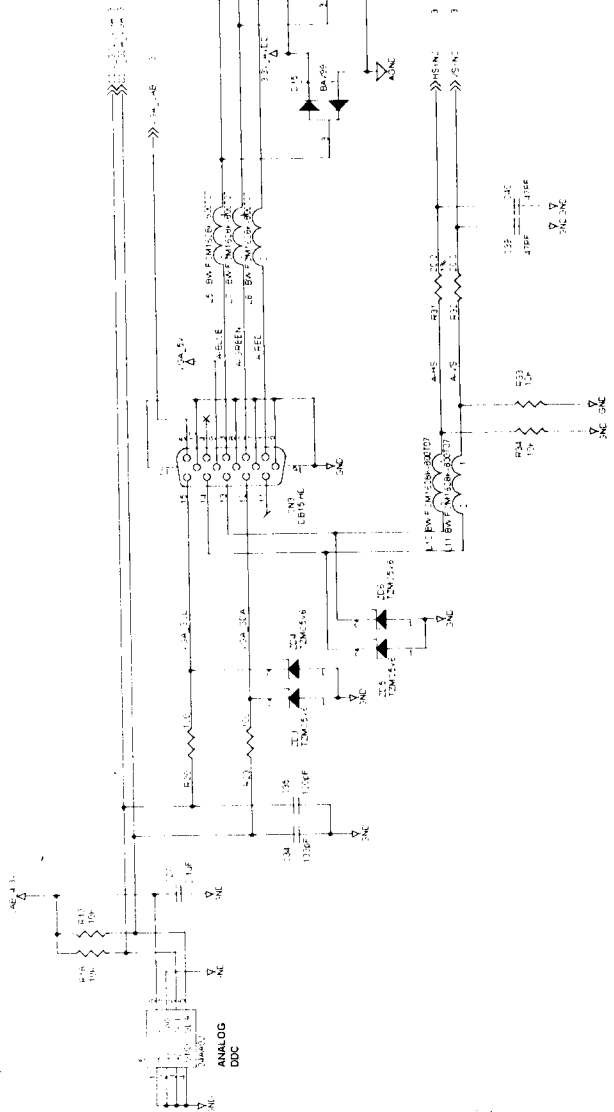




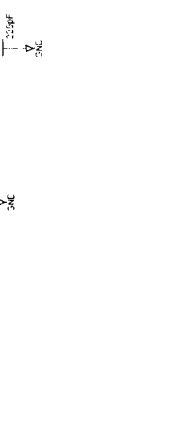
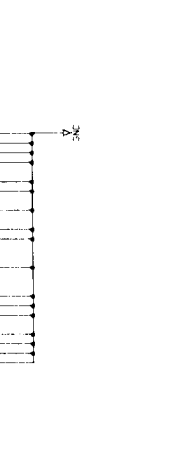
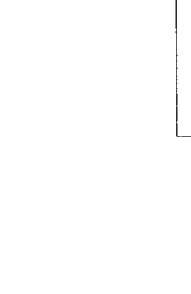
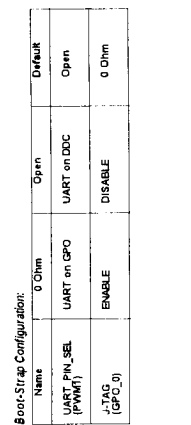
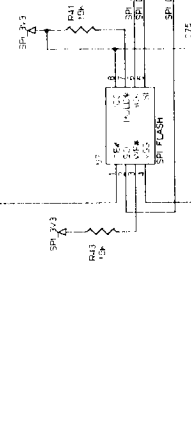
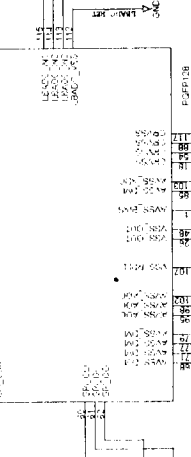
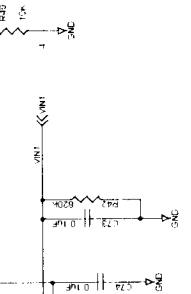
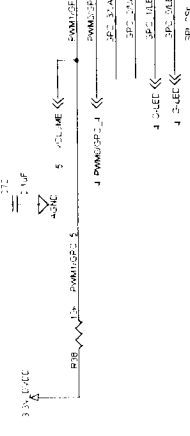
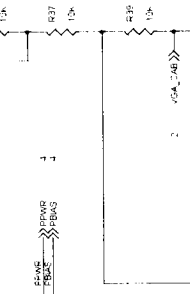
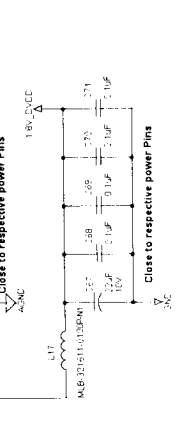
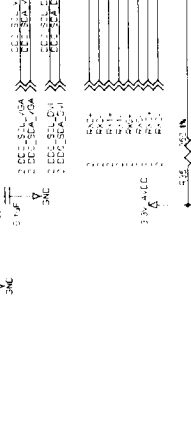
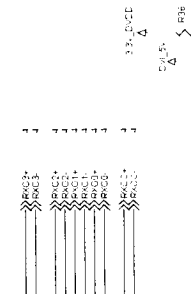
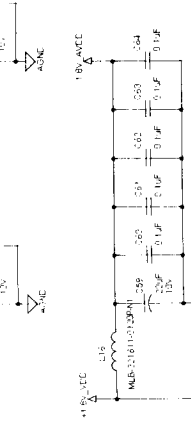
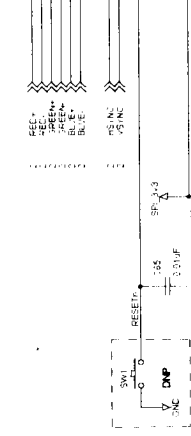
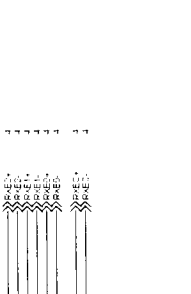
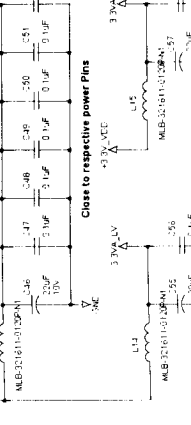
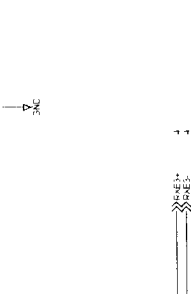
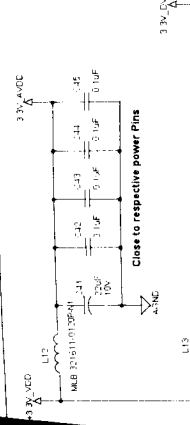
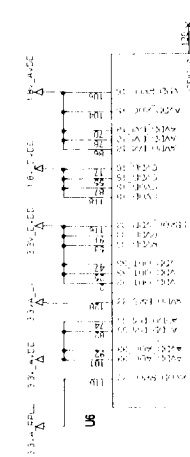
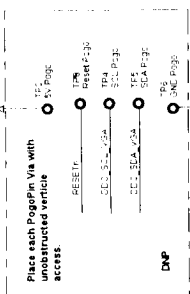
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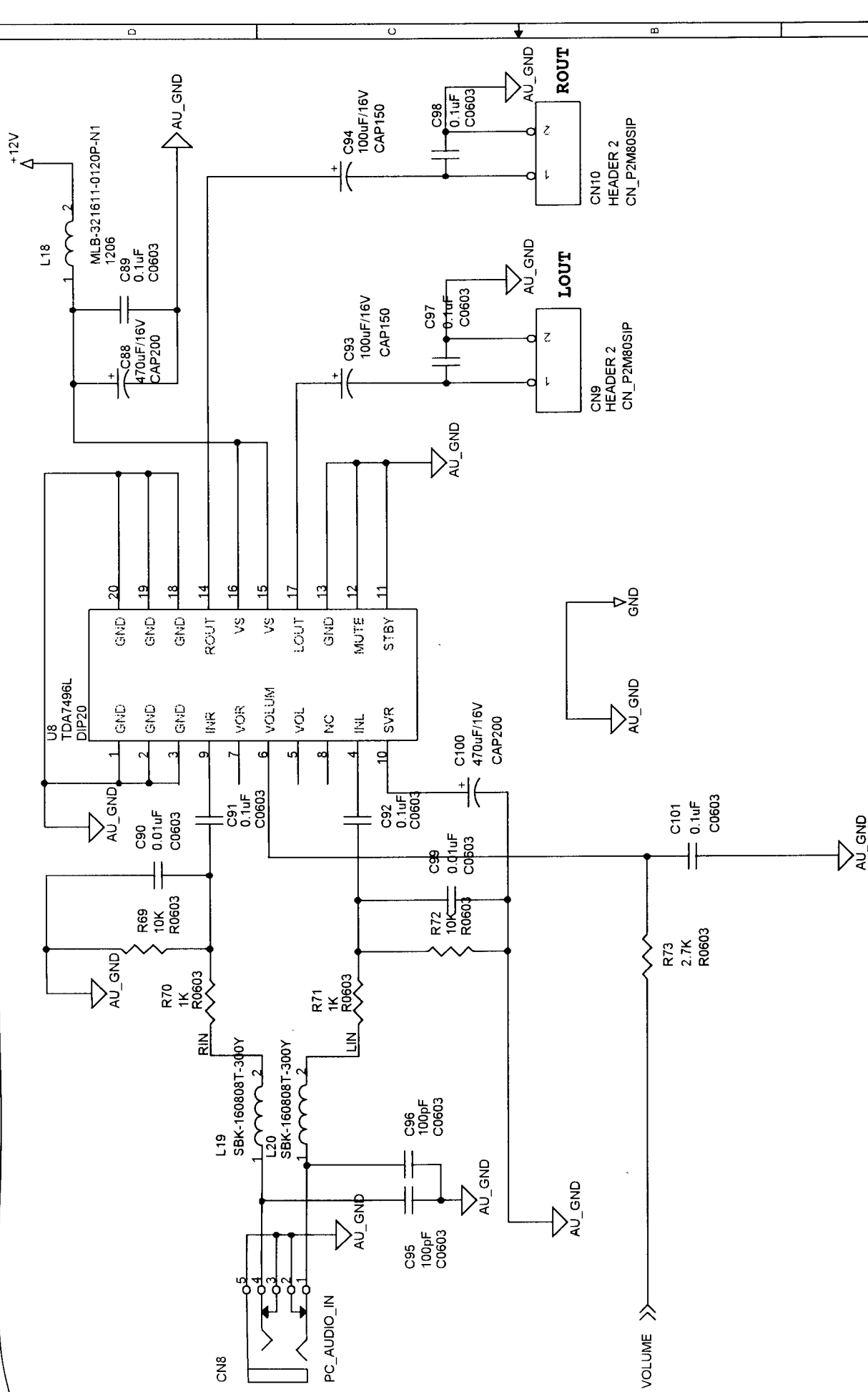


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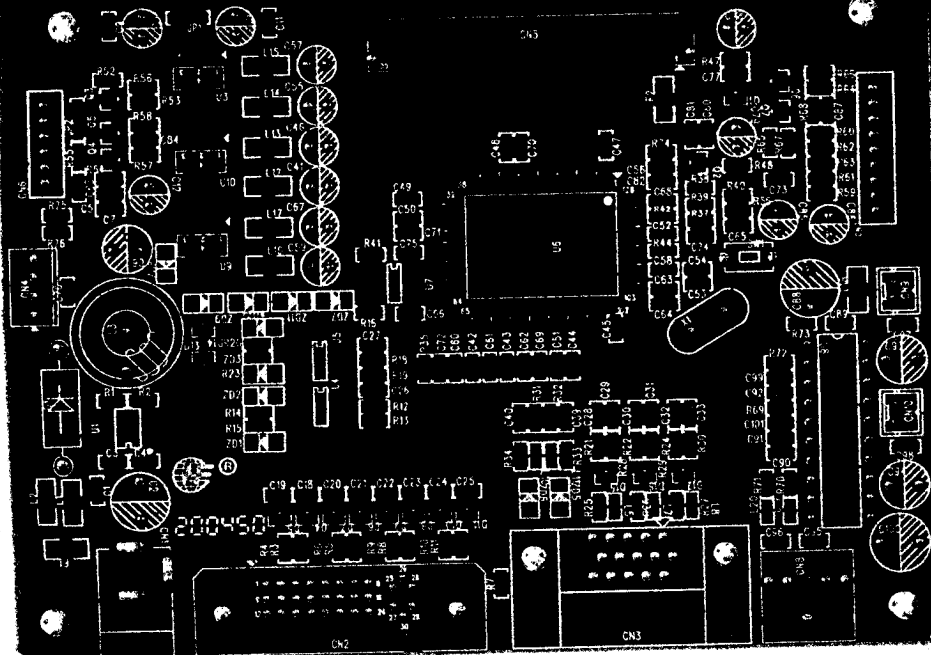


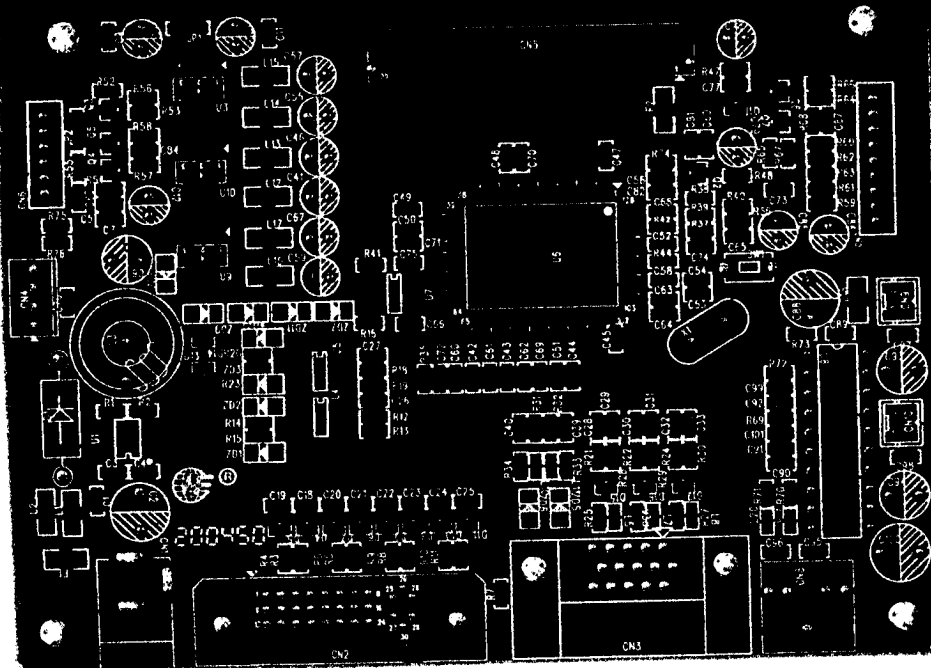
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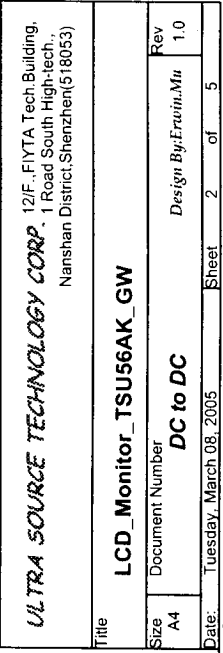


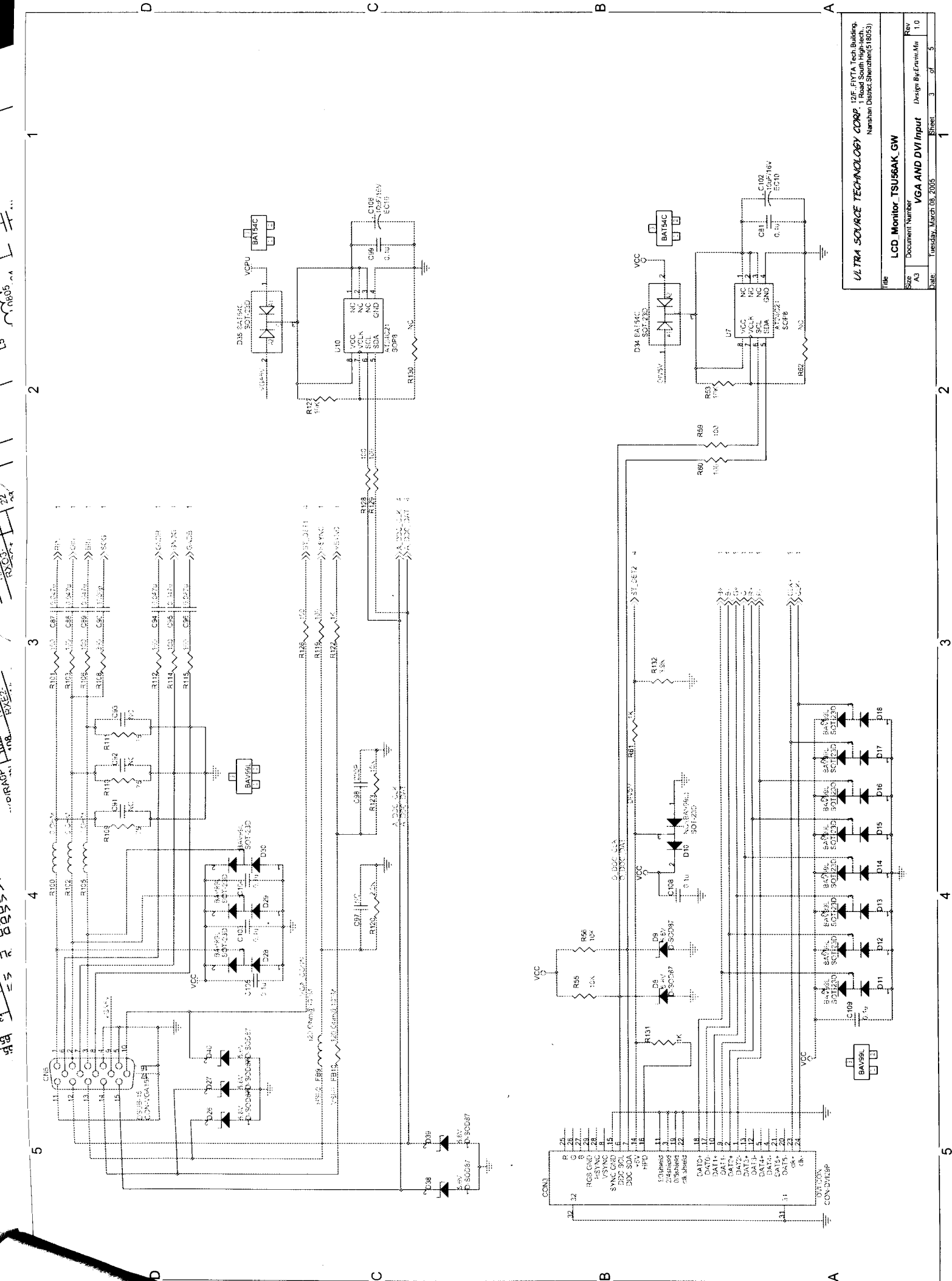


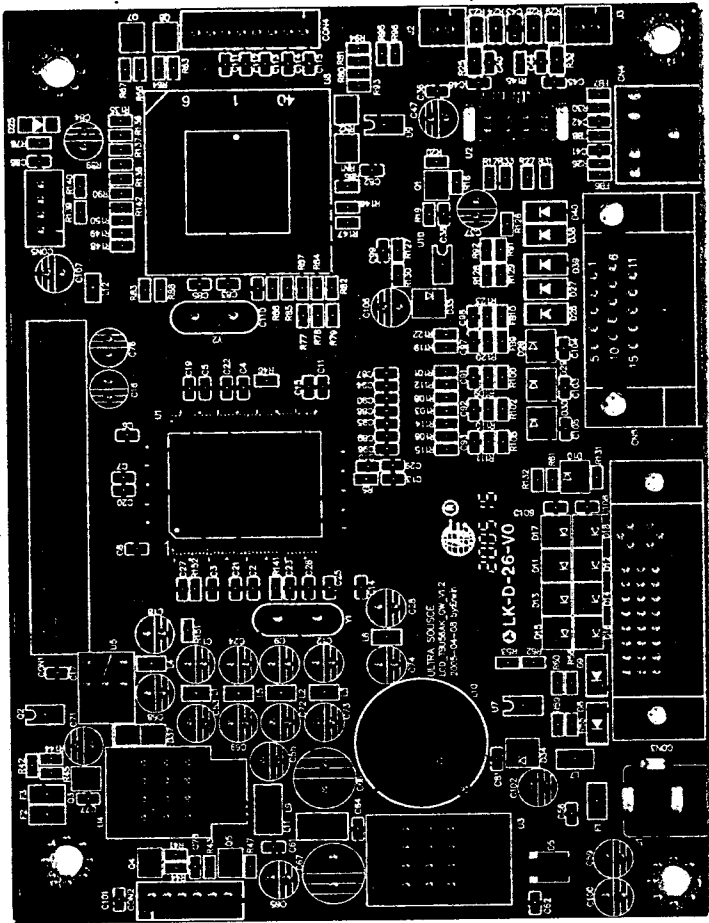
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2008 年 12 月