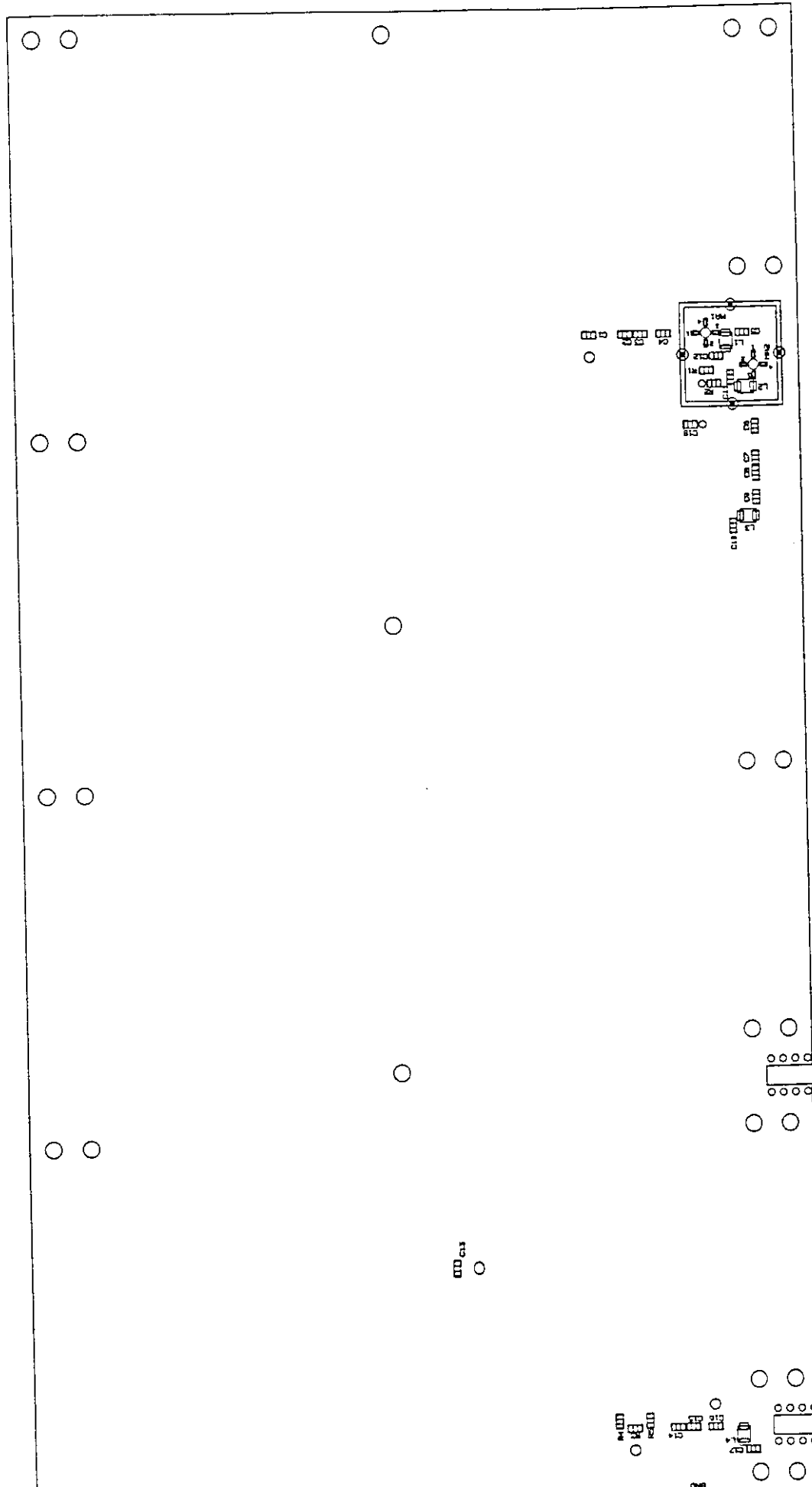
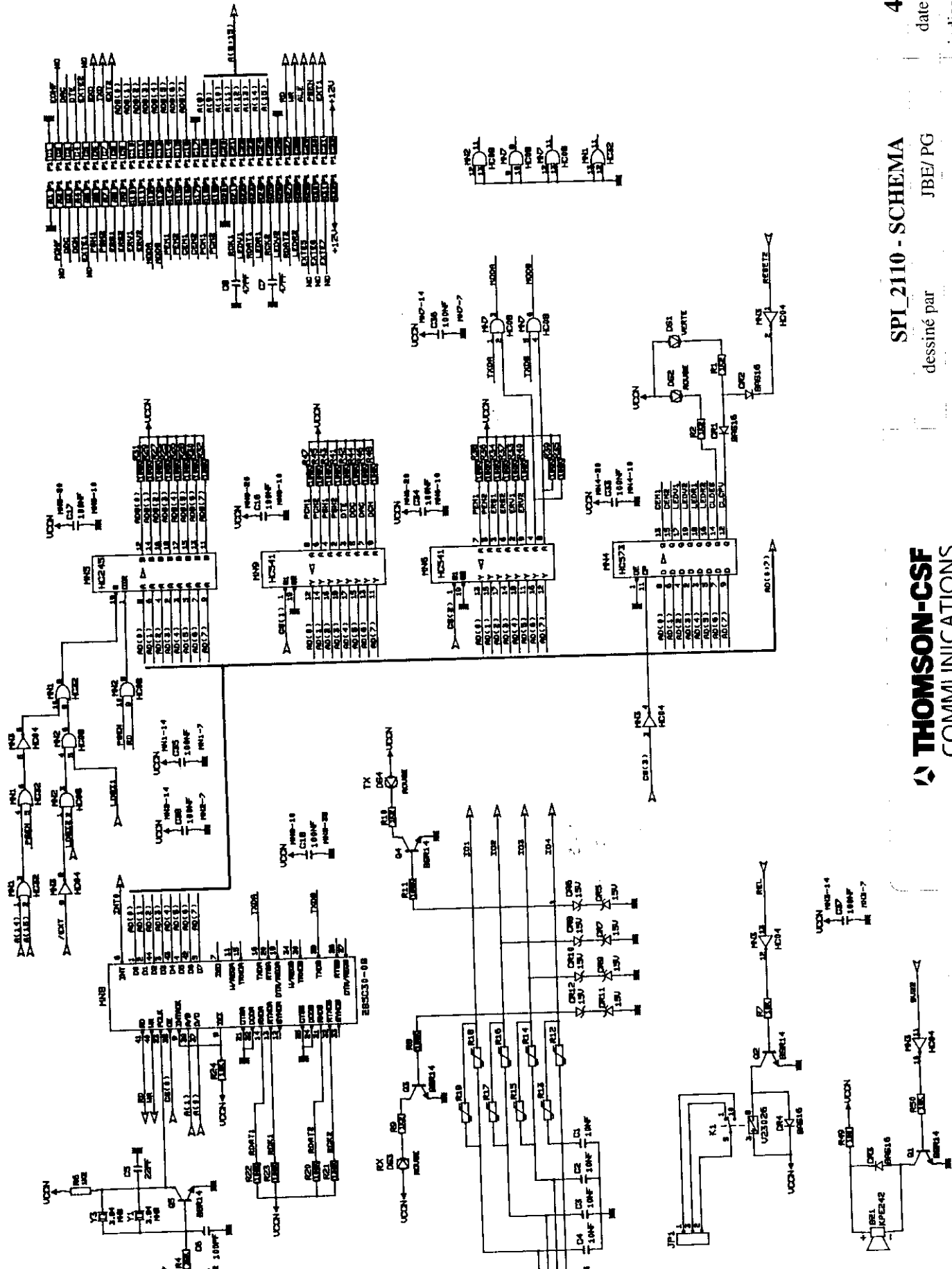




AT3_2749 - Components location
dessiné par JBE / PG
visa

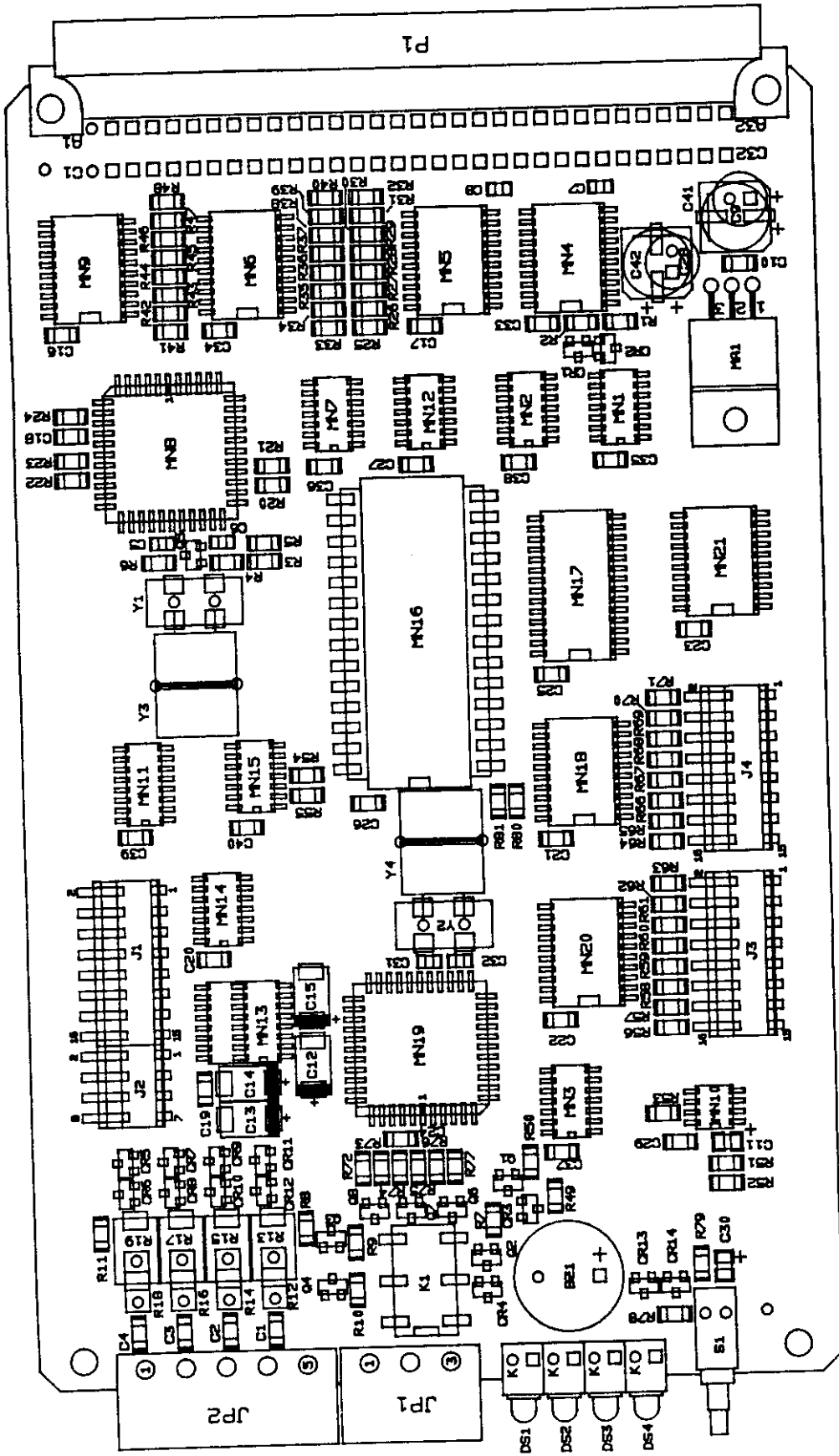


SPI 2110 - SCHEMA

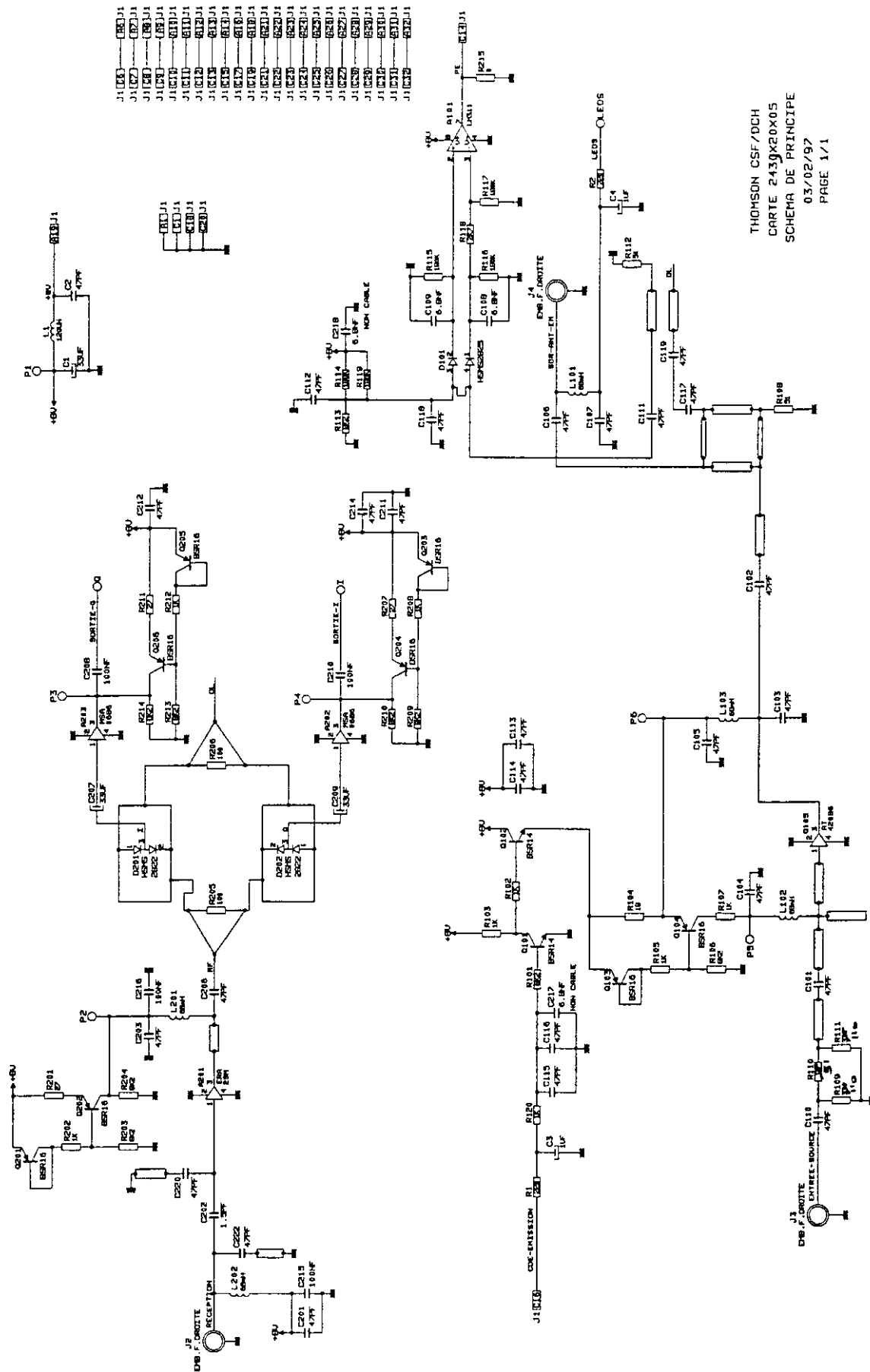
JBE/PG

dessiné par

visa



NOTA : NE PAS CABLER R55 ET R80



THOMSON CSF/DCH
 CARTE 2439x20x05
 SCHEMA DE PRINCIPE
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HF 2439 -SCHEMA

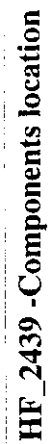
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visa

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date 10/4/98

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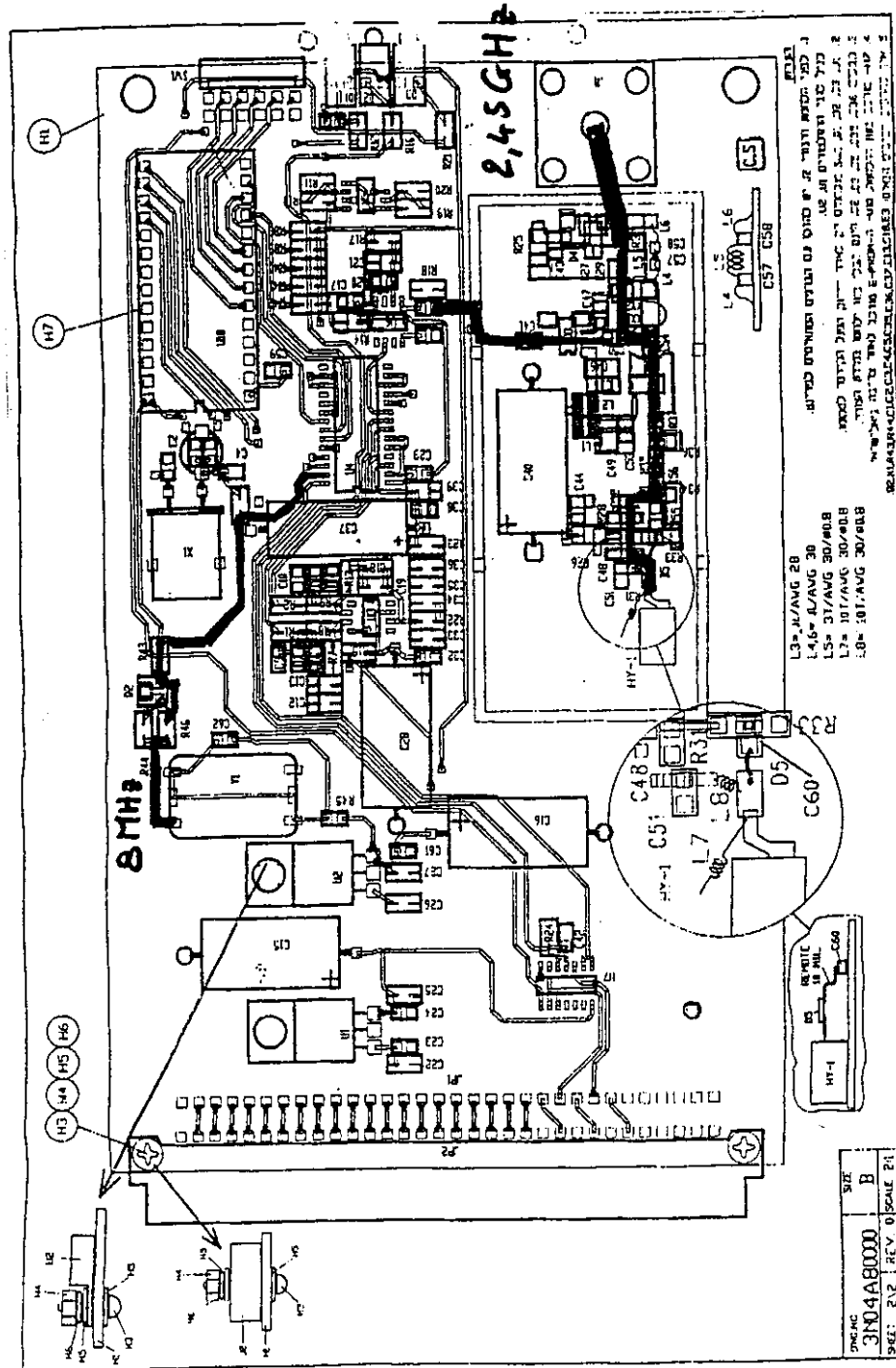
46322988/042

date 10/4/98

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4-4 Clock Path : SHF_2339 Module

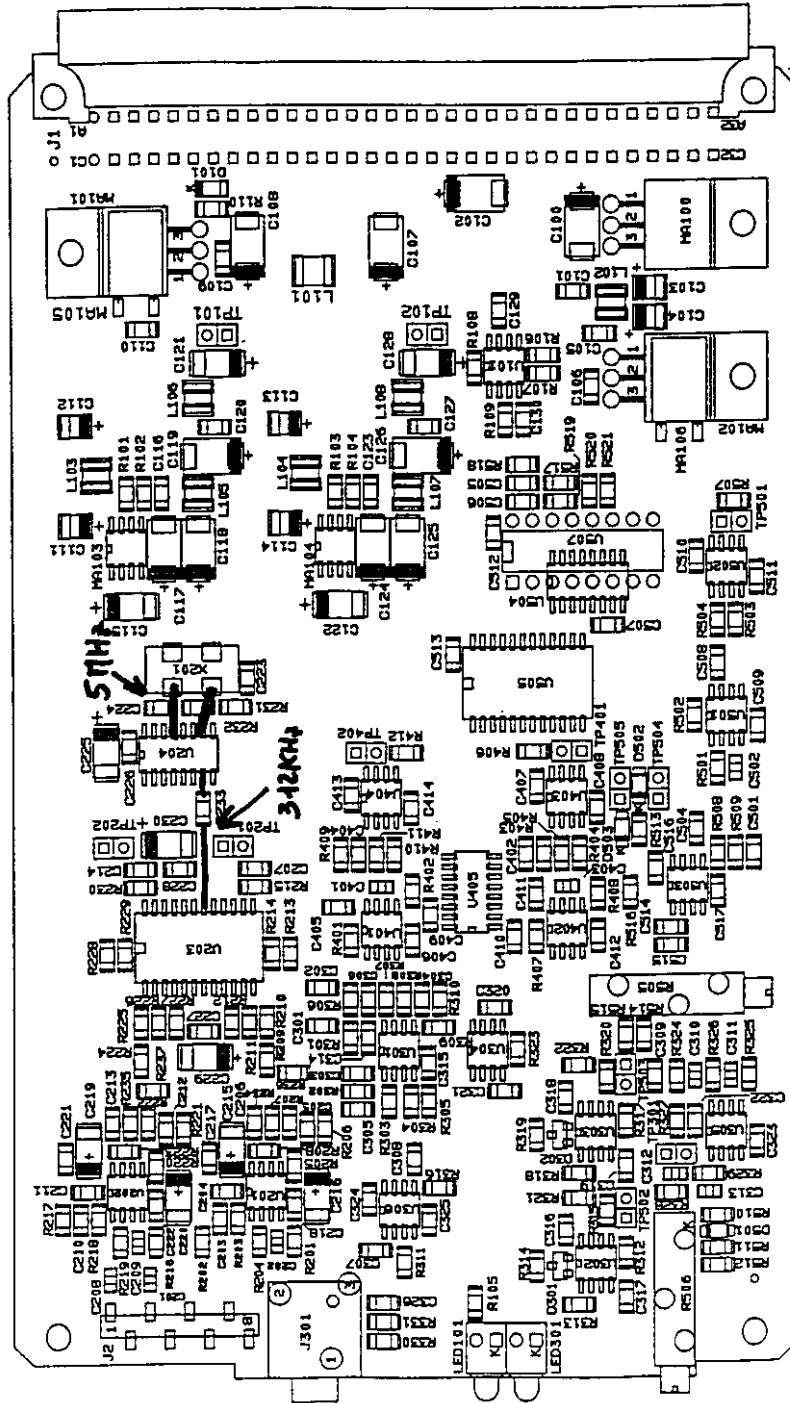


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date 3/4/98
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SHF_2339 - Clock path
dessiné par JBE/PG
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4-4 Clock Path (continued) : LAM-BF 2490 Module



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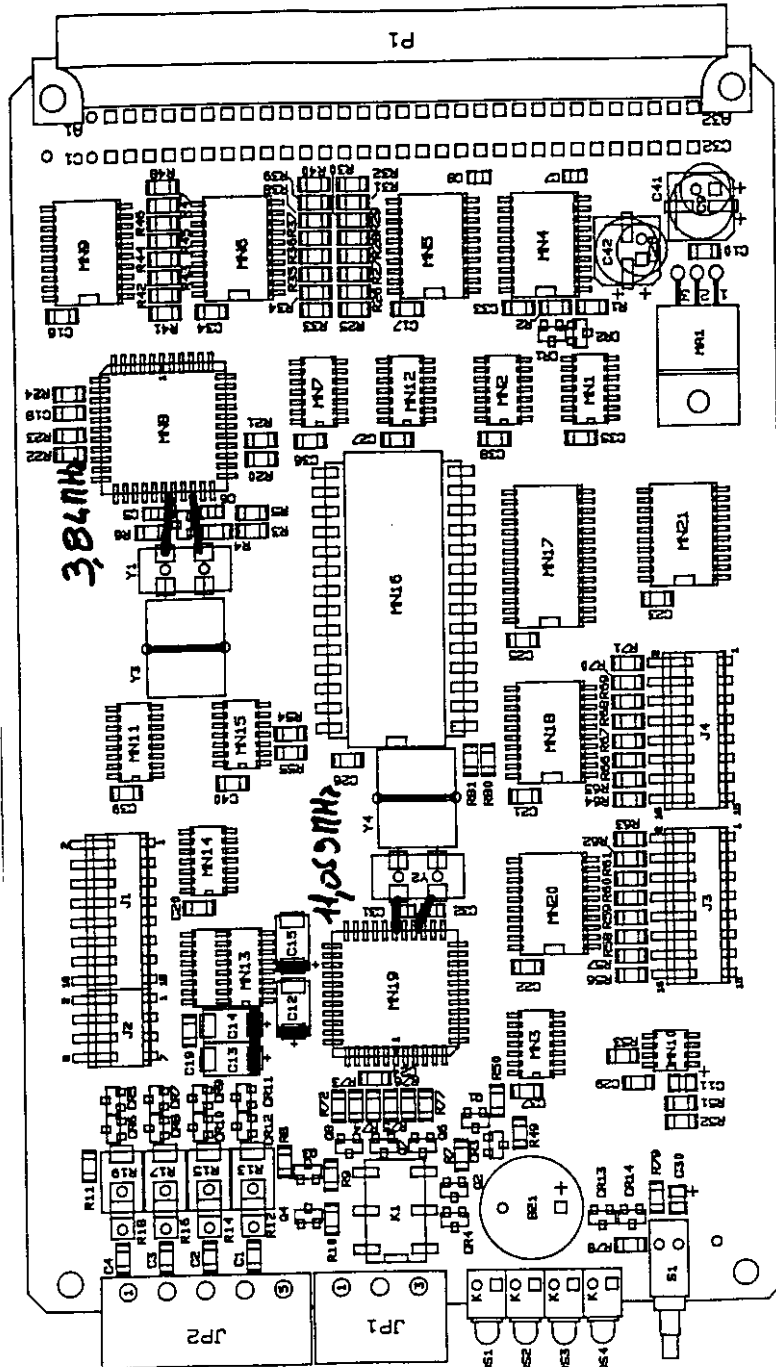
LAM-BF_2490 - Clock path

JBE/PG

visa

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4-4 Clock Path (continued) : SPI_2110 Module



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SPI_2110 - Clock path
dessiné par JBE/PG
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