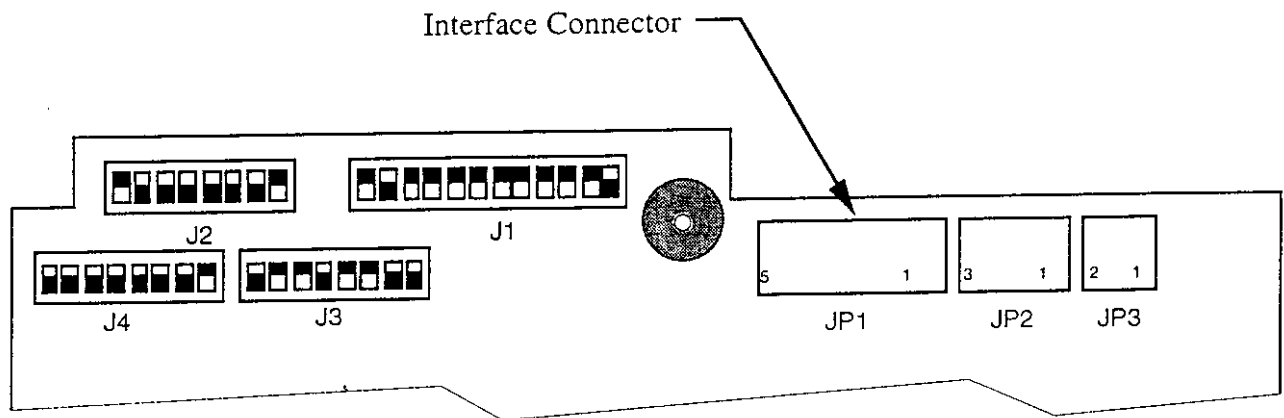
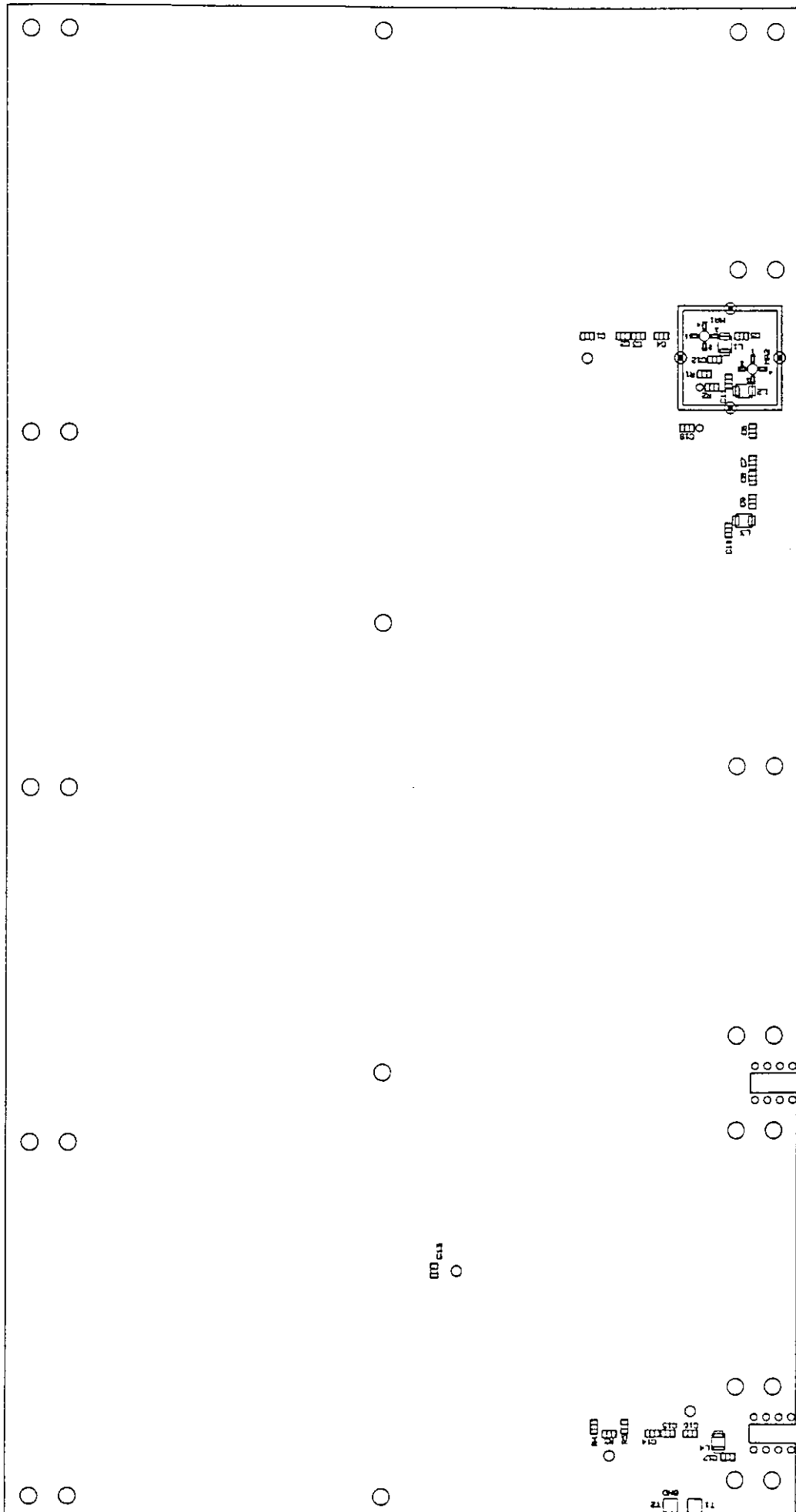
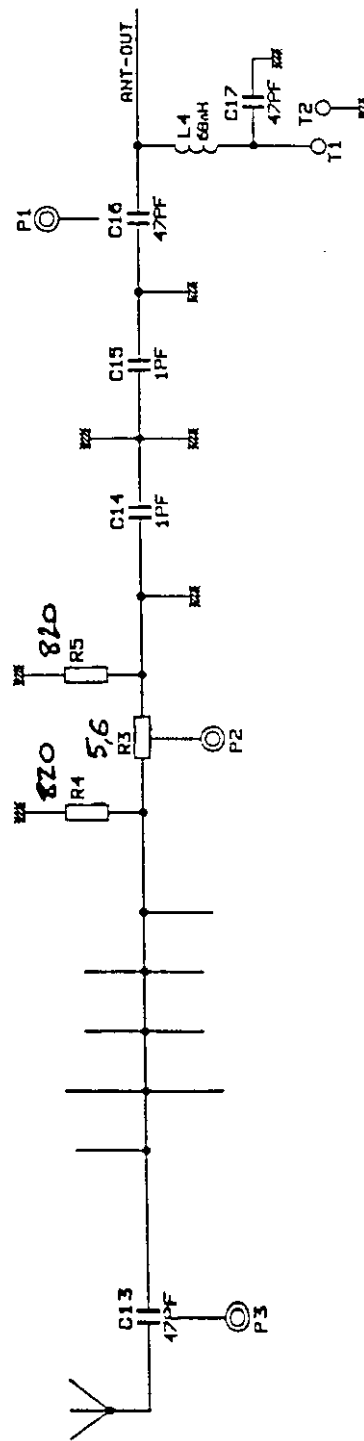
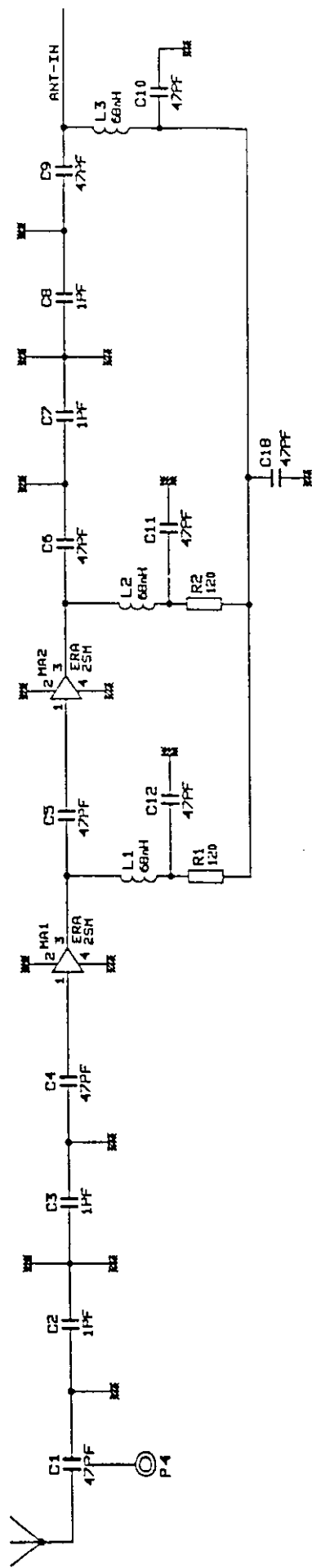


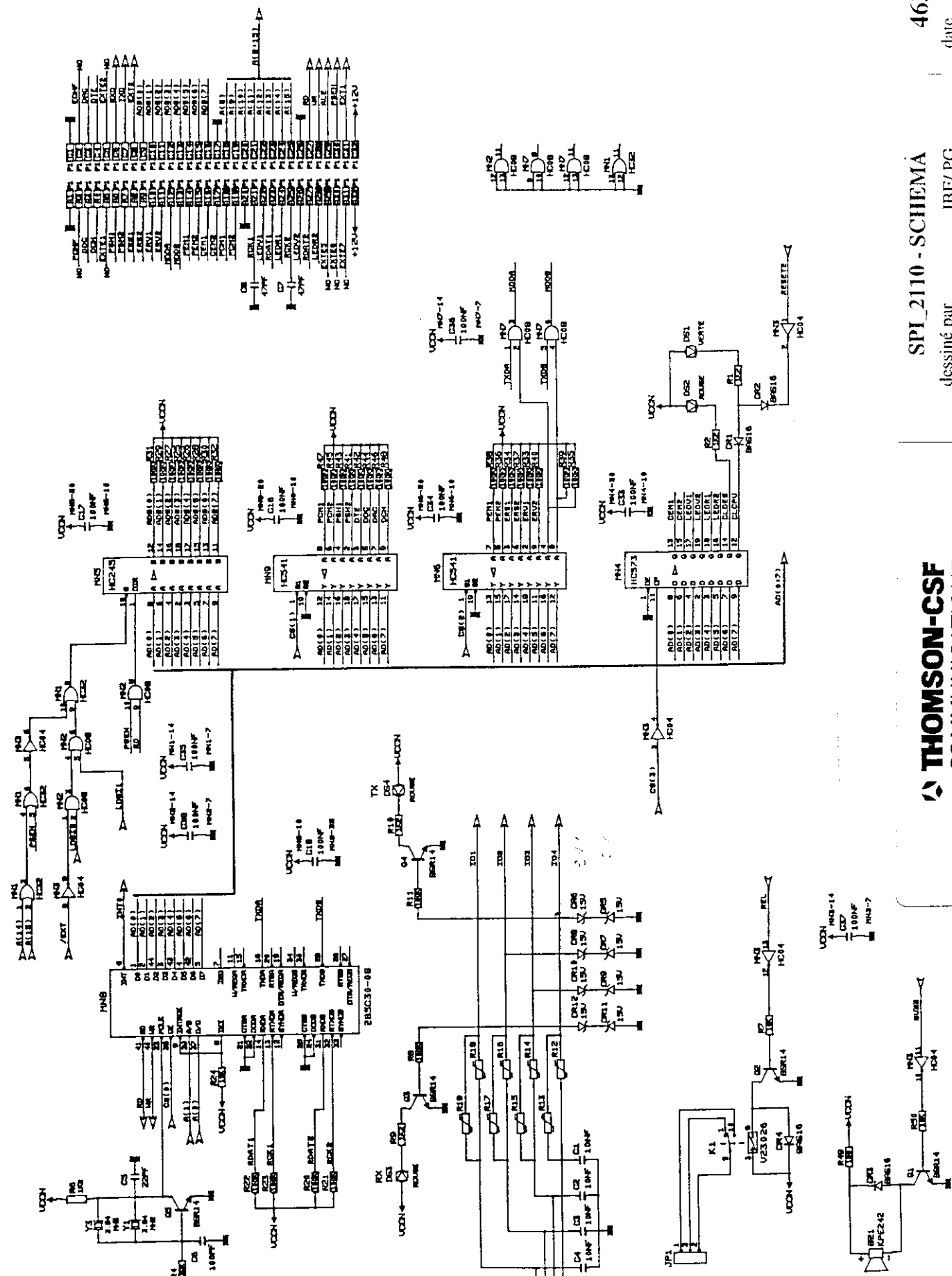
Interface connector on compact readers (JP1)

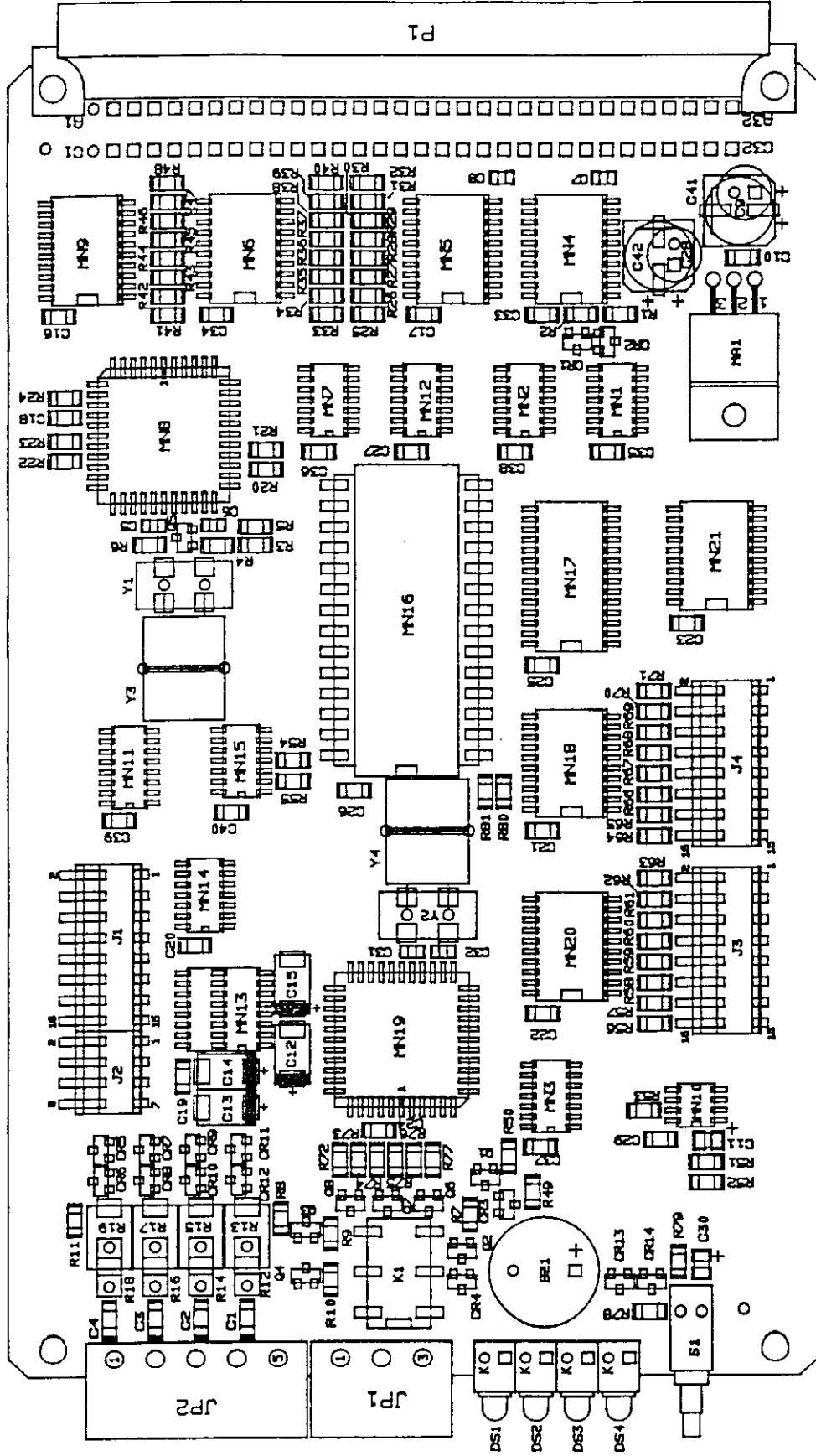


Pins 1 to 4 : I/O
Pin 5 : 0V (GND)

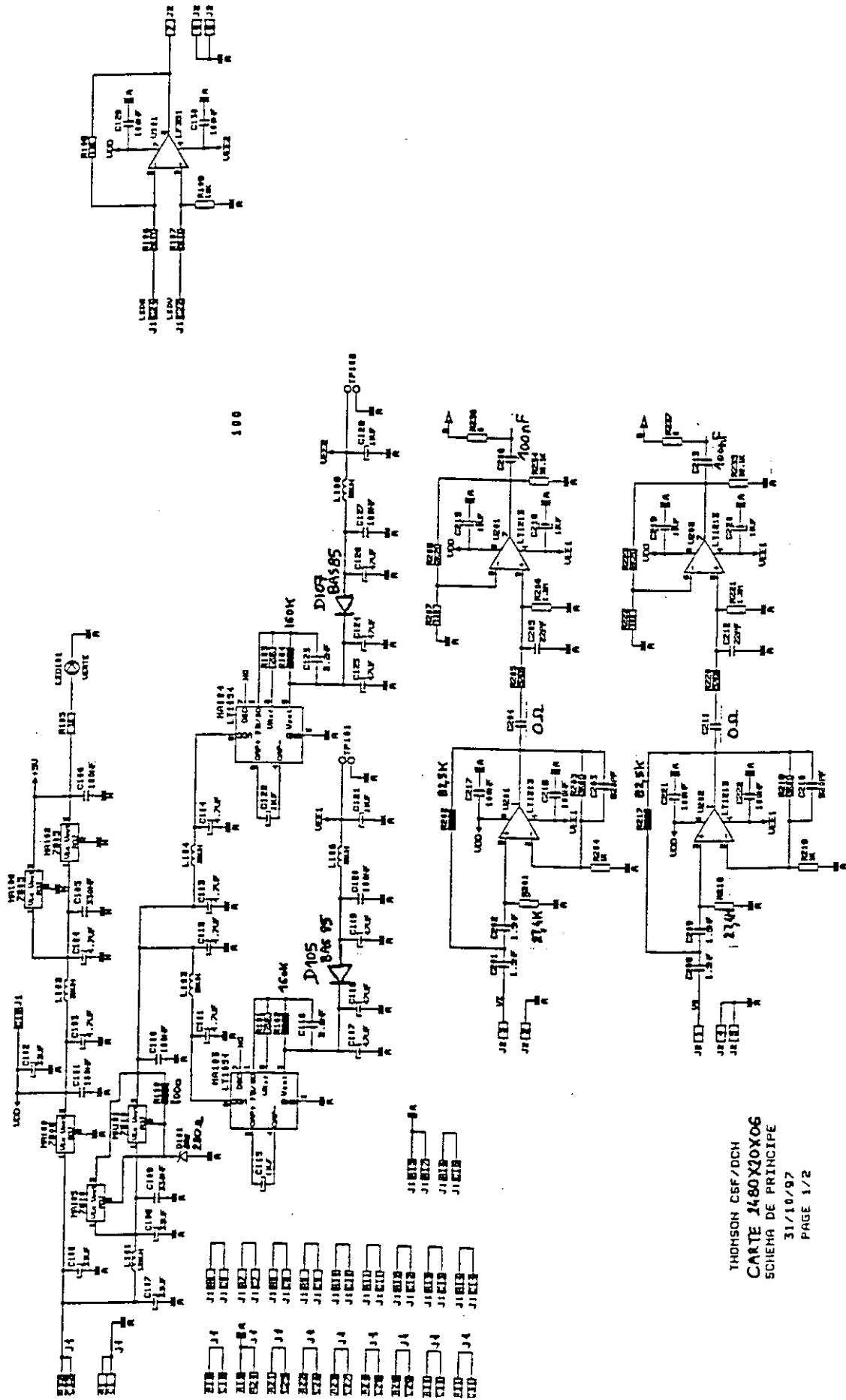








NOTA : NE PAS CABLER R55 ET R80



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CARTE 2480X20X06
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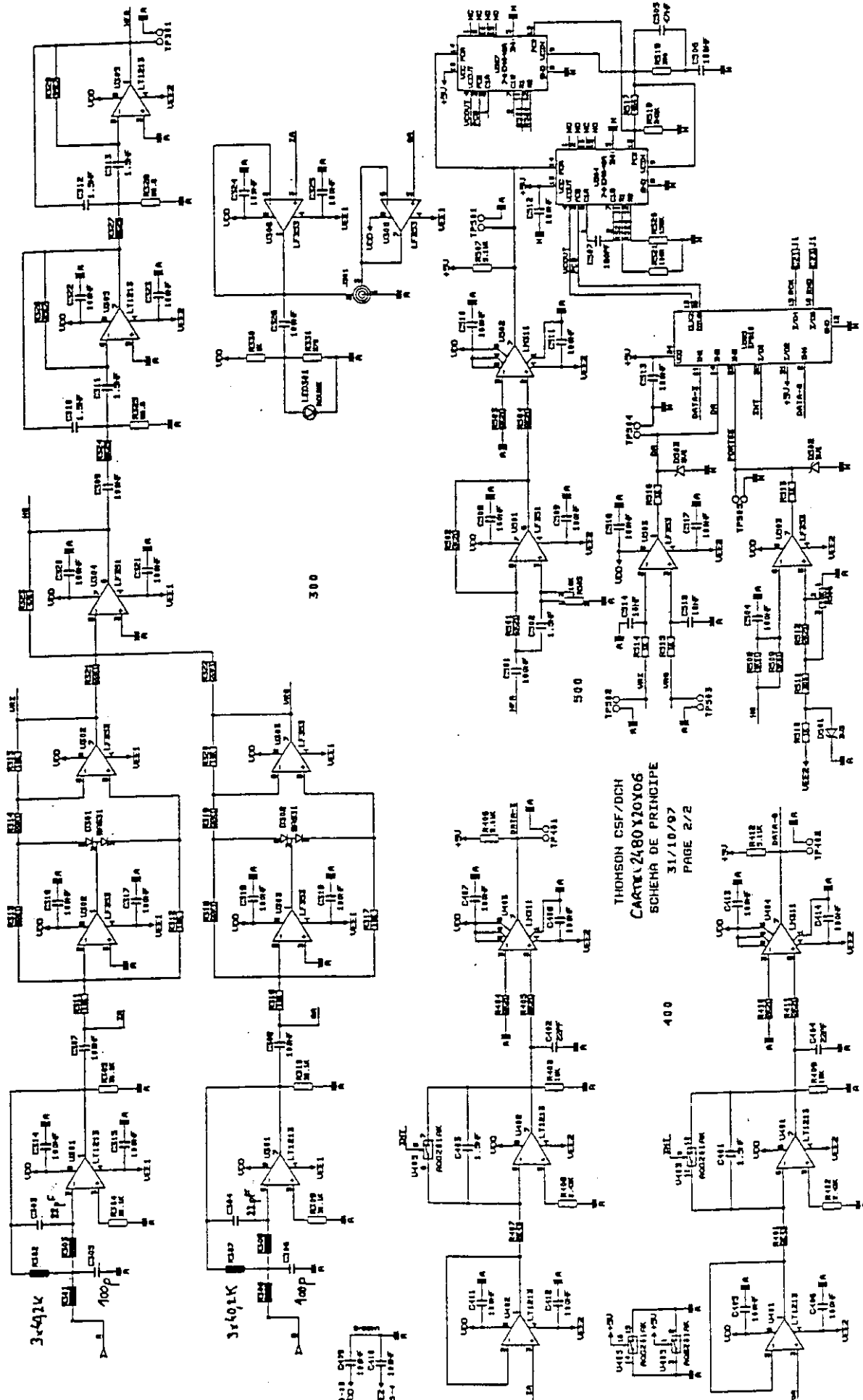
SAM-BF_2480 -SCHEMA
dessiné par JBE/ PG

46322981/020

date 10/4/98

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visa





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date 10/4/98



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visi

date 10/4/98

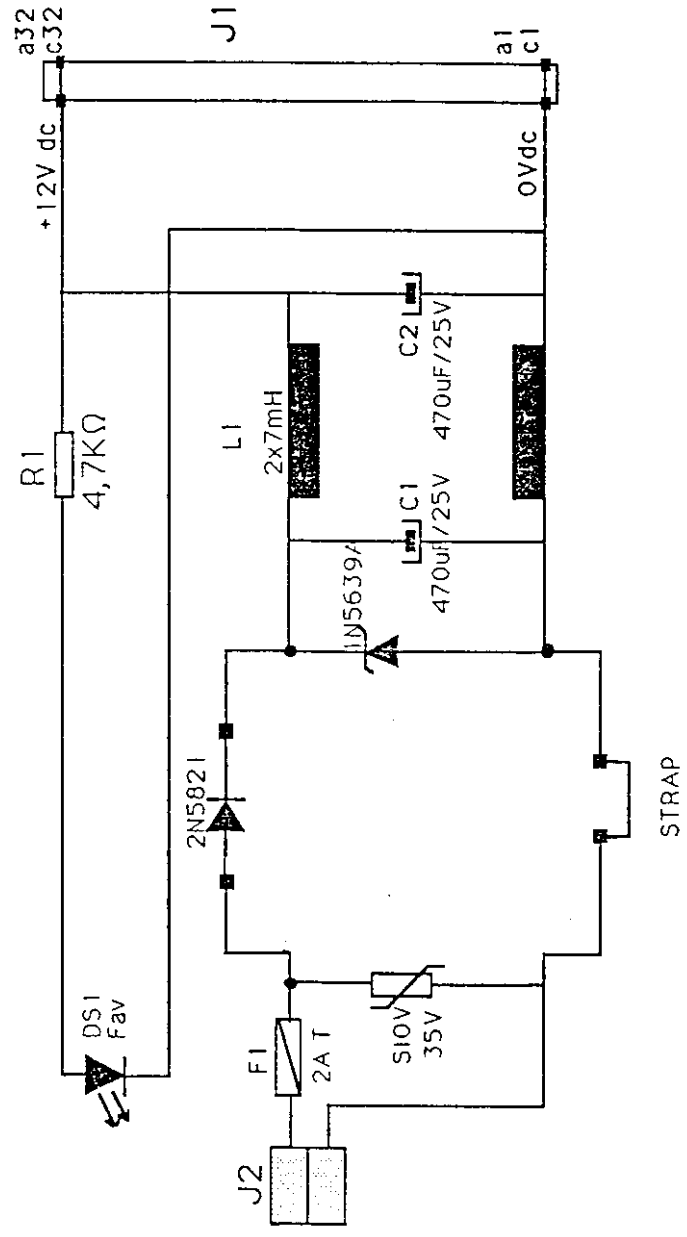
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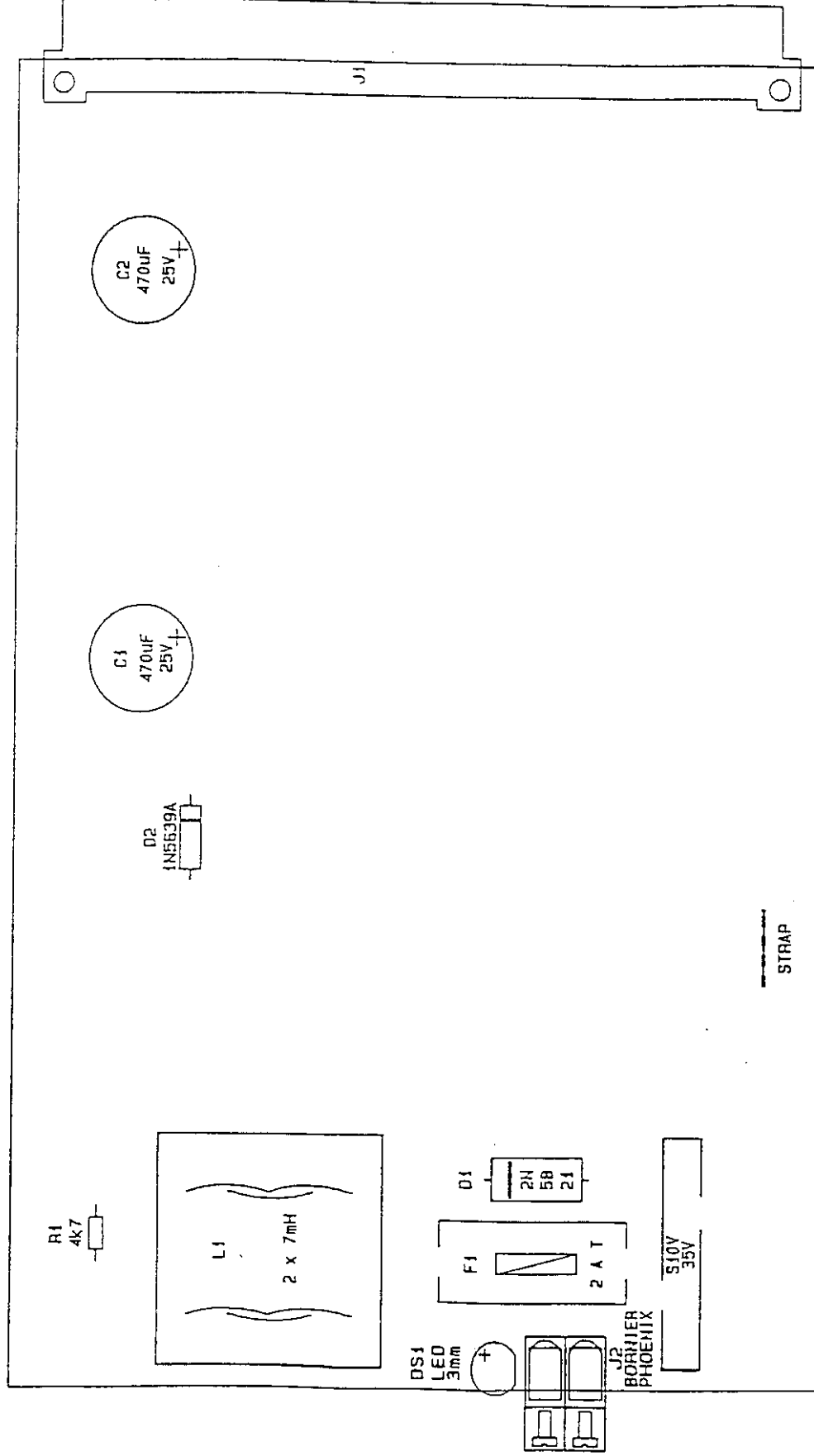


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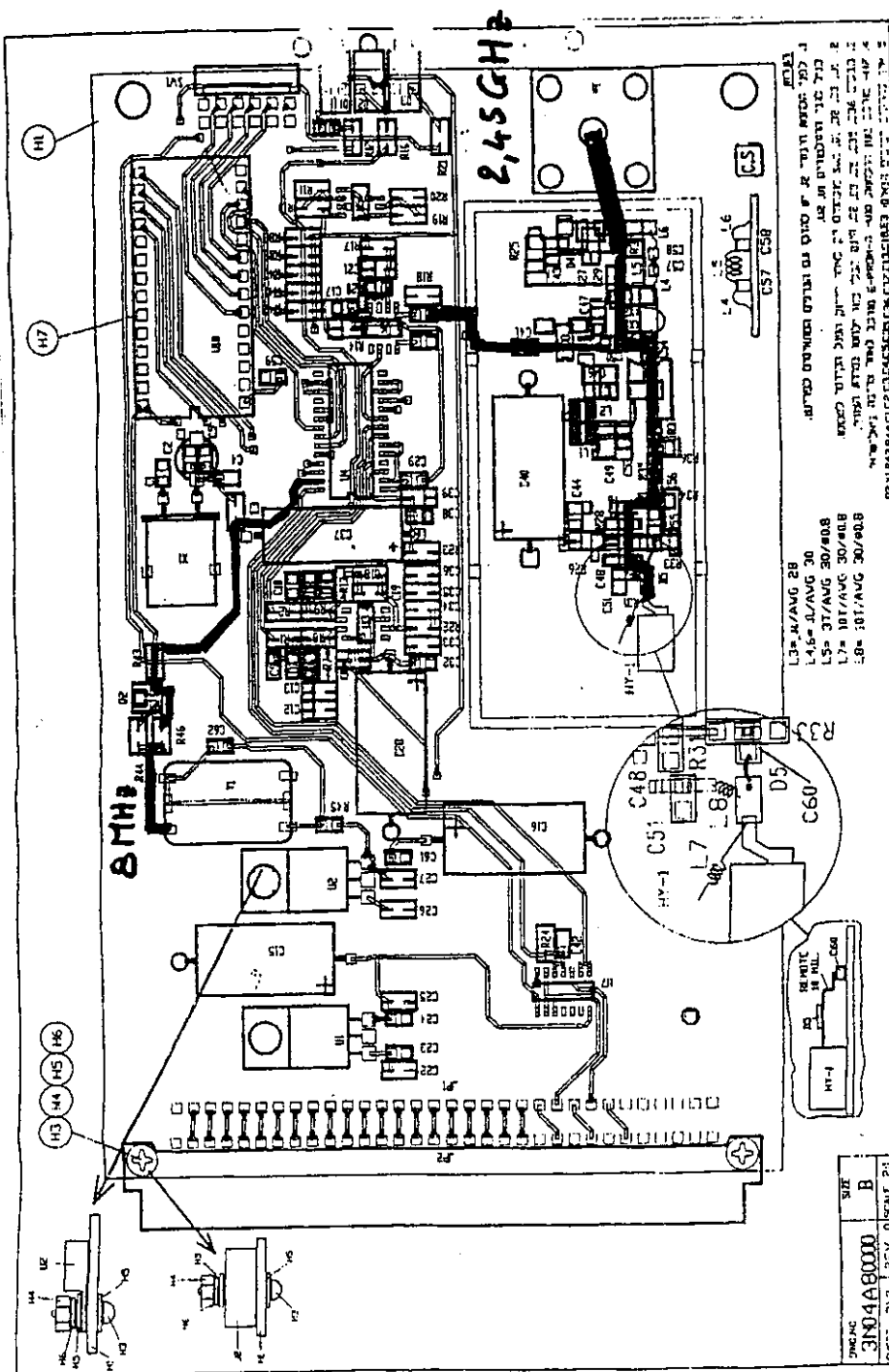
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4-4 Clock Path : SHF_2339 Module

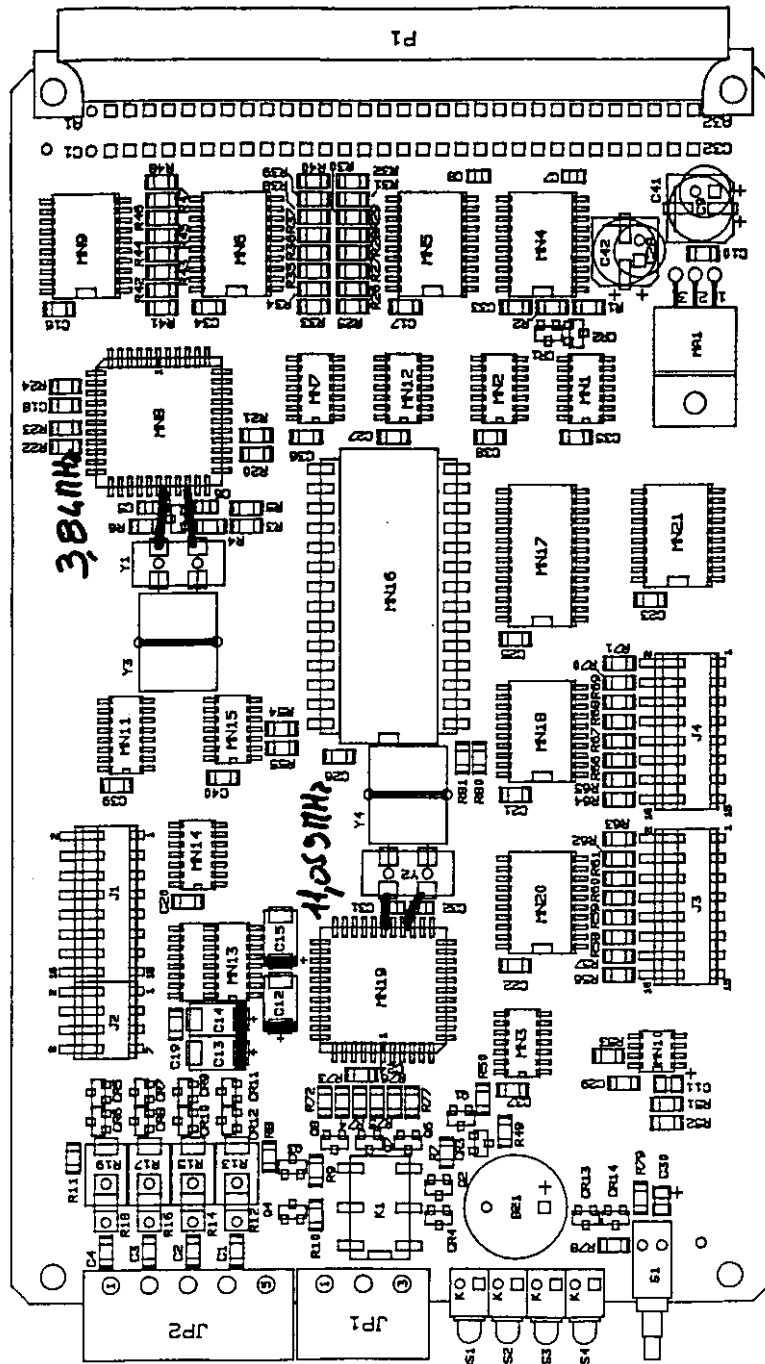


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date 3/4/98
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SHF_2339 - Clock path	JBE/PG
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4-4 Clock Path (continued) : SPI_2110 Module



THOMSON-CSF COMMUNICATIONS	SPI_2110 - Clock path		46322770
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