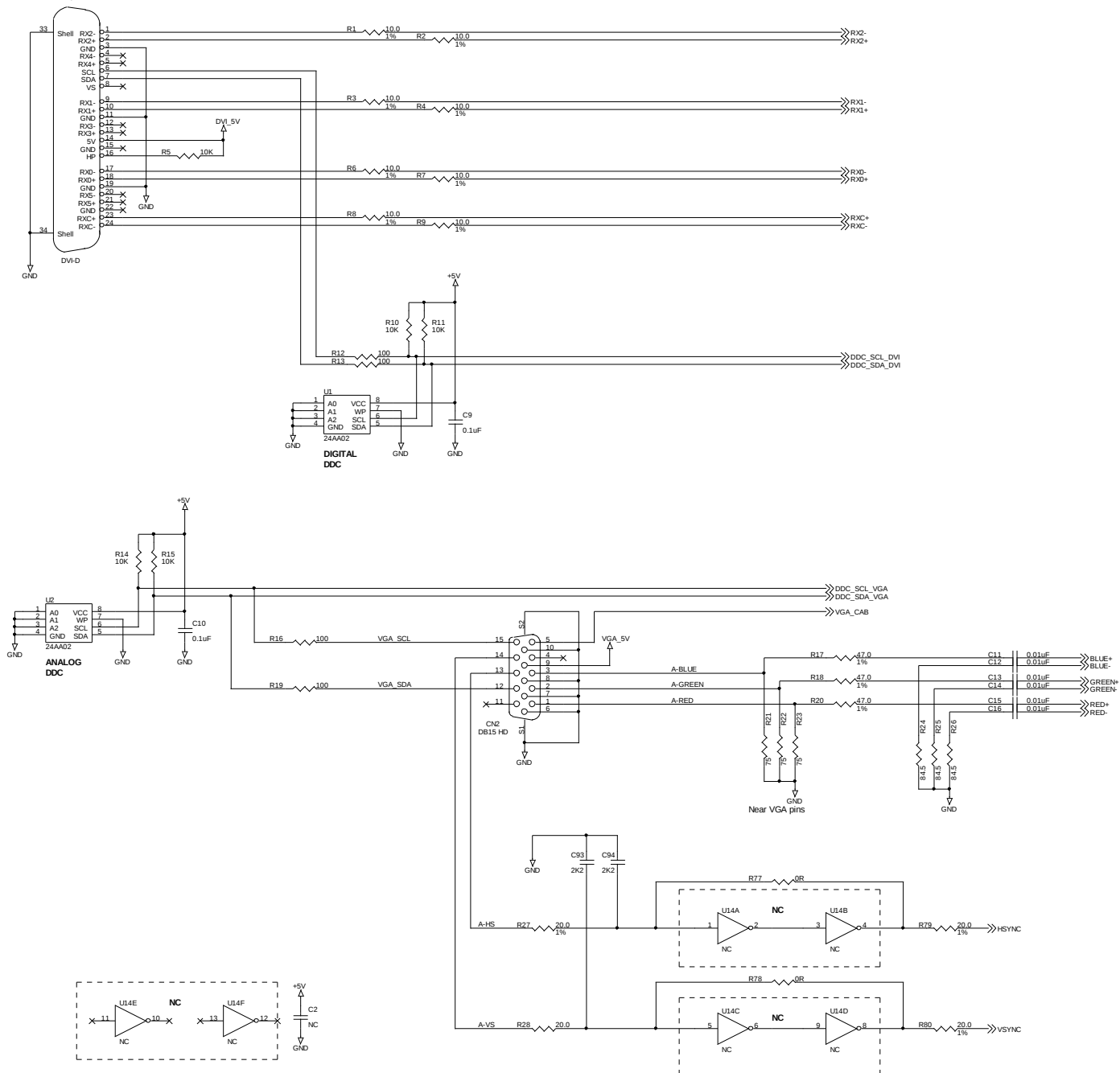
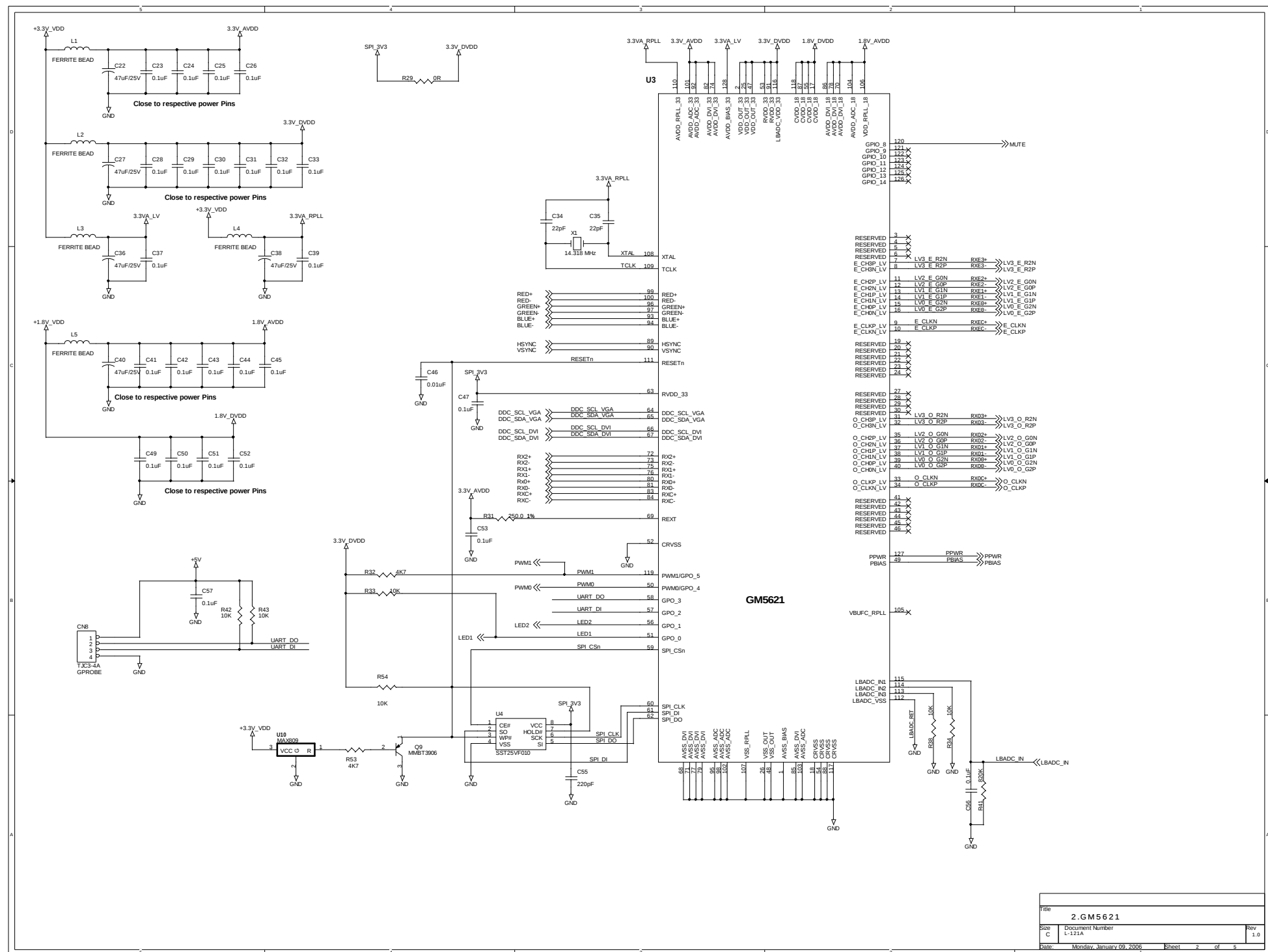


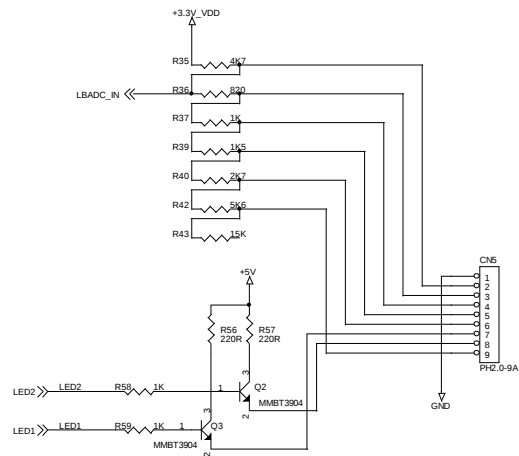
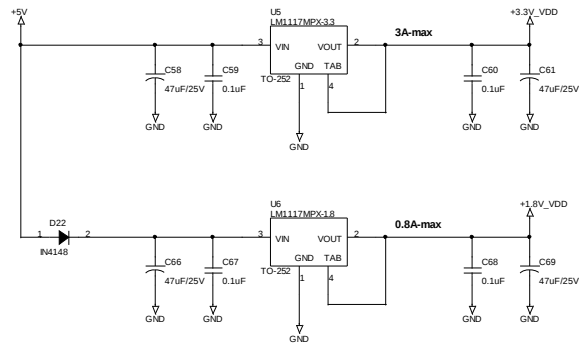
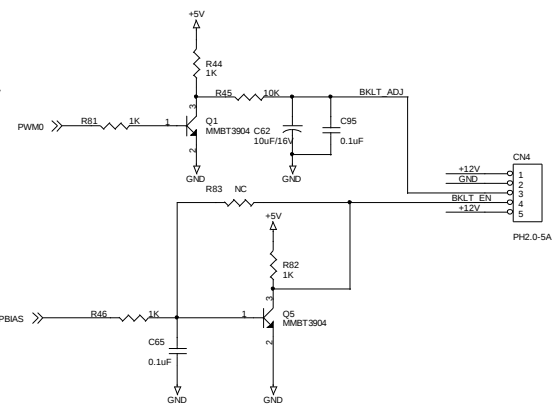
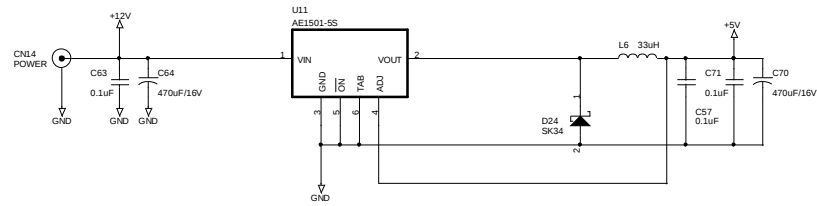
DVI CONNECTOR

CN1



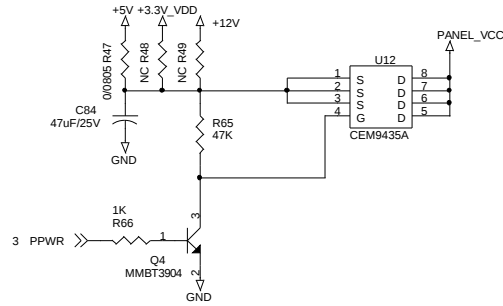
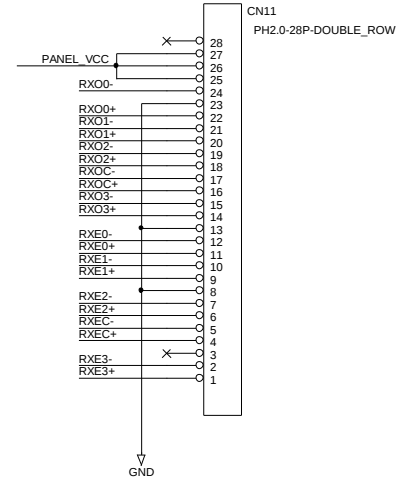
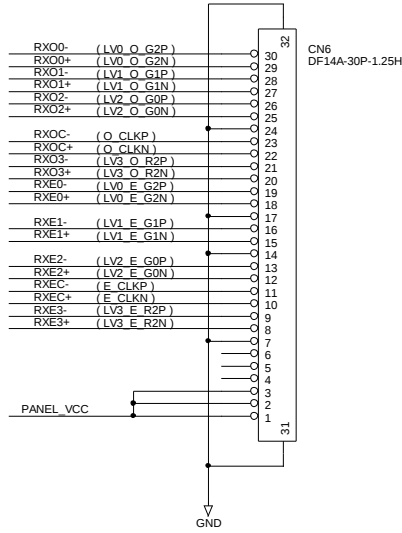
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1. Input connector		
Size	Document Number	Rev
C	L-121A	1.0
Date:	Monday, January 09, 2006	Sheet 1 of 5



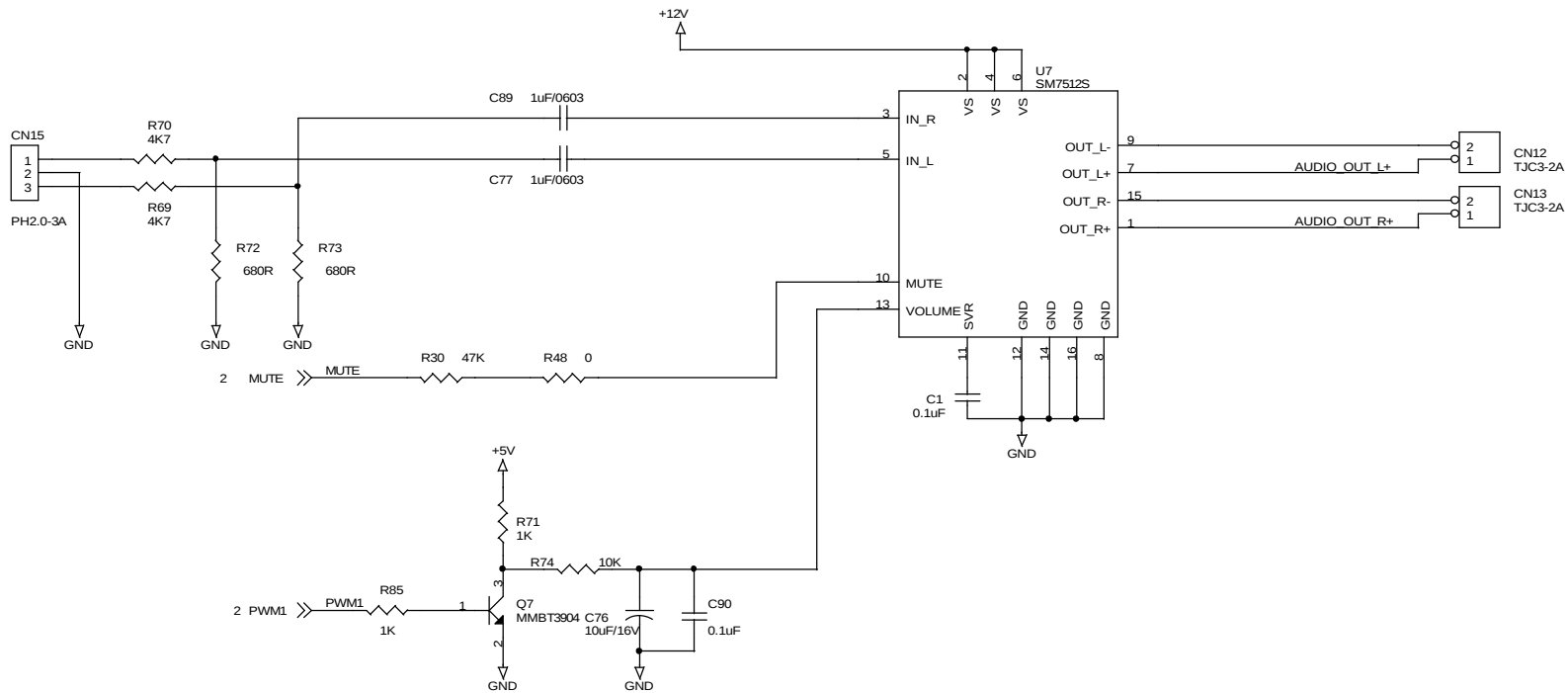


3	LV0_O_G2P	>>	LV0_O_G2P	RX00-	OD12P
3	LV0_O_G2N	>>	LV0_O_G2N	RX00+	OD12N
3	LV1_O_G1P	>>	LV1_O_G1P	RX01-	OD11P
3	LV1_O_G1N	>>	LV1_O_G1N	RX01+	OD11N
3	LV2_O_G0P	>>	LV2_O_G0P	RX02-	OD10P
3	LV2_O_G0N	>>	LV2_O_G0N	RX02+	OD10N
3	O_CLKP	>>	O_CLKP	RX0C-	OCLK_RS_P
3	O_CLKN	>>	O_CLKN	RX0C+	OCLK_RS_N
3	LV3_O_R2P	>>	LV3_O_R2P	RX03-	OD02P
3	LV3_O_R2N	>>	LV3_O_R2N	RX03+	OD02N

3	LV0_E_G2P	>>	LV0_E_G2P	RXE0-	ED12P
3	LV0_E_G2N	>>	LV0_E_G2N	RXE0+	ED12N
3	LV1_E_G1P	>>	LV1_E_G1P	RXE1-	ED11P
3	LV1_E_G1N	>>	LV1_E_G1N	RXE1+	ED11N
3	LV2_E_G0P	>>	LV2_E_G0P	RXE2-	ED10P
3	LV2_E_G0N	>>	LV2_E_G0N	RXE2+	ED10N
3	E_CLKP	>>	E_CLKP	RXEC-	ECLK_RS_P
3	E_CLKN	>>	E_CLKN	RXEC+	ECLK_RS_N
3	LV3_E_R2P	>>	LV3_E_R2P	RXE3-	ED02P
3	LV3_E_R2N	>>	LV3_E_R2N	RXE3+	ED02N



Title		
4.Display		
Size	Document Number	Rev
A3	L-121A	1.0
Date:	Monday, January 09, 2006	Sheet 4 of 5



Title		
5.Audio		
Size B	Document Number L-121A	Rev 1.0
Date:	Monday, January 09, 2006	Sheet 5 of 5