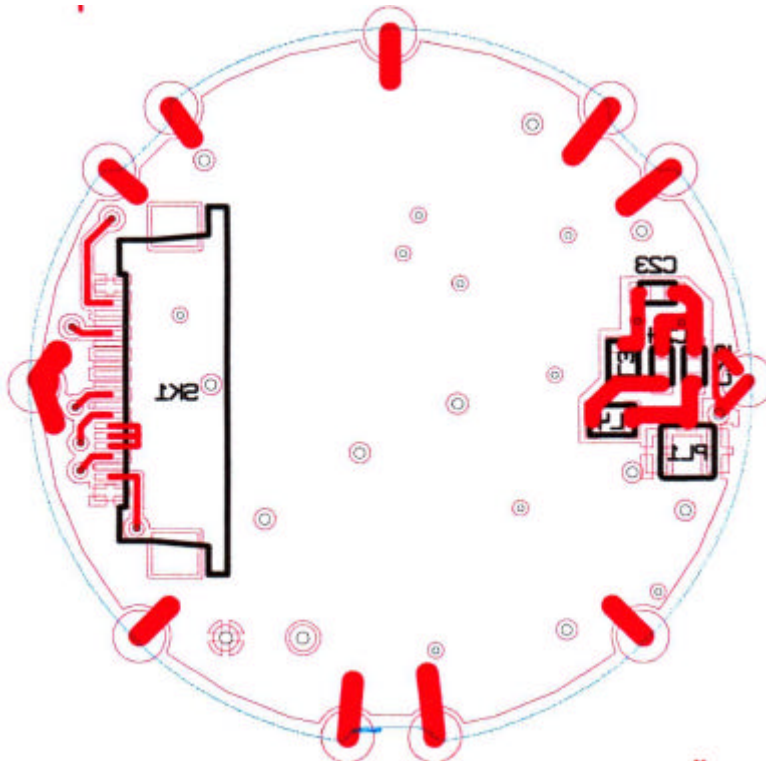
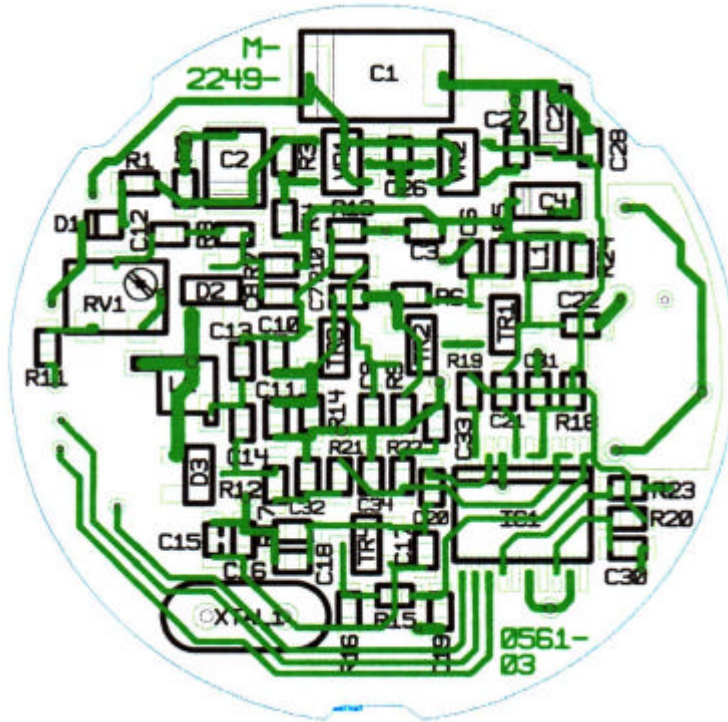
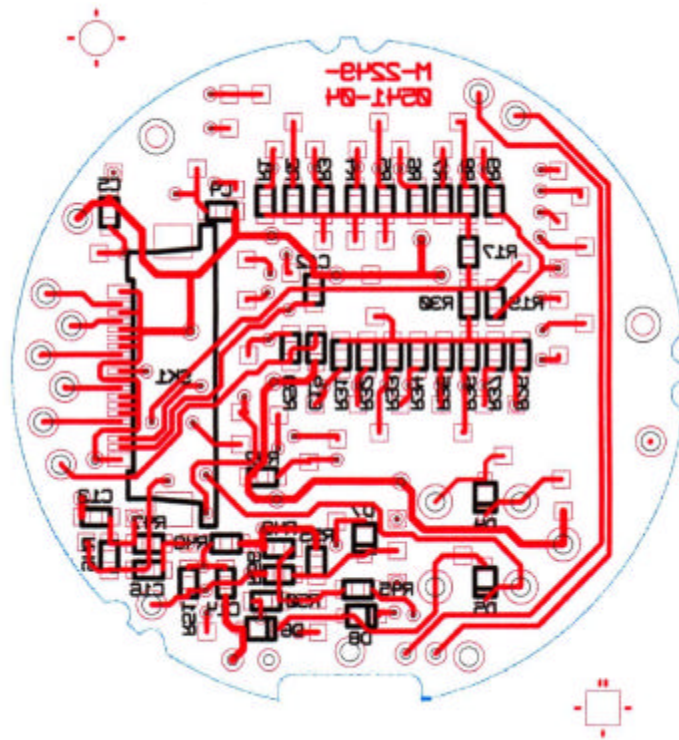
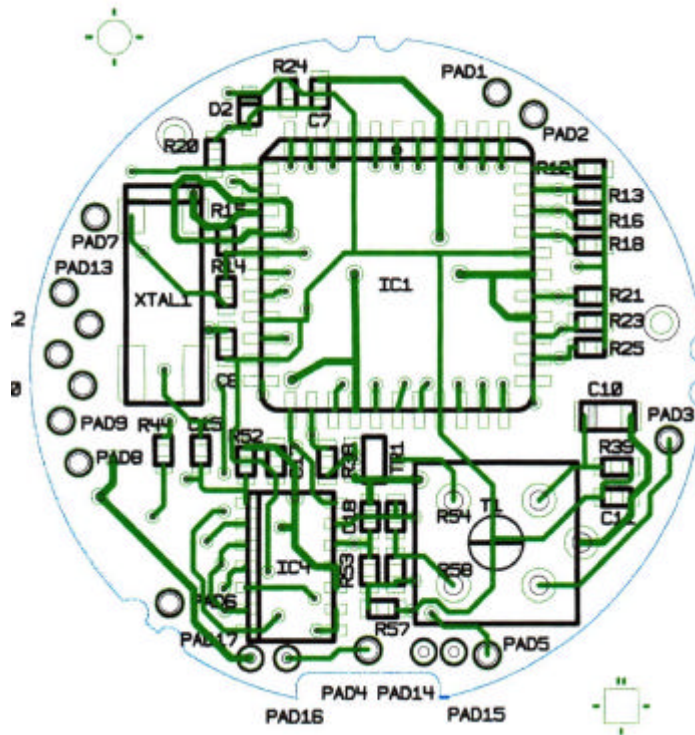


# RMP3 LAYOUT



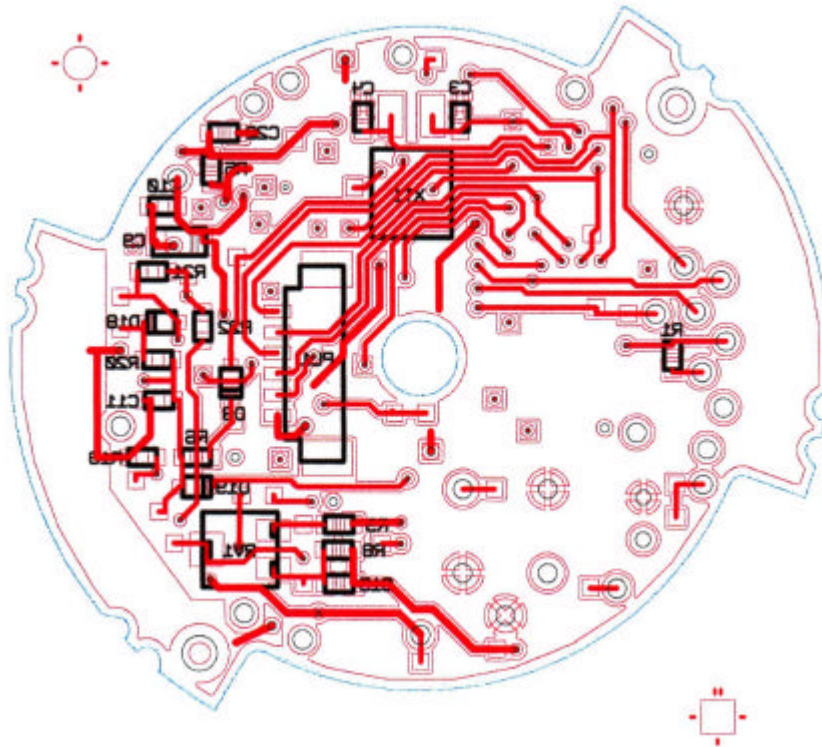
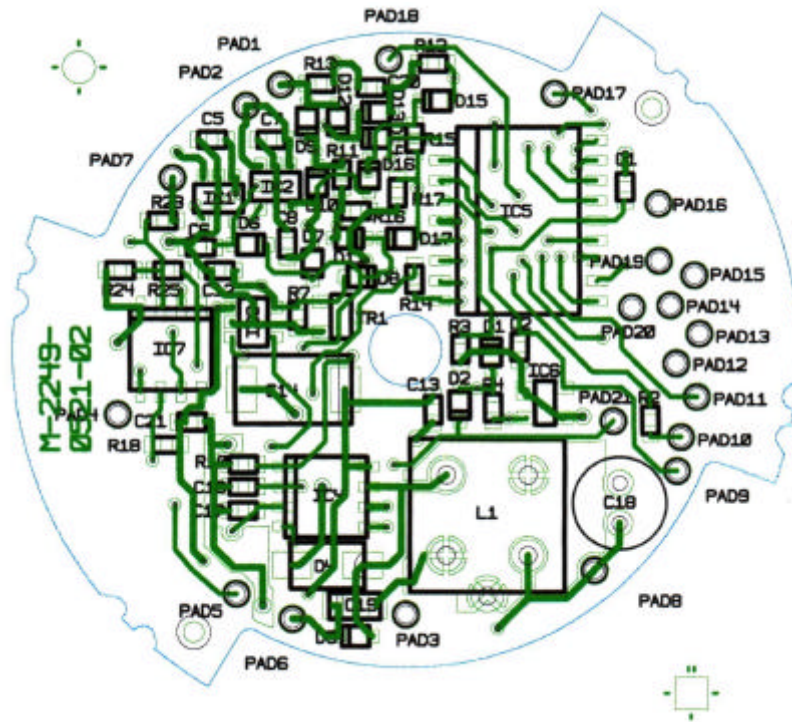
Title: Tx 224 MHz PWB top and bottom

# RMP3 LAYOUT



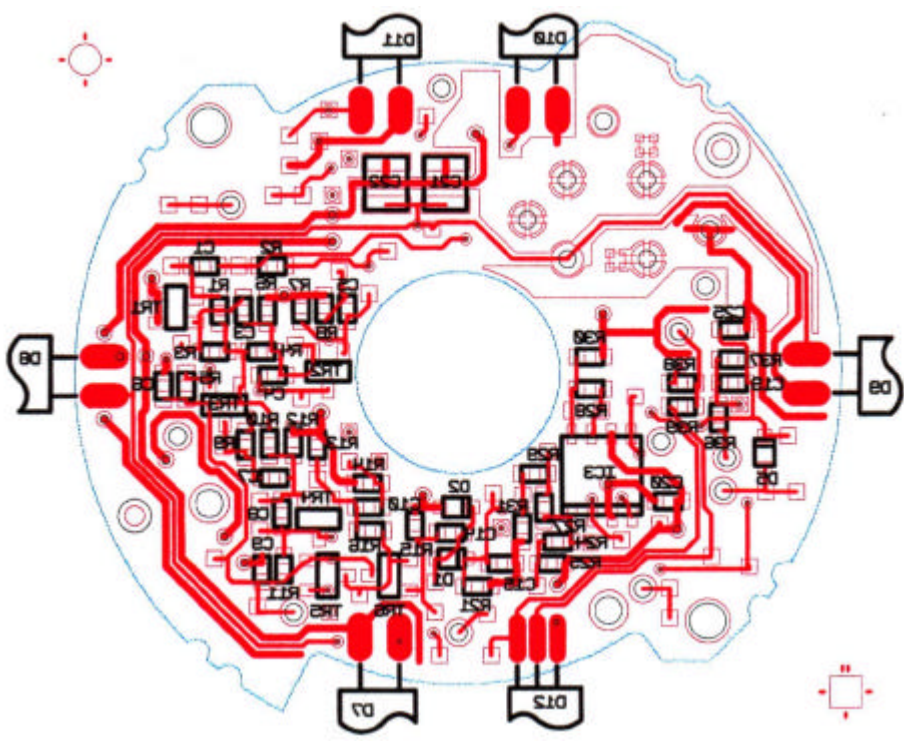
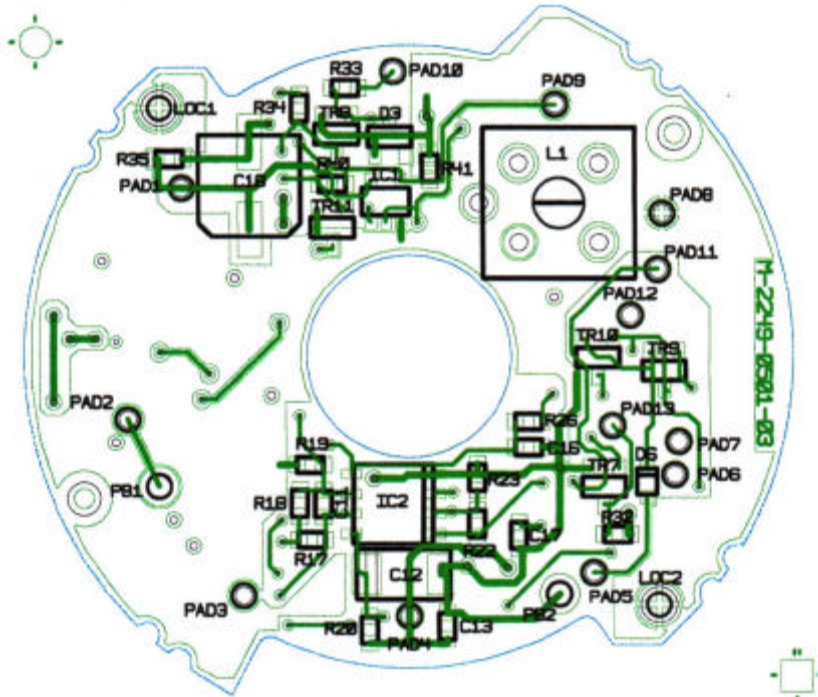
Title: FPGA PWB top and bottom

# RMP3 LAYOUT



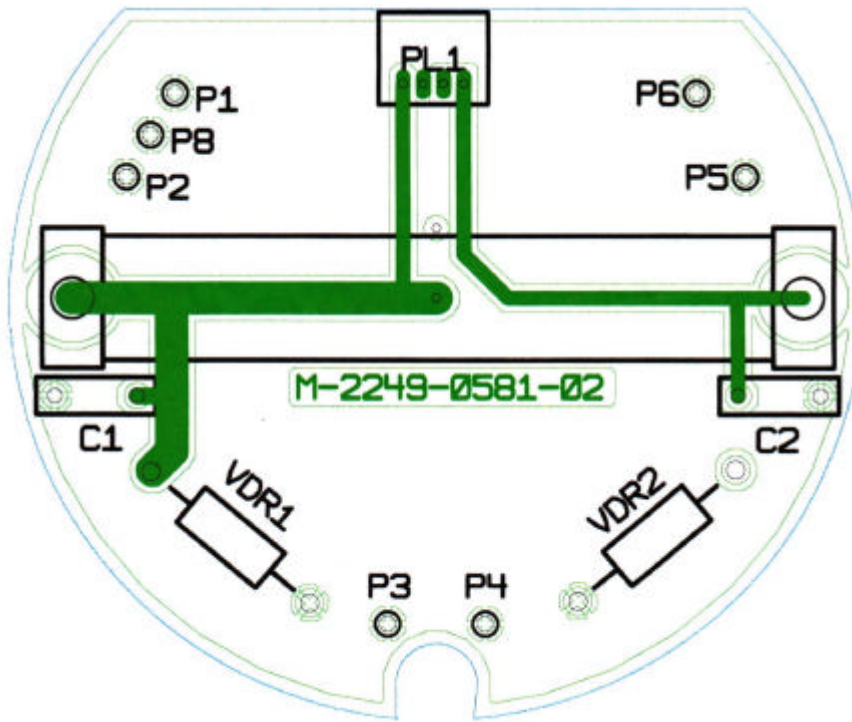
Title: Power/Pic PWB top and bottom

# RMP3 LAYOUT



Title: Optic PWB top and bottom

# RMP3 LAYOUT



Title: Centrifugal switch PWB top and bottom