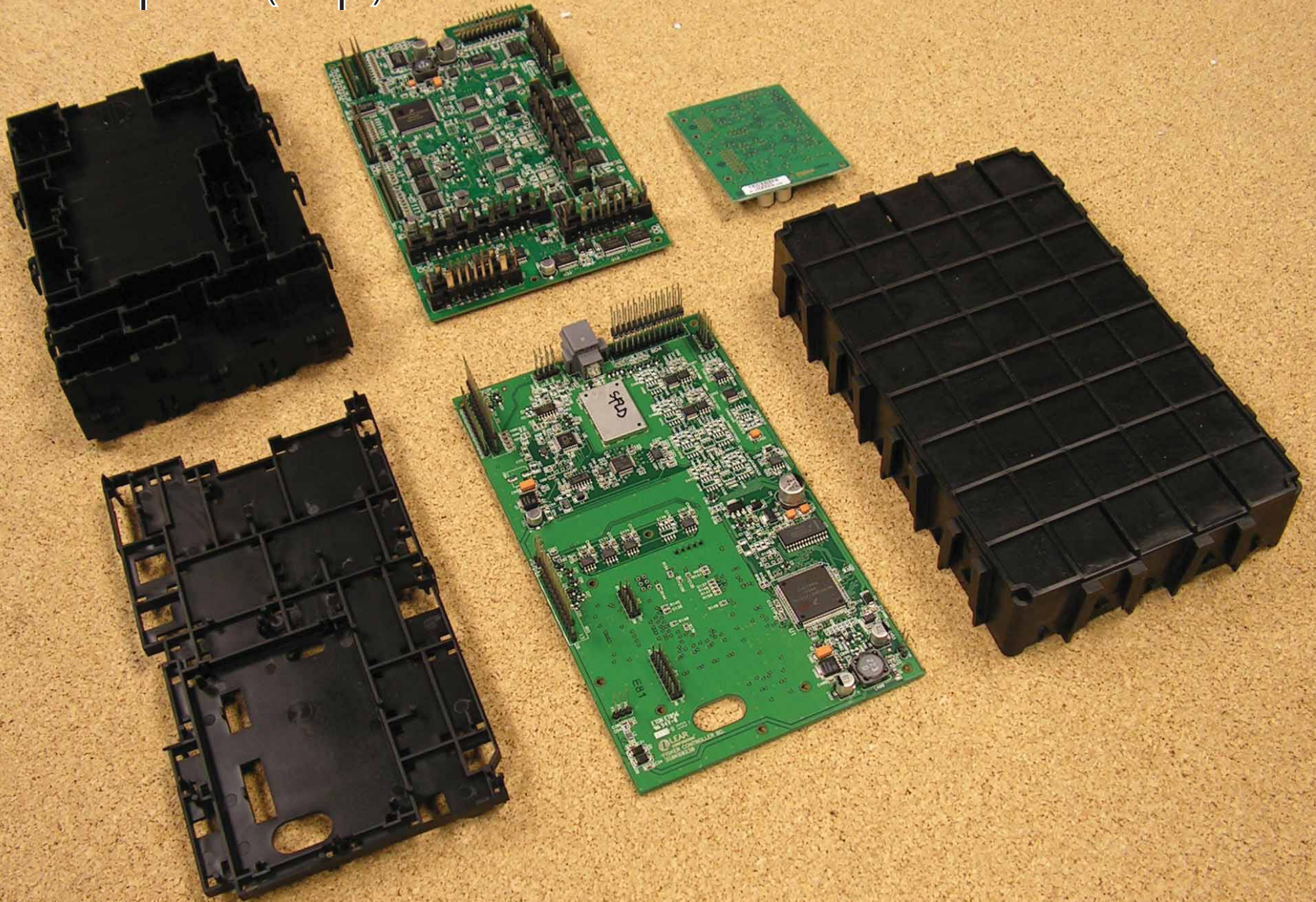


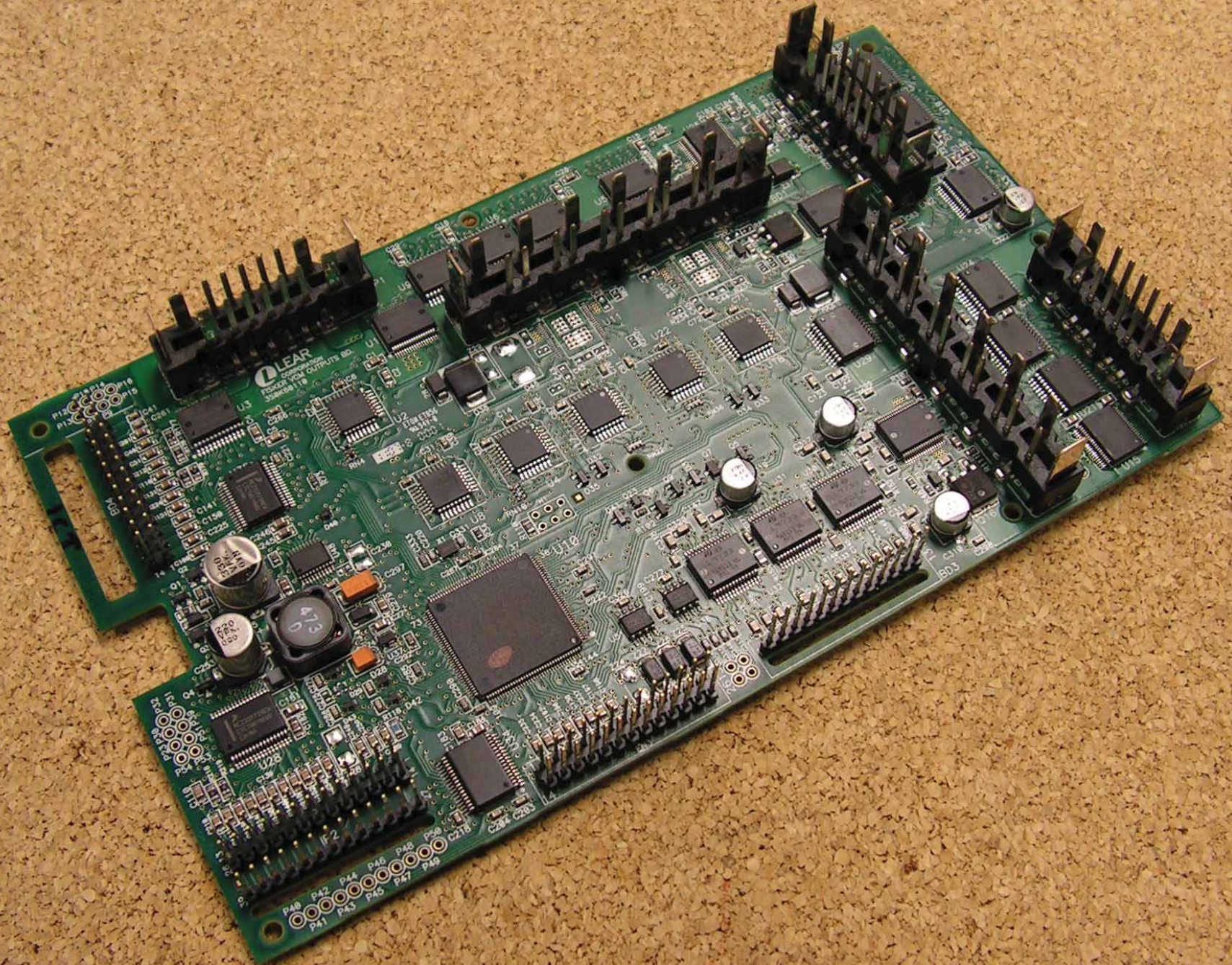
DUT apart (top)



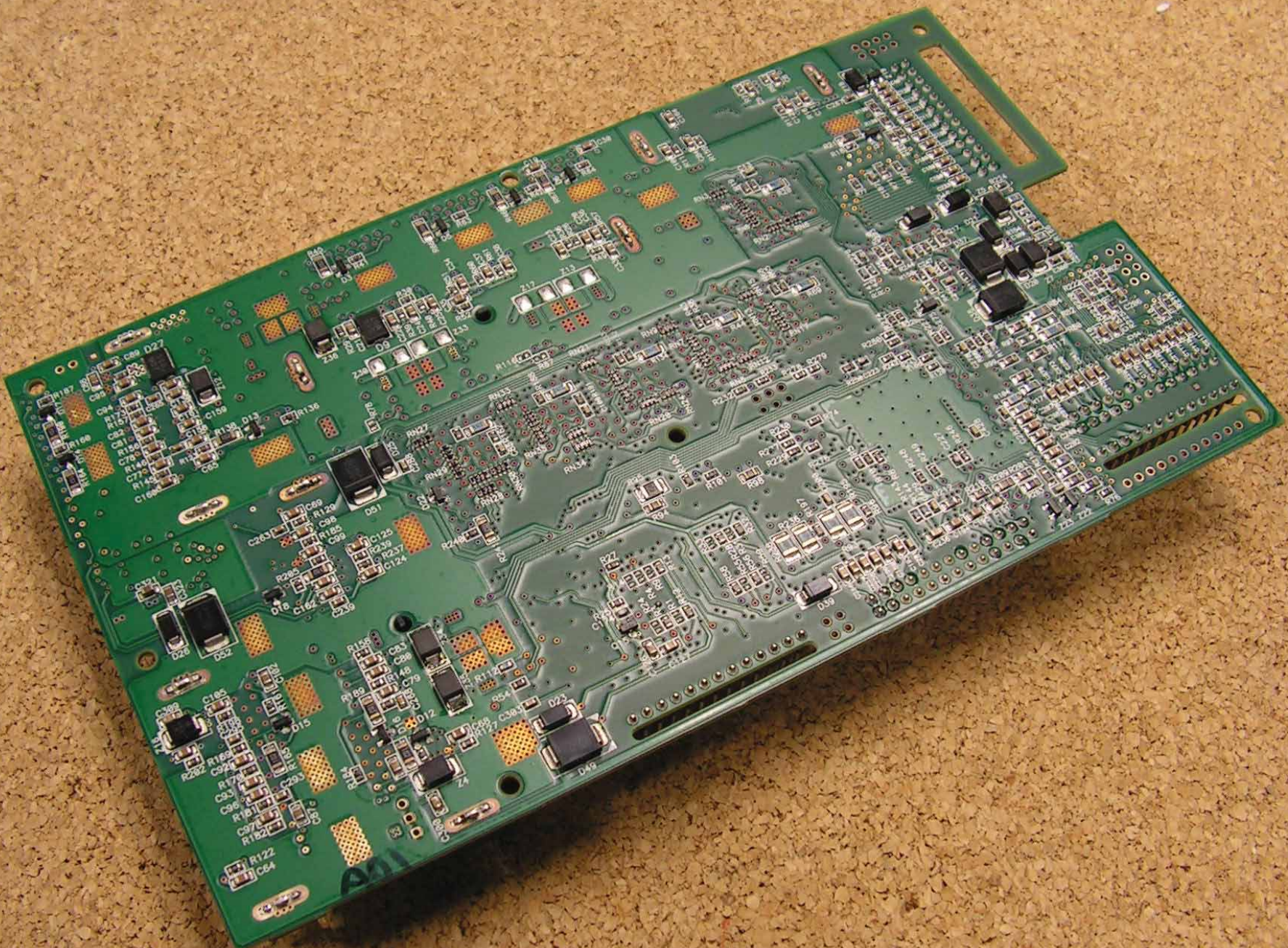
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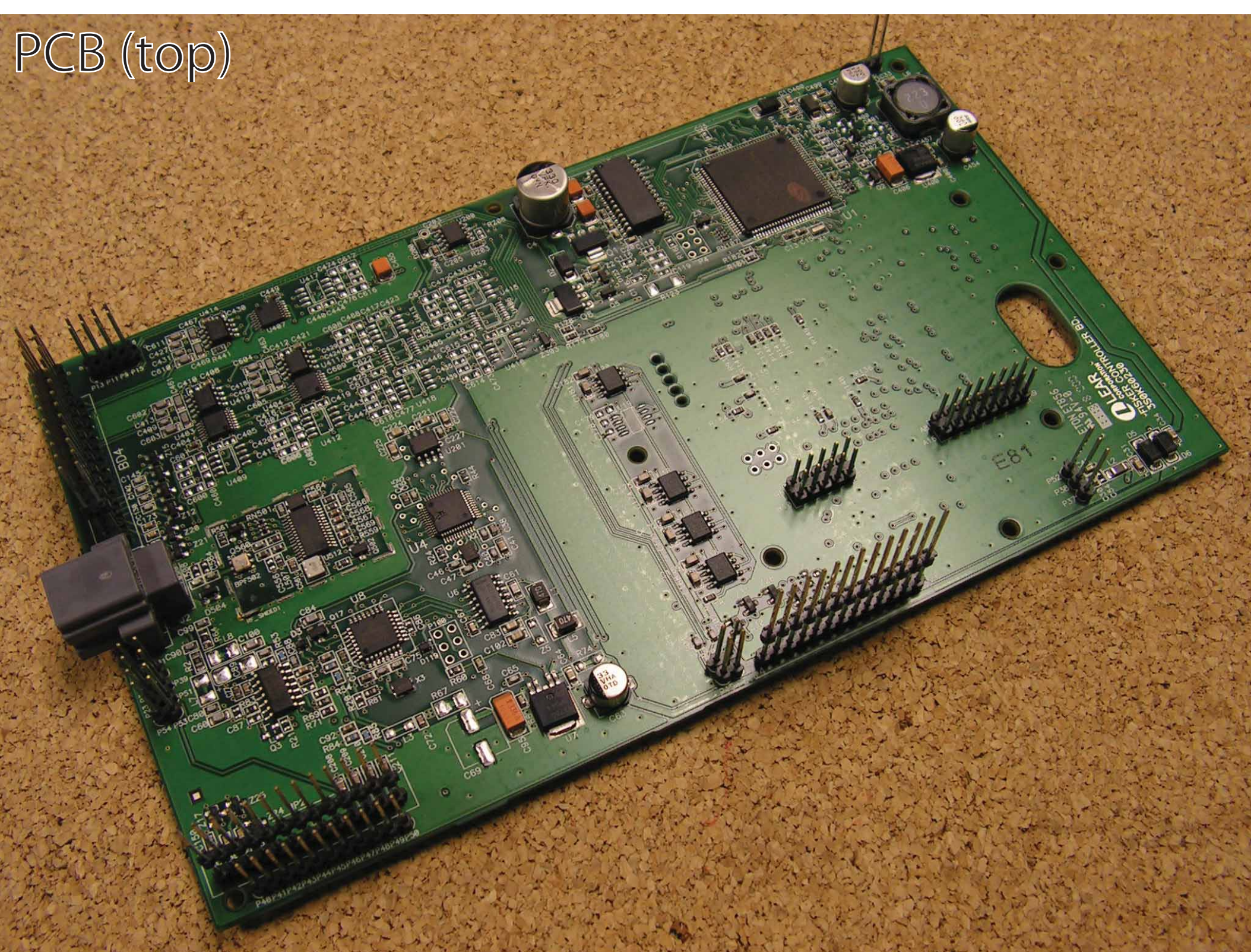
PCB (top)



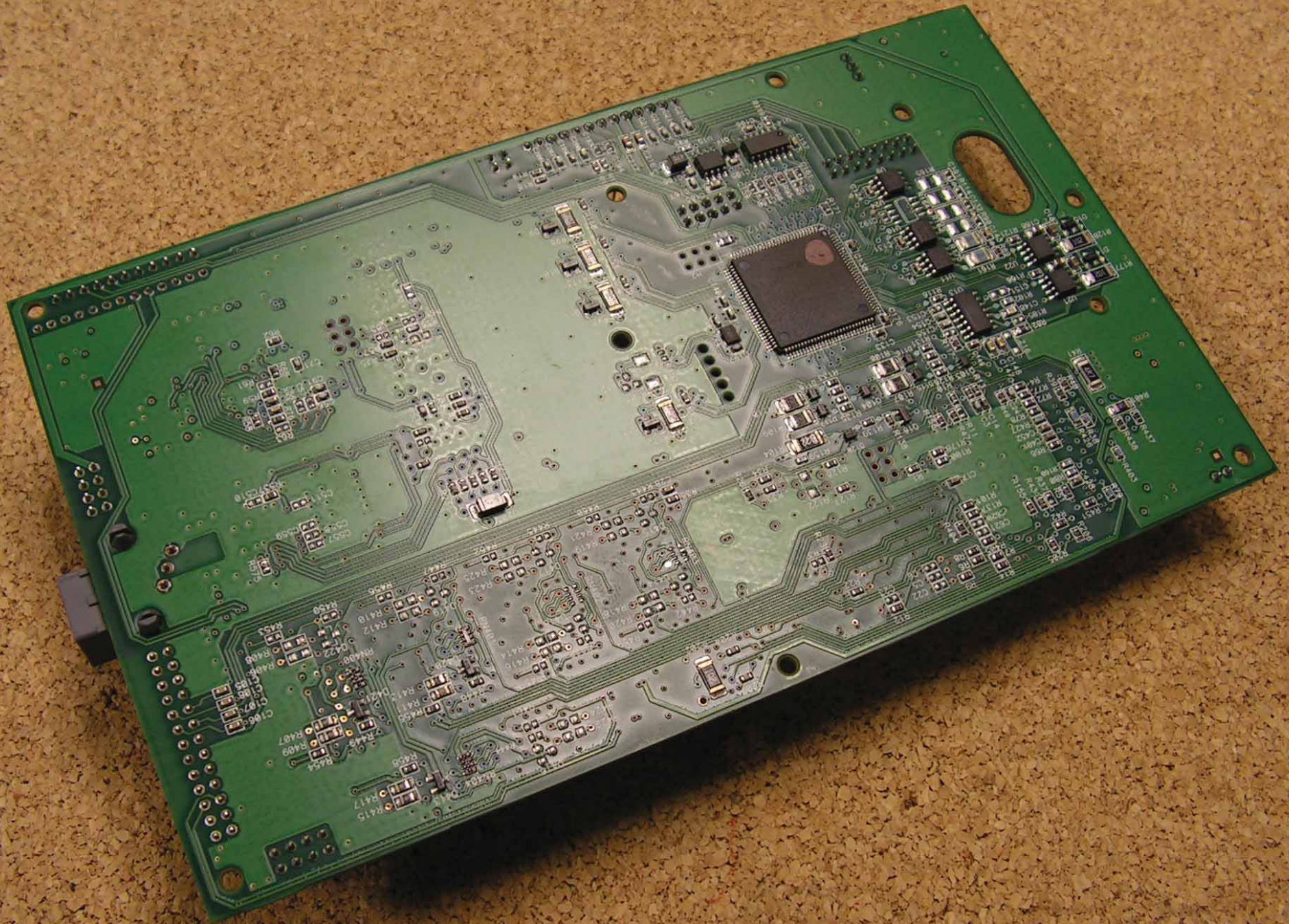
PCB (bottom)



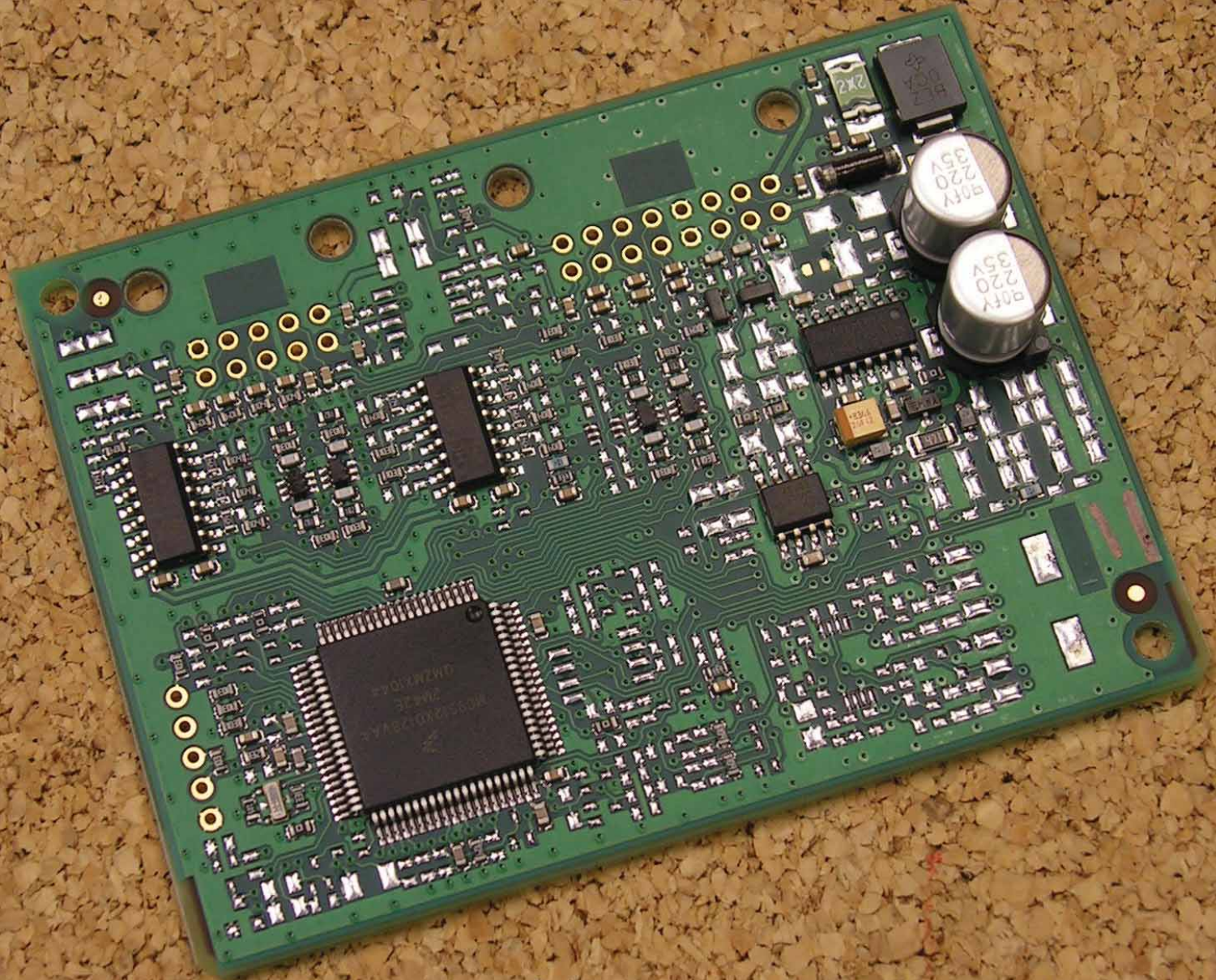
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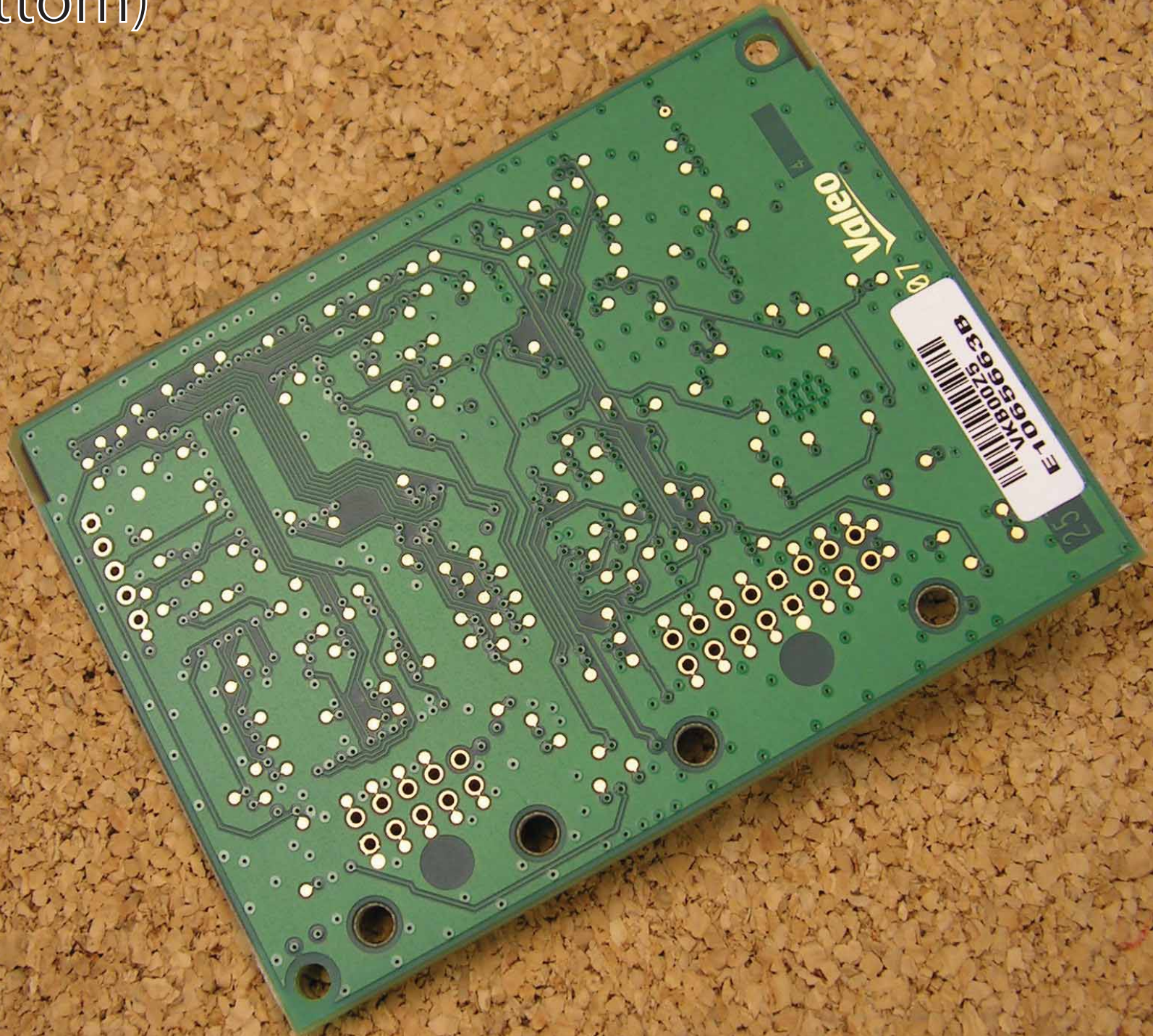
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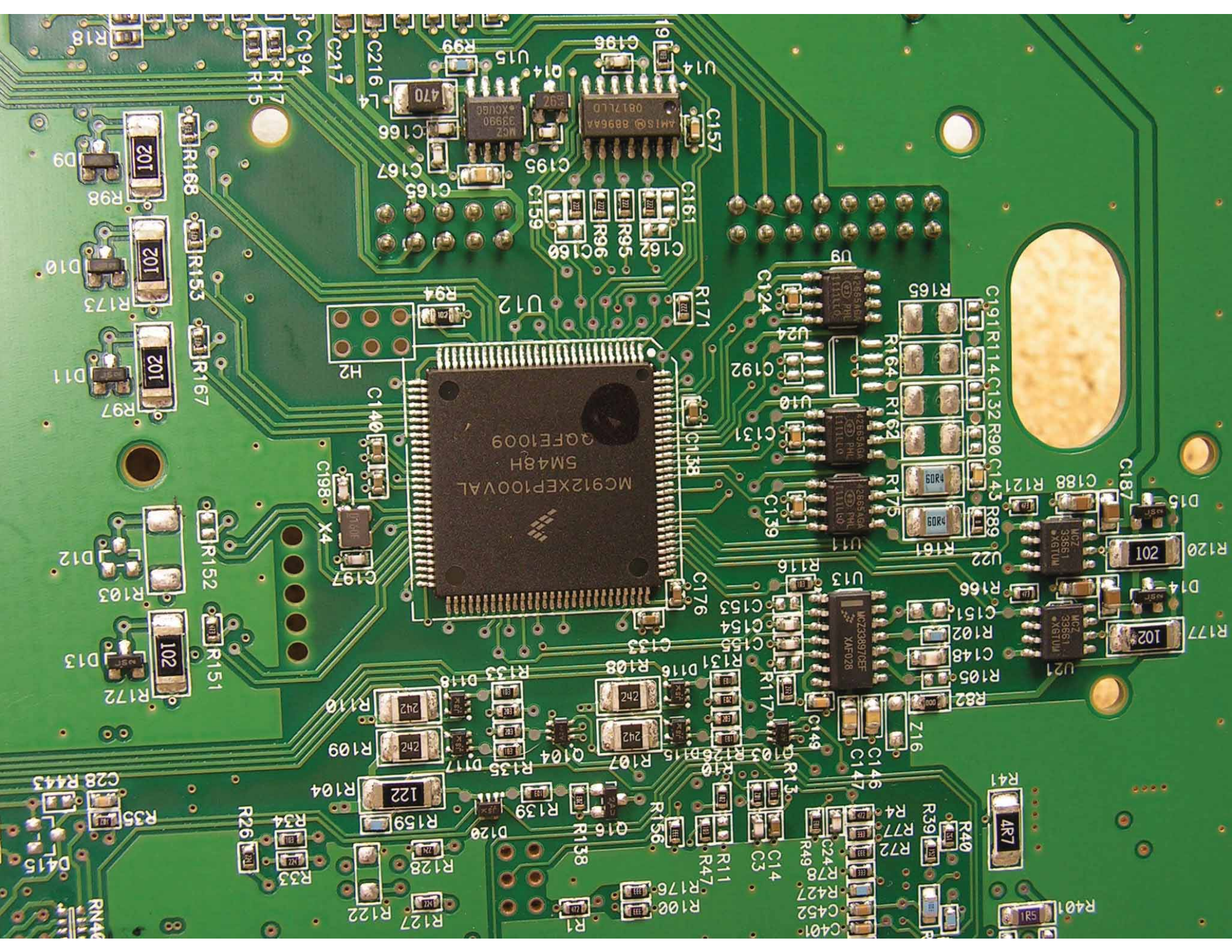


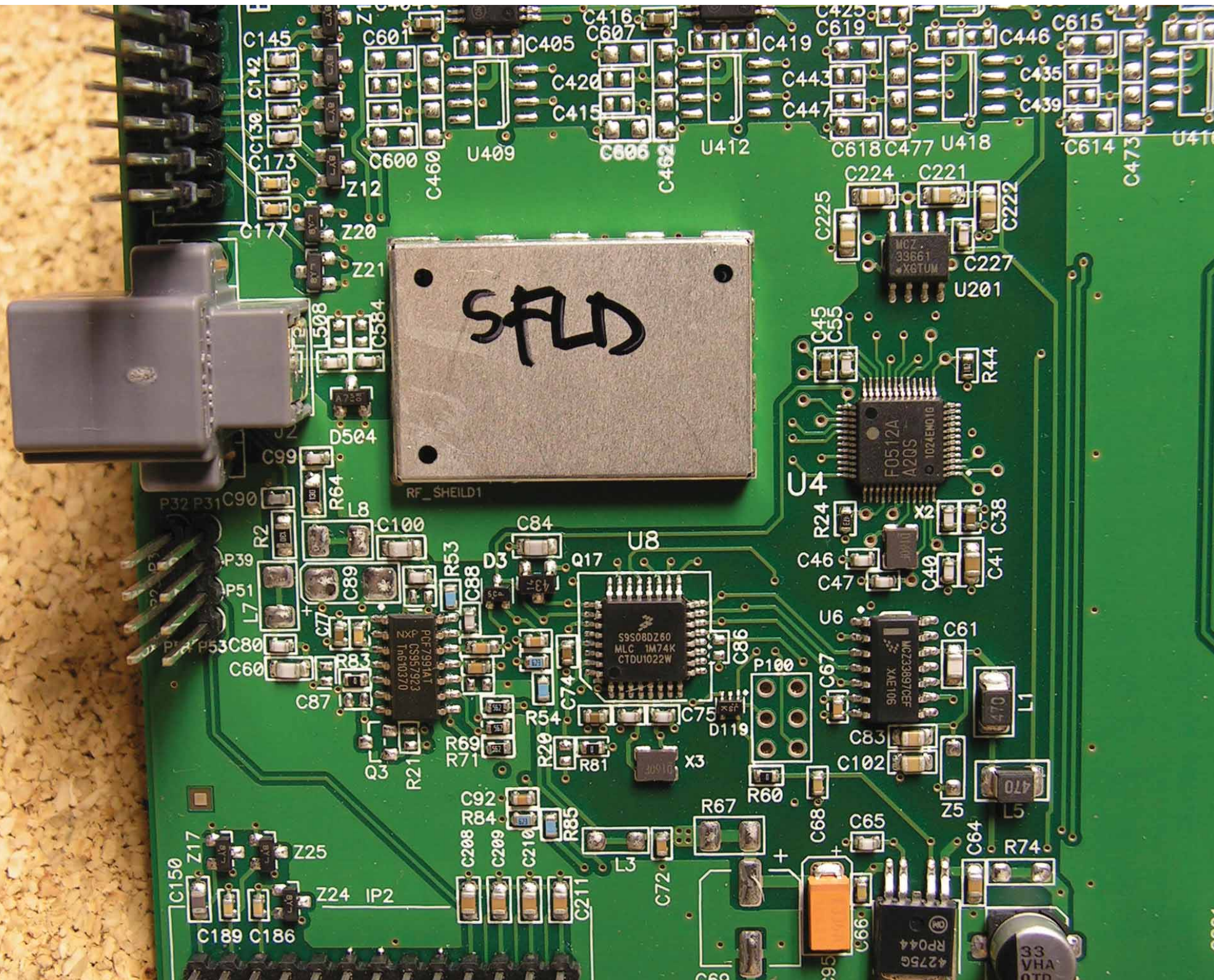
PCB (top)



PCB (bottom)







SFLD

RF_SHEILD1

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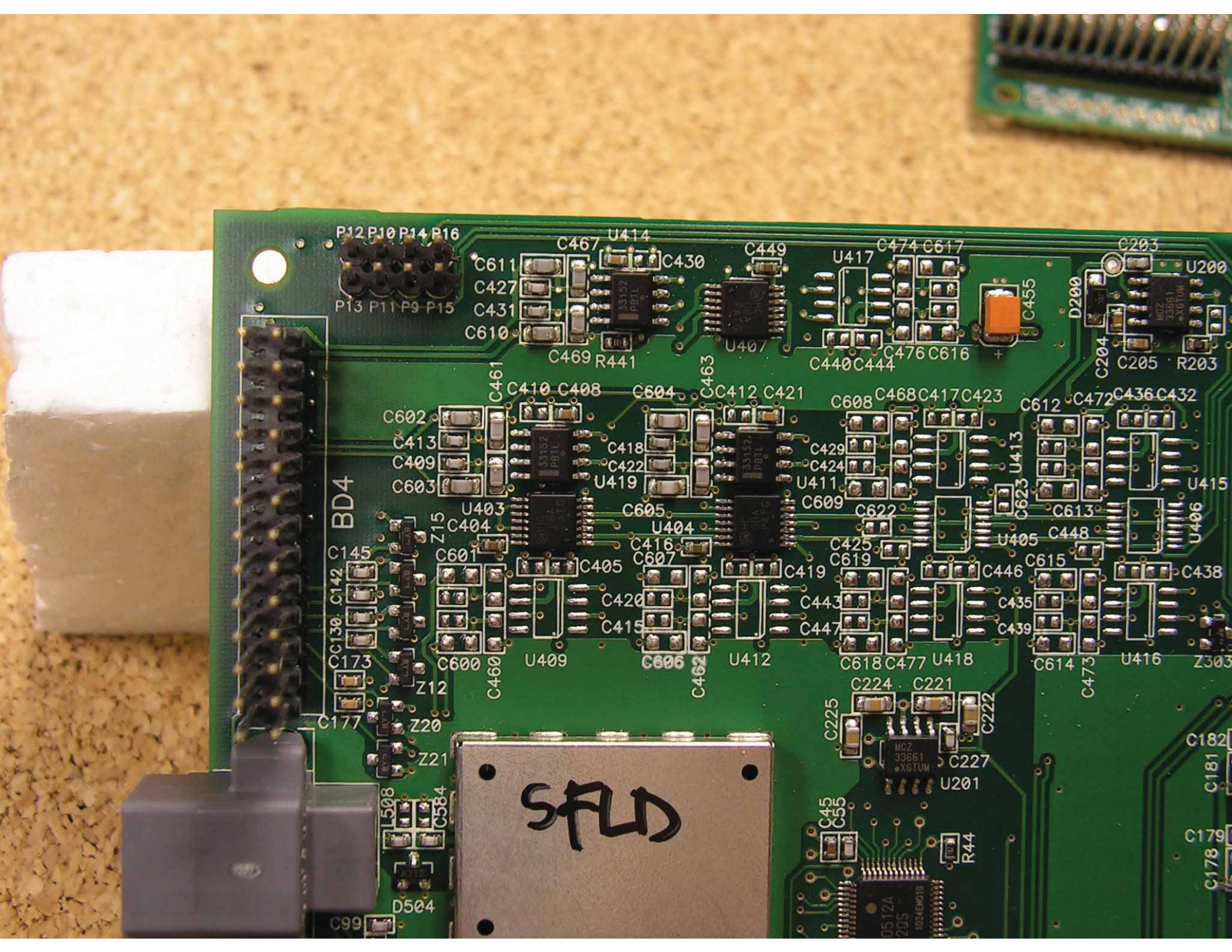
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P12 P10 P14 P16

P13 P11 P9 P15

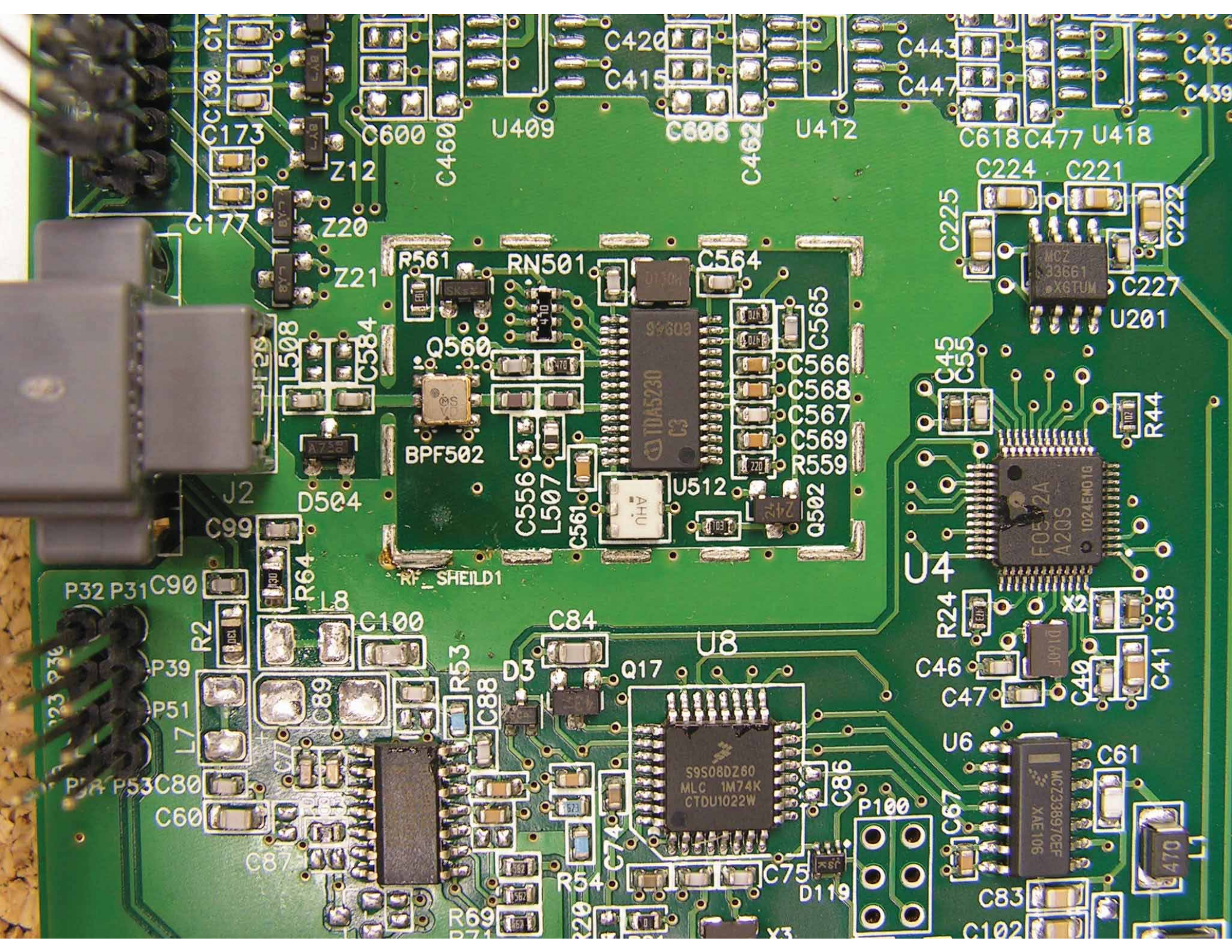
BD4

C99

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SFLD

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