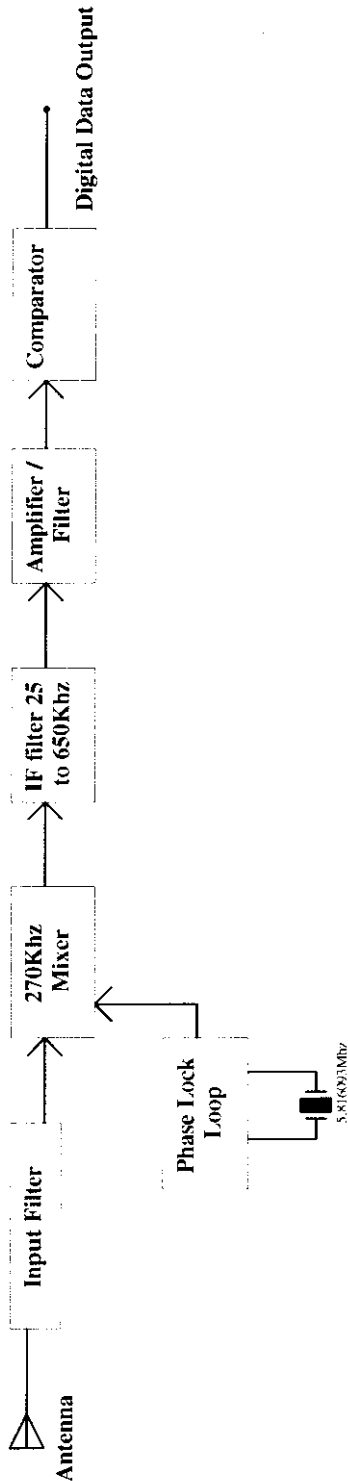


4 BLOCK DIAGRAM(S)



REV. Description		By	Date
Title			
372.5Mhz Superhetrodyne Receiver Block Diagram			
Size	Letter	Number	Revision
Date	20-May-1998	Time	07:27:46
Drawing Number:		Sheet	1 of 0



8 PHOTOS OF TESTED EUT

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The following photos are attached:

Photo 1. Front View

Photo 2. Front View, Cover Removed

Photo 3. Control Board, Component Side

Photo 4. Control Board, Solder Side

Photo 5. Receiver Board, Component Side

Photo 6. Receiver Board, Solder Side