

915B Block Diagrams

The following are the block diagrams for the RF and digital sections of the Circuit Card Assembly for the 915B radio. The blocks shown are as follows:

Top level
RF section
Ethernet PHY
Microprocessor/MAC memory
Microprocessor/MAC
Baseband processor interface

Block Diagrams

Top Level Block Diagram

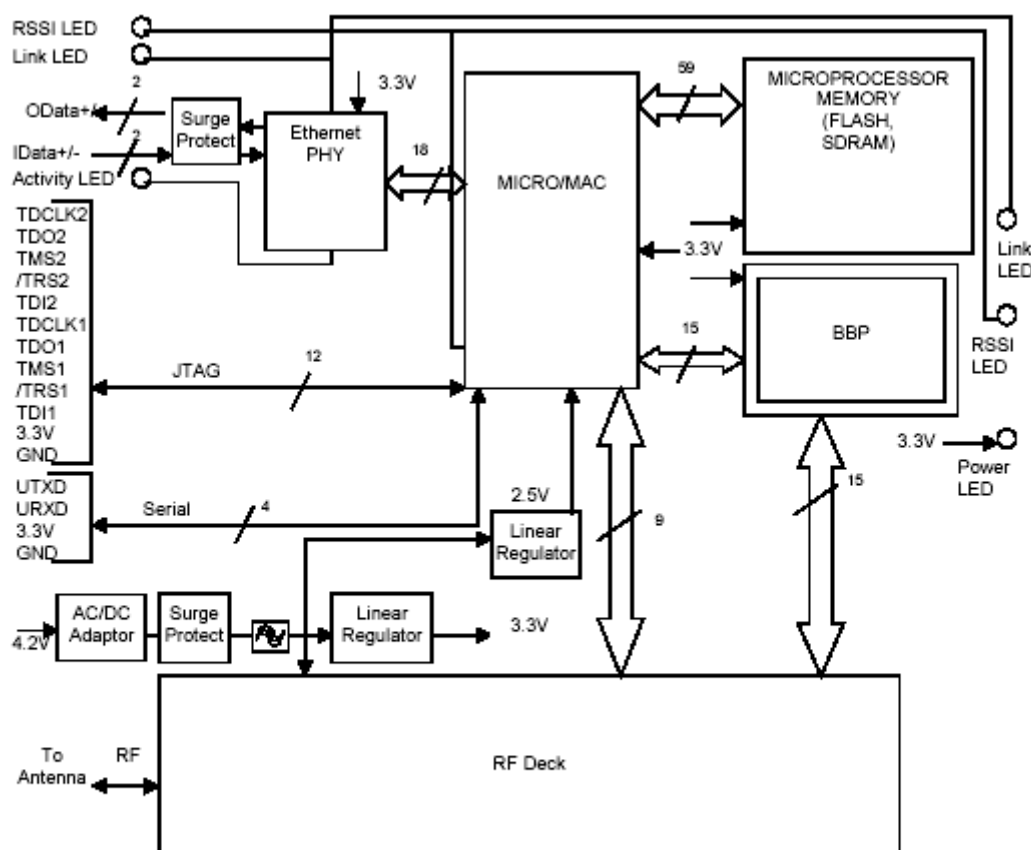


Figure 5.1 – Top Level Block Diagram

Figure 1.1 – Top Level Block Diagram

RF Section

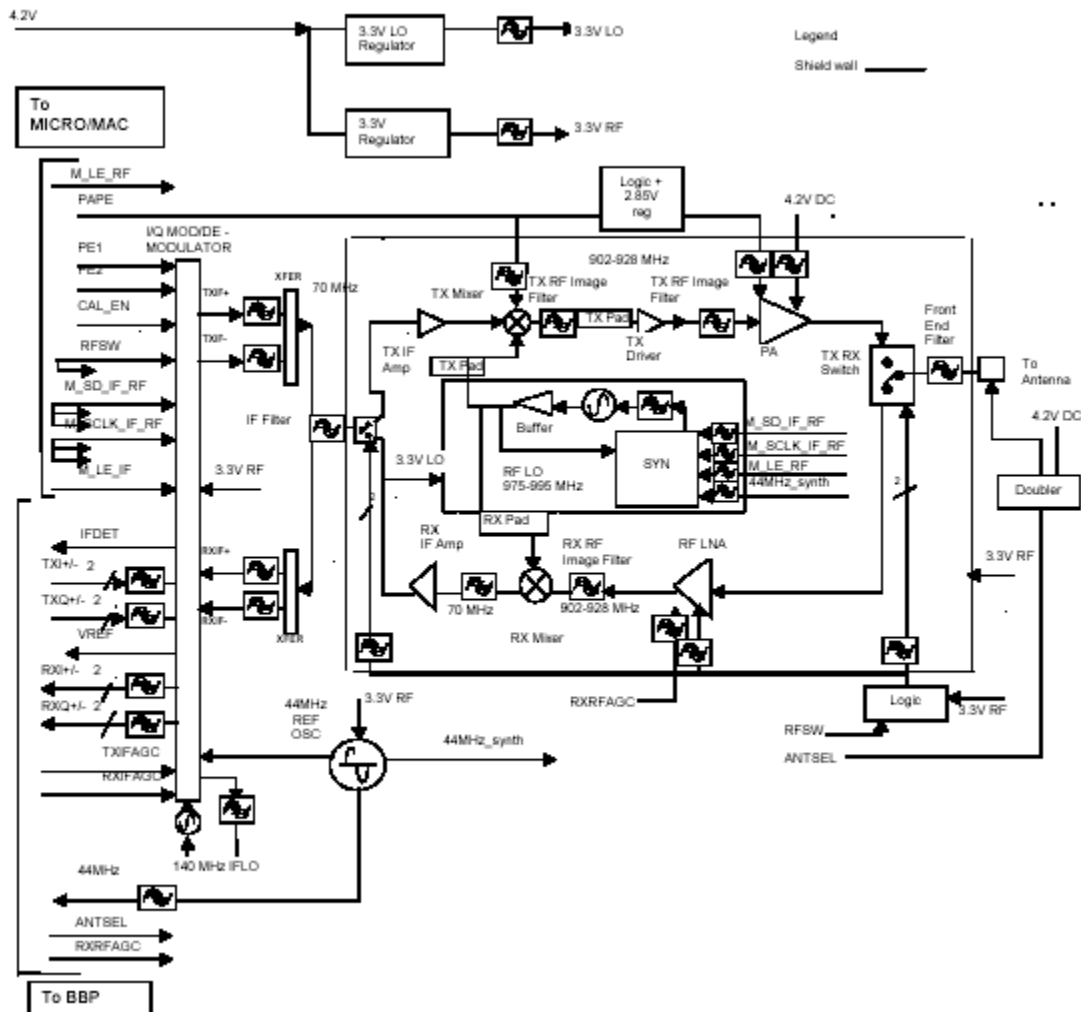
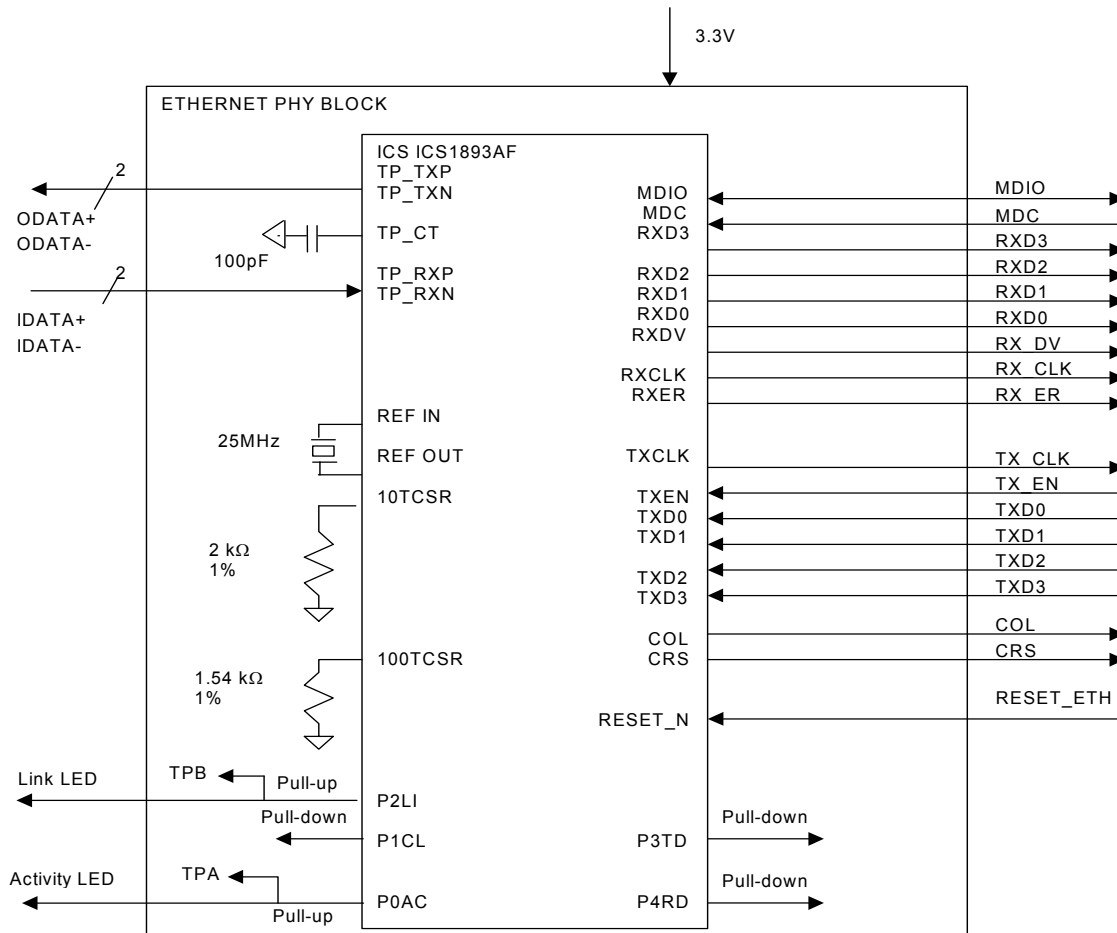


Figure 1.2 – RF Section Block Diagram

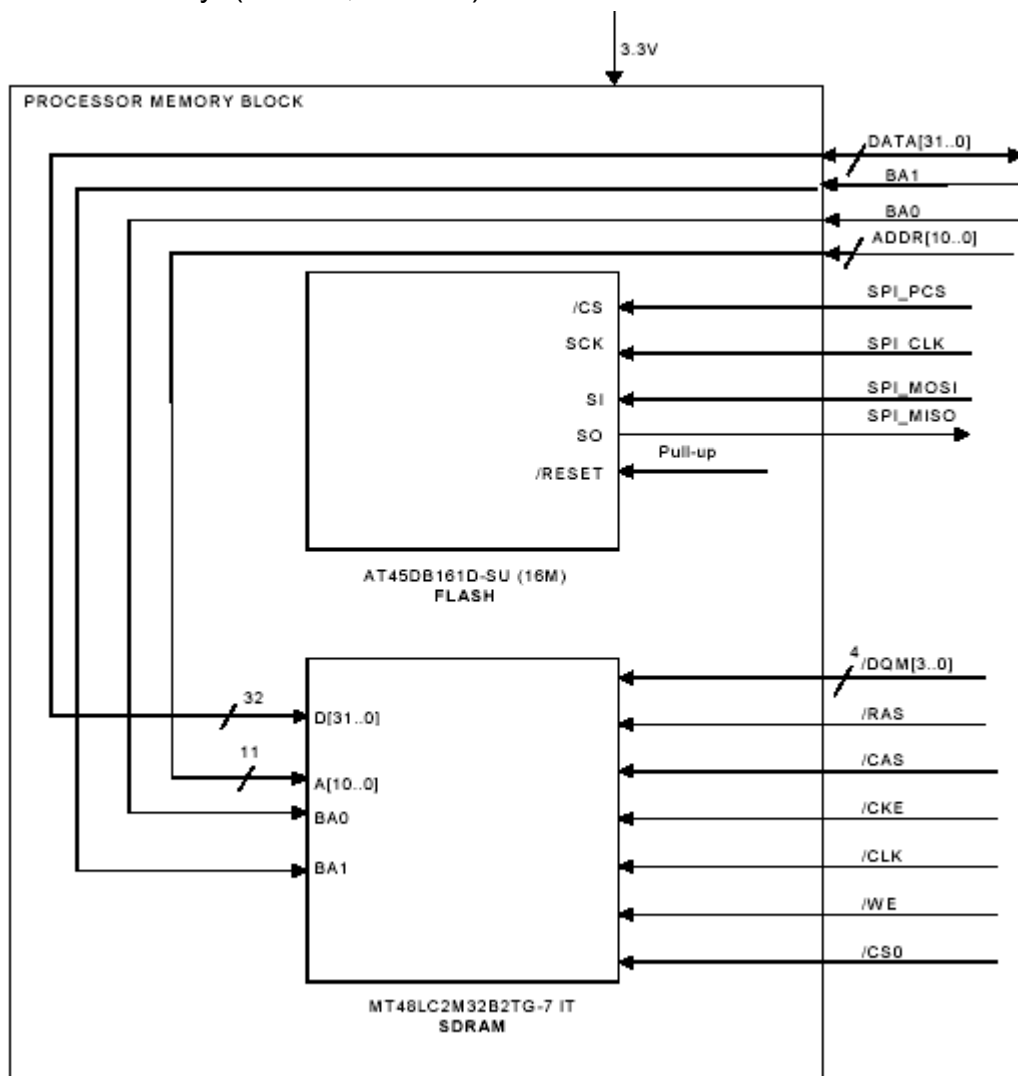
Digital Section

Ethernet PHY



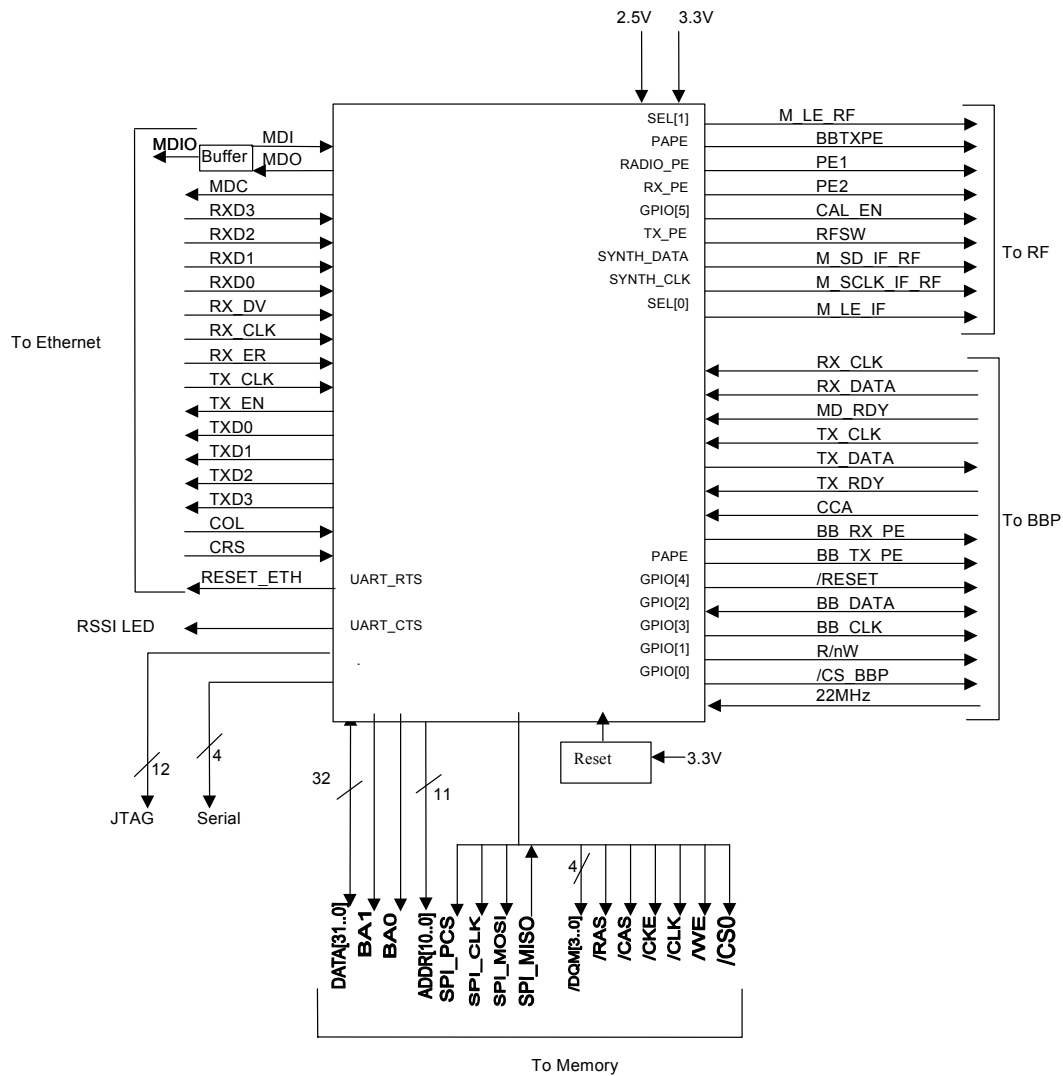
5.3.1 Ethernet PHY Block Diagram

Microprocessor/MAC Memory (FLASH,SDRAM)



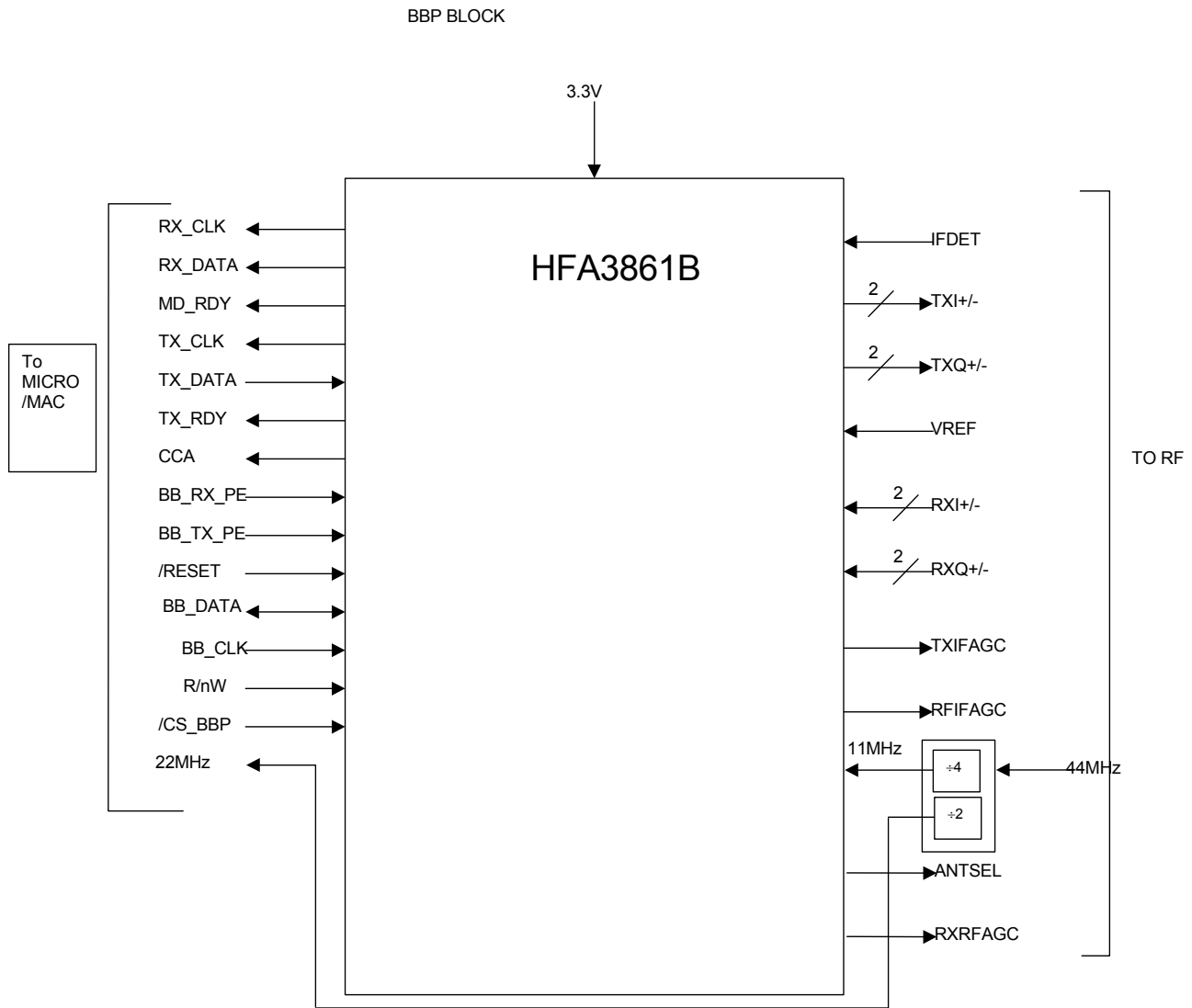
5.3.2 Microprocessor/MAC Memory Block Diagram

Microprocessor/MAC



5.3.3 Microprocessor/MAC Interface Block Diagram

BBP Interface



5.3.4 BBP Interface Block Diagram