

EXHIBIT 4

Test Report

Test Report

TTEMC-F98127

APPLICATION FOR CERTIFICATION
On Behalf of
Full Yes Industrial Corp.
VGA Card

Model : VGA 740

FCC ID : KC7-740

Prepared for : Full Yes Industrial Corp.
3F, No. 5,7, Lane 154, Pao Chiao Rd.,
Hsin Tien City, Taipei Hsien, Taiwan, R.O.C.

Prepared By : Taiwan Tokin EMC Eng. Corp.
No. 53-11, Tin-Fu Tsun, Lin-Kou,
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Tel: (02) 2609-9301, (02) 2609-2133

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Date of Test : Jul. 27 ~ Aug. 01, 1998
Date of Report : Aug. 04, 1998

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TEST REPORT CERTIFICATION

Applicant : Full Yes Industrial Corp.
 Manufacturer : Full Yes Industrial Corp.
 FCC ID : KC7-740
 EUT Description : VGA Card
 (A) MODEL NO. : VGA 740
 (B) SERIAL NO. : N/A
 (C) POWER SUPPLY : DC 5V, 12V (Via PC)

Measurement Procedure Used:

FCC RULES AND REGULATIONS PART 15 SUBPART B CLASS B OCTOBER 1996
 AND FCC / ANSI C63.4-1992

The device described above was tested by TAIWAN TOKIN EMC ENG. CORP. to determine the maximum emission levels emanating from the device. The maximum emission levels were compared to the FCC Part 15B Class B limits both radiated and conducted emissions.

The measurement results are contained in this test report and TAIWAN TOKIN EMC ENG. CORP. is assumed full responsibility for the accuracy and completeness of these measurements. Also, this report shows that the EUT to be technically compliance with the FCC official limits. TAIWAN TOKIN EMC ENG. CORP. recommends that this data was submitted for FCC certification purposes if a 6dB margin below FCC limits is obtained. This report applies to above tested sample only. This report shall not be reproduced in part without written approval of Taiwan Tokin EMC Eng. corp.

Date of Test : Jul. 27 ~ Aug. 01, 1998

Prepared by : Monica Chang 8/26/98
 (MONICA CHANG)

Test Engineer : Allen Wang 8/26/98
 (ALLEN WANG)

Approve & Authorized Signer : Jackie Deng 8/26/98
 (JACKIE DENG)

1. GENERAL INFORMATION

1.1. Description of Device (EUT)

Description	:	VGA Card (INTEL 740 AGP VGA Card)
Model Number	:	VGA 740
FCC ID	:	KC7-740
Applicant	:	Full Yes Industrial Corp. 3F, No. 5, 7, Lane 154, Pao Chiao Rd., Hsin Tien City, Taipei Hsien, Taiwan, R.O.C.
Manufacturer	:	Full Yes Industrial Corp. 3F, No. 5, 7, Lane 154, Pao Chiao Rd., Hsin Tien City, Taipei Hsien, Taiwan, R.O.C.
Date of Test	:	Jul. 27 ~ Aug. 01, 1998

1.2. Tested Supporting System Details

1.2.1. PERSONAL COMPUTER

Mother Board	:	Asus, M/N P2L97 FCC ID By DoC
CPU	:	Intel Pentium II 233MMX
S.P.S	:	Enlight, M/N EN-8257901
Floppy Drive Disk 3.5"	:	Mitsumi, M/N D359T6
Case	:	Enlight, MN EN-7230
Hard Disk Drive	:	Seagate, M/N ST32122A
VGA Card (EUT)	:	Full Yes, M/N VGA 740 FCC ID KC7-740
Disk Ctrl Card	:	Within Mother Board
Serial/Parallel Card	:	Within Mother Board
S-Video Cable	:	Shielded, Detachable, 1.5m
Composite Video Cable	:	Shielded, Detachable, 1.8m
Power Cord	:	Non-Shielded, Detachable, 1.8m

1.2.2. MONITOR

Model Number	:	CM752ET
Serial Number	:	T8E004368
FCC ID	:	FCC ID By DoC
Manufacturer	:	Hitachi
Data Cable	:	Shielded, Detachable, 1.7m
Power Cord	:	Non-Shielded, Detachable, 1.8m

1.2.3. KEYBOARD

Model Number	:	BTC-5139
Serial Number	:	73B304241
FCC ID	:	E5XKBM111
Manufacturer	:	Behavior Tech Computer Corp.
Data Cable	:	Shielded, Undetachable, 1.2m

1.2.4. PRINTER

Model Number	:	2225C
Serial Number	:	2526S40437
FCC ID	:	BS46XU2225C
Manufacturer	:	Hewlett Packard
Power Cord	:	Nonshielded, Undetachable, 1.8m
Data Cable	:	Shielded, Detachable, 1.2m

1.2.5. MODEM #1

Model Number	:	1414
Serial Number	:	950098203
FCC ID	:	IFAXDM1414
Manufacturer	:	Aceex
Data Cable	:	Shielded, Detachable, 1.2m
Power Adapter	:	Amigo, Model AM-91000A Non-Shielded, Undetachable, 1.8m

1.2.6. MODEM #2

Model Number	:	1414
Serial Number	:	970024520
FCC ID	:	IFAXDM1414
Manufacturer	:	Aceex
Data Cable	:	Shielded, Detachable, 1.2m
Power Adapter	:	Amigo, Model AM-91000A Non-Shielded, Undetachable, 1.8m

1.2.7. MOUSE

Model Number	:	M-S34
Serial Number	:	LZA65200980
FCC ID	:	DZL210472
Manufacturer	:	Logitech
Data Cable	:	Non-Shielded, Undetachable, 1.9m

1.2.8. USB GAMEPAD #1

Model Number : INT-002
 Serial Number : N/A
 FCC ID : CWTEAK032
 Manufacturer : Alps
 Data Cable : Shielded, Undetachable, 1.6m
 Bonded a ferrite core

1.2.9. USB GAMEPAD #2

Model Number : INT-003
 Serial Number : N/A
 FCC ID : CWTEAK032
 Manufacturer : Alps
 Data Cable : Shielded, Undetachable, 2.3m
 Bonded a ferrite core

1.2.10. TELEVISION (Connect to EUT)

Model Number : PA5020C
 Serial Number : N/A
 Manufacturer : Philips
 S Cable : Shielded, Detachable, 1.5m
 AV Cable : Nonshielded, Detachable, 1.5m
 Power Cord : Nonshielded, Undetachable, 3m

1.3. Description of Test Facility

Site Description : Jul. 15, 1996 Re-file on
 (No. 2 Open Site) Federal Communication Commission
 FCC Engineering Laboratory
 7435 Oakland Mills Road
 Columbia, MD 21046, U.S.A.

Name of Firm : Taiwan Tokin EMC Eng. Corp.

Site Location : No. 53-11, Tin-Fu Tsun, Lin-Kou,
 Taipei Hsien, Taiwan, R.O.C.

NVLAP Lab Code : 200077-0

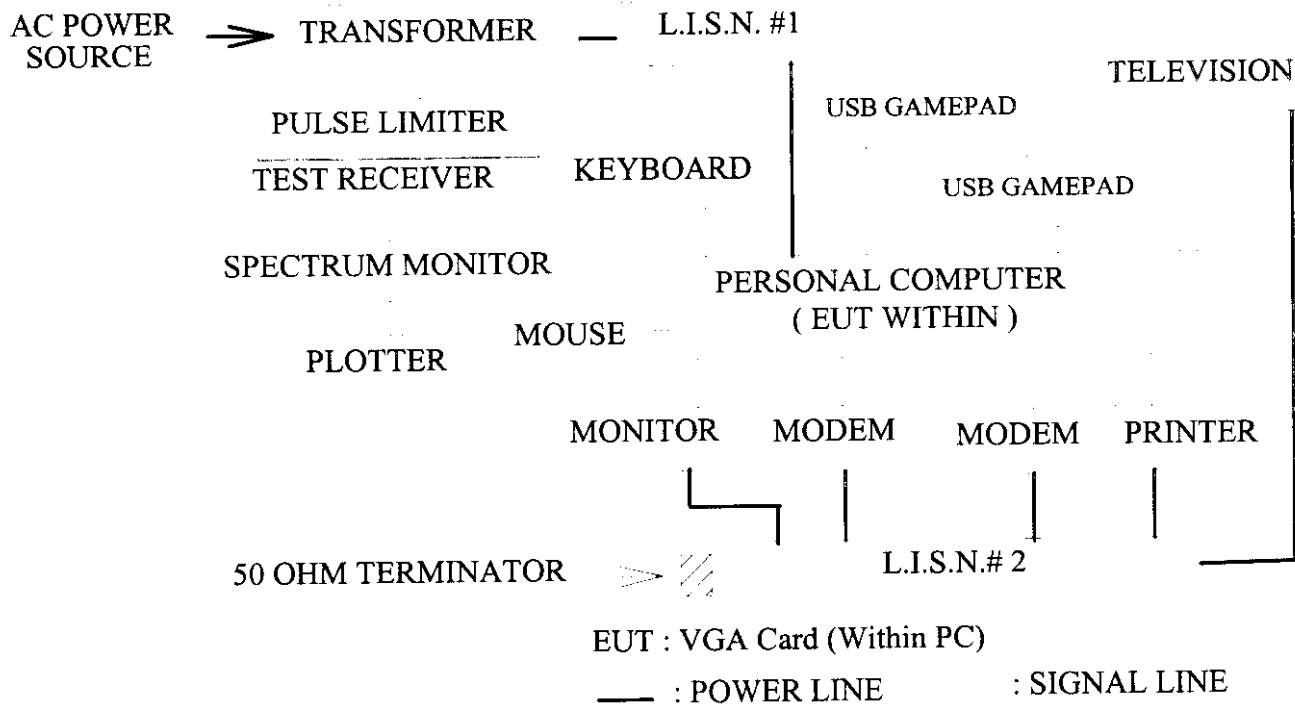
2. POWERLINE CONDUCTED TEST

2.1. Test Equipment

The following test equipments are used during the power line conducted tests :

Item	Type	Manufacturer	Model No.	Serial No.	Last Cal.	Cal. Interval
1.	Test Receiver	Rohde & Schwarz	ESH3	880647/035	Jun.24, 98'	1 Year
2.	L.I.S.N. # 1	Kyoritsu	KNW-407	8-855-9	Apr.14, 98'	1 Year
3.	L.I.S.N. # 2	Kyoritsu	KNW-407	8-881-13	Apr.14, 98'	1 Year

2.2. Block Diagram of Test Setup



2.3. Powerline Conducted Emission Limit (CLASS B)

Frequency	Maximum RF Line Voltage	
	uV	dBuV
0.45MHz ~ 30MHz	250	48

REMARKS : RF LINE VOLTAGE (dBuV) = 20 log RF LINE VOLTAGE (uV)

2.4. EUT's Configuration during Compliance Measurement

The following equipments were installed on RF LINE VOLTAGE measurement to meet the Commission requirement and operating in a manner which tended to maximize its emission characteristics in a normal application.

2.4.1. VGA Card (EUT)

Model Number	:	VGA 740
Serial Number	:	N/A
FCC ID	:	KC7-740
Manufacturer	:	Full Yes Industrial Corp.

2.4.2. Supporting System : As in section 1.2

2.5. Operating Condition of EUT

- 2.5.1. Setup the EUT and simulator as shown on 2.2.
- 2.5.2. Turned on the power of all simulators.
- 2.5.3. Personal Computer read data from disk.
- 2.5.4. Personal Computer send "H" character to monitor through VGA Card (EUT) and the screen of monitor displayed "H" pattern by EUT's Resolution.
- 2.5.5. Personal Computer send "H" character to TV through VGA Card (EUT) and the screen of TV displayed "H" pattern.
- 2.5.6. The other peripheral devices were drove and operated in turn during all testing.

2.6. Test Procedure

The EUT (within PC) was connected to the power mains through a line impedance stabilization network (L.I.S.N. #1) and the other peripheral devices power cord were connected to the power mains through a line impedance stabilization network (L.I.S.N. #2) This provided a 50 ohm coupling impedance for the measuring equipment. (Please refer to the block diagram of the test setup and photographs.) Both sides of A.C. line were checked for maximum conducted interference. In order to find the maximum emission, the relative positions simulators of the interface cables should be manipulated according to FCC ANSI C63.4-1992 during conducted measurement.

The bandwidth of the R&S Test Receiver ESH3 was set at 10KHz.

The frequency range from 450KHz to 30MHz was checked.

Six kinds of horizontal working frequency are investigated during pre-scanning and reported the two worst test modes (2), (6) in section 2.7., the others test data are attached within Appendix I. The details of test modes are as follows :

- (1)31.5KHz (640x480, 60Hz) w/ TV Out
- (2)38KHz (800x600, 60Hz)
- (3)48KHz (1024x768, 60Hz)
- (4)64KHz (1280x1024, 60Hz)
- (5)80KHz (1280x1024, 75Hz)
- (6)93.8KHz (1600x1200, 75Hz)

2.7. Line Conducted RF Voltage Measurement Results

The frequency range from 450KHz to 30 MHz was investigated.
All emissions not report below are too low against the prescribed limits.

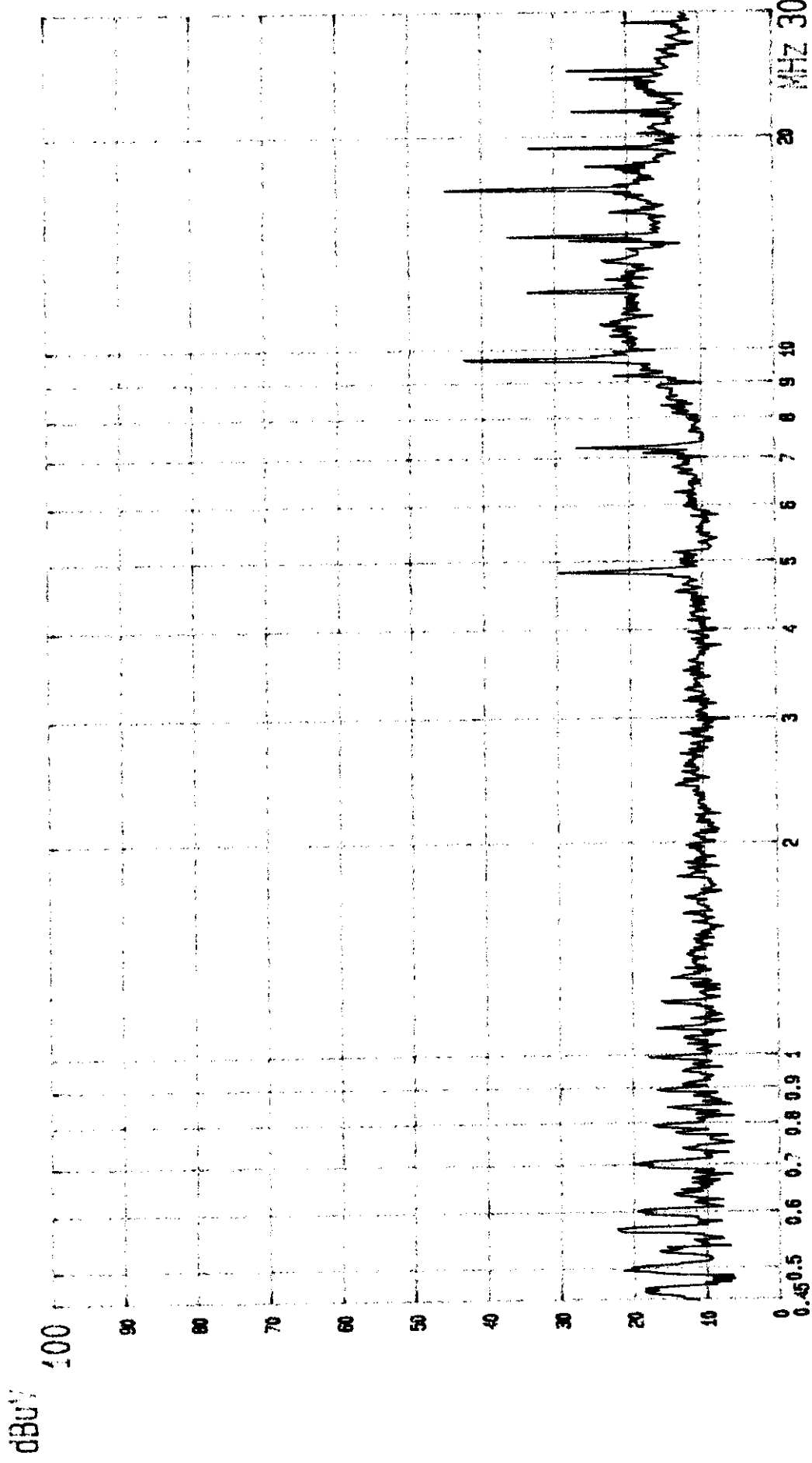
Date of Test : Jul. 27, 1998 Temperature : 28 °C

EUT : VGA Card Humidity : 50 %

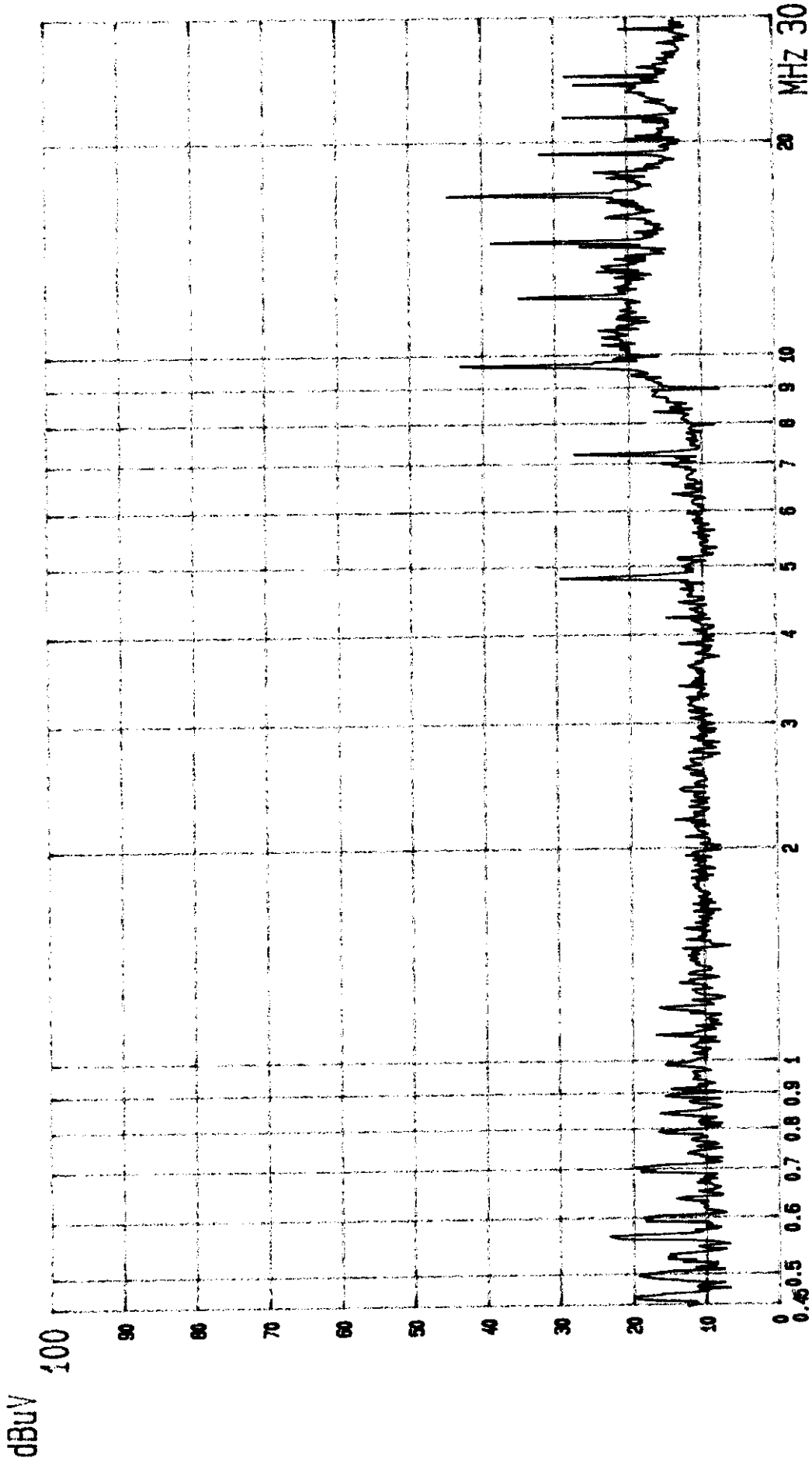
Test Mode : 38KHz (800x600, 60Hz)

Frequency (MHz)	Factor dB	Measurement (dBuV)		Reading (dBuV)		Limits (dBuV)	Margin (dBuV)	
		VA	VB	VA	VB		VA	VB
0.5601	0.5	19.9	*	20.4	*	48.0	27.6	*
0.5602	0.5	*	20.1	*	20.6	48.0	*	27.4
4.8465	0.8	*	26.2	*	27	48.0	*	21
4.8468	0.8	26.3	*	27.1	*	48.0	20.9	*
9.6931	0.8	39.7	*	40.5	*	48.0	7.5	*
9.6939	0.8	*	40.1	*	40.9	48.0	*	7.1
12.1133	1.0	33.3	33.9	34.3	34.9	48.0	13.7	*
14.5421	1.0	34.4	*	35.4	*	48.0	12.6	*
14.5434	1.0	*	35.2	*	36.2	48.0	*	11.8
16.9431	1.0	42.8	*	43.8	*	48.0	4.2	*
16.9481	1.0	*	43.5	*	44.5	48.0	*	3.5

- Remark :
1. All readings are Quasi-Peak values.
 2. Factor = Insertion Loss + Cable Loss
 3. The worst emission was detected at 16.9481MHz with corrected signal level of 44.5dBuV (limit is 48dBuV) when the VB side of the EUT was connected to L.I.S.N.



Date 27. JUL '98 Time 14:56:26
 Full Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740
 Line: VA. MEMO: 38KHz (800x600; 60Hz); N/TV (PEAK VALUE) ITEM: PAGE: 004.

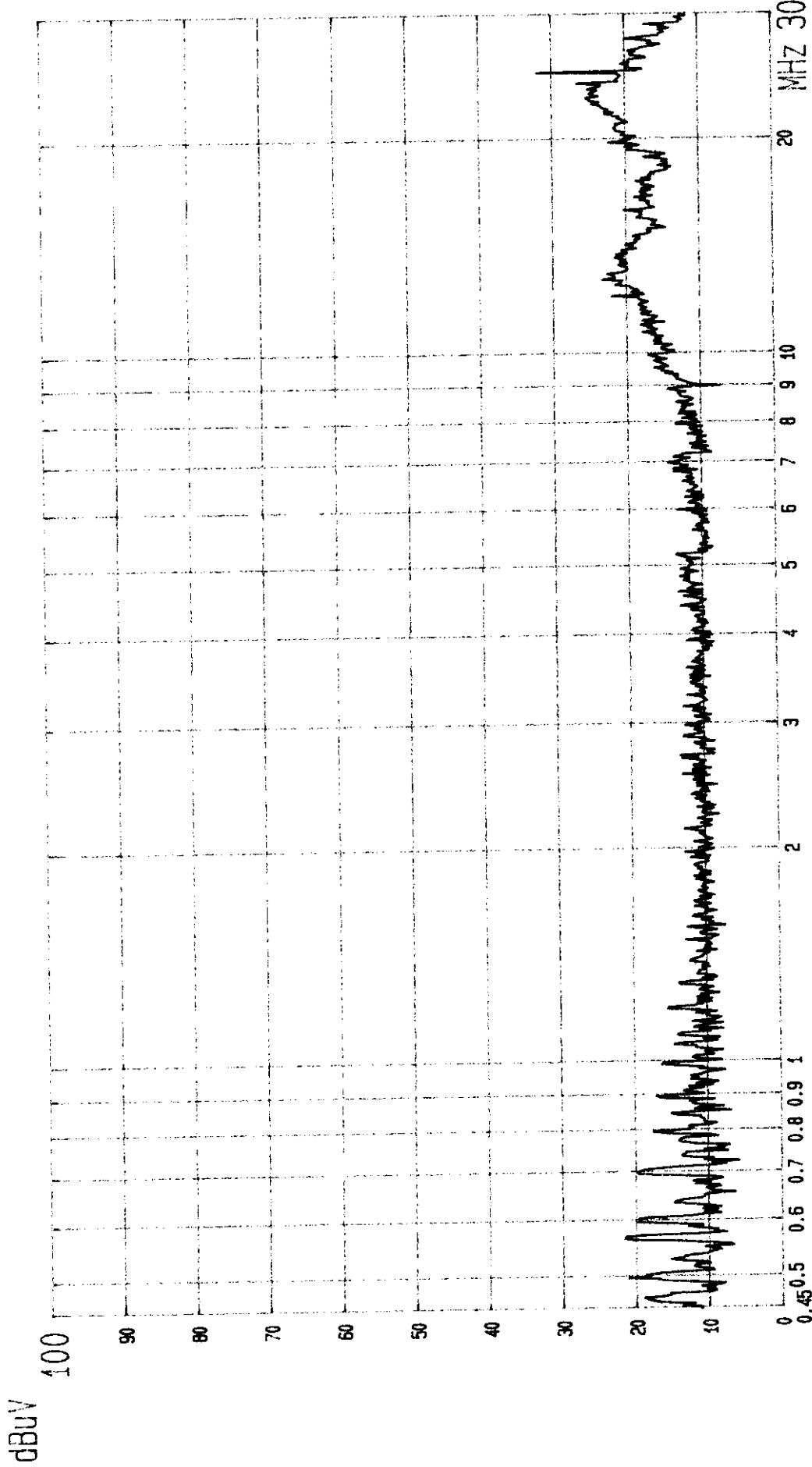


Date 27. JUL '98 Time 14:50:52
 FULL Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740
 LINE: VB. MEMO: 38KHz (800X600; 60Hz): W/TV
 (PEAK VALUE) ITEM: PAGE: 003.

Date of Test : Jul. 27, 1998 Temperature : 28 °C
 EUT : VGA Card Humidity : 50 %
 Test Mode : 93.8KHz (1600x1200, 75Hz)

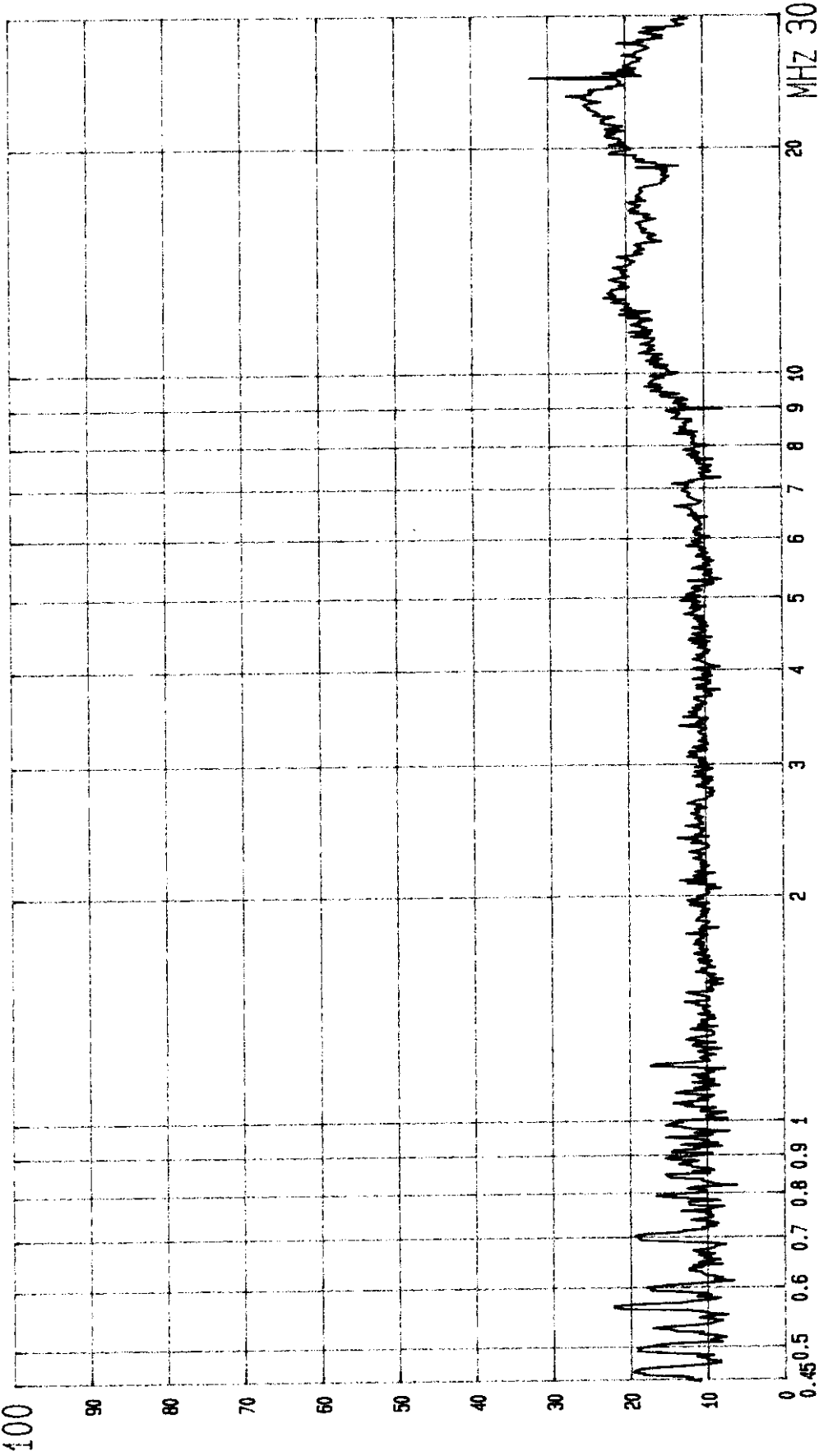
Frequency (MHz)	Factor dB	Measurement (dBuV)		Reading (dBuV)		Limits (dBuV)	Margin (dBuV)	
		VA	VB	VA	VB		VA	VB
0.4550	0.5	*	16.4	*	16.9	48.0	*	31.1
0.4551	0.5	15.7	*	16.2	*	48.0	31.8	*
0.5601	0.5	*	19.8	*	20.3	48.0	*	27.7
0.5602	0.5	19.7	*	20.2	*	48.0	27.8	*
0.7001	0.5	*	14.3	*	14.8	48.0	*	33.2
0.9895	0.5	11.4	*	11.9	*	48.0	36.1	*
12.6516	1.0	*	17.2	*	18.2	48.0	*	29.8
12.7142	1.0	15.9	*	16.9	*	48.0	31.1	*
12.8479	1.0	22.5	*	23.5	*	48.0	24.5	*
23.5413	1.1	*	19.3	*	20.4	48.0	*	27.6
24.9617	1.1	*	31.2	*	32.3	48.0	*	15.7
24.9619	1.1	30.7	*	31.8	*	48.0	16.2	*

- Remark :
1. All readings are Quasi-Peak values.
 2. Factor = Insertion Loss + Cable Loss
 3. The worst emission was detected at 24.9617MHz with corrected signal level of 32.3dBuV (limit is 48dBuV) when the VB side of the EUT was connected to L.I.S.N.



Date 27. JUL '98 Time 16: 18: 23
 FULL Yes EUT: FYI - INTEL 740 AGP VGA Card M/N: VGA 740 PAGE: 012.
 LINE: VA. MEMO: 93.8KHZ (1600X1200; 75HZ) (PEAK VALUE) ITEM: C.

dBuV



--- Date 27. JUL '98 Time 16: 14: 42
Full Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740
LINE: VB. MEMO: 93.8KHZ (1600X1200; 75HZ) (PEAK VALUE) ITEM: 011.

3. RADIATED EMISSION TEST

3.1. Test Equipment

The following test equipments are used during the radiated emission tests :

3.1.1. For Radiation Measurement (Anechoic Chamber)

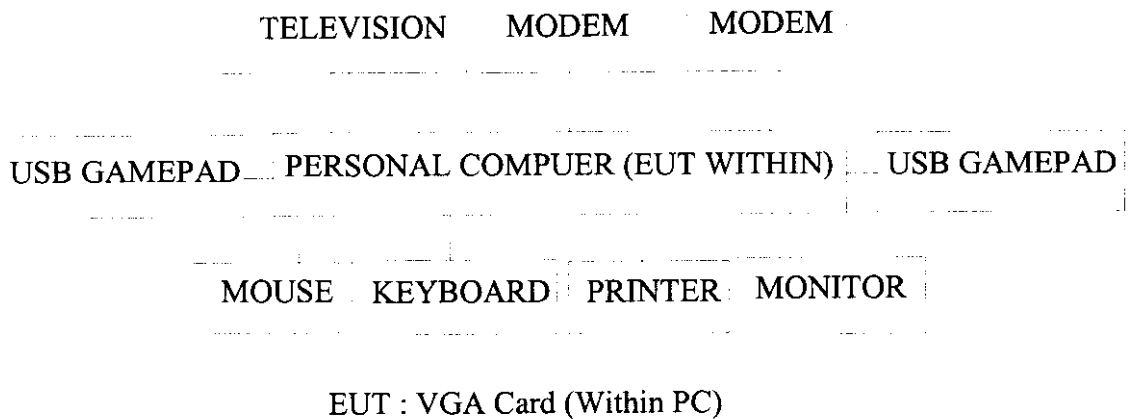
Item	Type	Manufacturer	Model No.	Serial No.	Last Cal.	Cal. Interval
1.	Spectrum Analyzer	HP	8593A	3212A01727	Jul.25, 98'	1 Year
2.	Pre-Amplifier	HP	8447D	2944A06305	May.13,98'	1 Year
3.	Broadband Antenna	Schwarzbeck	BBA 9106	A3L	Dec.24, 97'	1 Year
4.	Broadband Antenna	Schwarzbeck	UHALP 9107	A3H	Dec.24, 97'	1 Year

3.1.2. For Radiation Measurement (No. 2 Open Field Site)

Item	Type	Manufacturer	Model No.	Serial No.	Last Cal.	Cal. Interval
1.	Test Receiver	Rohde&Schwarz	ESVP	893202/001	Jul.24, 98'	1 Year
2.	Broadband Antenna	Chase	VBA6106A	1240	Jan. 14, 98'	1 Year
3.	Broadband Antenna	Schwarzbeck	UHALP 9108-A	0139	Jan. 14, 98'	1 Year

3.2. Block Diagram of Test Setup

3.2.1. Block Diagram of connection between EUT and simulators



3.2.2. Anechoic Chamber and Open Field Test Site (3m) Setup Diagram

ANTENNA TOWER

ANTENNA ELEVATION VARIES FROM 1METER TO 4 METERS

3 METERS

EUT

0.8
METER

TURN TABLE

GROUND PLANE

3.3. Radiation Limit (CLASS B)

FREQUENCY	DISTANCE	FIELD STRENGTHS LIMITS	
		uV/M	dBuV/M
MHz	Meters		
30 ~ 88	3	100	40.0
88 ~ 216	3	150	43.5
216 ~ 960	3	200	46.0
960 ~ 1000	3	500	54.0

- Remark :
- (1) Emission level (dBuV/M) = 20 log Emission level (uV/M)
 - (2) The tighter limit applies at the edge between two frequency bands.
 - (3) Distance refers to the distance in meters between the measuring instrument antenna and the closed point of any part of the device or system.

3.4. EUT's Configuration during Compliance Measurement

The configuration of EUT and its simulators are same as those used in conducted measurement. Please refer to 2.4.

3.5. Operating Condition of EUT

Same as conducted measurement which is listed in 2.5.

3.6. Test Procedure

The EUT and its simulators were placed on a turn table which was 0.8 meter above ground. The turn table rotated 360 degrees to determine the position of the maximum emission level. EUT was set 3 meters away from the receiving antenna which was mounted on a antenna tower. The antenna moved up and down between 1 meter and 4 meters to find out the maximum emission level. Broadband antenna (calibrated biconical and log periodical antenna) and dipole antenna were used as receiving antenna. Both horizontal and vertical polarization of the antenna were set on measurement. In order to find the maximum emission, all of the interface cables were manipulated according to ANSI C63.4-1992 during radiated measurement.

The bandwidth of the R&S TEST RECEIVER ESVP was set at 120KHz.

The frequency range from 30MHz to 1000MHz was checked.

The following operating conditions were measured within Anechoic Chamber and all the scanning waveform were attached within Appendix II, which include :

- (1)31.5KHz (640x480, 60Hz) w/ TV Out
- (2)38KHz (800x600, 60Hz)
- (3)48KHz (1024x768, 60Hz)
- (4)64KHz (1280x1024, 60Hz)
- (5)80KHz (1280x1024, 75Hz)
- (6)93.8KHz (1600x1200, 75Hz)

Finally, re-measured the worst operating situation (93.8KHz) on No. 2 Open Field Test Site and all the test results are listed in section 3.7.

3.7. Radiated Emission Noise Measurement Results

The frequency spectrum from 30 MHz to 1000 MHz was investigated. All the emissions not report below are too low against the FCC CLASS B limit..

Date of Test : Aug. 01, 1998 Temperature : 32 °C
 EUT : VGA Card Humidity : 60 %
 Test Mode : 93.8KHz (1600x1200, 75Hz)

Frequency MHz	Antenna Factor dB/m	Cable Meter Reading		Emission Level		
		Loss dB	Horizontal dBuV	Horizontal dBuV/m	Limits dBuV/m	Margin dBuV/m
50.793	15.57	2.00	7.00	24.57	40.00	15.43
76.192	13.18	2.43	13.00	28.61	40.00	11.39
126.984	19.87	3.12	8.90	31.89	43.50	11.61
152.380	21.03	3.49	4.50	29.02	43.50	14.48
203.137	22.17	4.08	8.00	34.25	43.50	9.25
253.971	23.48	4.70	1.30	29.48	46.00	16.52
279.367	25.18	4.86	4.80	34.84	46.00	11.16
304.660	13.62	5.19	4.85	23.66	46.00	22.34
330.116	13.94	5.43	6.20	25.57	46.00	20.43
482.550	17.34	6.97	2.21	26.52	46.00	19.48
684.698	20.01	8.32	6.80	35.13	46.00	10.87
711.113	20.54	8.42	7.50	36.46	46.00	9.54
736.507	21.29	8.78	7.00	37.07	46.00	8.93
* 761.908	21.24	8.89	7.40	37.53	46.00	8.47

- Remark :
1. All readings are Quasi-Peak values.
 2. The worst emission was detected at 761.908MHz with corrected signal level of 37.53dBuV/m (limit is 46.0dBuV/m) when the antenna was at horizontal polarization and was at 2.1m high and the turn table was at 10° .
 3. 0° was the table front facing the antenna. Degree is calculated from 0° clockwise facing the antenna.

Date of Test : Aug. 01, 1998 Temperature : 32 °C
 EUT : VGA Card Humidity : 60 %
 Test Mode : 93.8KHz (1600x1200, 75Hz)

Frequency MHz	Antenna Factor dB/m	Cable Meter Reading		Emission Level		
		Loss dB	Vertical dBuV	Vertical dBuV/m	Limits dBuV/m	Margin dBuV/m
50.794	15.17	2.00	10.90	28.07	40.00	11.93
76.191	14.87	2.43	17.20	34.50	40.00	5.50
* 126.987	18.99	3.12	18.20	40.31	43.50	3.19
152.384	20.72	3.49	12.60	36.81	43.50	6.69
203.176	21.34	4.08	12.20	37.62	43.50	5.88
253.971	23.82	4.70	4.60	33.12	46.00	12.88
279.370	24.81	4.86	1.60	31.27	46.00	14.73
304.760	14.17	5.19	3.80	23.16	46.00	22.84
330.158	14.27	5.43	5.00	24.70	46.00	21.30
355.558	14.68	5.69	-1.00	19.37	46.00	26.63
406.262	16.08	6.10	5.40	27.58	46.00	18.42
482.542	17.36	6.97	0.80	25.13	46.00	20.87
533.335	19.23	7.47	1.40	28.10	46.00	17.90

- Remark :
1. All readings are Quasi-Peak values.
 2. The worst emission was detected at 126.987MHz with corrected signal level of 40.31dBuV/m (limit is 43.5dBuV/m) when the antenna was at vertical polarization and was at 1m high and the turn table was at 190 ° .
 3. 0 ° was the table front facing the antenna. Degree is calculated from 0 ° clockwise facing the antenna.

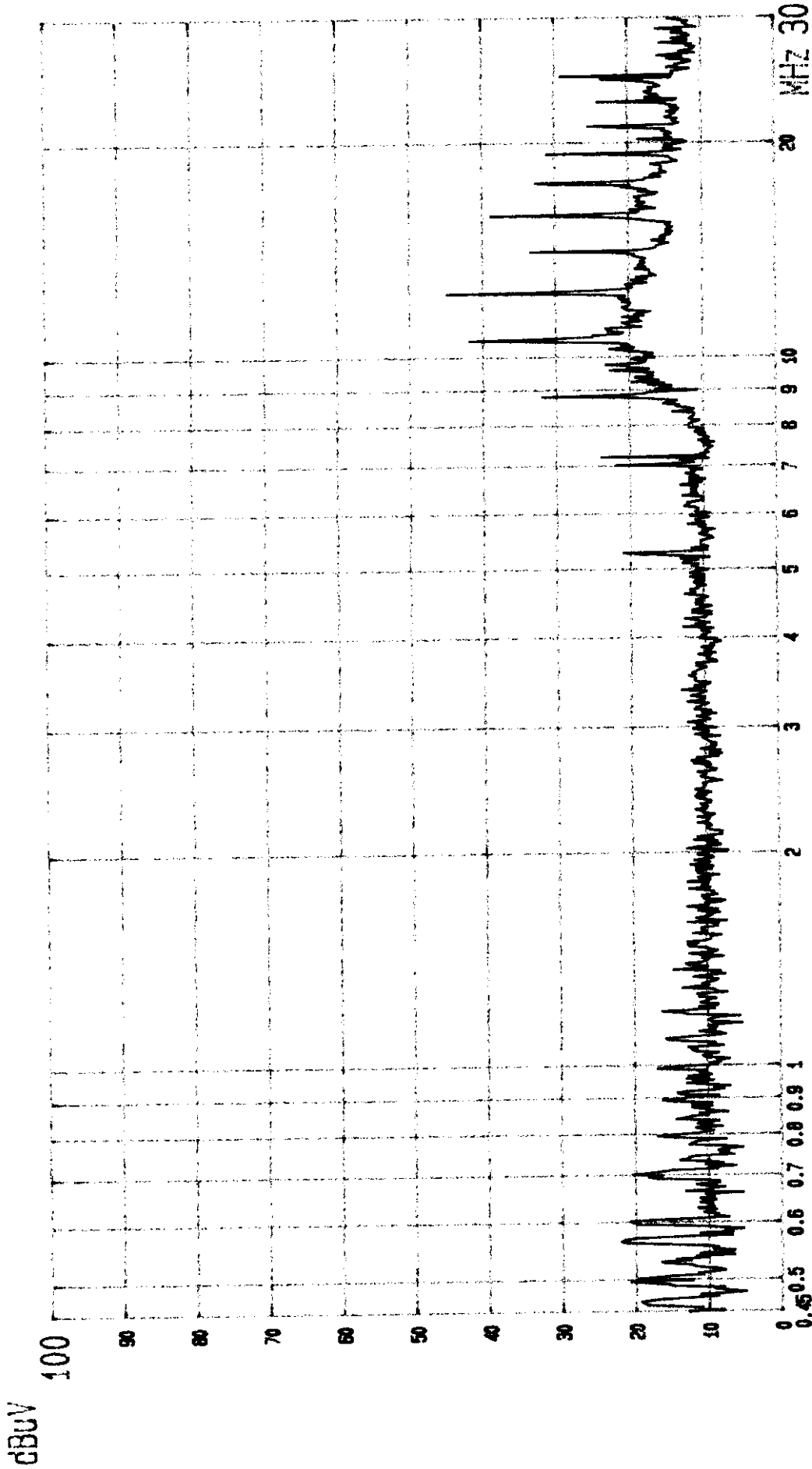
4. MODIFICATIONS TO EUT

1. Changed 0ohm resistors R25, R26 into 33ohm resistors.
2. Changed capacitor C125, C150 into 15pF capacitors.
3. Changed 22pF capacitors C76 ~ C81 into 47pF capacitors.
4. Added a 0.1uf bypass capacitor on Pin 4 of J6.
5. Added a 33ohm resistor and a 15pF bypass capacitor on Pin 5 of Y1.

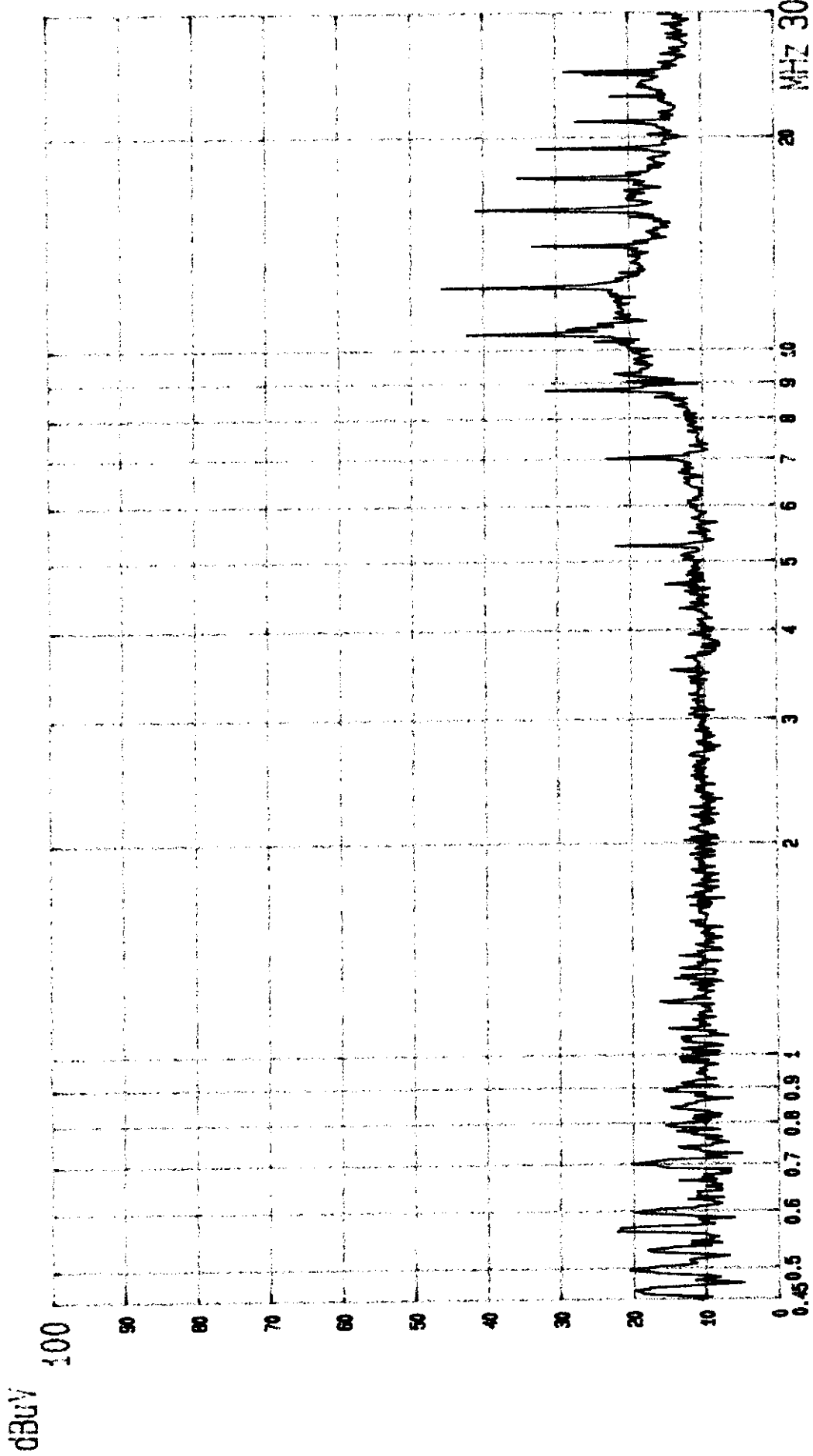
5. DEVIATION TO TEST SPECIFICATIONS

【 NONE 】

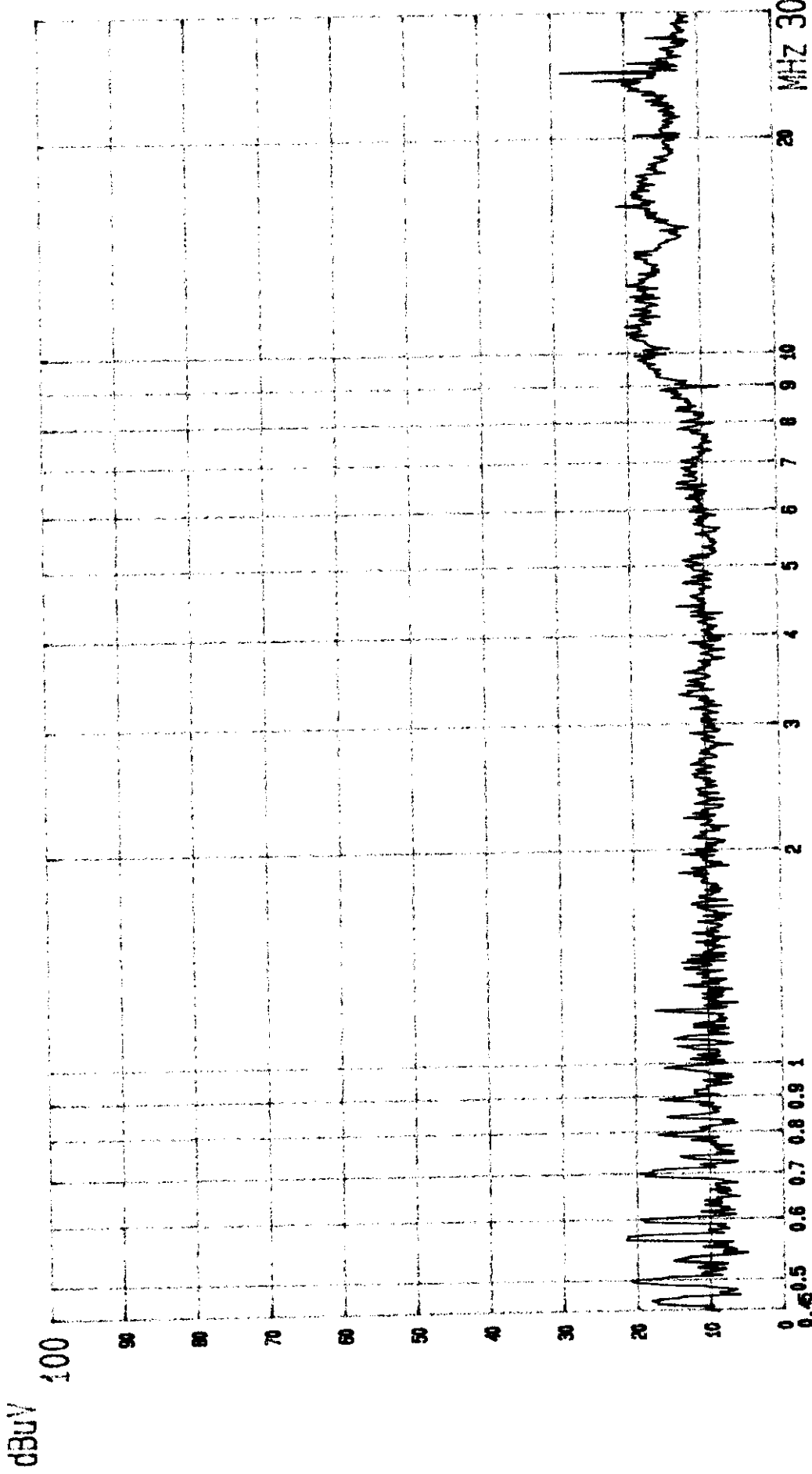
APPENDIX I



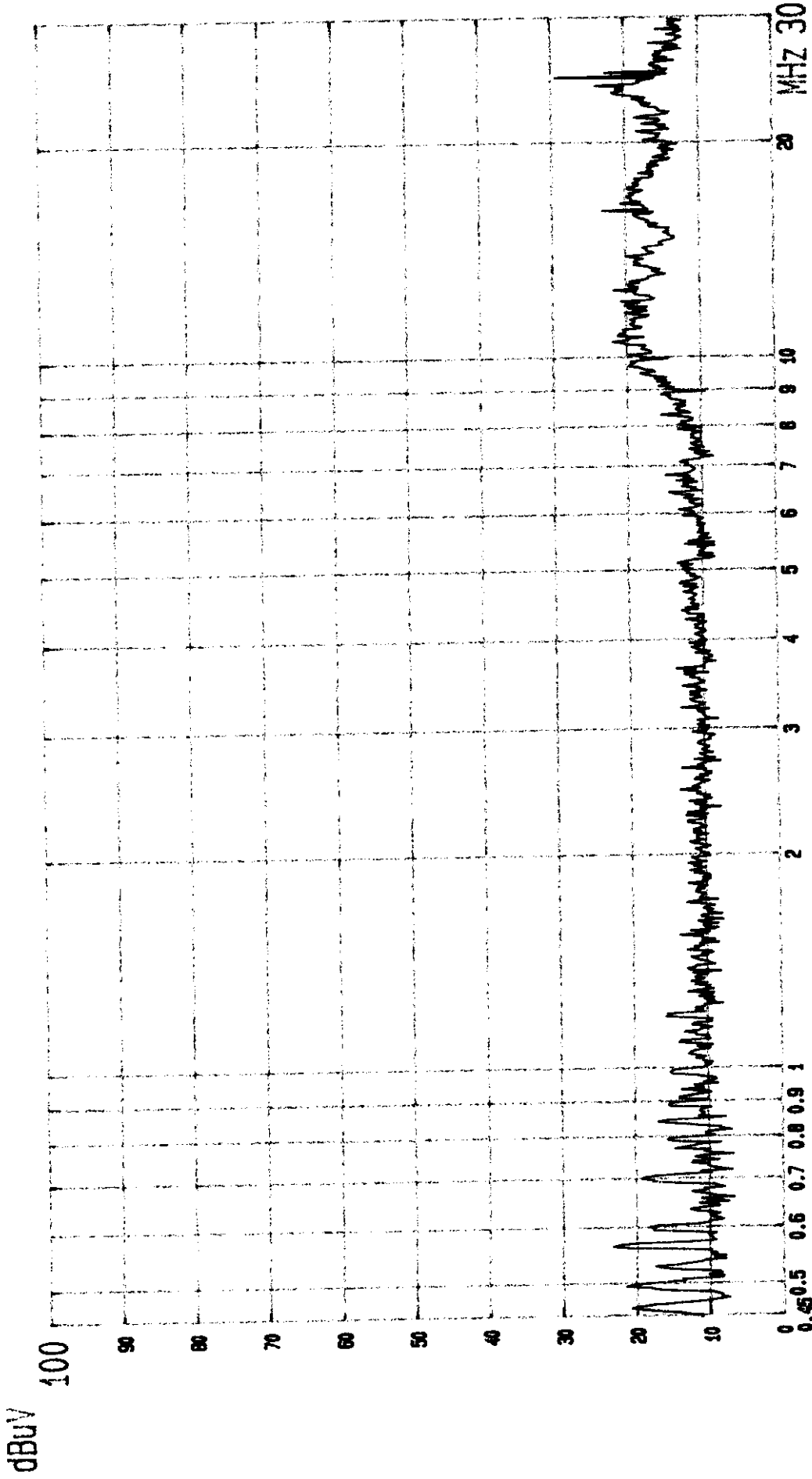
Date 27 JUL '98 Time 14:33:36
 Full Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740
 LINE: VA. MEMO: 31.5KHZ (640X480; 60Hz); W/TV (PEAK VALUE) ITEM: PAGE: 001



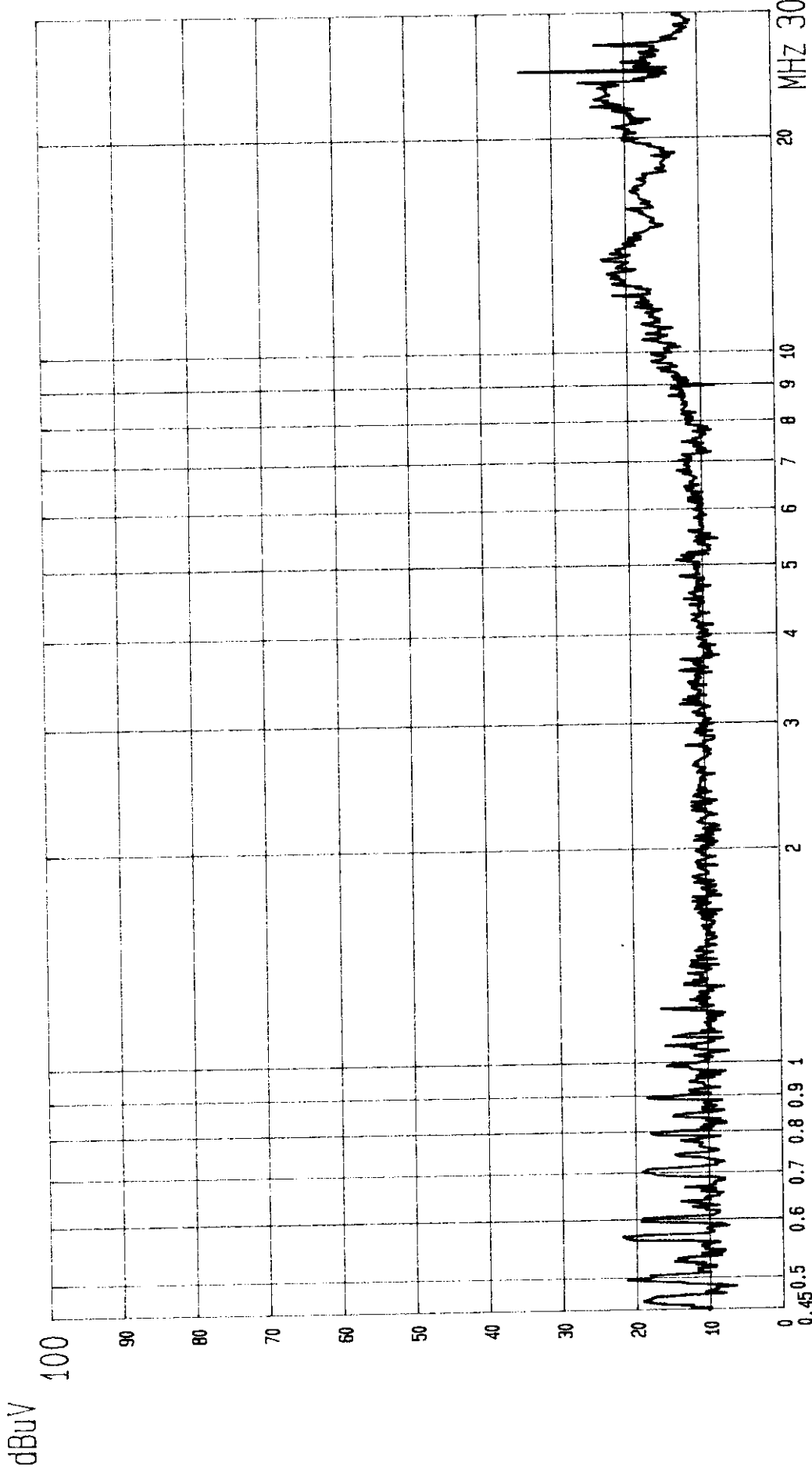
Date 27 . JUL '98 Time 14: 38: 35
 FULL Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740
 LINE: VB. MEMO: 31.5KHZ (640X480; 60HZ); W/TV (PEAK VALUE) TTEMC. PAGE: 002.



Date 27 JUL '98 Time 15:33:03
 FULL Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: V6A 740 PAGE: 005.
 LINE: VA. MEMO: 48KHZ (1024X768; 60HZ) (PEAK VALUE) ITEM: C

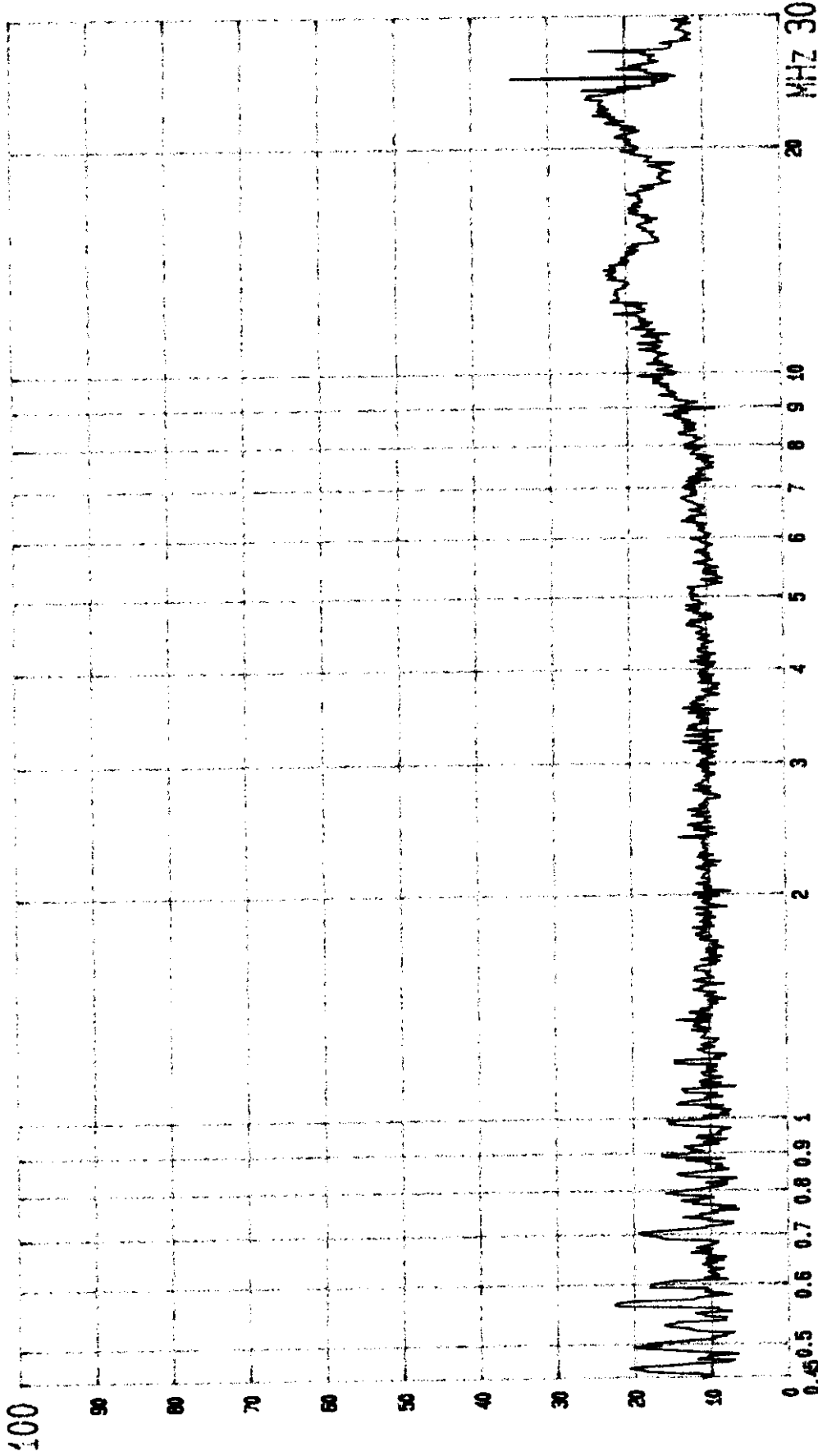


Date 27. JUL '98 Time 15:38:09
 FULL Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740 PAGE: 006.
 LINE: VB. MEMO: 48KHZ (1024X768; 60HZ) (PEAK VALUE) TTEMC.



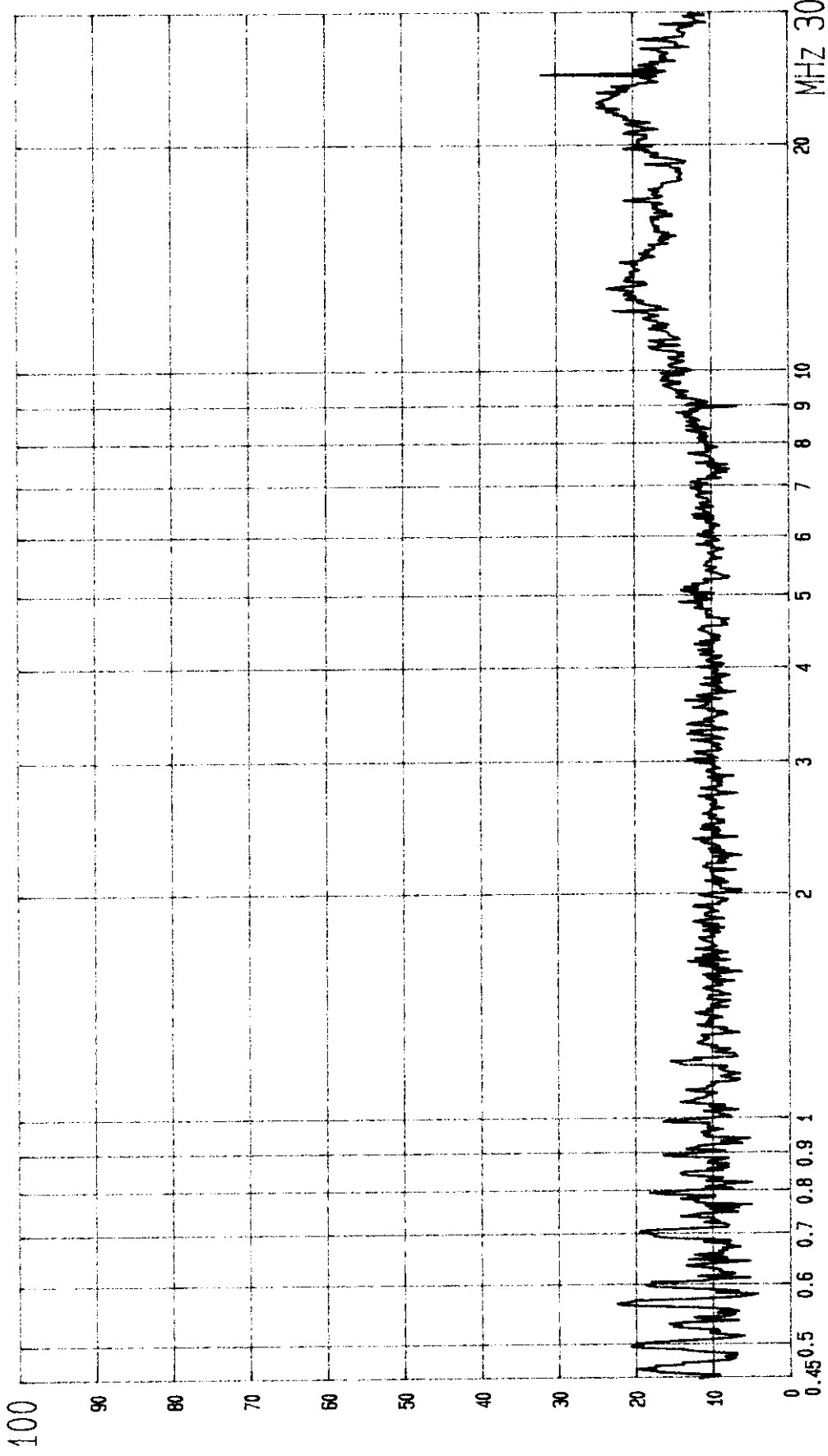
Date 27. JUL '98 Time 15:58:24
 FULL Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740
 LINE: VA. MEMO: 64KHZ (1280X1024; 60HZ) (PEAK VALUE) TTEMC. PAGE: 008.

dBuV



----- Date 27. JUL '98 Time 15:53:08
FULL Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740 PAGE: 007.
LINE: V8. MEMO: 64KHZ (1280X1024; 60HZ) (PEAK VALUE) ITEM: C.

dBuV



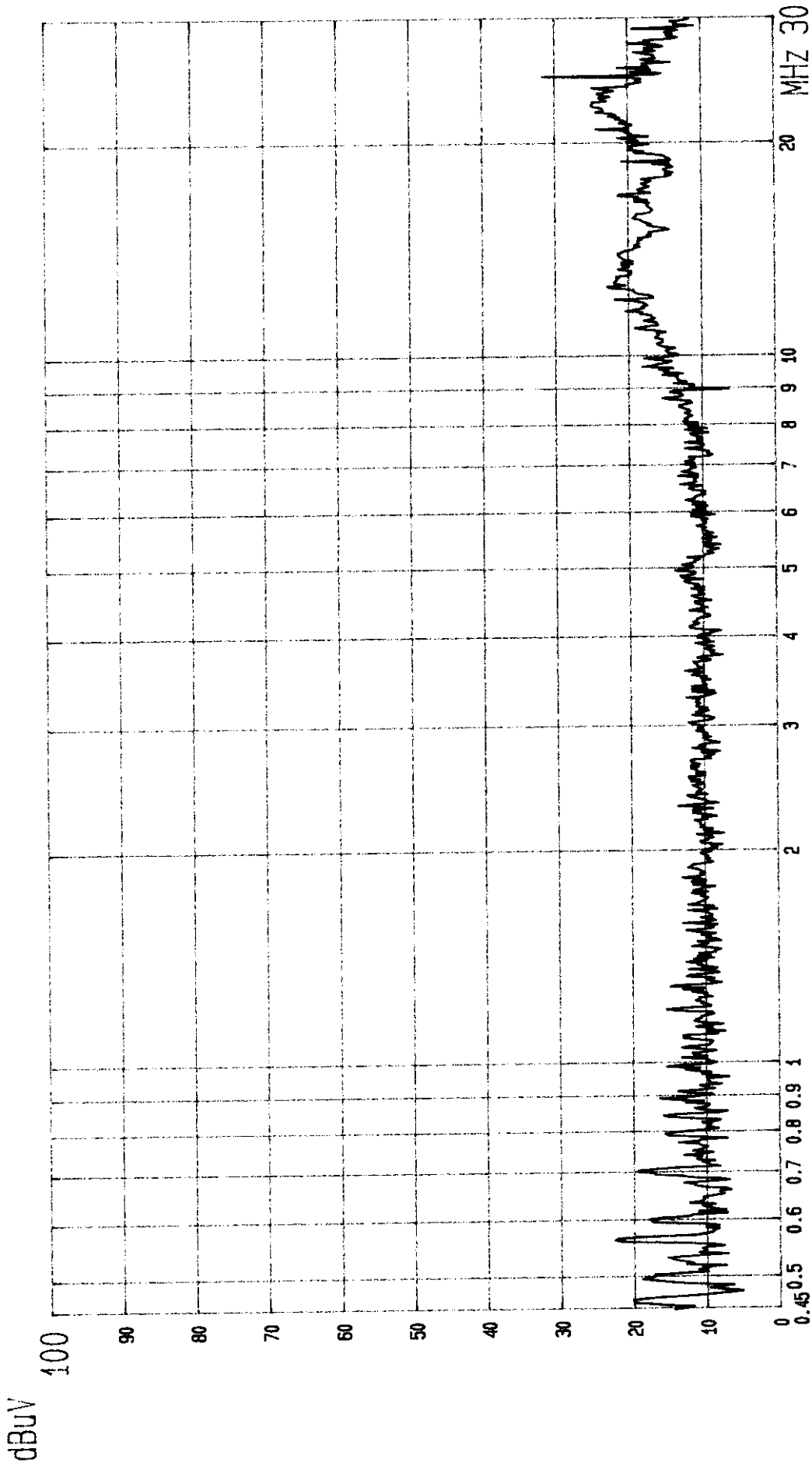
--- Date 27. JUL '98 Time 16:06:26

Full Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740

LINE: VA. MEMO: 80KHZ (1280X1024; 75HZ)

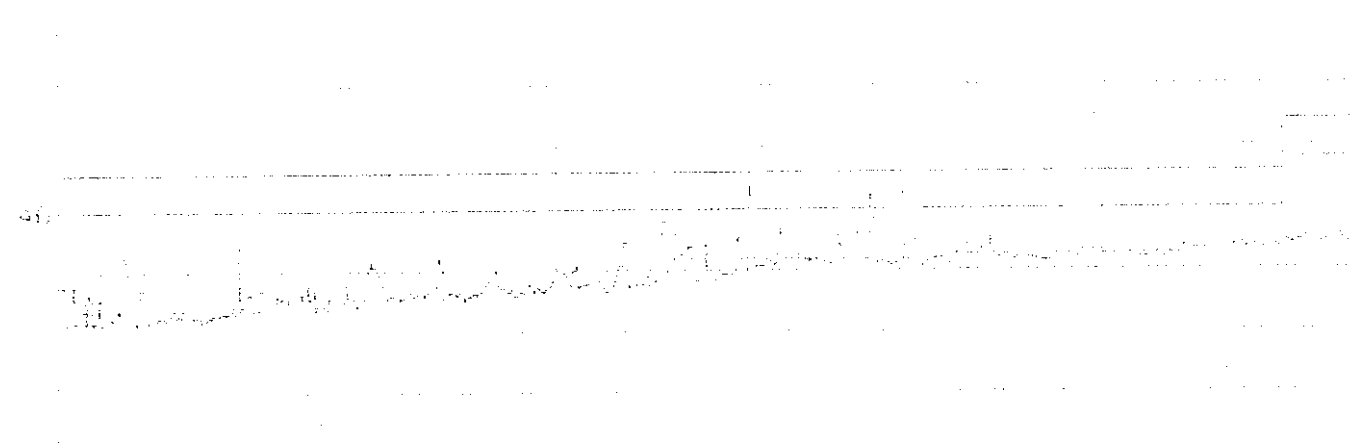
PAGE: 009.

(PEAK VALUE) TTEMC.

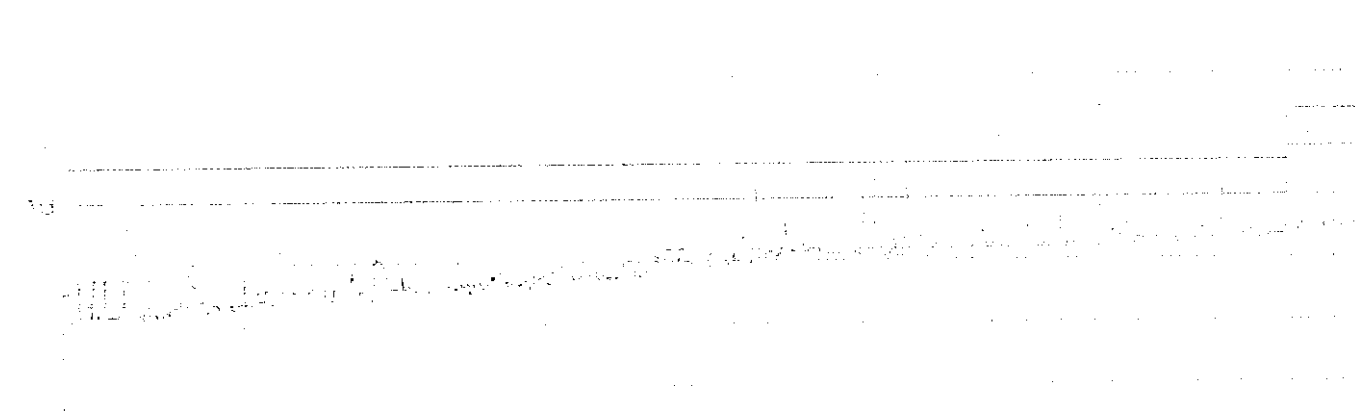


Date 27. JUL '98 Time 16:09:01
 Full Yes EUT: FYI-INTEL 740 AGP VGA Card M/N: VGA 740
 LINE: VB. MEMO: 80KHz (1280X1024; 75Hz) (PEAK VALUE) ITEM: C.

APPENDIX II

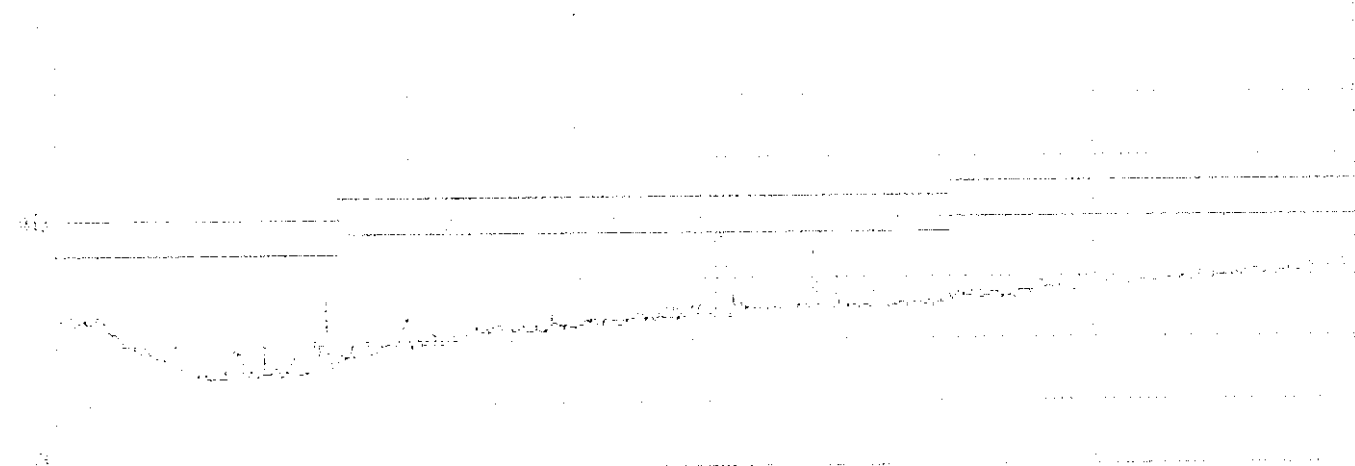


200 MHz 400 800 1200 1600 2000
Unit: FCC CLASS-B 3u Probe: HP11917B(1001200) VERT: 100
EUT: USA CARD M/1: INTEL 745 Power: 1200-0-000u
Margin: 8dB Standard: 0 Trace: 000 0 0 0 0
Name: 40X(18248768:160Hz)



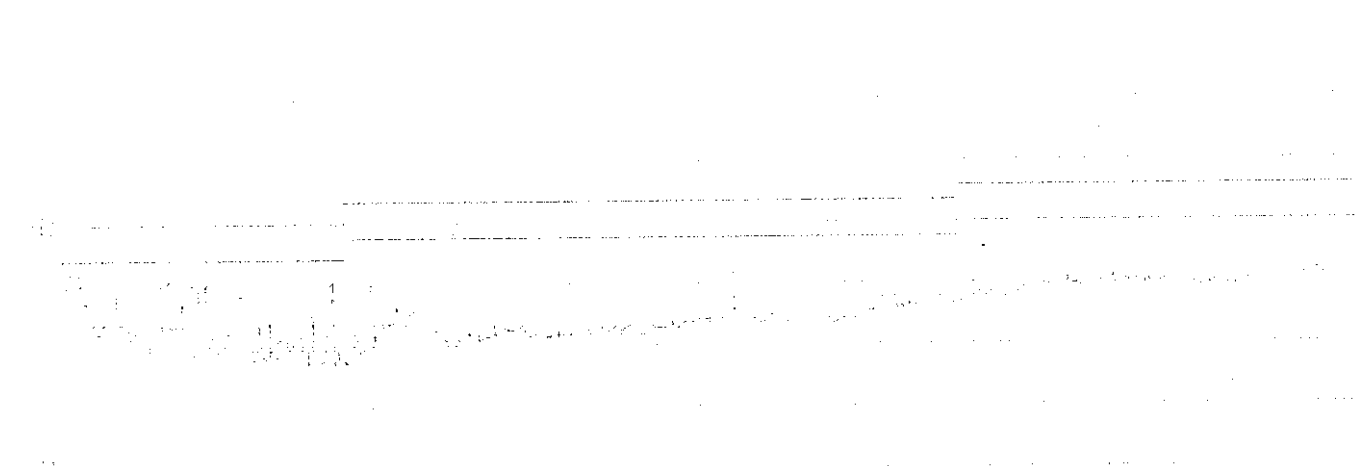
200 MHz 400 800 1200 1600 2000
Unit: FCC CLASS-B 3u Probe: HP11917B(1001200) VERT: 100
EUT: USA CARD M/1: INTEL 745 Power: 1200-0-000u
Margin: 8dB Standard: 0 Trace: 000 0 0 0 0
Name: 40X(18248768:160Hz)

Case#01 01 SP Filed: 01/11/2004 Date: 02-27-2004 Time: 12:10:10
ADD#01 01

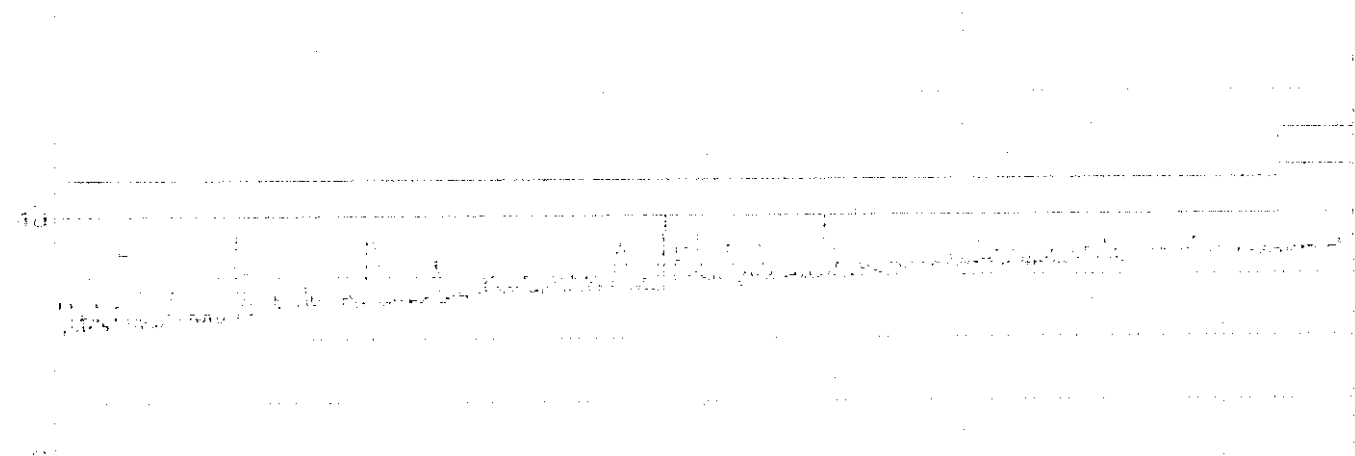


00 01 100 200
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Case: 01/11/2004-01
Date: 02-27-2004
Time: 12:10:10

Case#01 01 SP Filed: 01/11/2004 Date: 02-27-2004 Time: 12:10:10
ADD#01 01



00 01 100 200
File: 01/11/2004-01
Case: 01/11/2004-01
Date: 02-27-2004
Time: 12:10:10



200 MHz 400 500 700 800 1000
Limit : 100 CLASS-B 3m Probe: UMIL 9010701001220 HORIZONTAL
EUT : 004 CARD M-N: INTEL 740 Power : 120Watt 500W
Margin : 6dB Standard: 0 Trace: 200, 0, 0, 0, 0
Memo : 80X(1200x1024:75Hz)



200 MHz 400 500 700 800 1000
Limit : 100 CLASS-B 3m Probe: UMIL 9010701001220 HORIZONTAL
EUT : 004 CARD M-N: INTEL 740 Power : 120Watt 500W
Margin : 6dB Standard: 0 Trace: 200, 0, 0, 0, 0
Memo : 80X(1200x1024:75Hz)

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Date: 10/10/2010
Time: 10:10:10 AM
User: Administrator

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Time: 10:10:10 AM
User: Administrator

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User: Administrator

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User: Administrator

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User: Administrator

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Time: 10:10:10 AM
User: Administrator

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Date: 10/10/2010
Time: 10:10:10 AM
User: Administrator

Page 8 of 1
Date: 10/10/2010
Time: 10:10:10 AM
User: Administrator

1. The first part of the document discusses the importance of maintaining accurate records of all transactions. It emphasizes that proper record-keeping is essential for the integrity of the financial system and for the ability to detect and prevent fraud.

2. The second part of the document outlines the various methods used to collect and analyze data. It describes the use of statistical techniques to identify trends and anomalies in the data, and the importance of using reliable sources of information.

3. The third part of the document discusses the role of the auditor in the financial reporting process. It explains how the auditor's independent review of the financial statements provides assurance to investors and other stakeholders that the information is reliable and free from material misstatement.

4. The fourth part of the document discusses the importance of transparency and disclosure in financial reporting. It explains that providing clear and concise information about the company's financial performance and risks is essential for investors to make informed decisions.

5. The fifth part of the document discusses the role of the board of directors in overseeing the financial reporting process. It explains that the board is responsible for ensuring that the financial statements are prepared in accordance with applicable accounting standards and that the company's financial reporting is transparent and accurate.

6. The sixth part of the document discusses the importance of internal controls in the financial reporting process. It explains that internal controls are designed to prevent and detect errors and fraud, and that they are essential for the reliability of the financial statements.