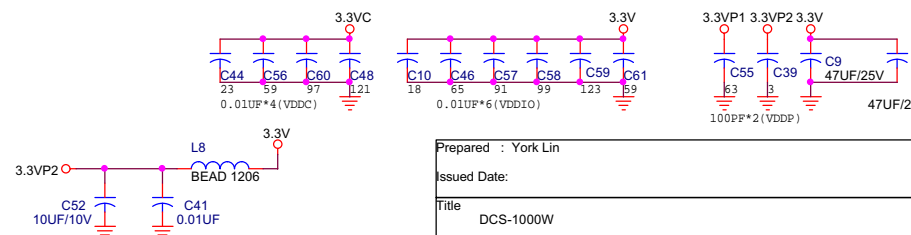
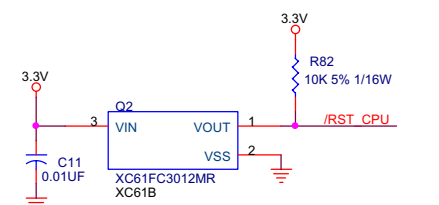
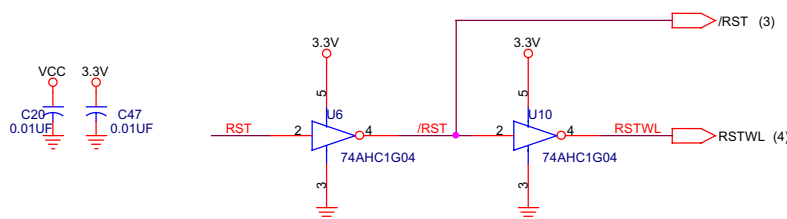
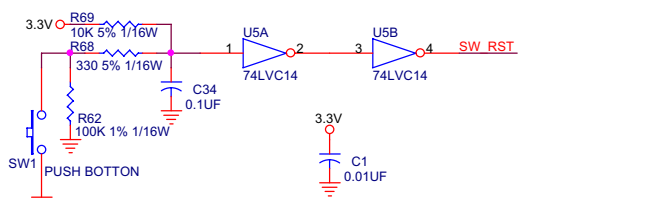
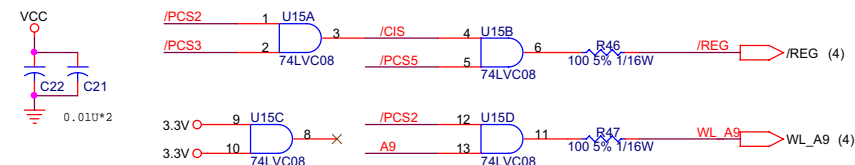
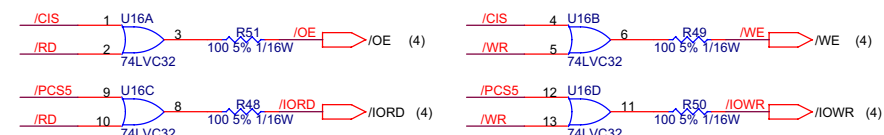



```


25MHZ*4=100MHZ
Pin 116(TXD1_3/PD1VD1) = PU
Pin 115(TXD1_2/PD1VD0) = PU
Pin 114(TXD1_1/PFREQ1) = PD
Pin 112(TXD1_0/PFREQ0) = PU
Pin 28(DTR1/SBWSSEL)=PU : UART1 disable

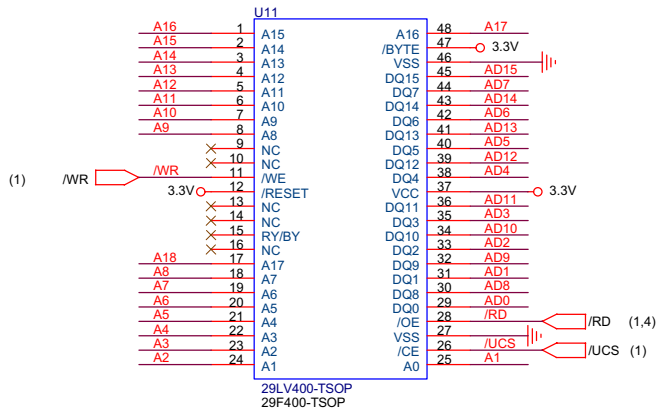
```

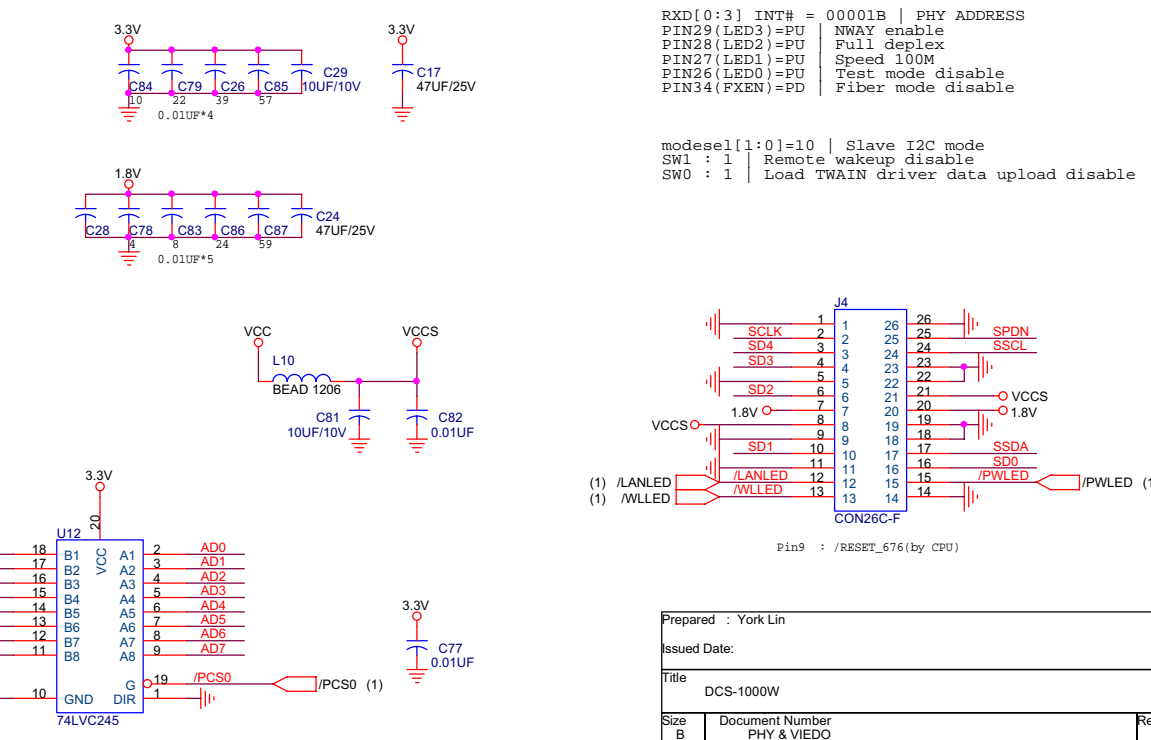
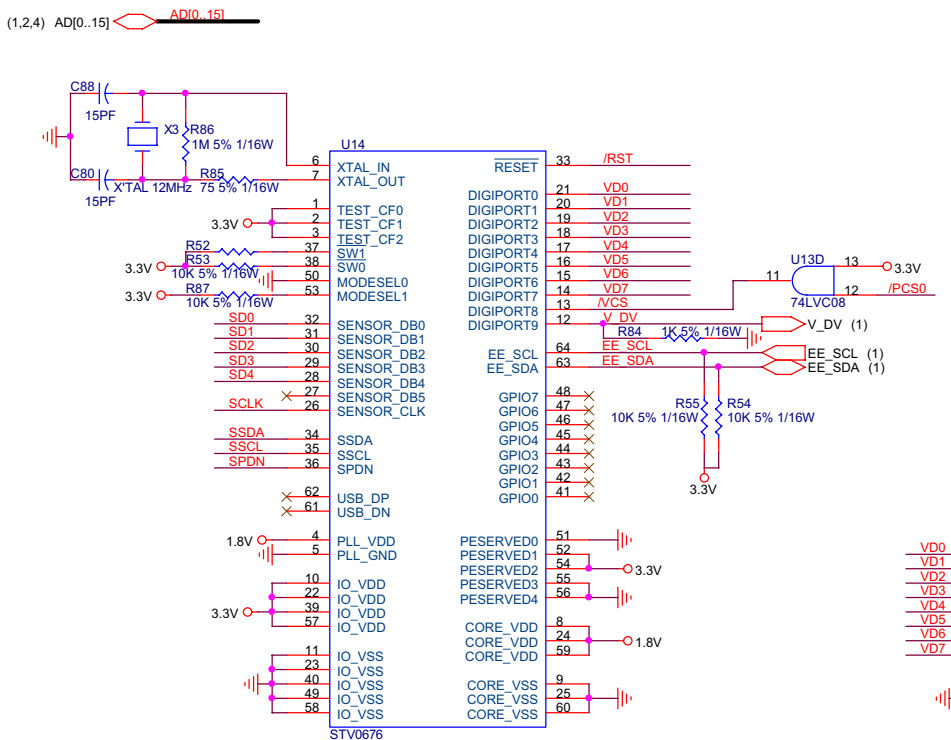
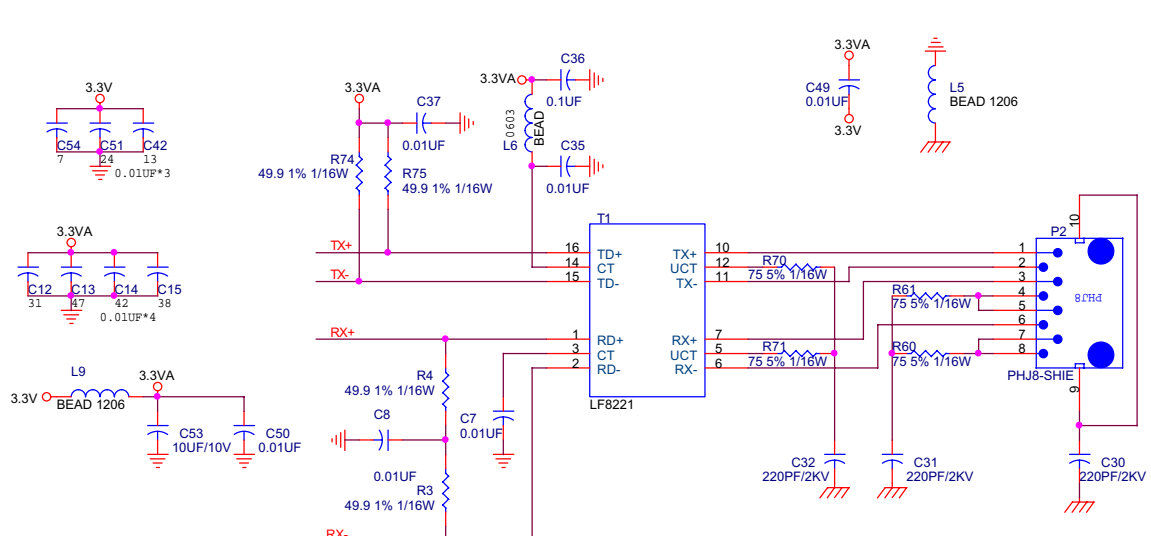
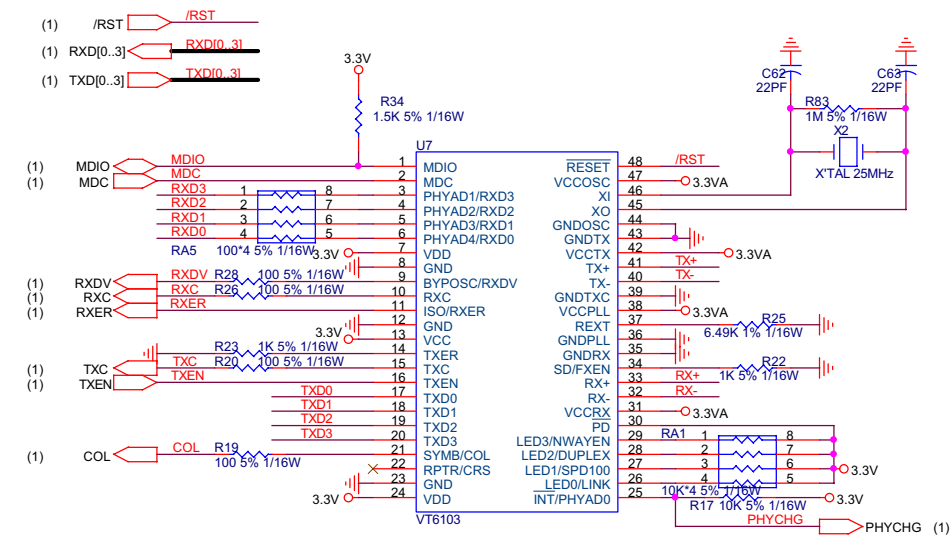


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(1,3,4) AD[0..15]  AD[0..15]

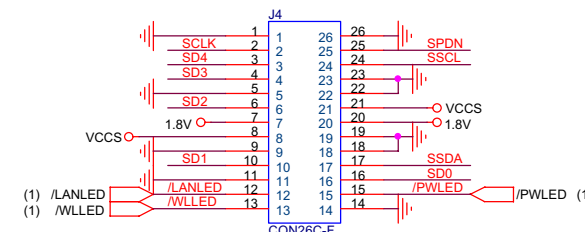
(1,4) A[0..18]  A[0..18]





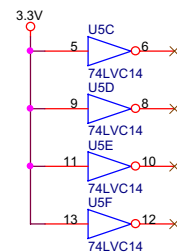
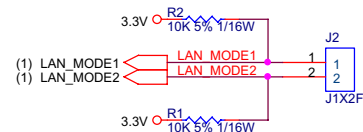
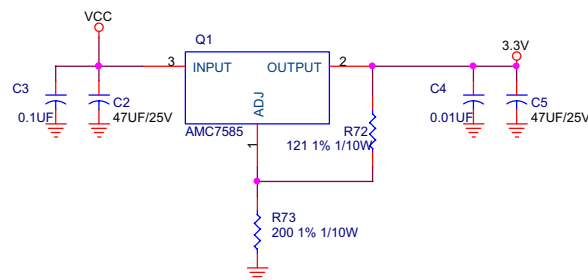
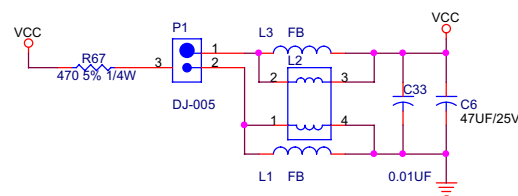
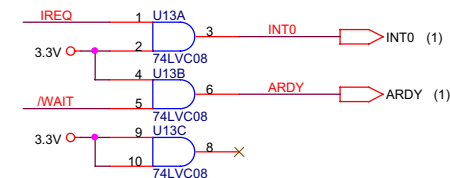
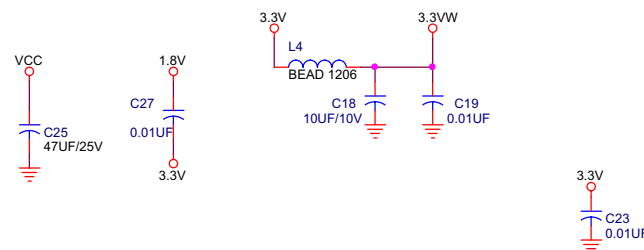
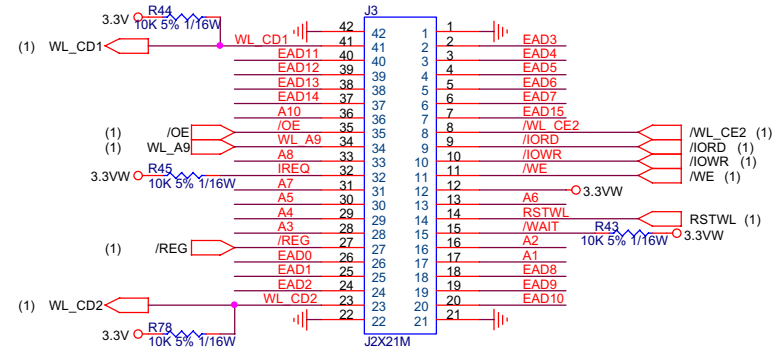
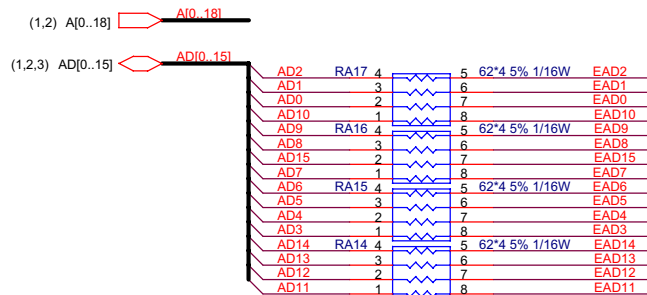
RXD[0:3] INT# = 00001B | PHY ADDRESS  
 PIN29(LED3)=PU | NWAY enable  
 PIN28(LED2)=PU | Full duplex  
 PIN27(LED1)=PU | Speed 100M  
 PIN26(LED0)=PU | Test mode disable  
 PIN34(FXEN)=PD | Fiber mode disable

modese1[1:0]=10 | Slave I2C mode  
 SW1 : 1 | Remote wakeup disable  
 SW0 : 1 | Load TWAIN driver data upload disable



Pin9 : /RESET\_676(by CPU)

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