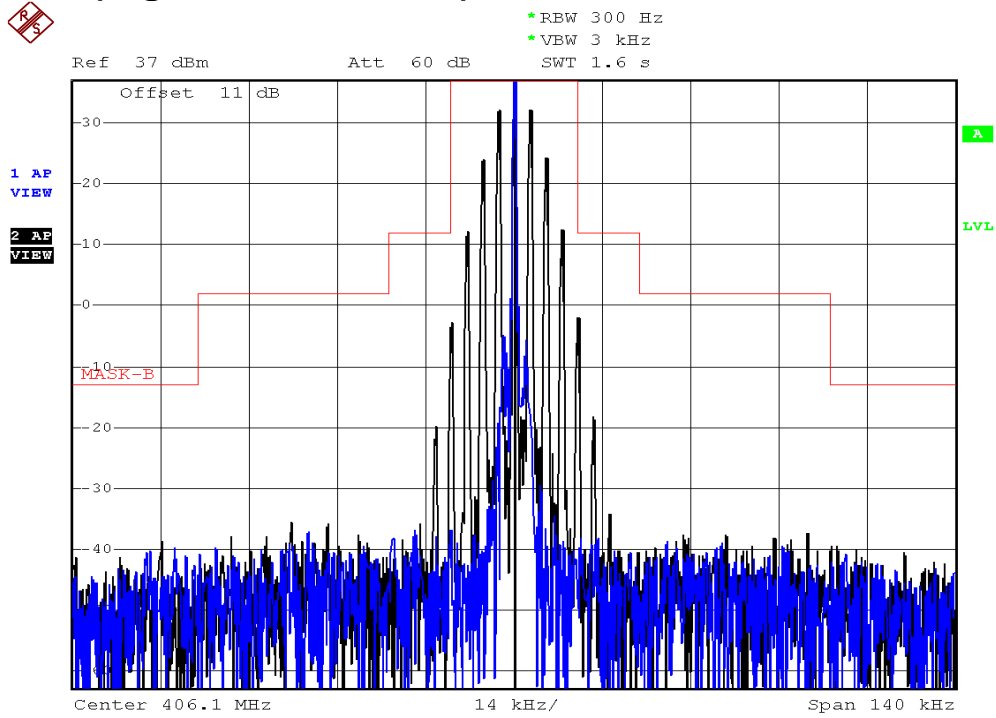


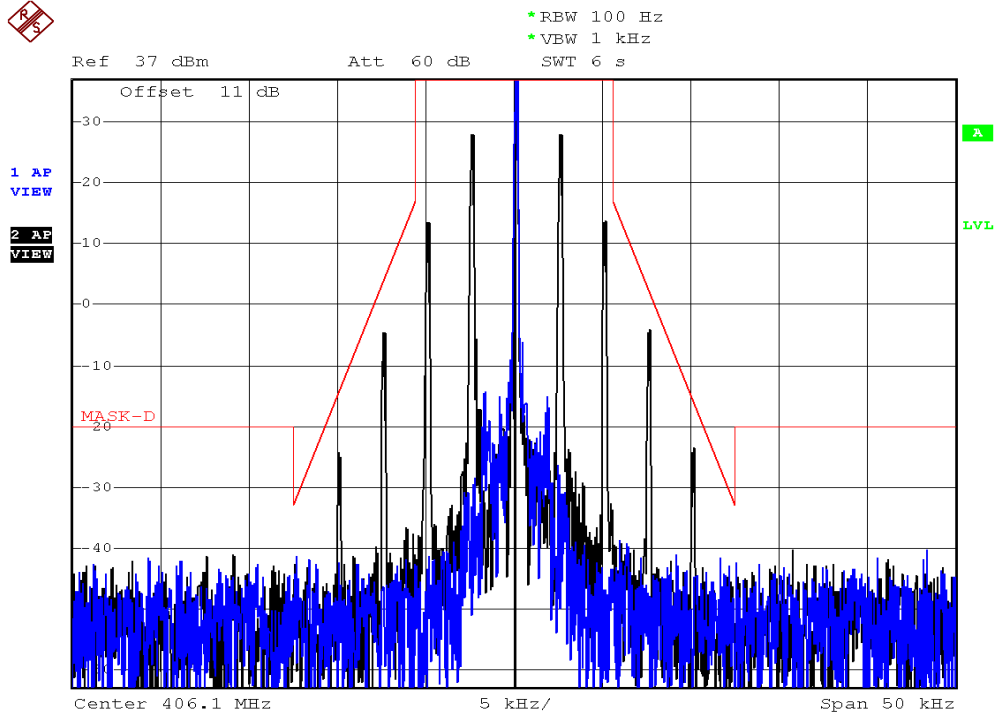
INTERTEK TESTING SERVICES

Emission Mask

Ch1 (High Power, Mask B)



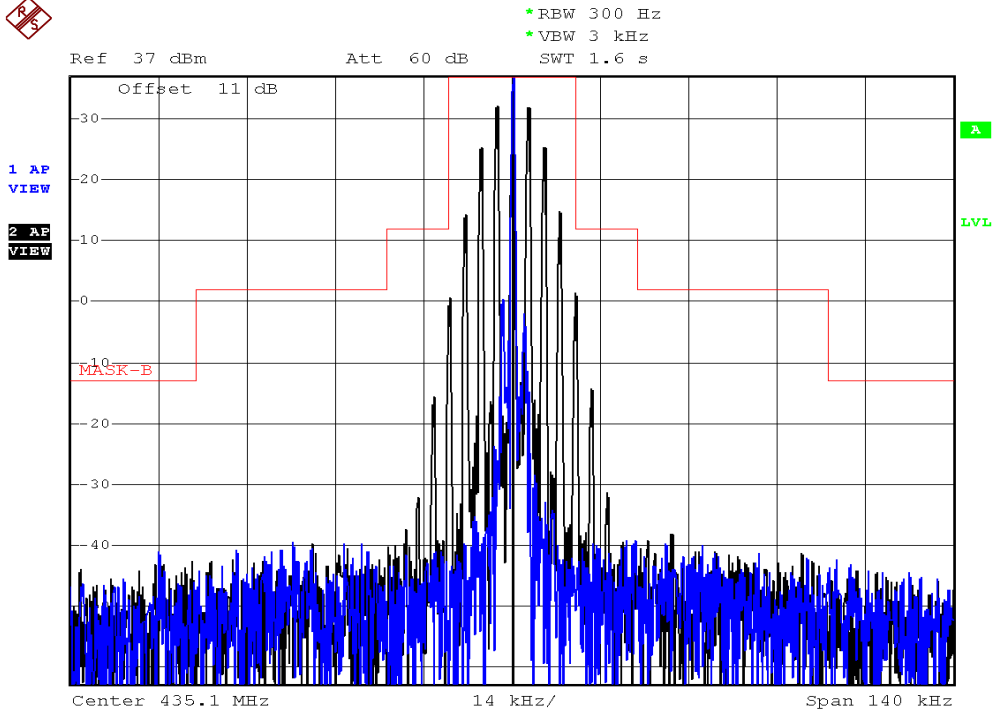
Ch1 (High Power, Mask D)



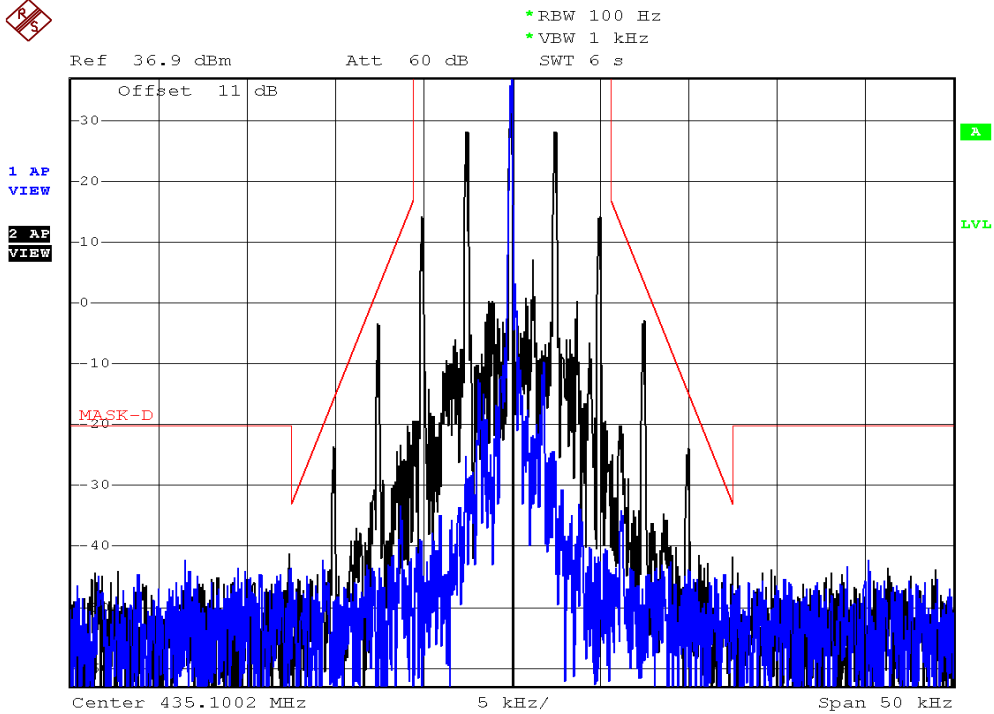
INTERTEK TESTING SERVICES

Emission Mask

Ch16 (High Power, Mask B)



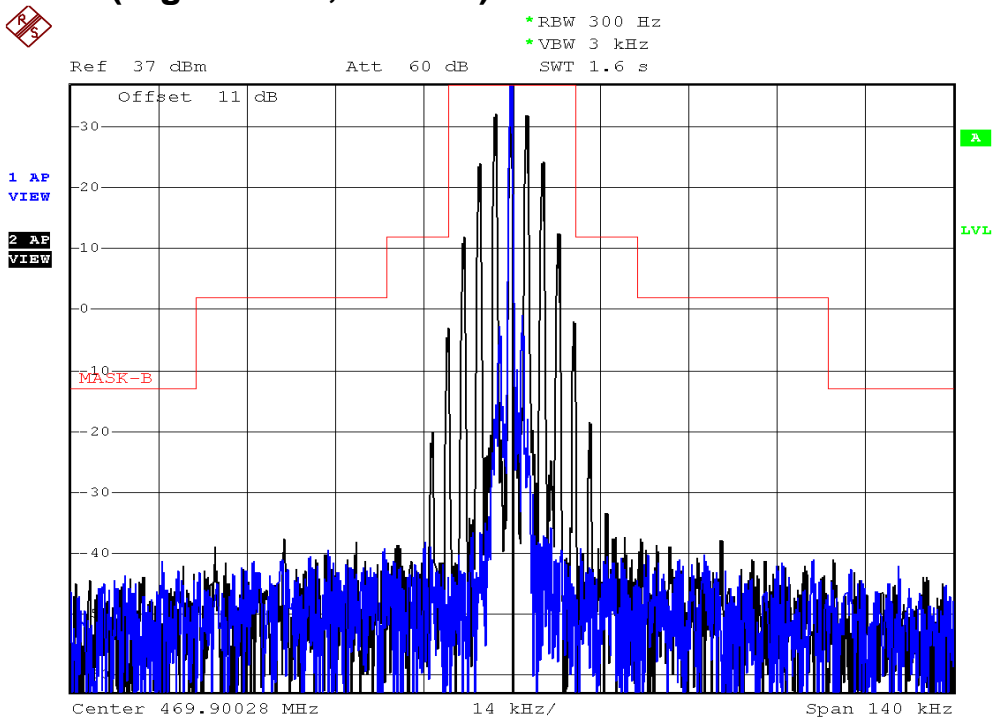
Ch16 (High Power, Mask D)



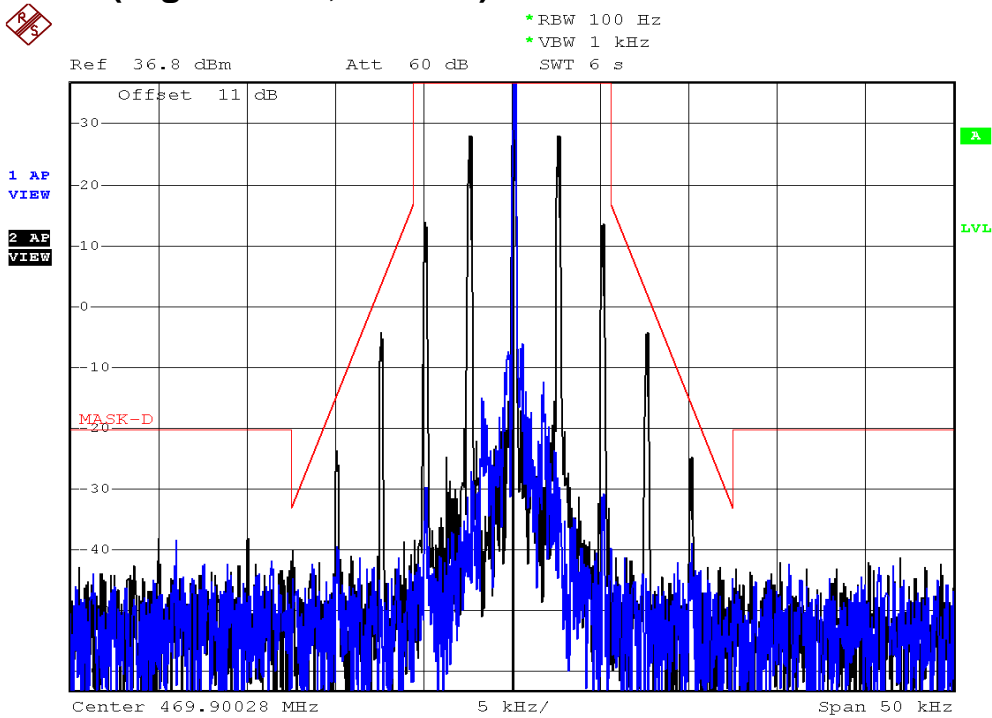
INTERTEK TESTING SERVICES

Emission Mask

Ch32 (High Power, Mask B)

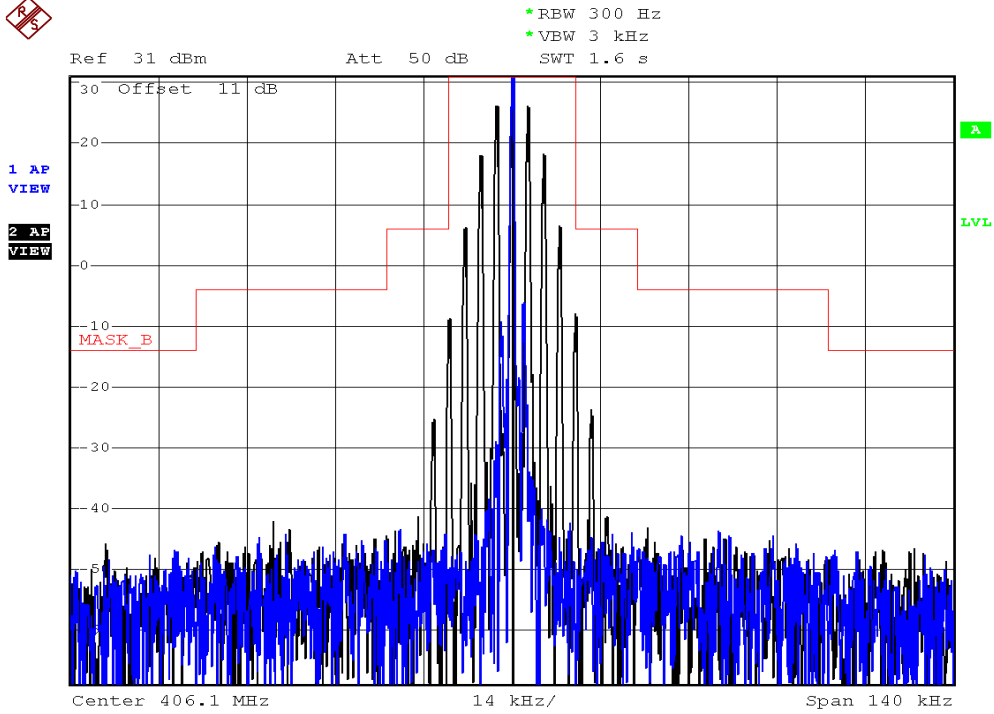


Ch32 (High Power, Mask D)

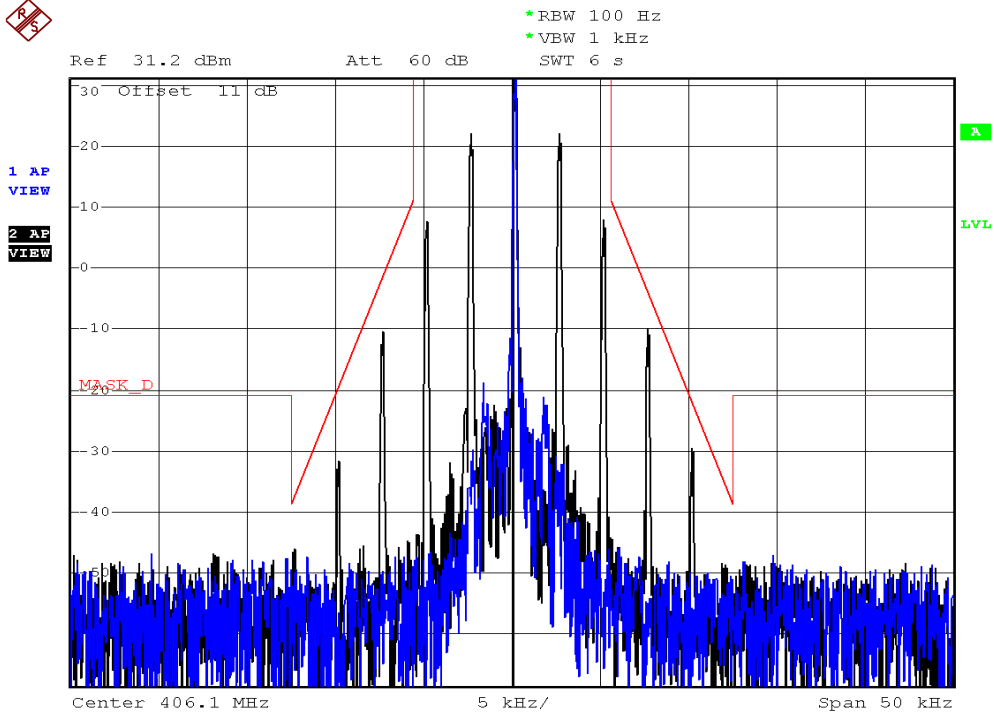


Emission Mask

Ch1 (Low Power, Mask B)



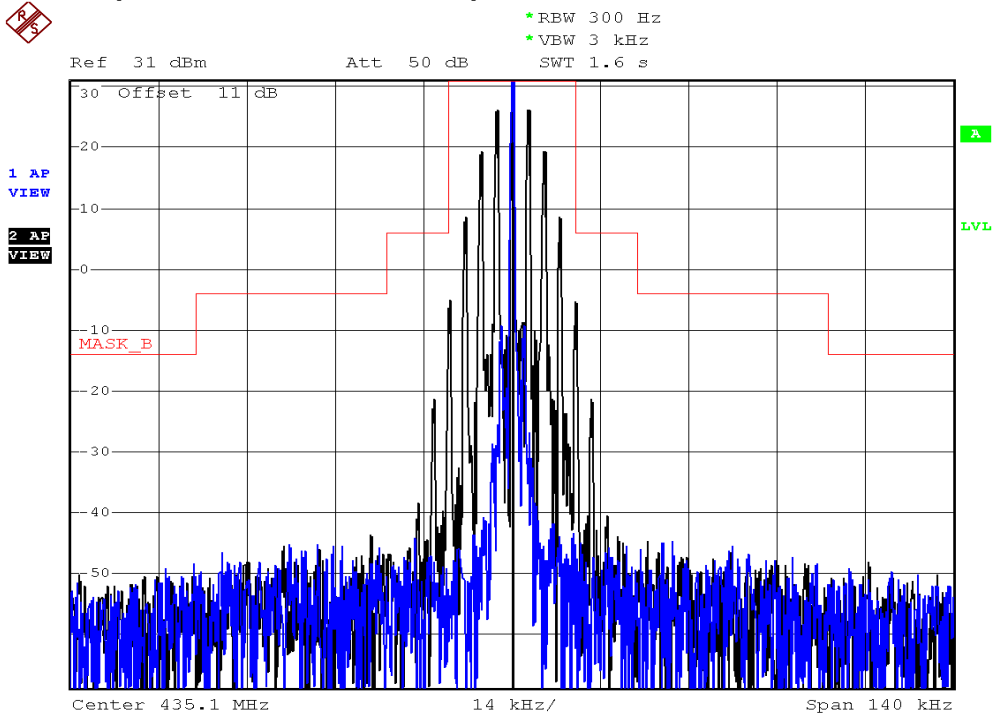
Ch1 (Low Power, Mask D)



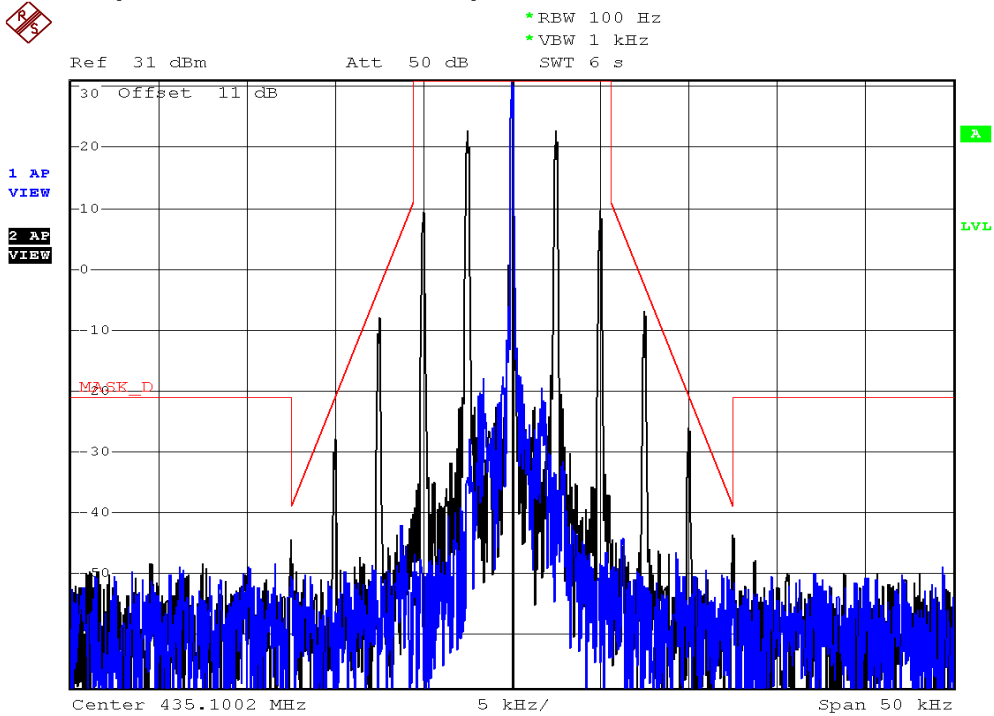
INTERTEK TESTING SERVICES

Emission Mask

Ch16 (Low Power, Mask B)



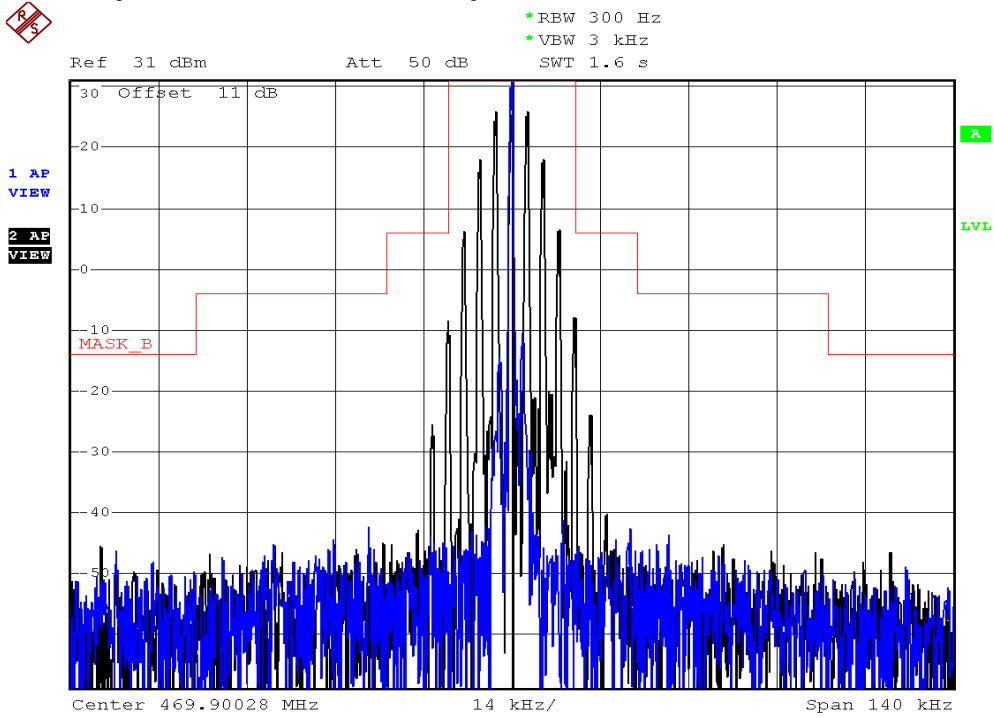
Ch16 (Low Power, Mask D)



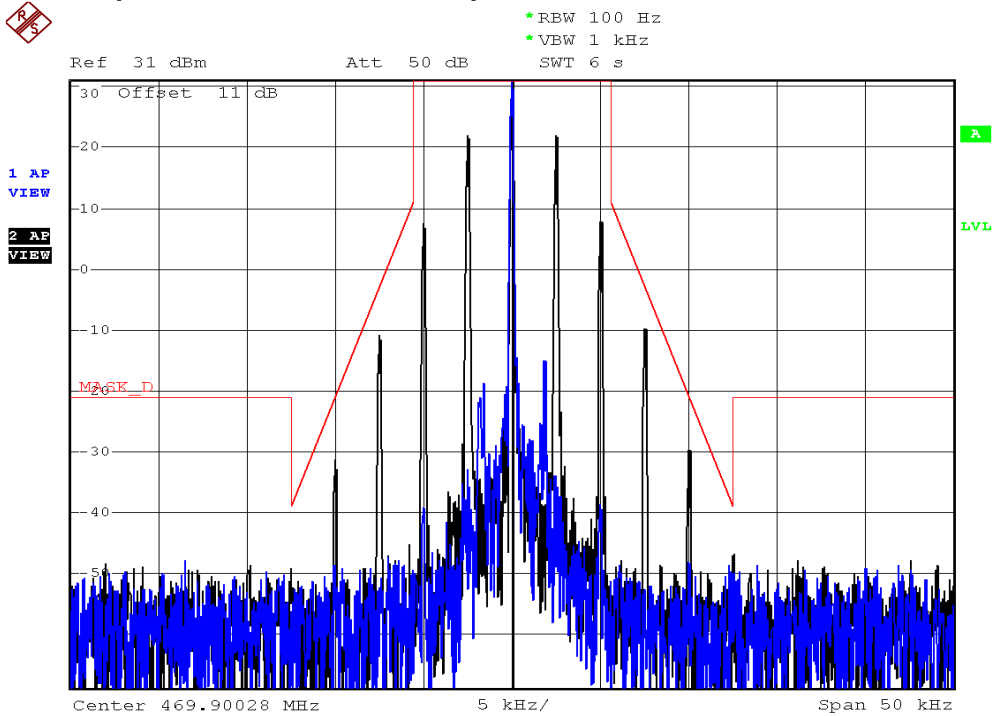
INTERTEK TESTING SERVICES

Emission Mask

Ch32 (Low Power, Mask B)



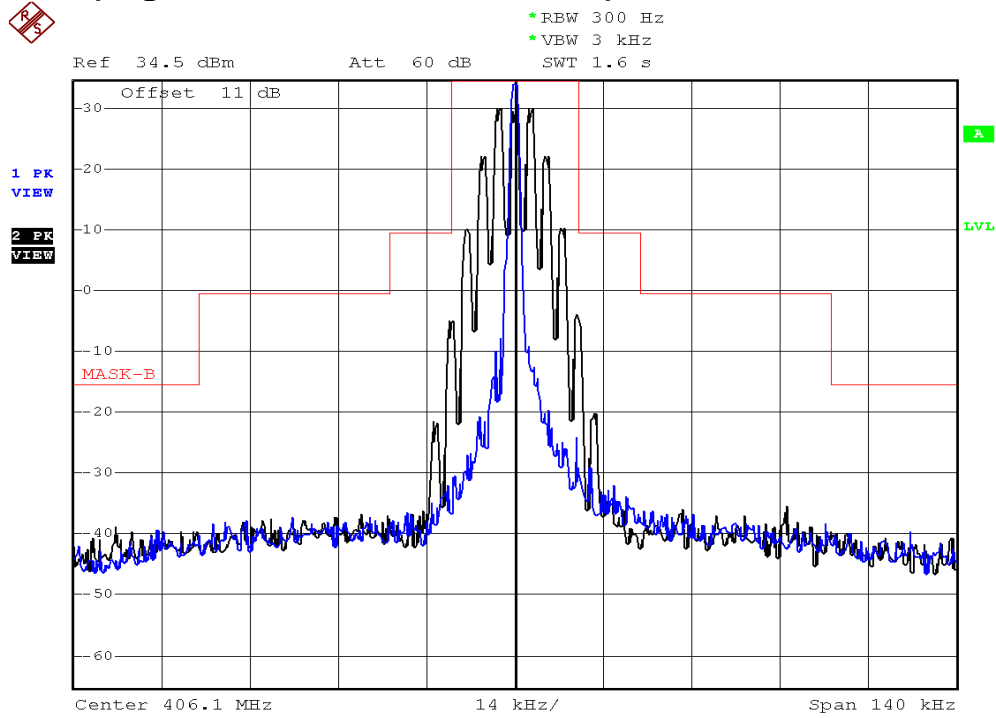
Ch32 (Low Power, Mask D)



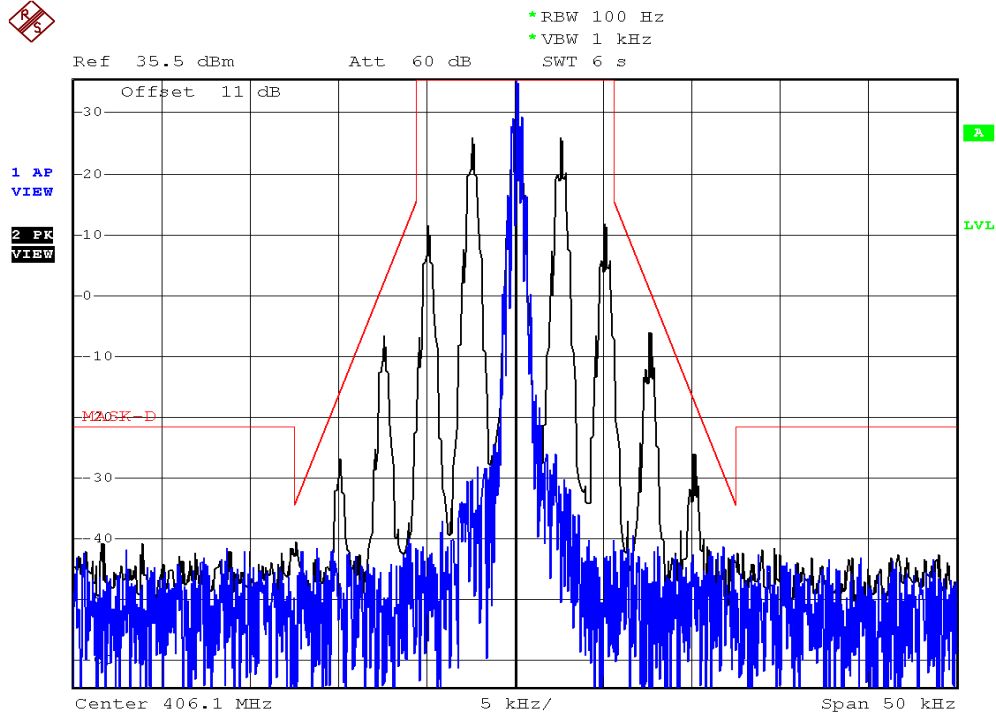
INTERTEK TESTING SERVICES

Emission Mask

Ch1 (High Power, Mask B, CTCSS)



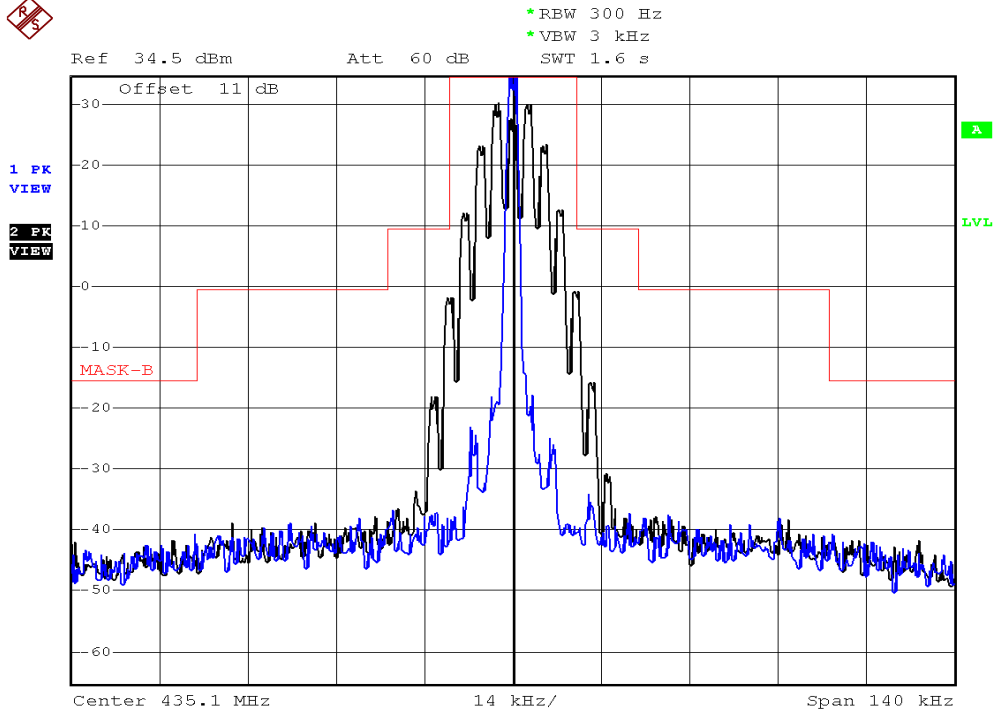
Ch1 (High Power, Mask D, CTCSS)



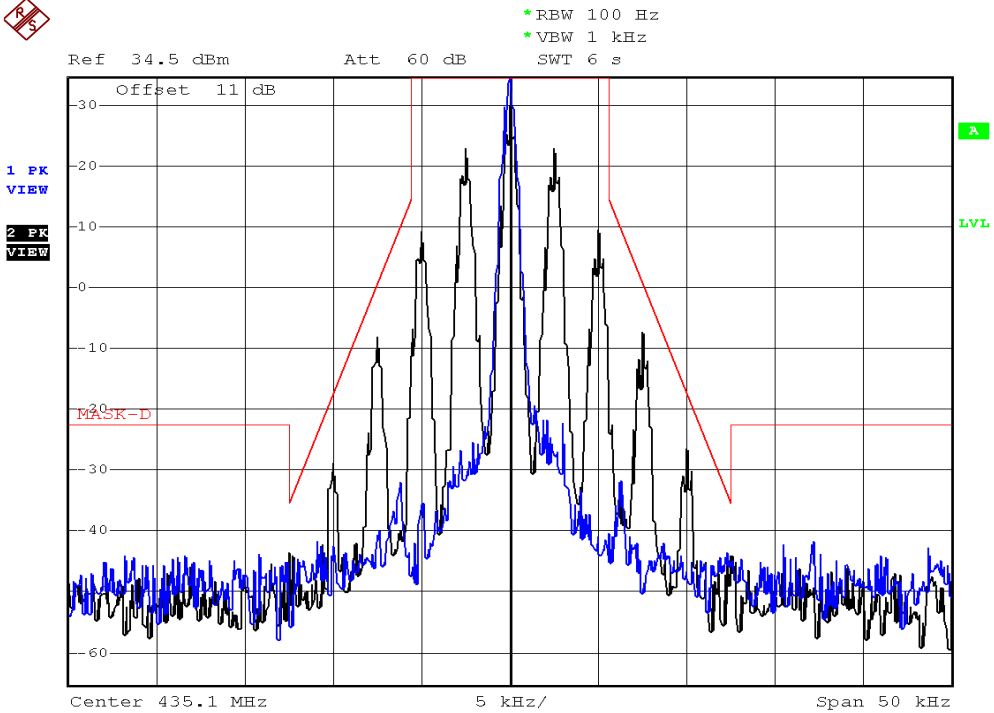
INTERTEK TESTING SERVICES

Emission Mask

Ch16 (High Power, Mask B, CTCSS)



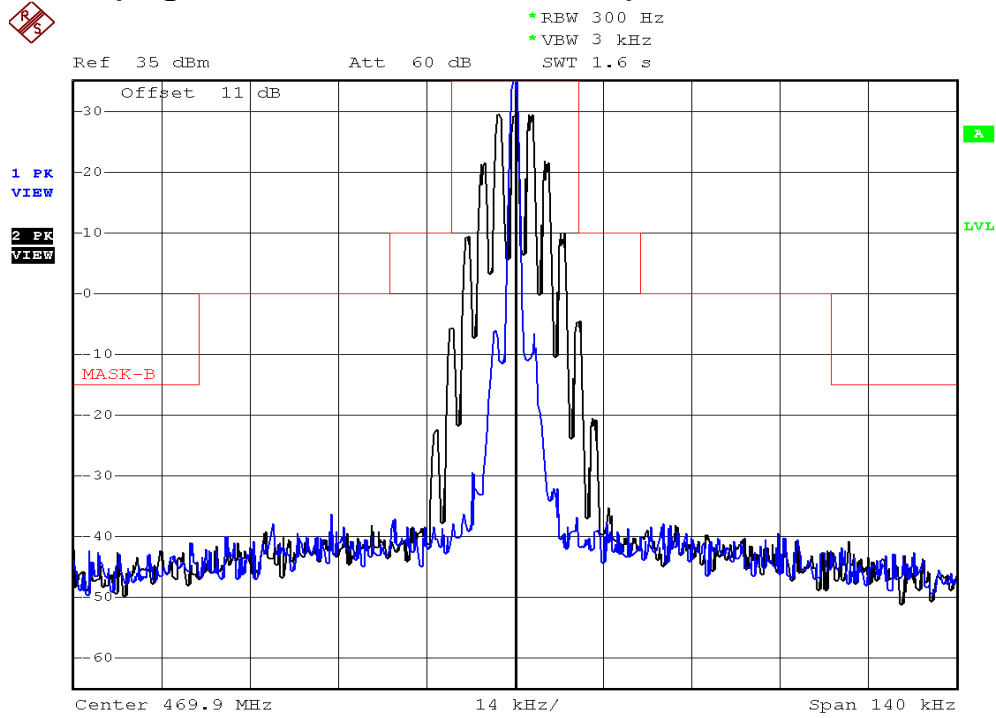
Ch16 (High Power, Mask D, CTCSS)



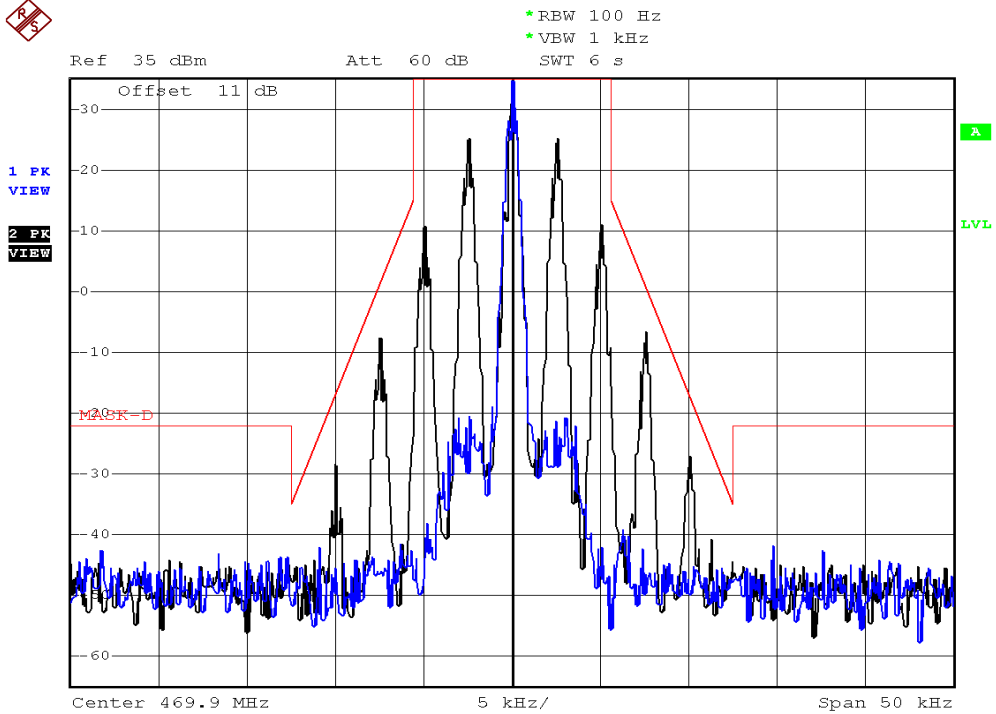
INTERTEK TESTING SERVICES

Emission Mask

Ch32 (High Power, Mask B, CTCSS)



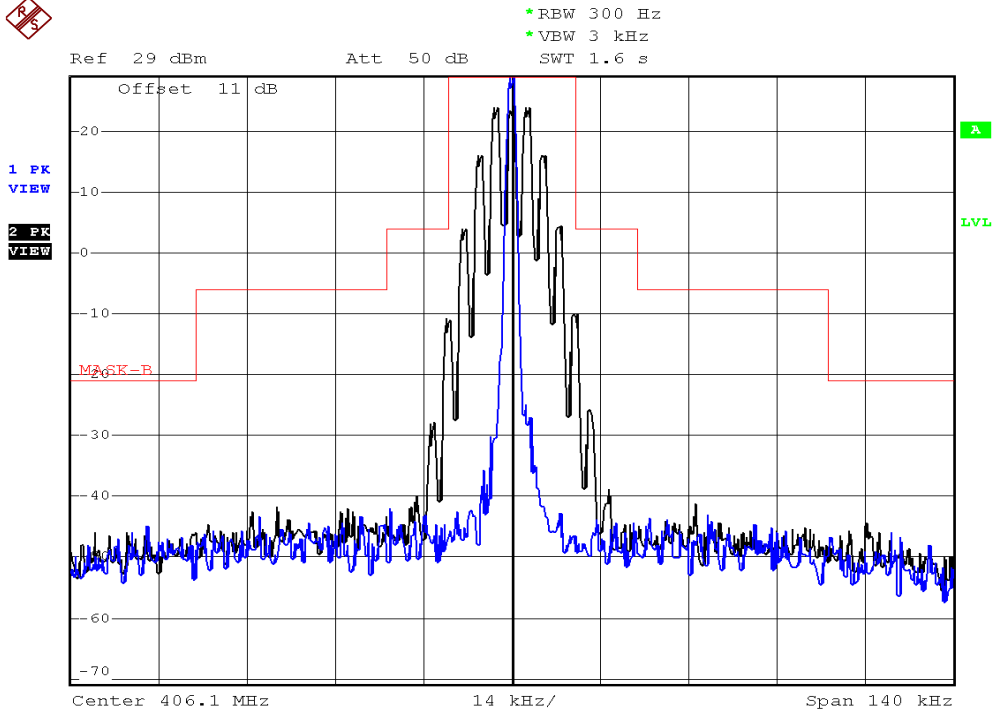
Ch32 (High Power, Mask D, CTCSS)



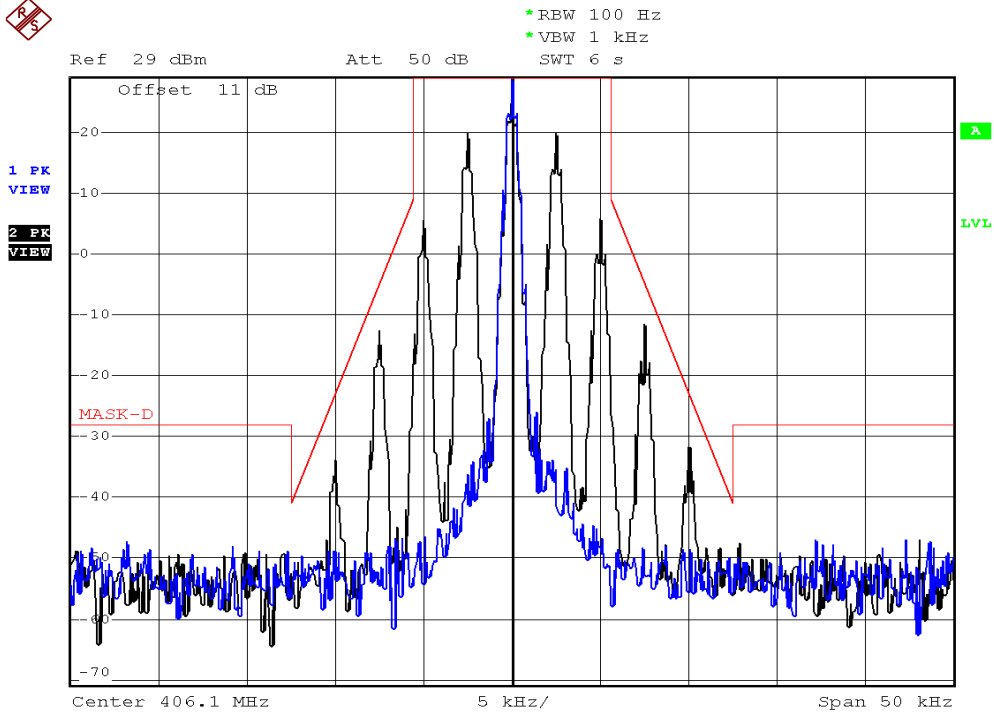
INTERTEK TESTING SERVICES

Emission Mask

Ch1 (Low Power, Mask B, CTCSS)



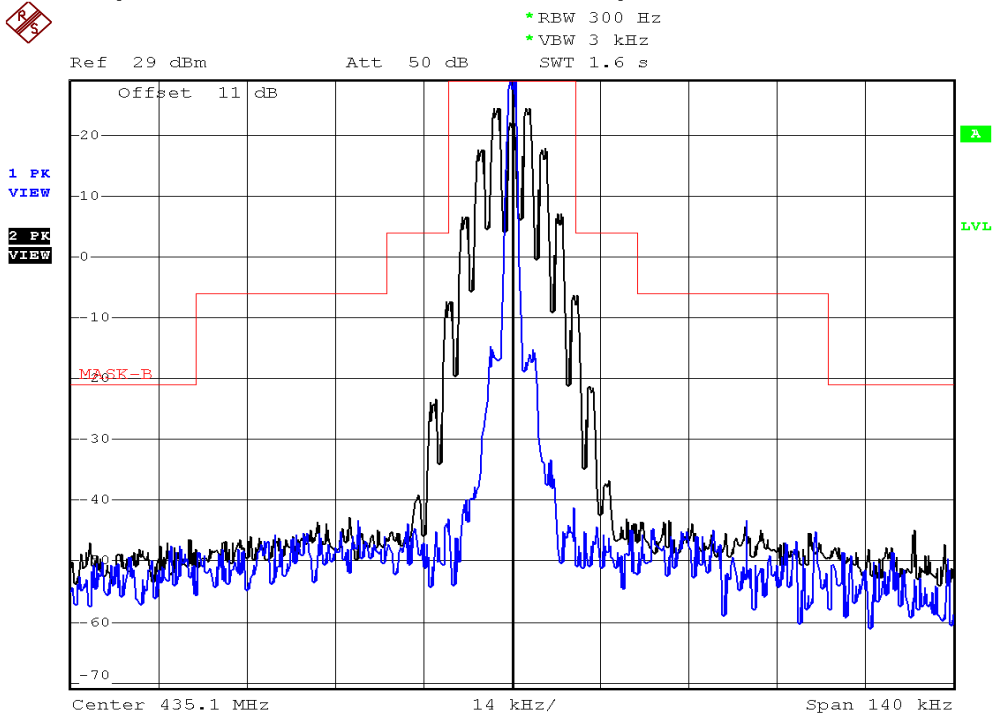
Ch1 (Low Power, Mask D, CTCSS)



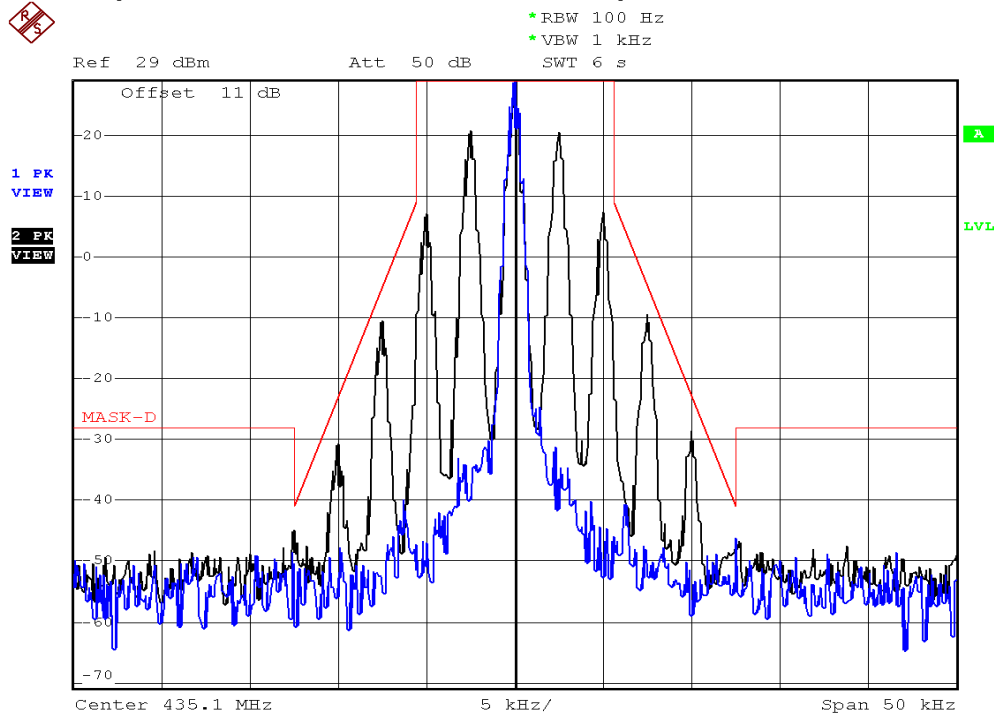
INTERTEK TESTING SERVICES

Emission Mask

Ch16 (Low Power, Mask B, CTCSS)



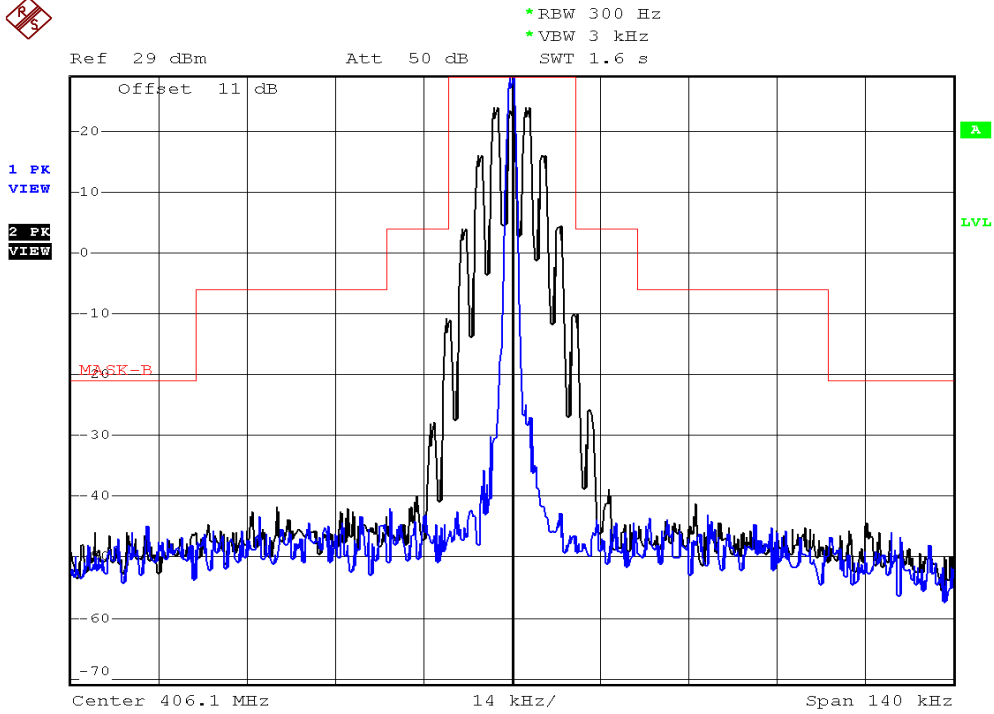
Ch16 (Low Power, Mask D, CTCSS)



INTERTEK TESTING SERVICES

Emission Mask

Ch32 (Low Power, Mask B, CTCSS)



Ch32 (Low Power, Mask D, CTCSS)

