

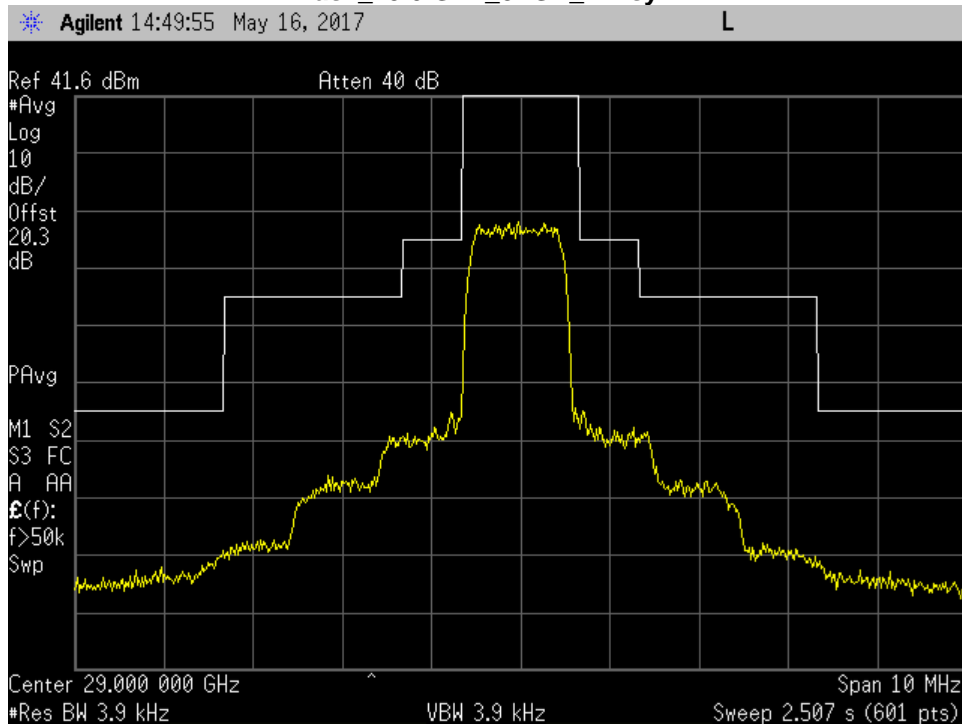


# **Annex A**

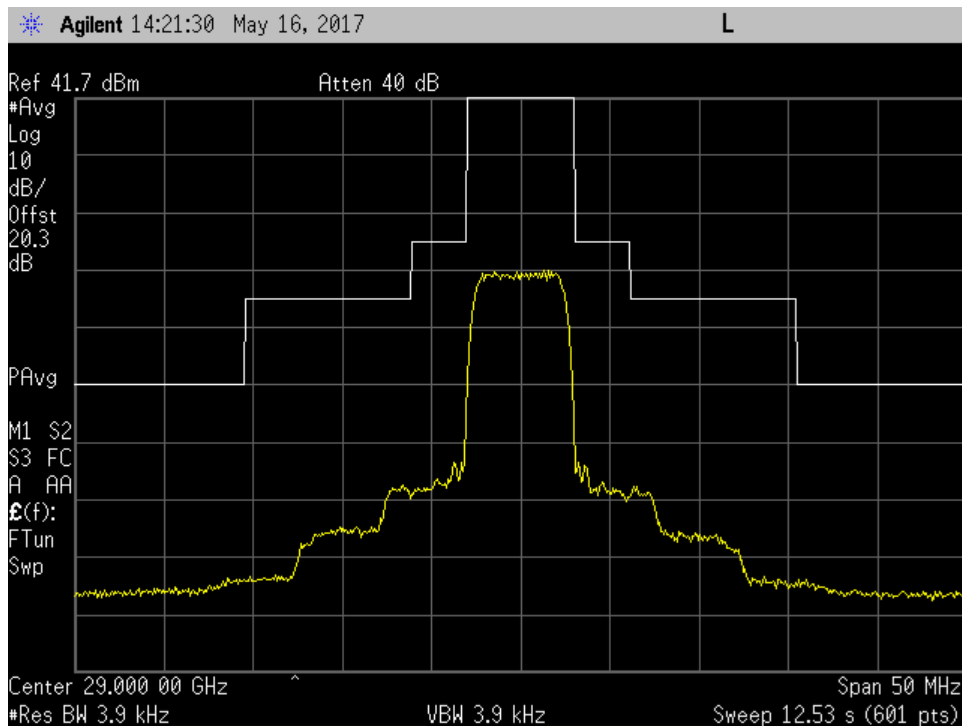
## **Emission Mask**



### Mask\_29.0 GHz\_8PSK\_1 Msym

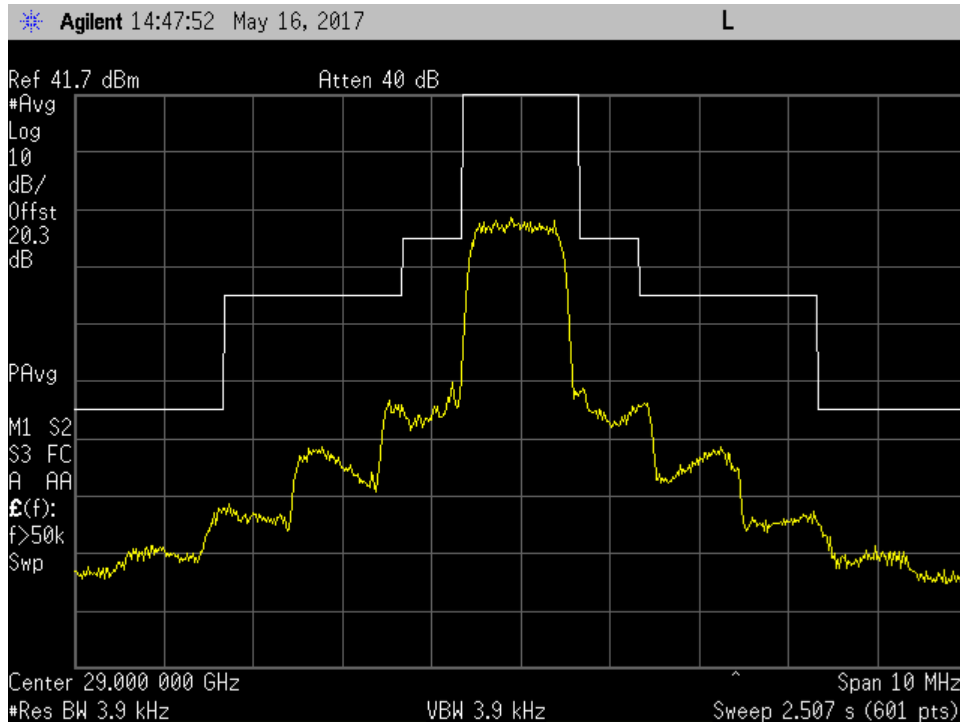


### Mask\_29.0 GHz\_8PSK\_5 Msym

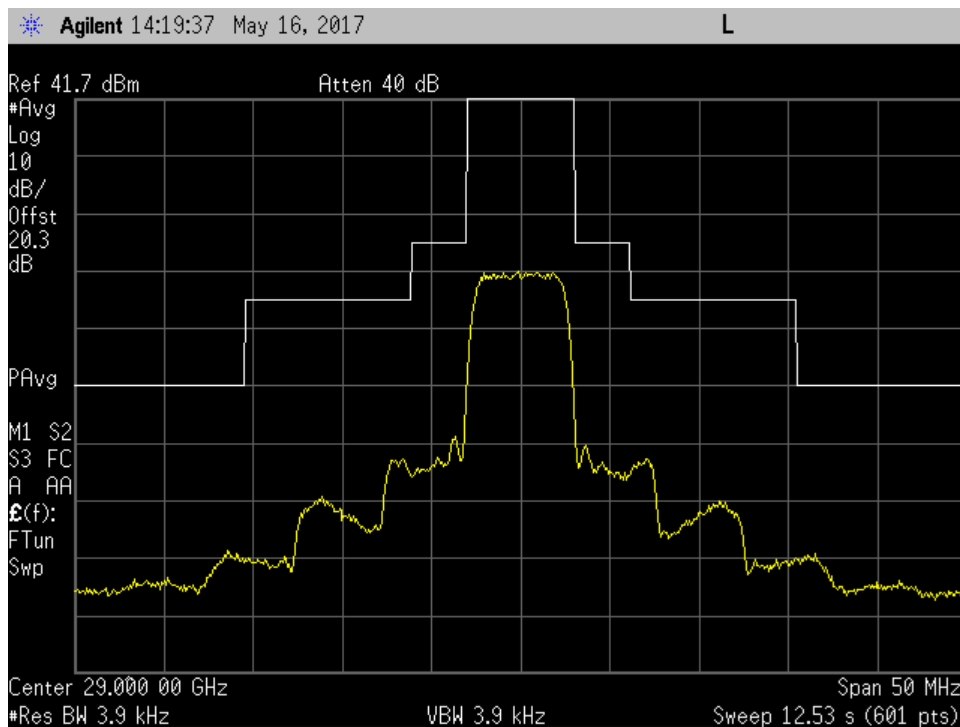




### Mask\_29.0 GHz\_BPSK\_1 Msym

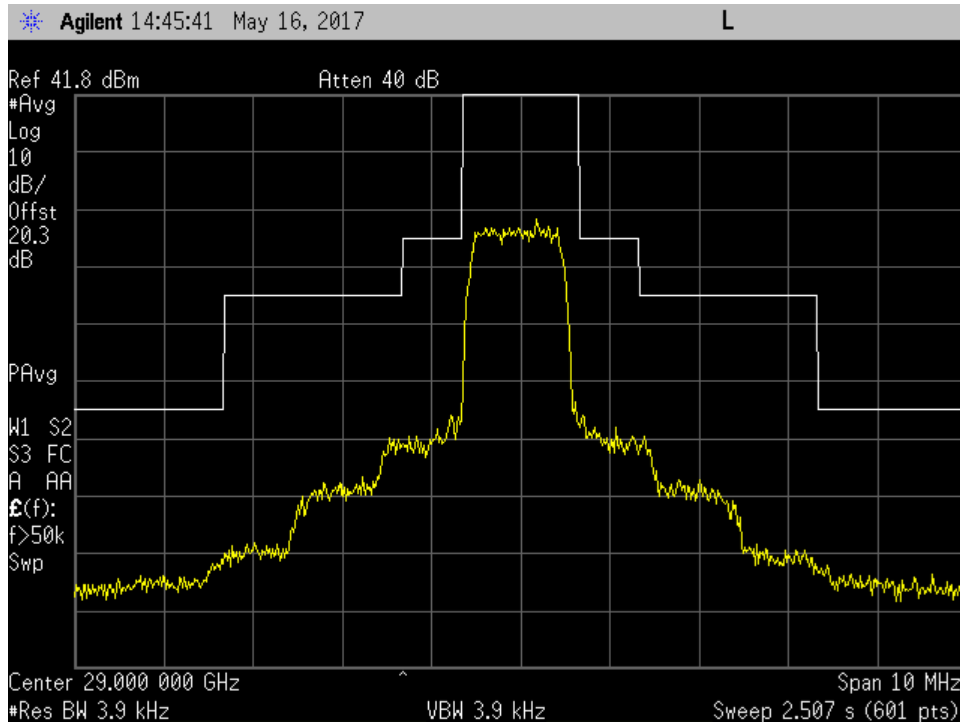


### Mask\_29.0 GHz\_BPSK\_5 Msym

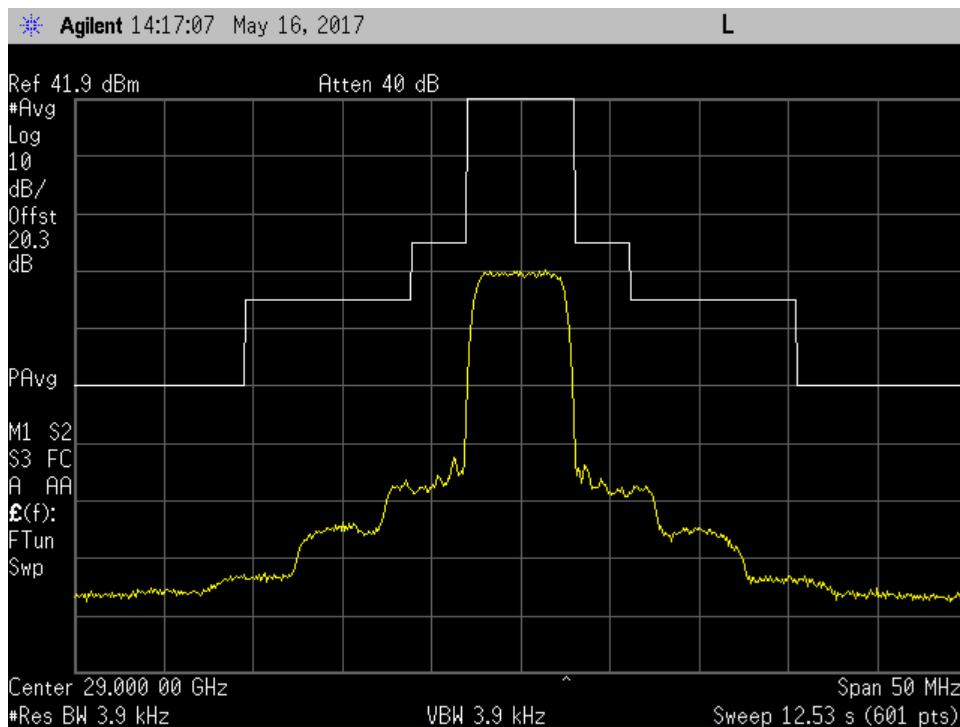




### Mask\_29.0 GHz\_QPSK\_1 Msym

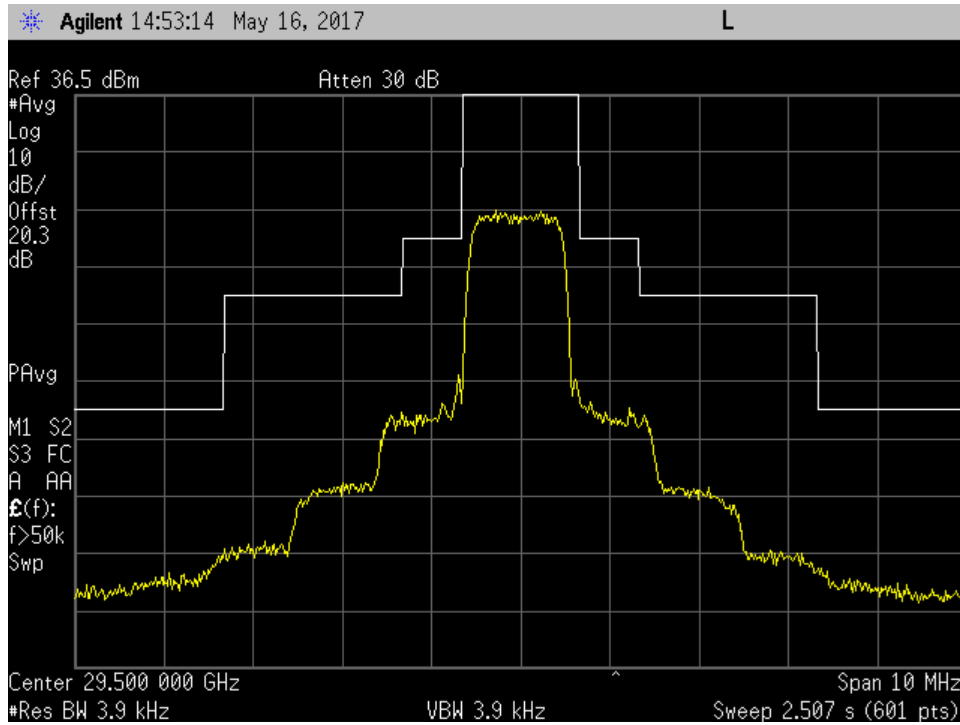


### Mask\_29.0 GHz\_QPSK\_5 Msym

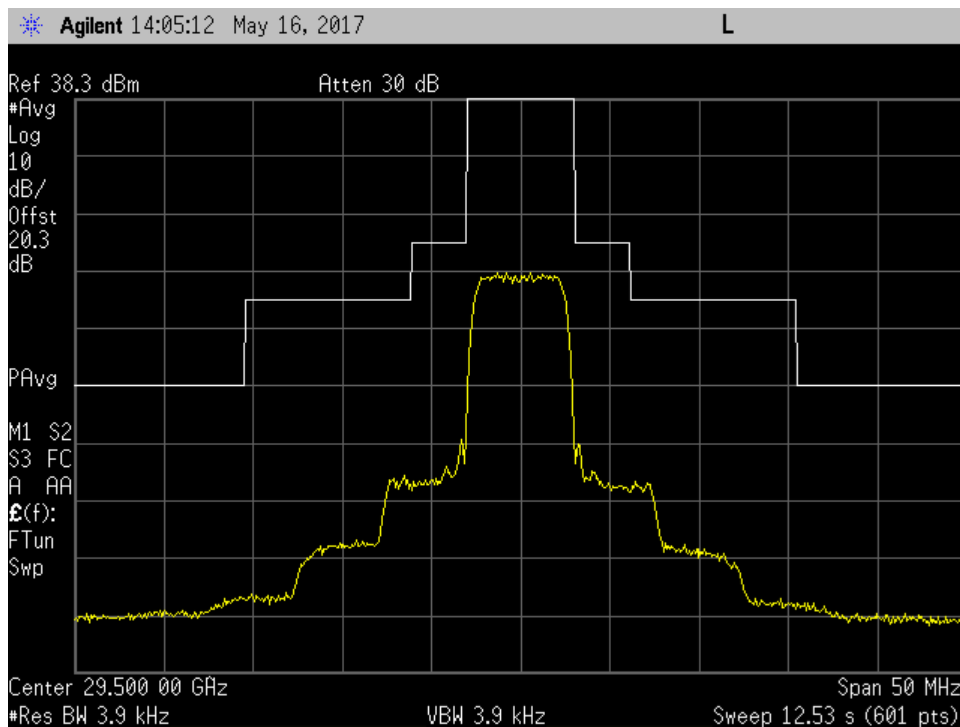




### Mask\_29.5 GHz\_8PSK\_1 Msym

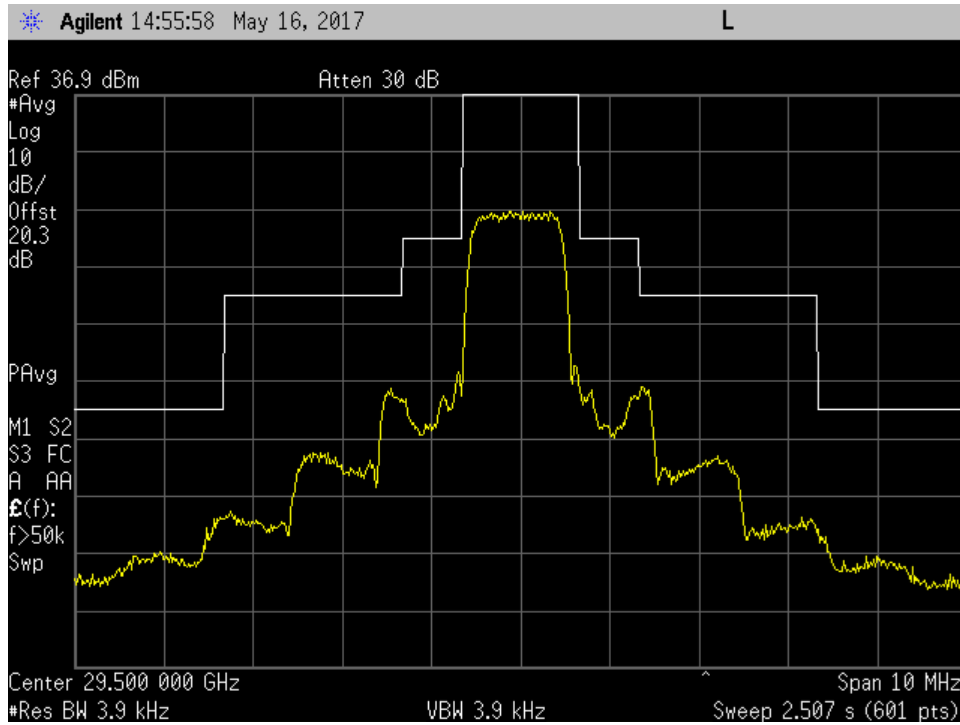


### Mask\_29.5 GHz\_8PSK\_5 Msym

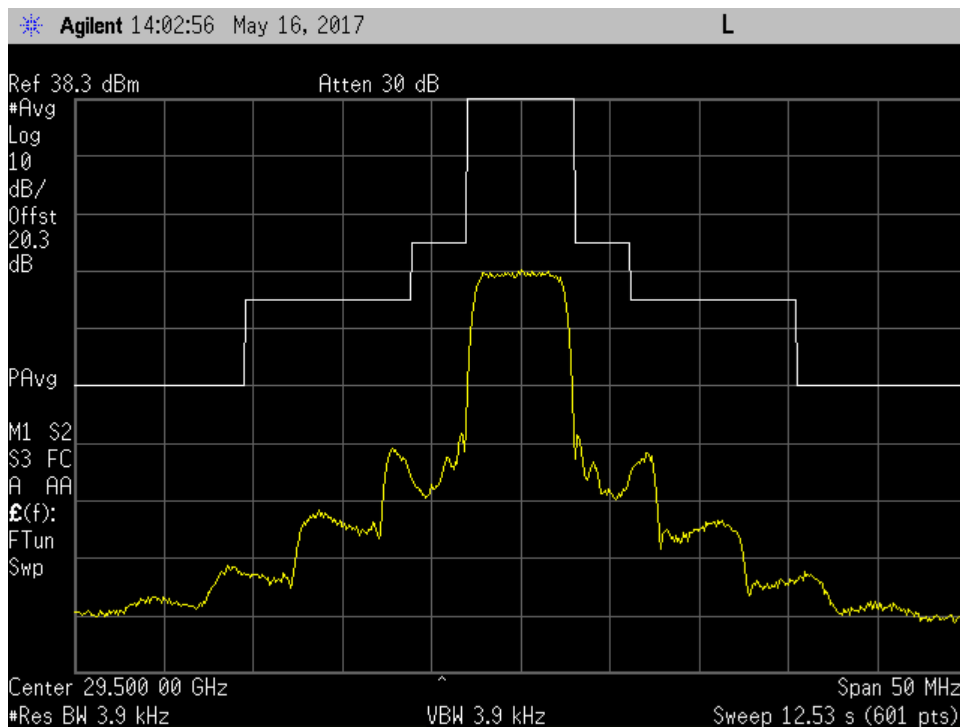




### Mask\_29.5 GHz\_BPSK\_1 Msym

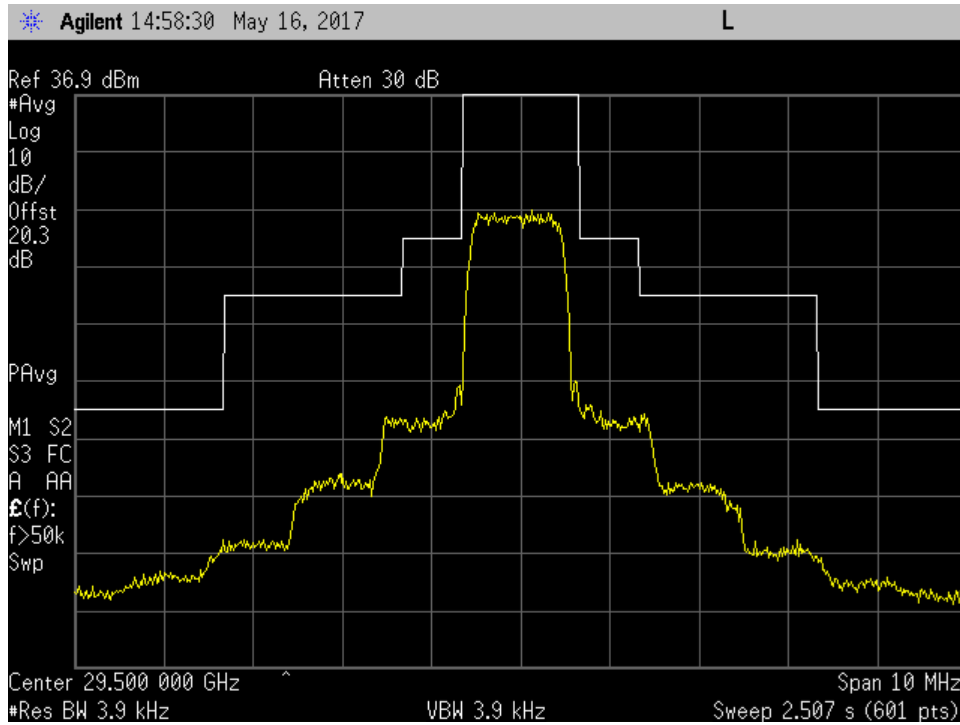


### Mask\_29.5 GHz\_BPSK\_5 Msym

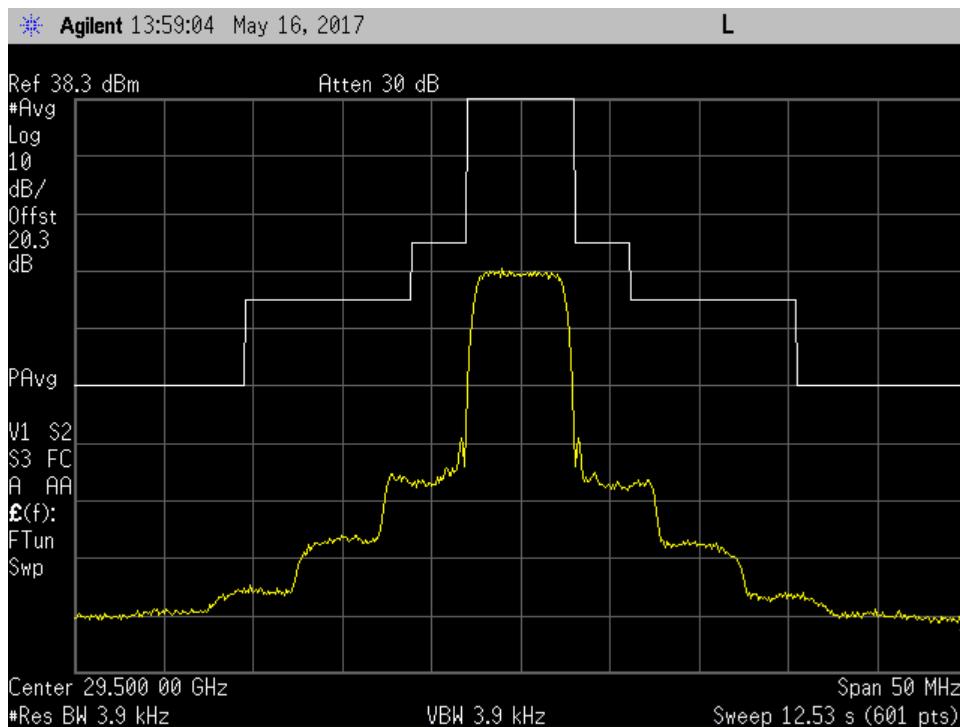




### Mask\_29.5 GHz\_QPSK\_1 Msym

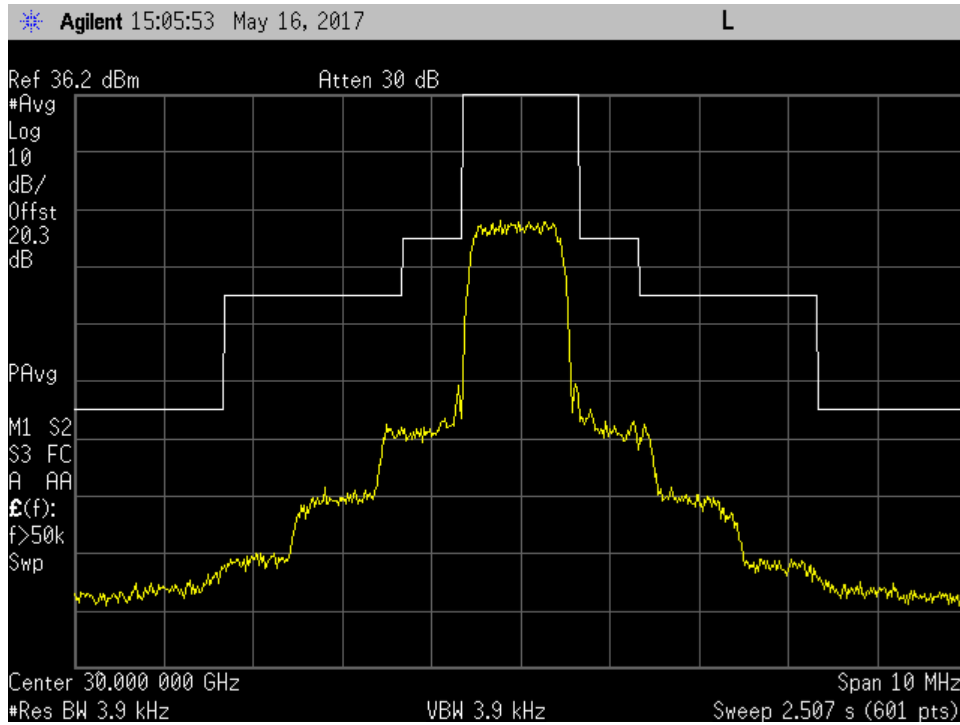


### Mask\_29.5 GHz\_QPSK\_5 Msym

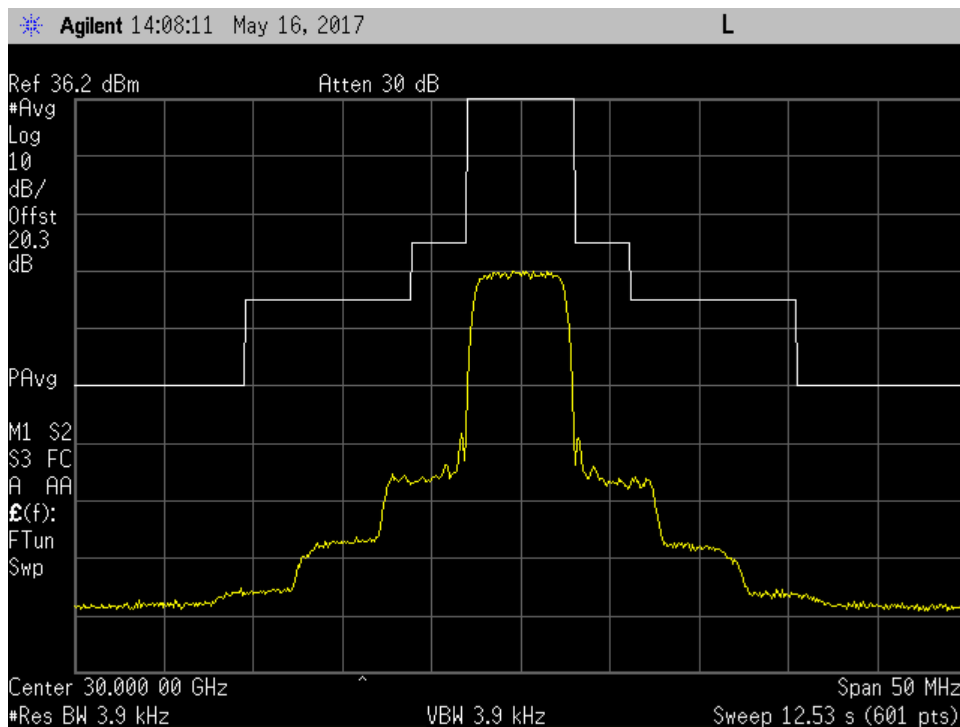




### Mask\_30.0 GHz\_8PSK\_1 Msym



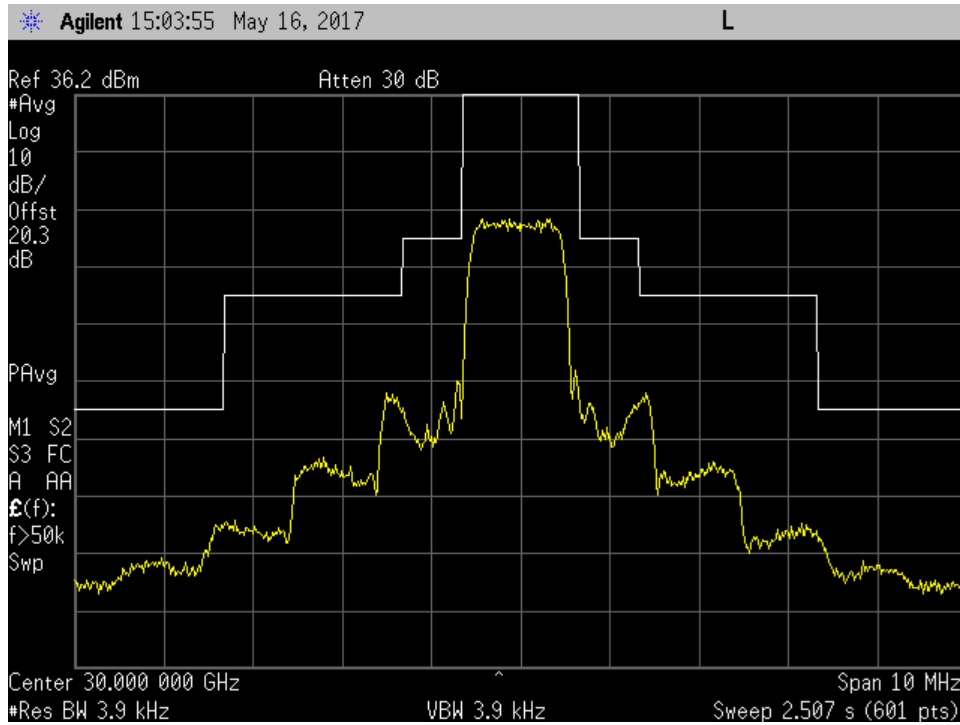
### Mask\_30.0 GHz\_8PSK\_5 Msym



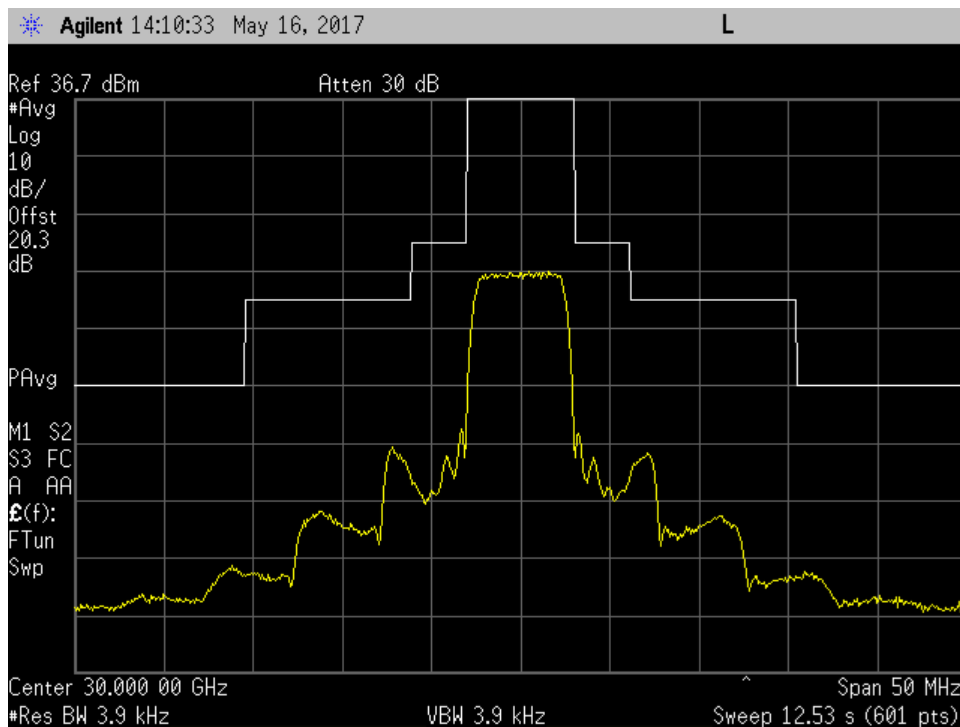




### Mask\_30.0 GHz\_BPSK\_1 Msym

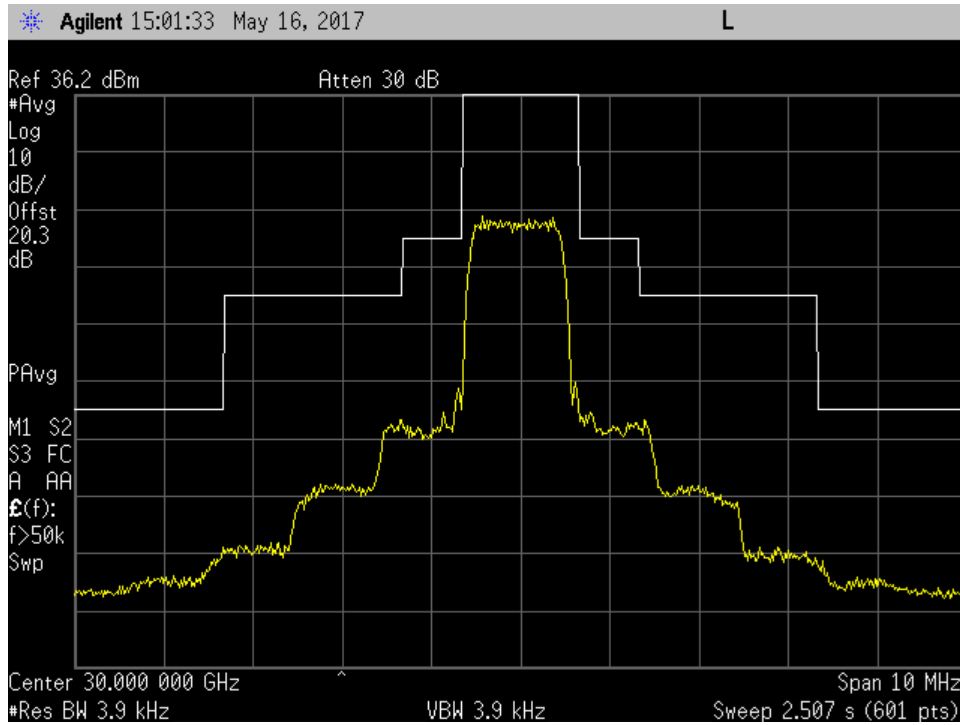


### Mask\_30.0 GHz\_BPSK\_5 Msym





### Mask\_30.0 GHz\_QPSK\_1 Msym



### Mask\_30.0 GHz\_QPSK\_5 Msym

