



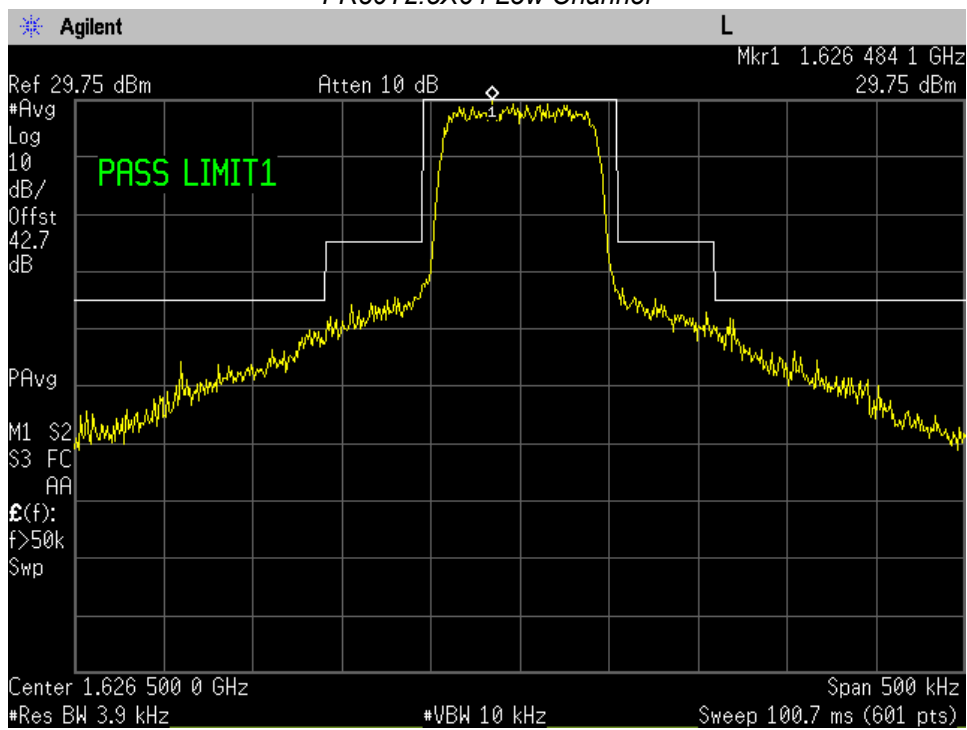
Annex D

**Low and Mid Channel Mask 189 a)
High Channel Mask 189 i)**

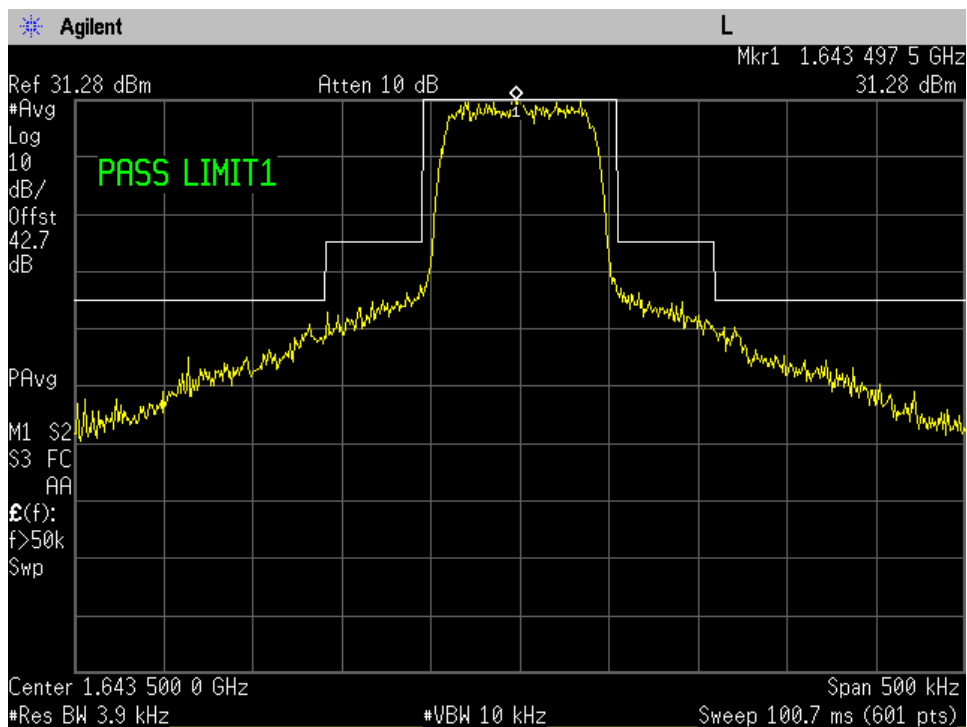


Low and Mid Channel Mask A

FR80T2.5X64 Low Channel

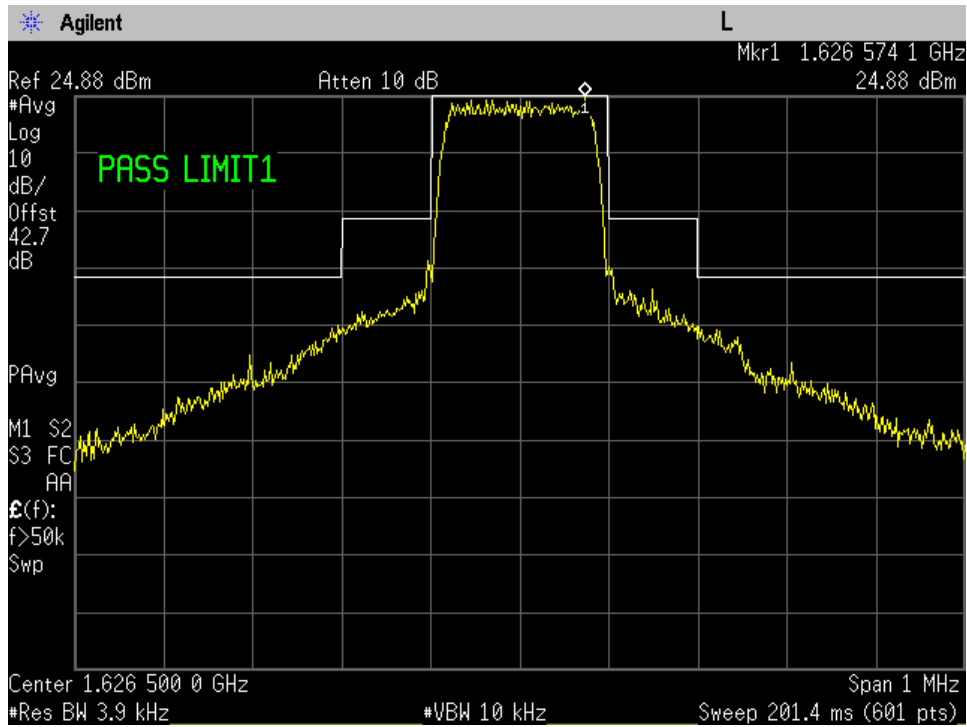


FR80T2.5X64 Mid Channel

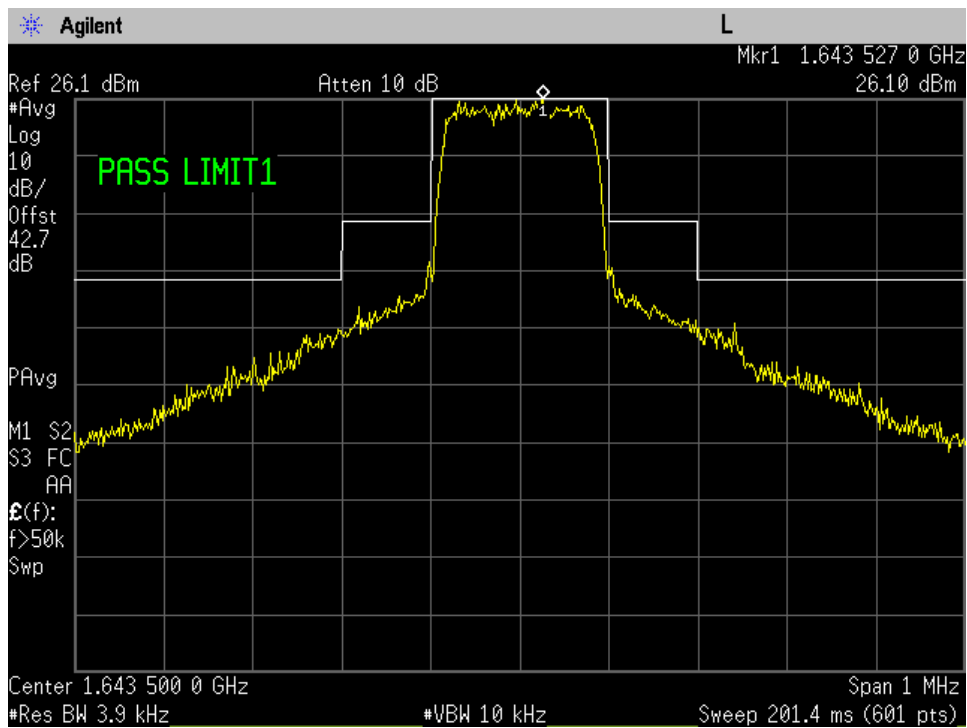




FR80T5X64 Low Channel

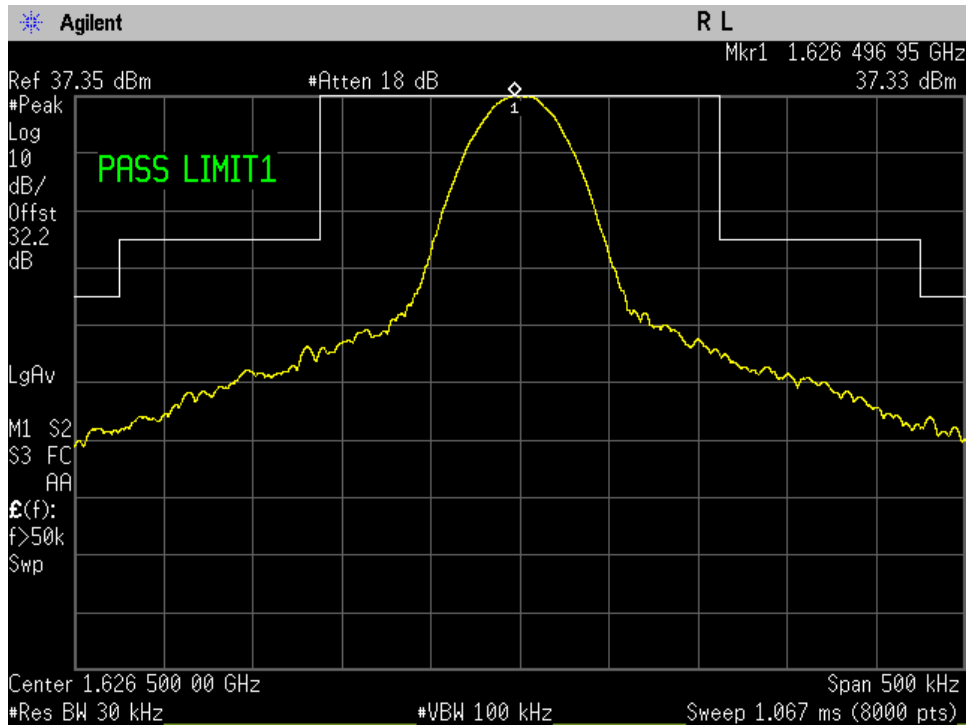


FR80T5X64 Mid Channel

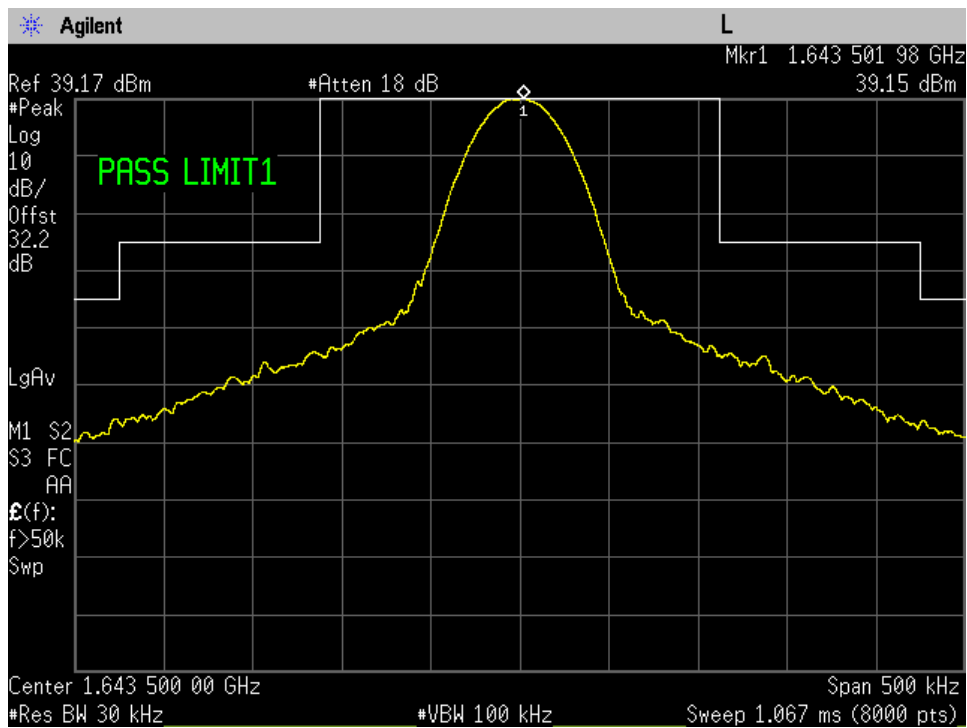




R20T0.5QD Low Channel

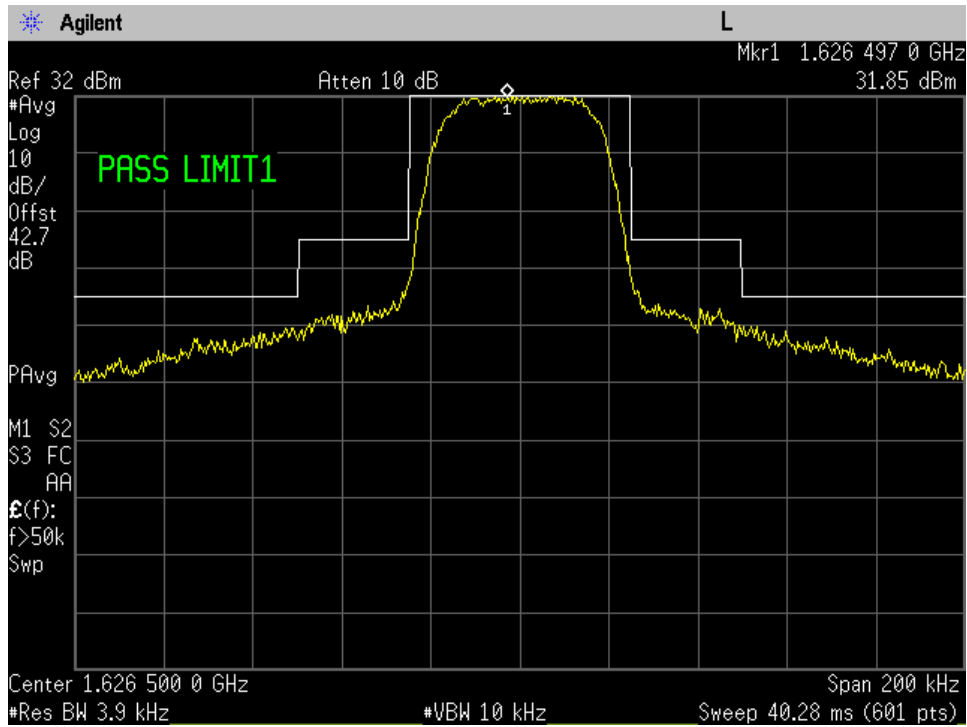


R20T0.5QD Mid Channel

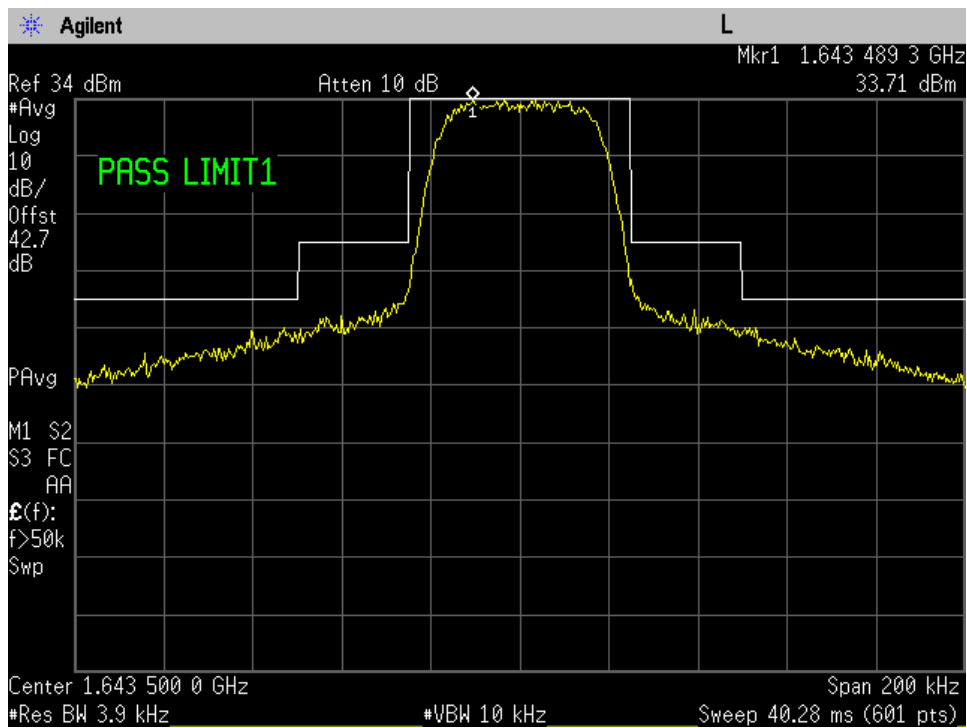




R20T1QD Low Channel

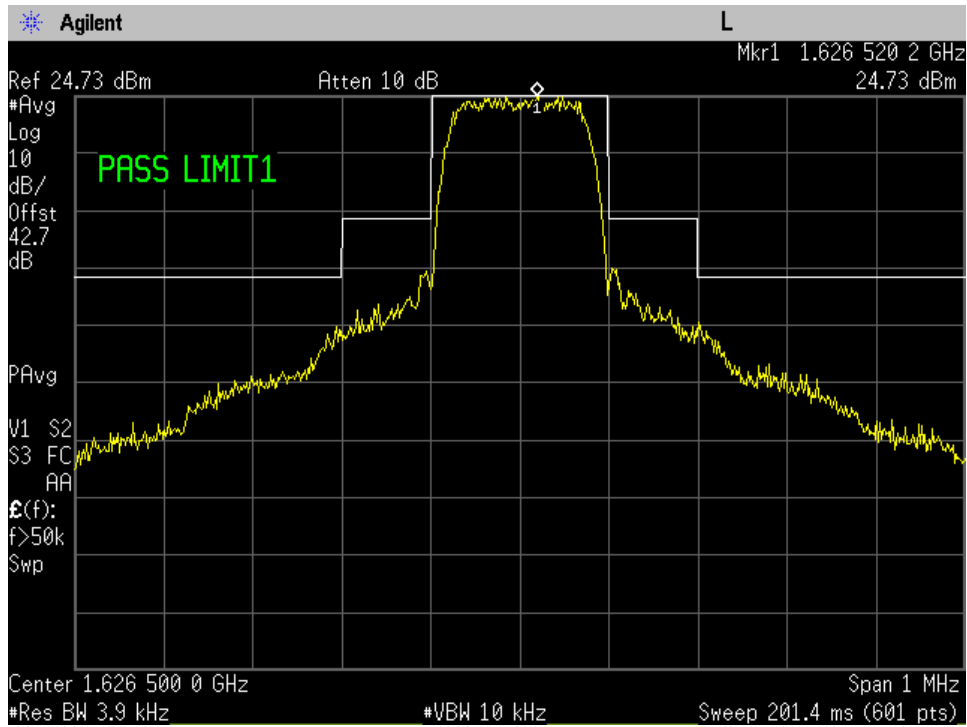


R20T1QD Mid Channel

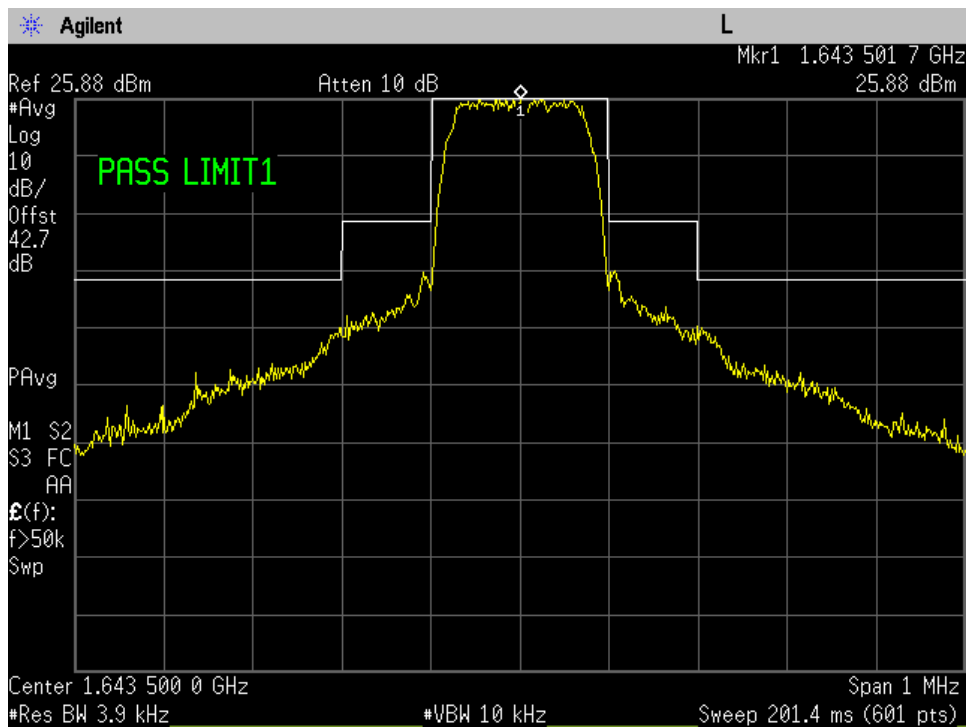




R20T4.5XD Low Channel

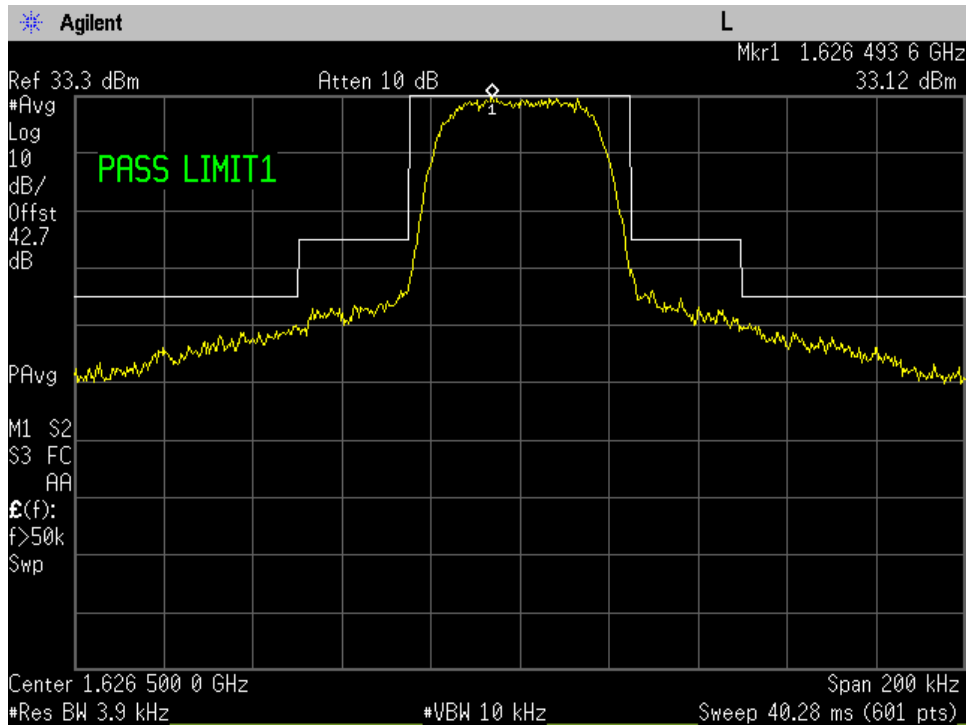


R20T4.5XD Mid Channel

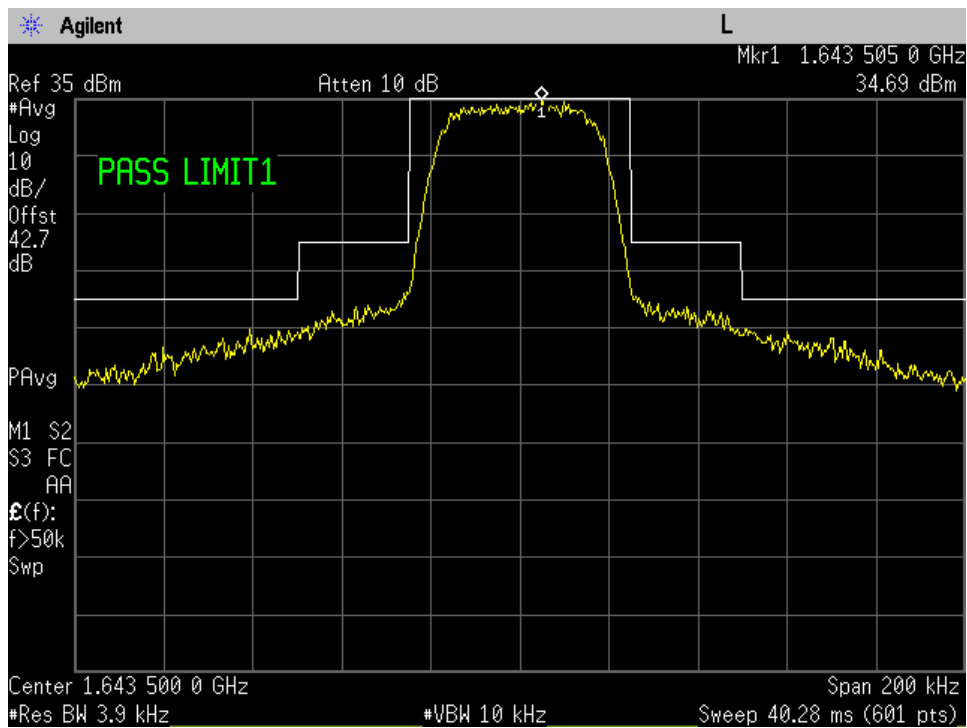




R5T1XD Low Channel

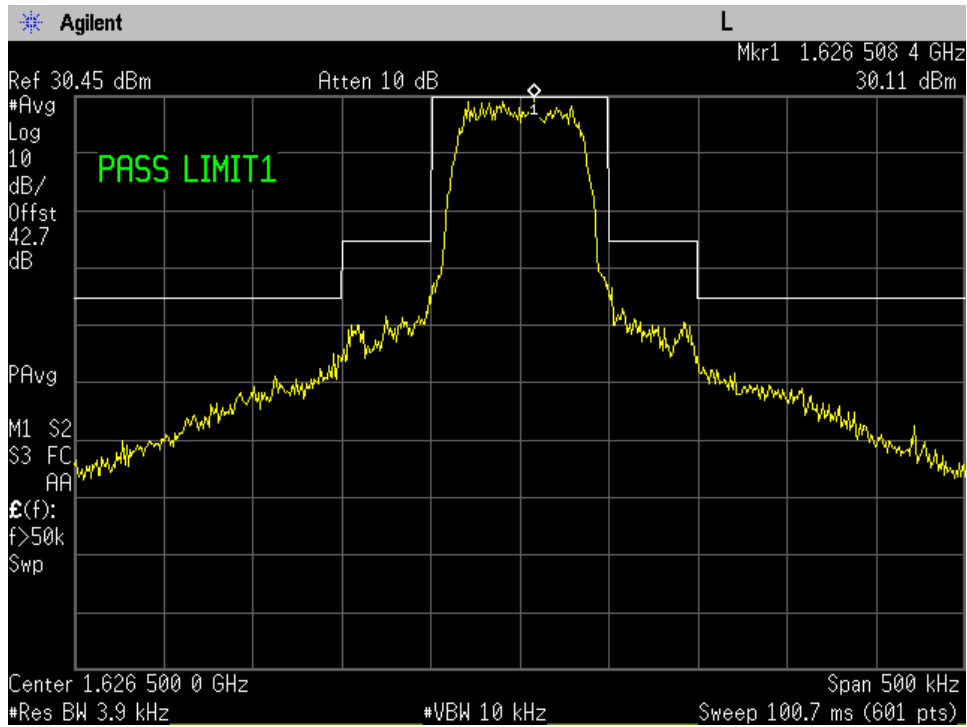


R5T1XD Mid Channel

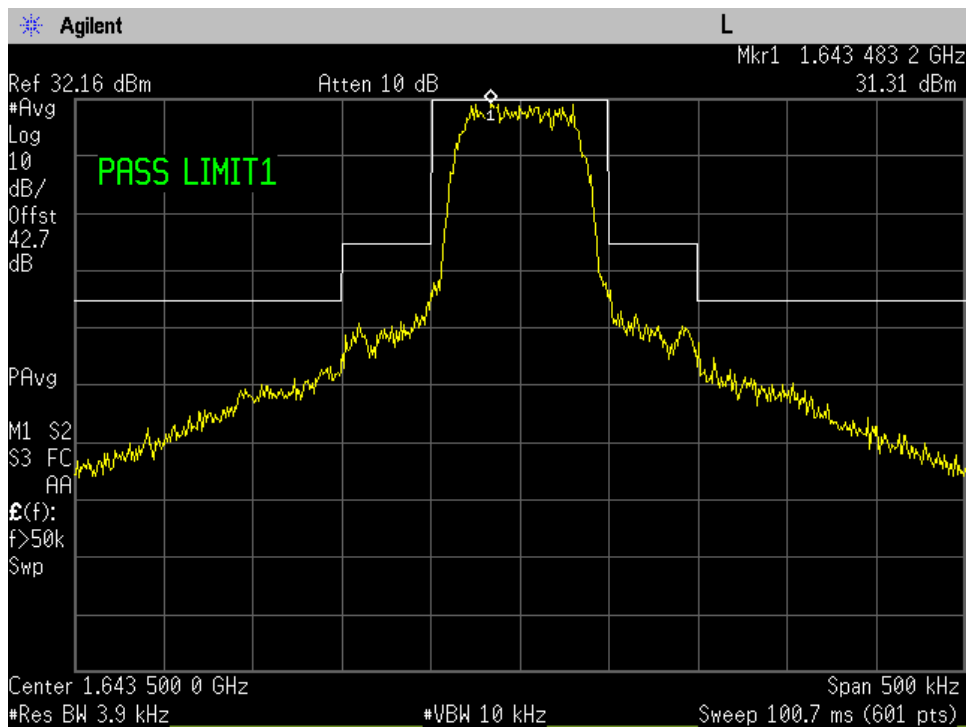




R5T2QD Low Channel

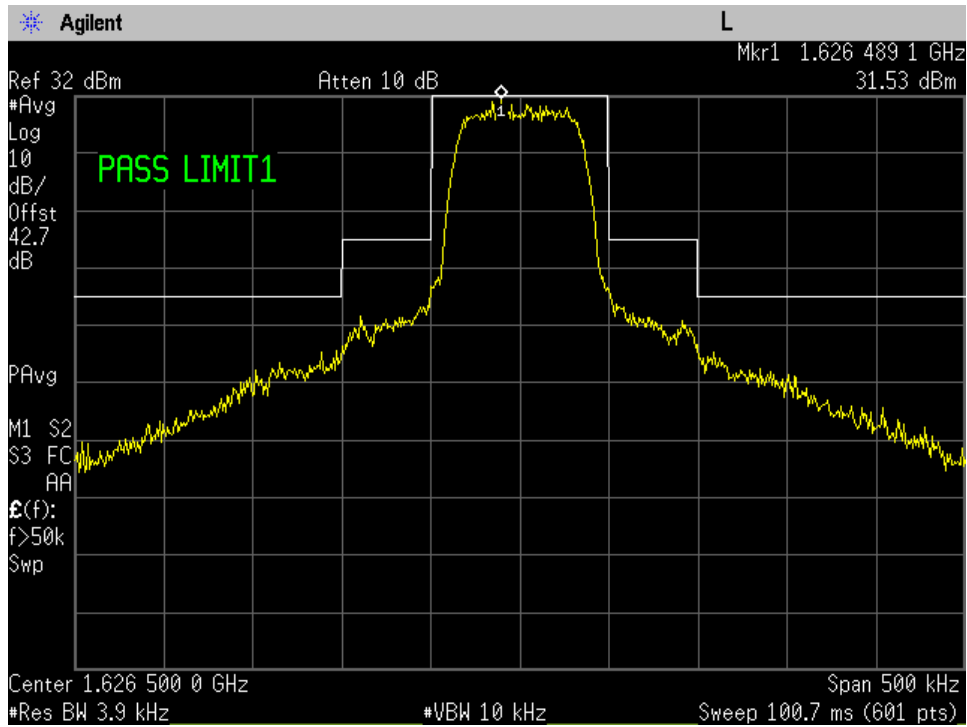


R5T2QD Mid Channel

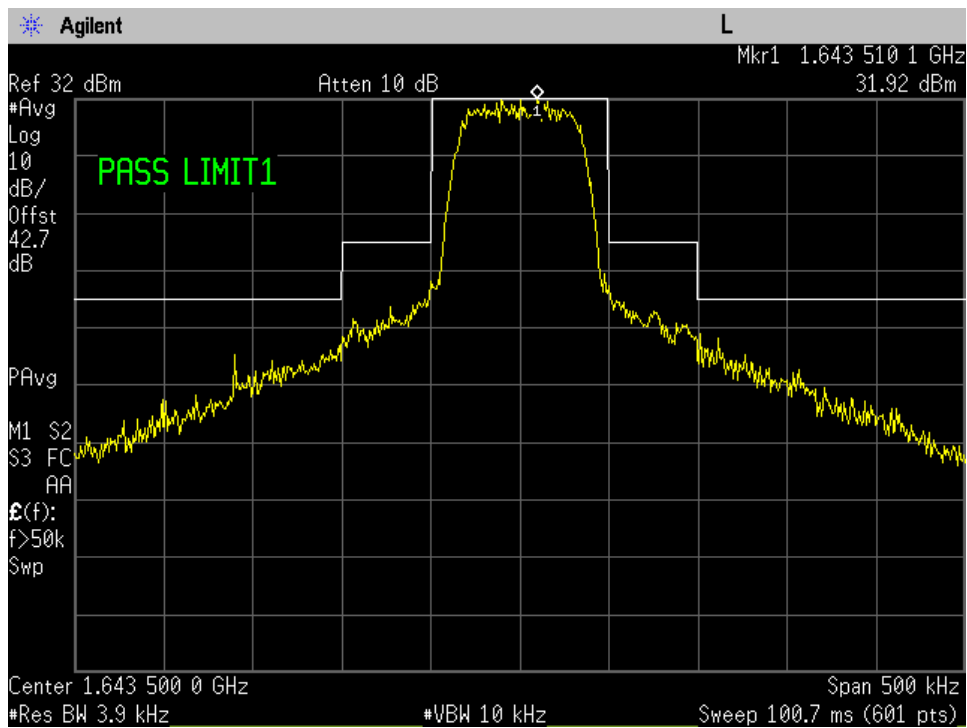




R5T2XD Low Channel

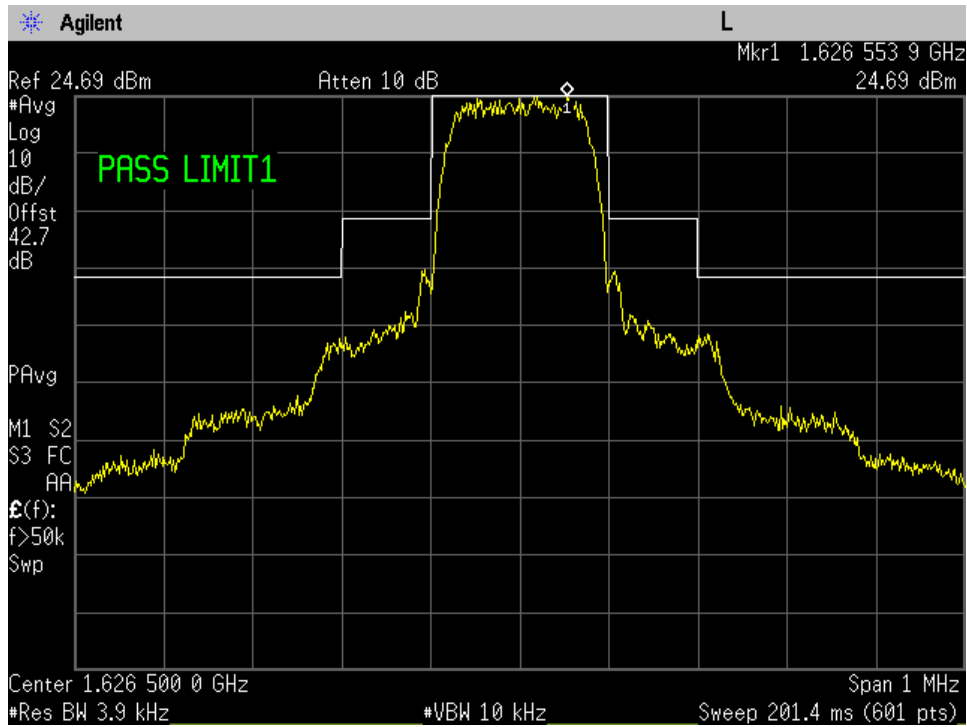


R5T2XD Mid Channel

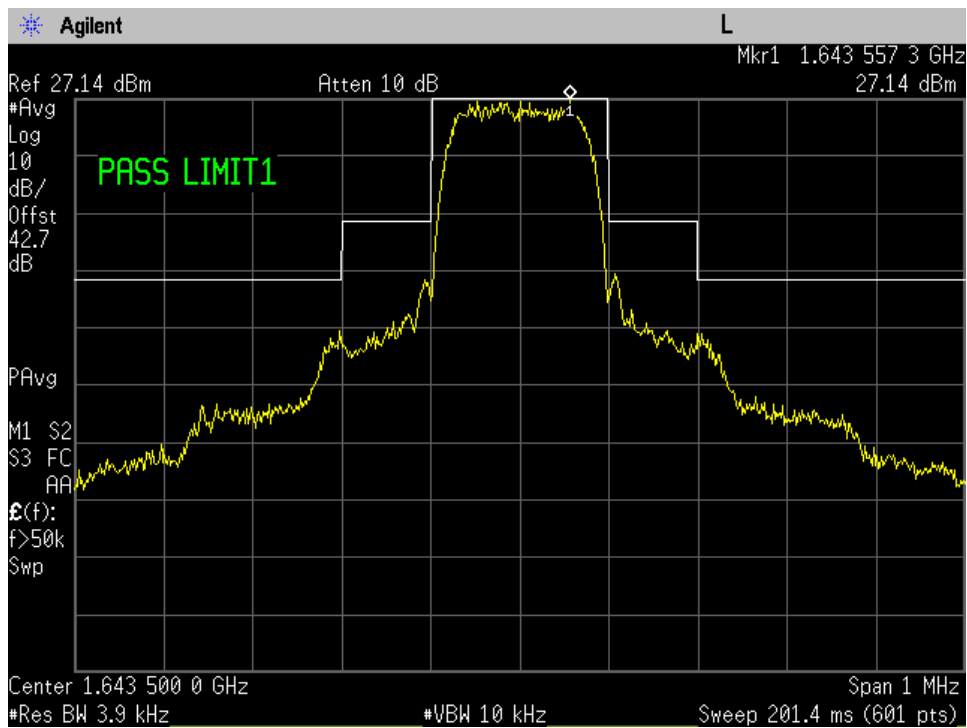




R5T4.5QD Low Channel



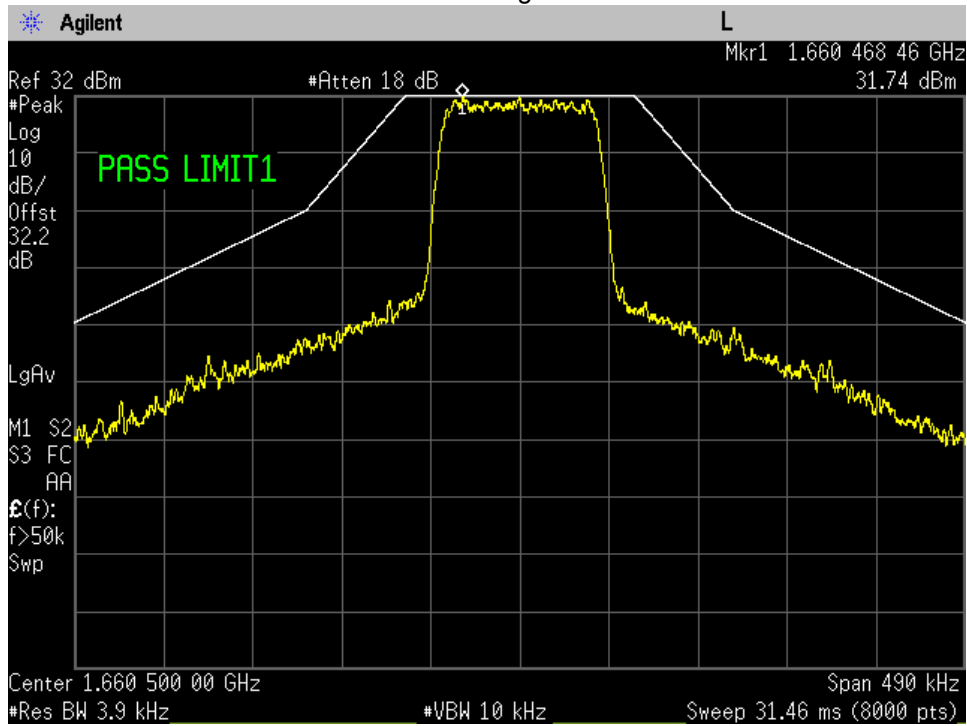
R5T4.5QD Mid Channel



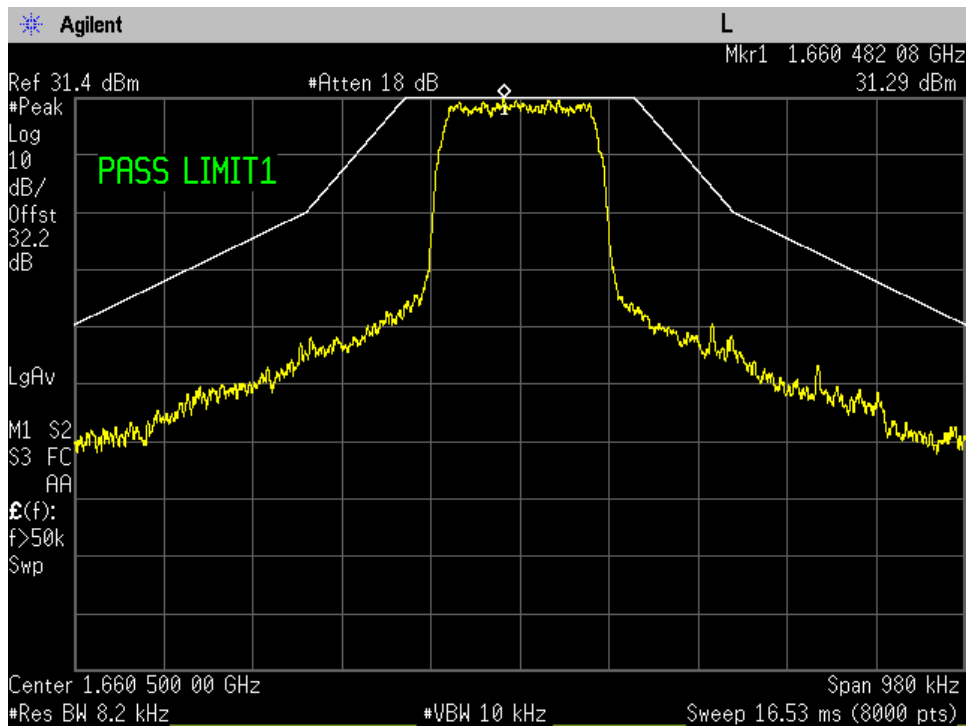


High Channel Mask i

FR80T2.5X64 High Channel

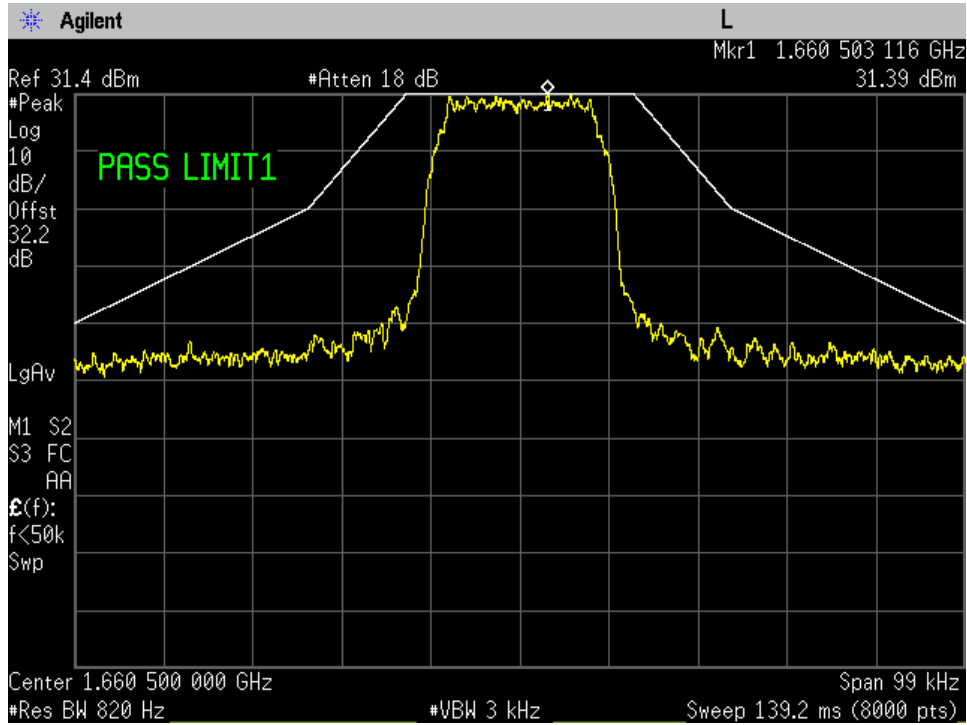


FR80T5X64 High Channel

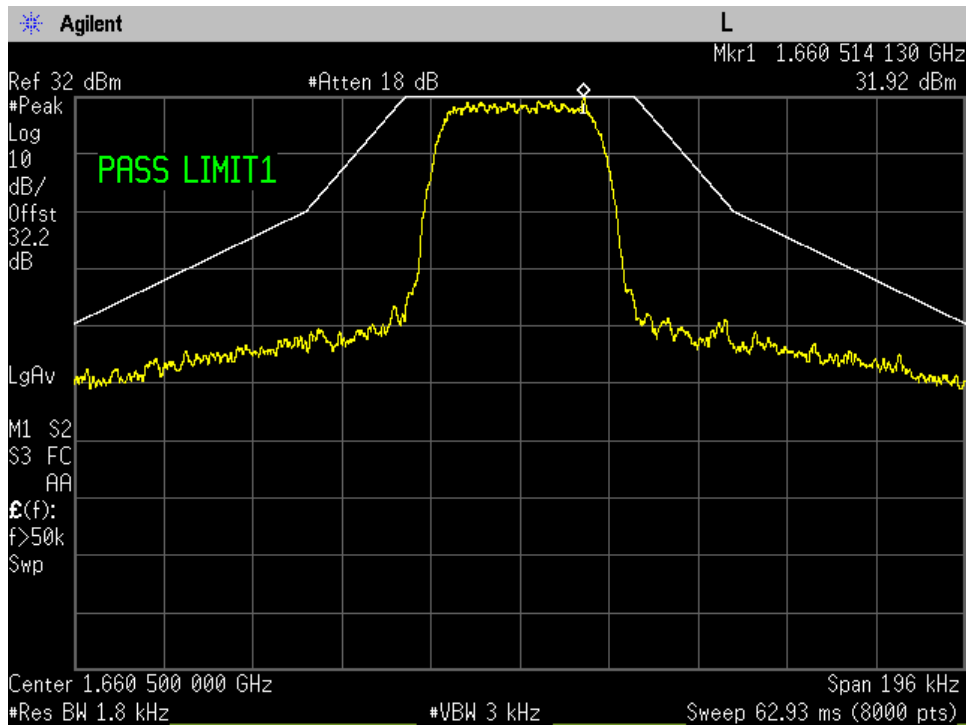




R20T0.5QD High Channel

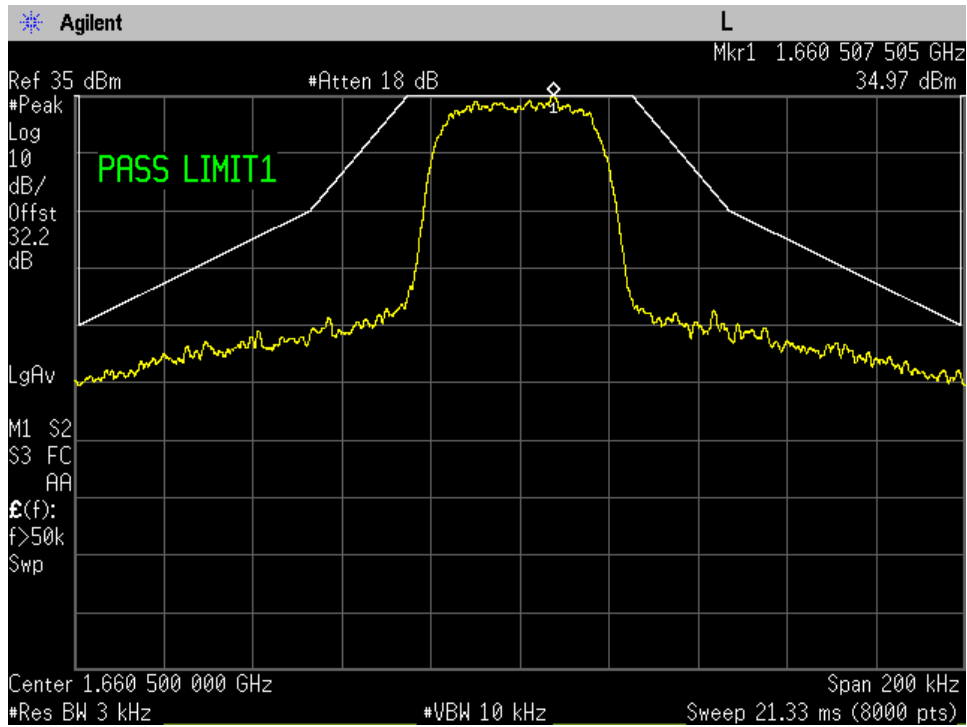


R20T1QD High Channel

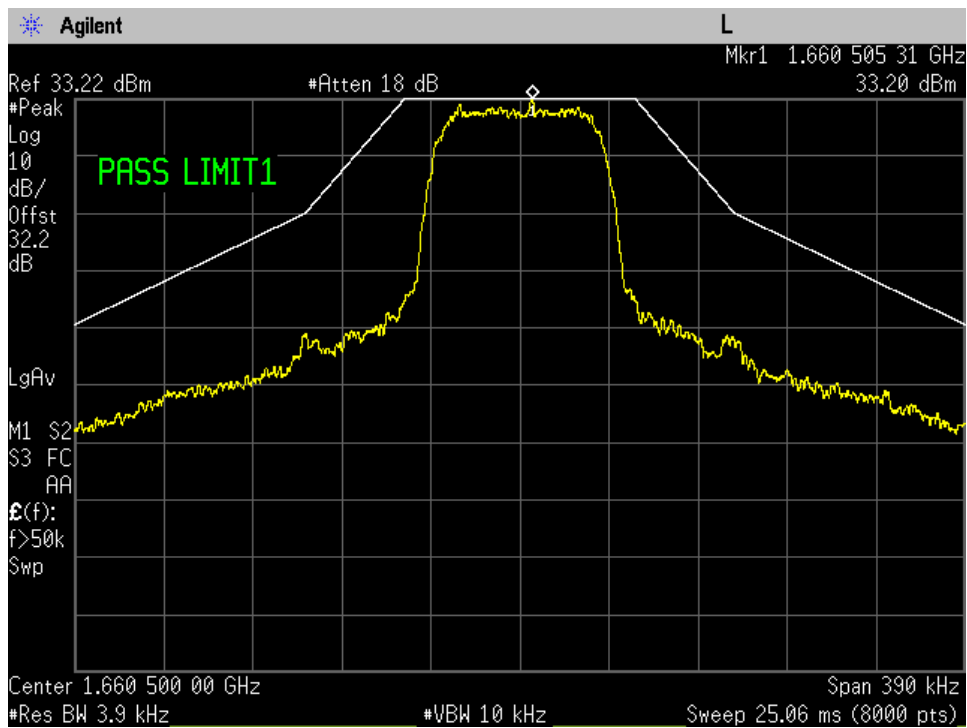




R5T1XD High Channel

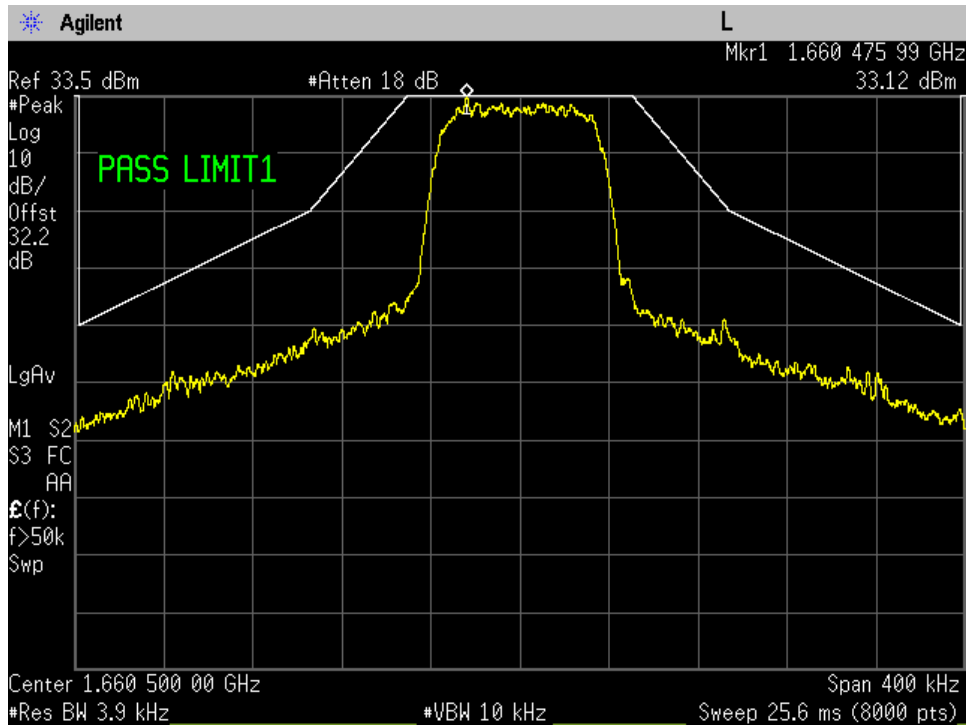


R5T2QD High Channel

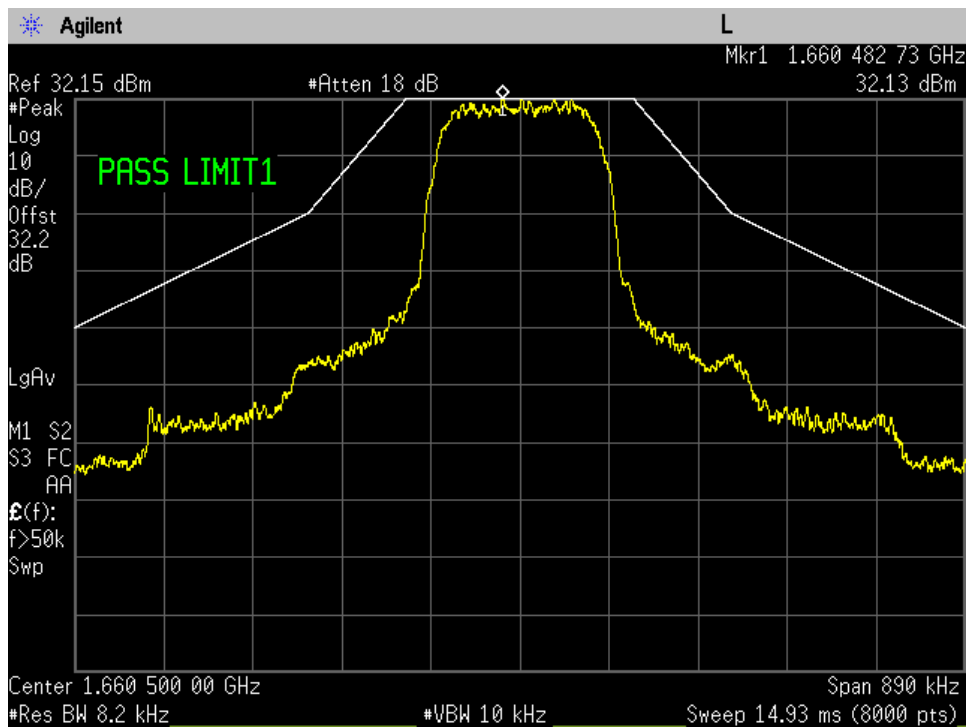




R5T2XD High Channel

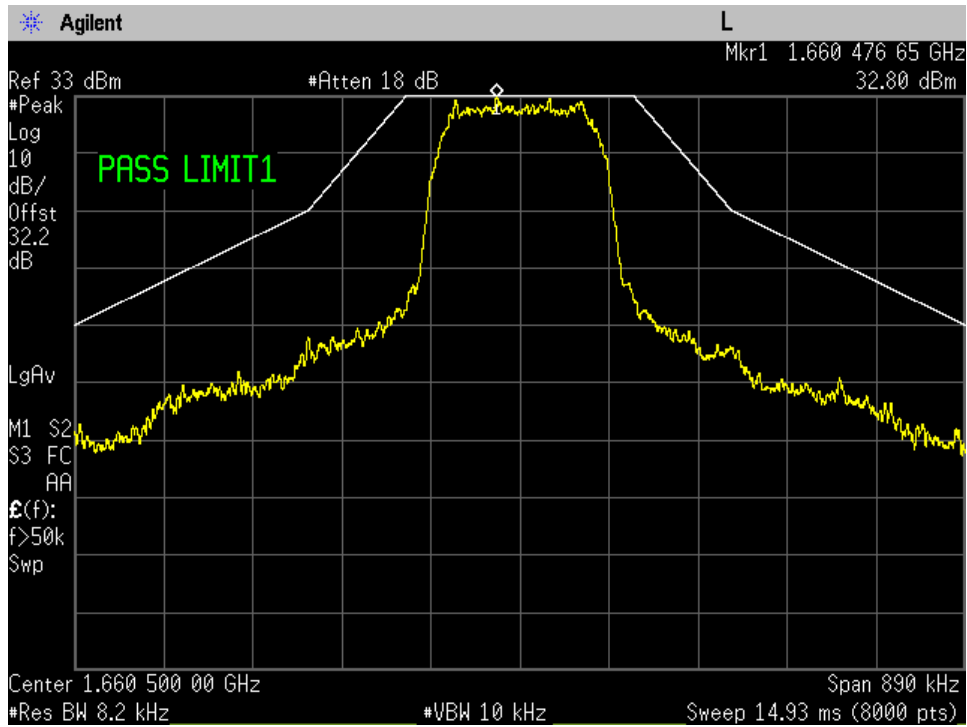


R5T4.5QD High Channel

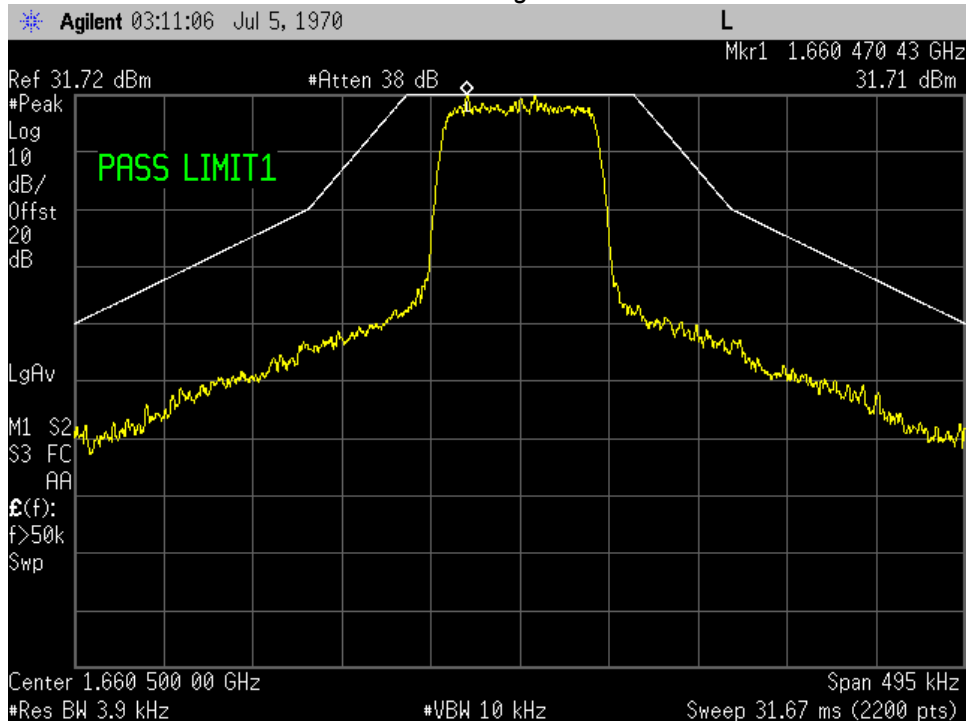




R5T4.5XD High Channel

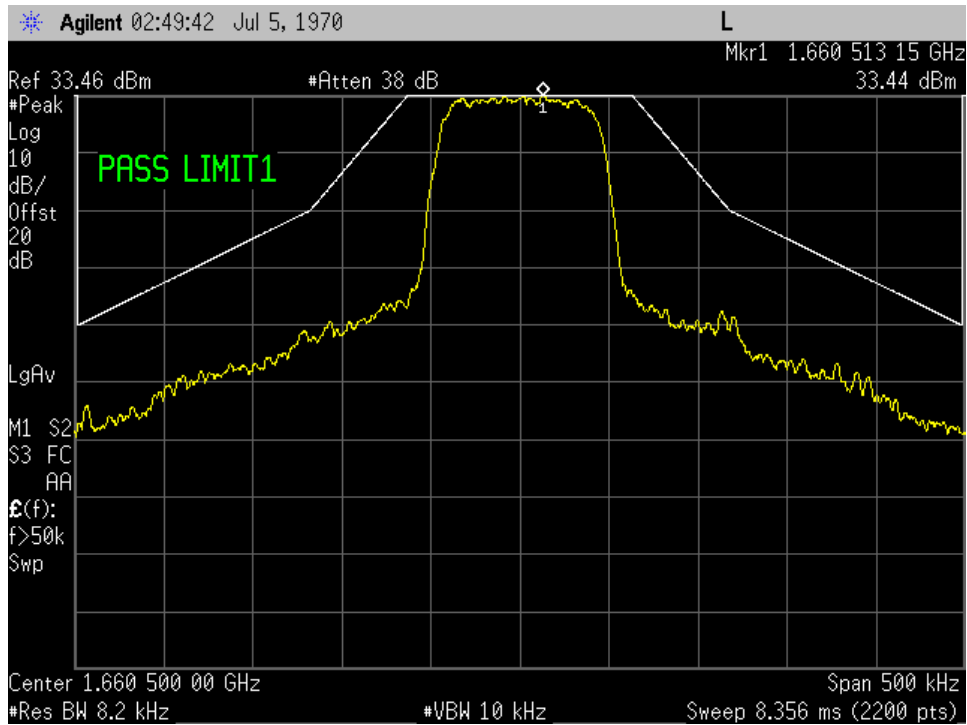


FR80T25X16 High Channel

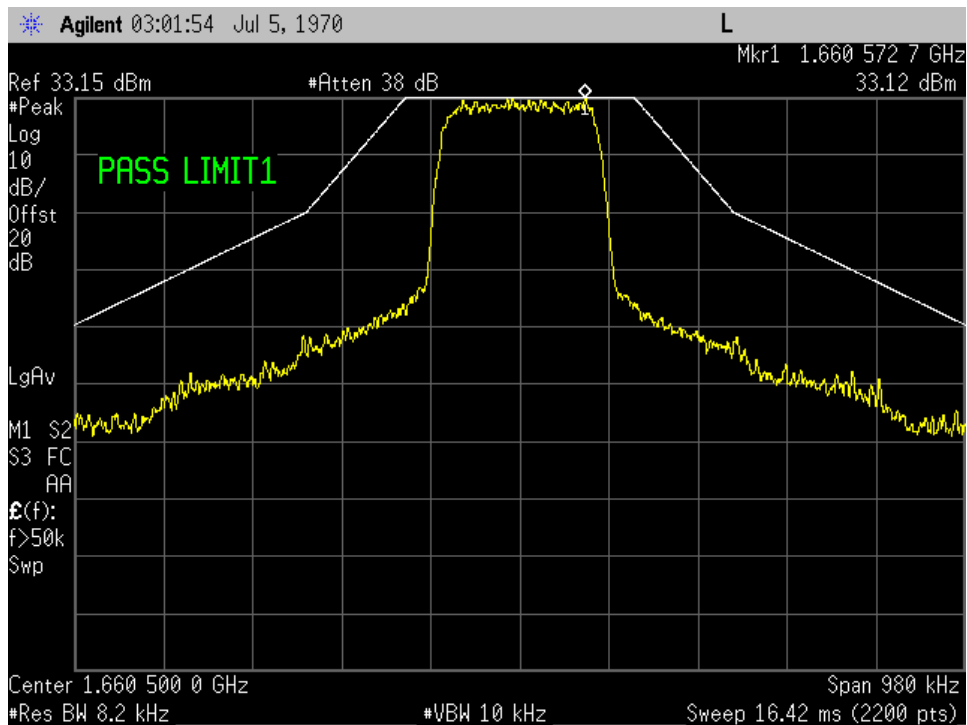




FR80T25X32 High Channel



FR80T5X16 High Channel





FR80T5X32 High Channel

