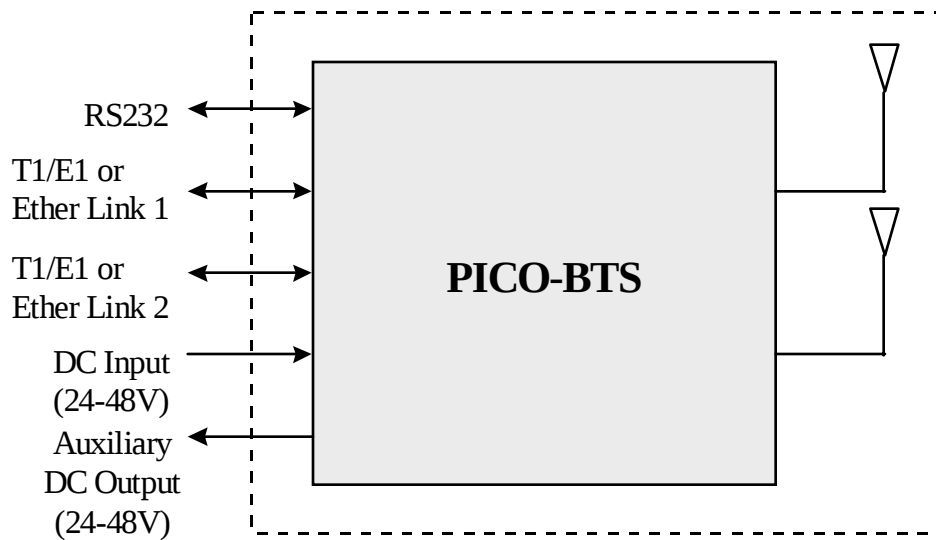
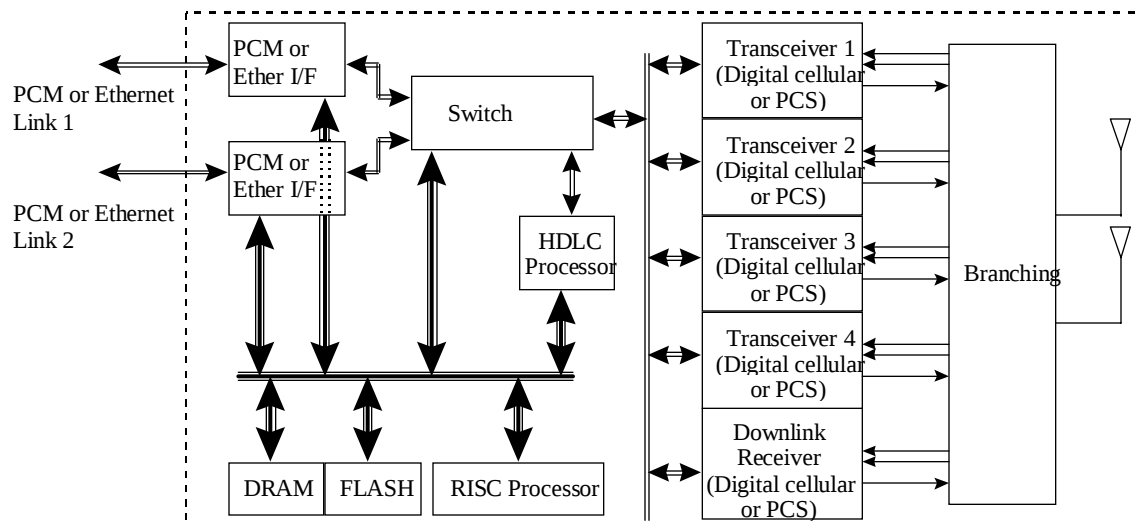


## **EXHIBIT 5**

**BLOCK DIAGRAM of 1900 MHz PCOCELL SYSTEM**



**Figure 5.1: PICOCELL 1900 MHz System Architecture**



**Figure 5.2: PICOCELL System Internal Architecture**