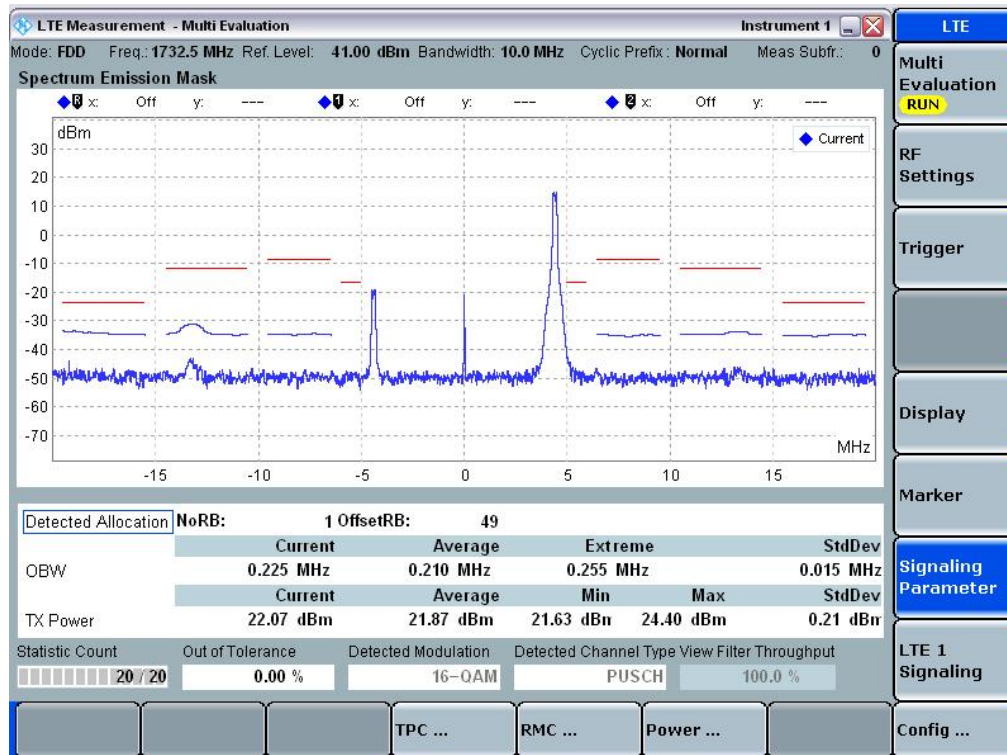
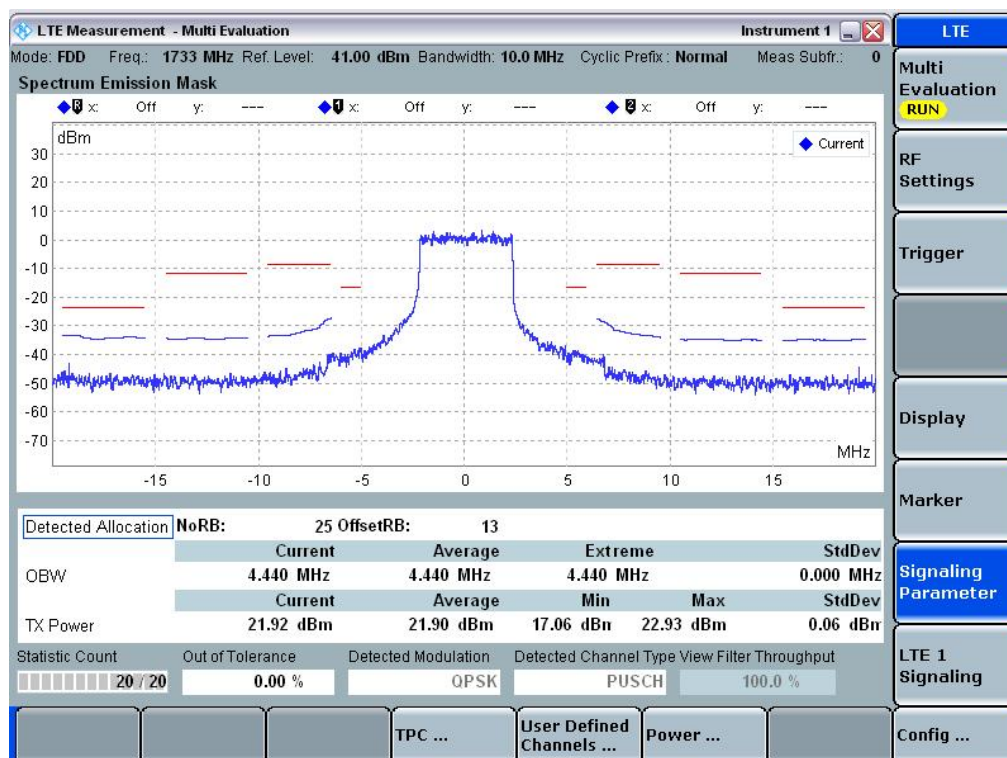


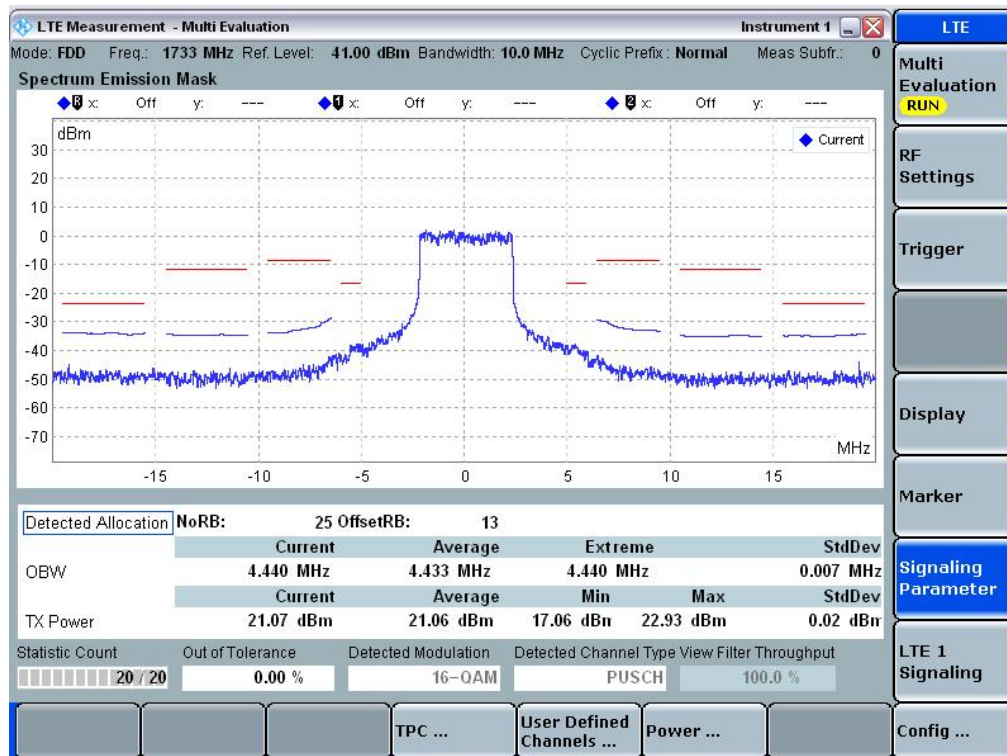
B4 10MHz 20175CH RB1, Offset49, 16QAM Plot



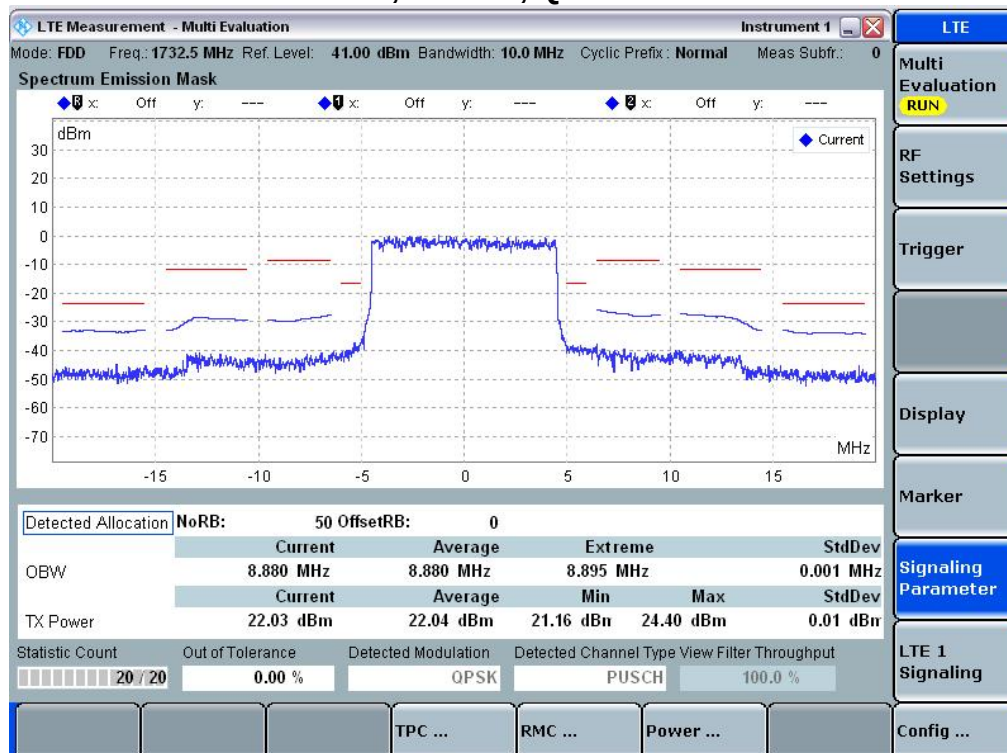
B4 10MHz 20175CH RB25, Offset13, QPSK Plot



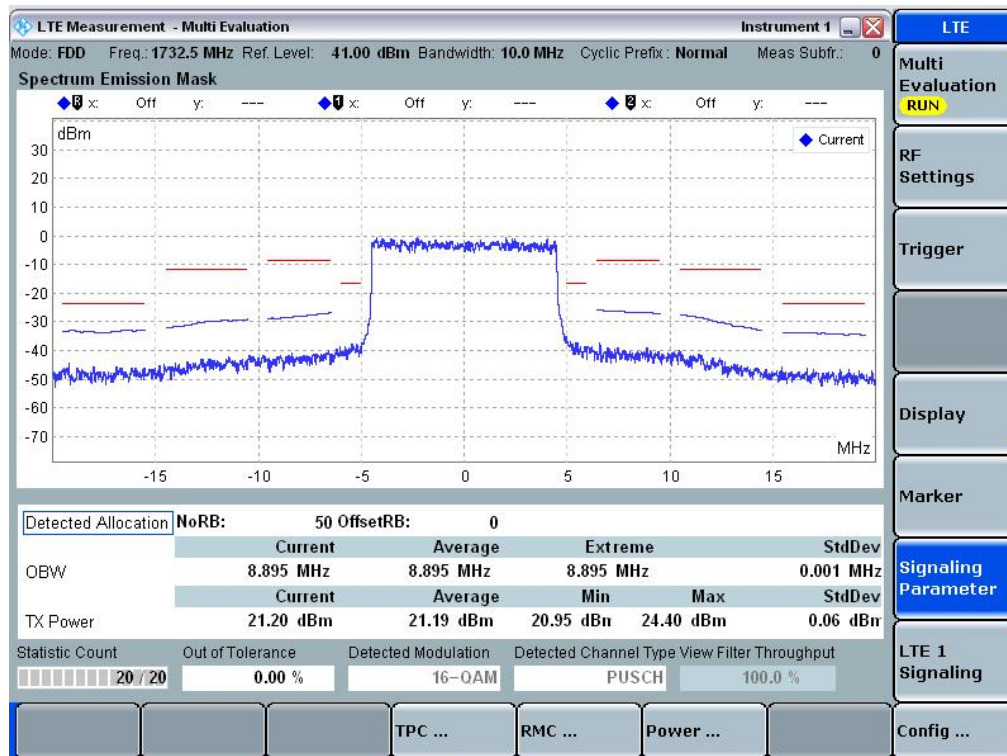
B4 10MHz 20175CH RB25, Offset13, 16QAM Plot



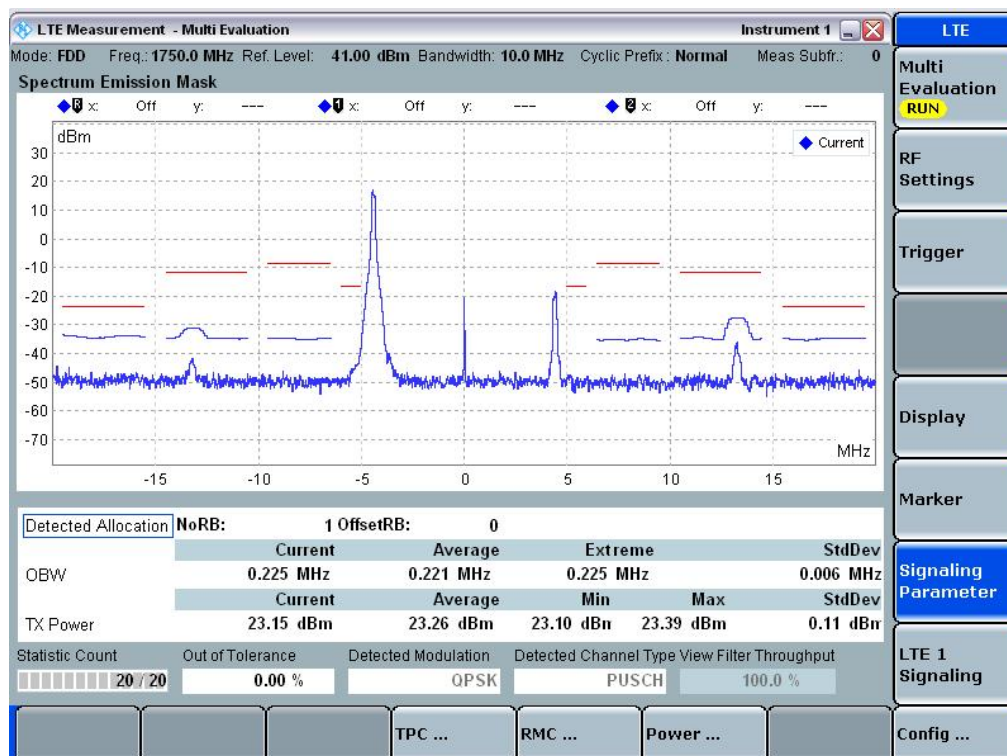
B4 10MHz 20175CH RB50, Offset0, QPSK Plot



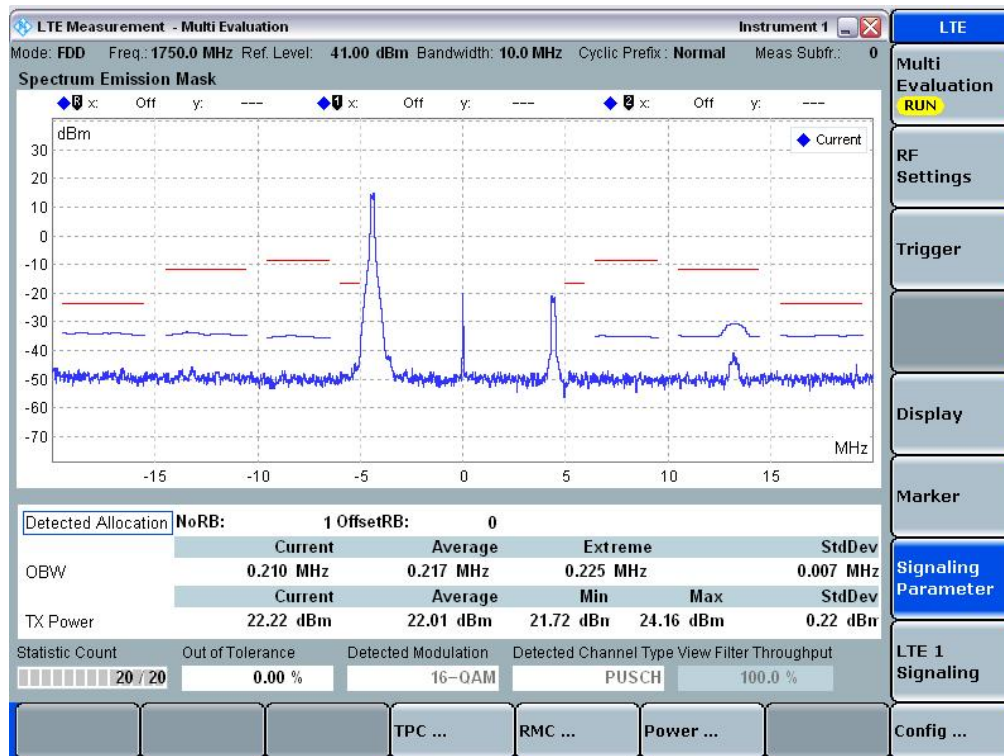
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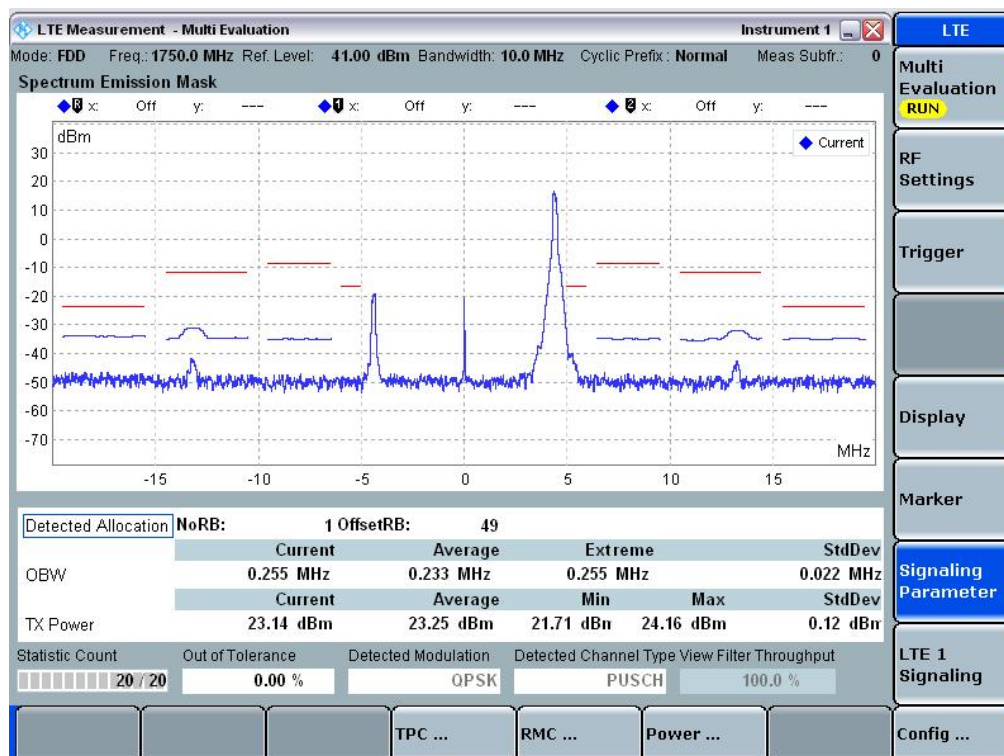
B4 10MHz 20350CH RB1, Offset0, QPSK Plot



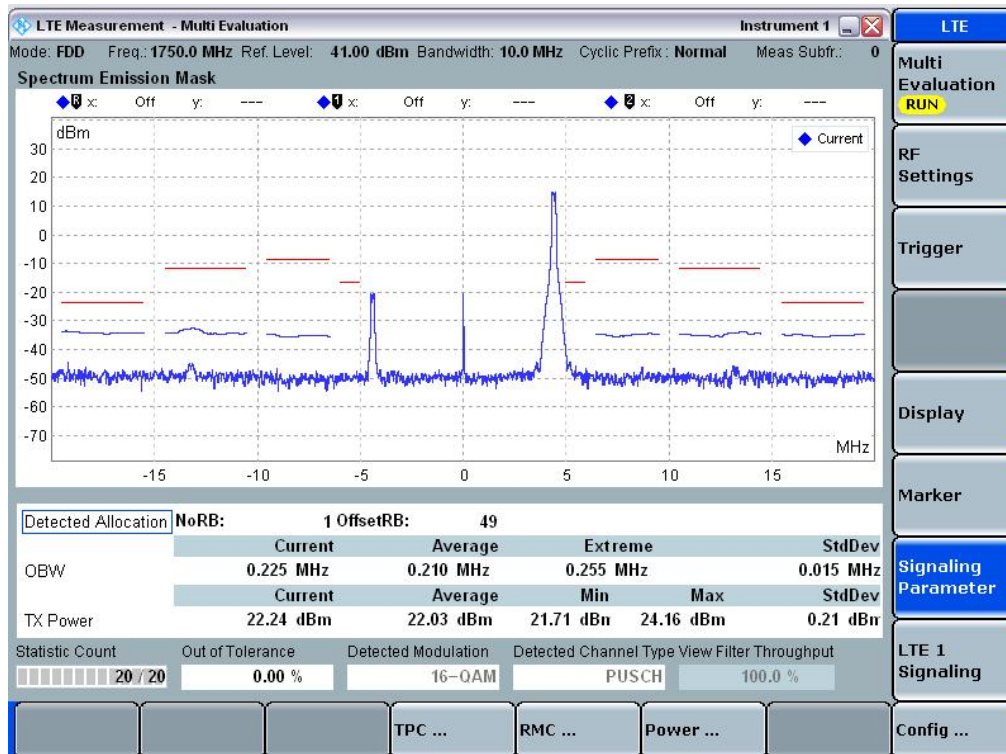
B4 10MHz 20350CH RB1, Offset0, 16QAM Plot



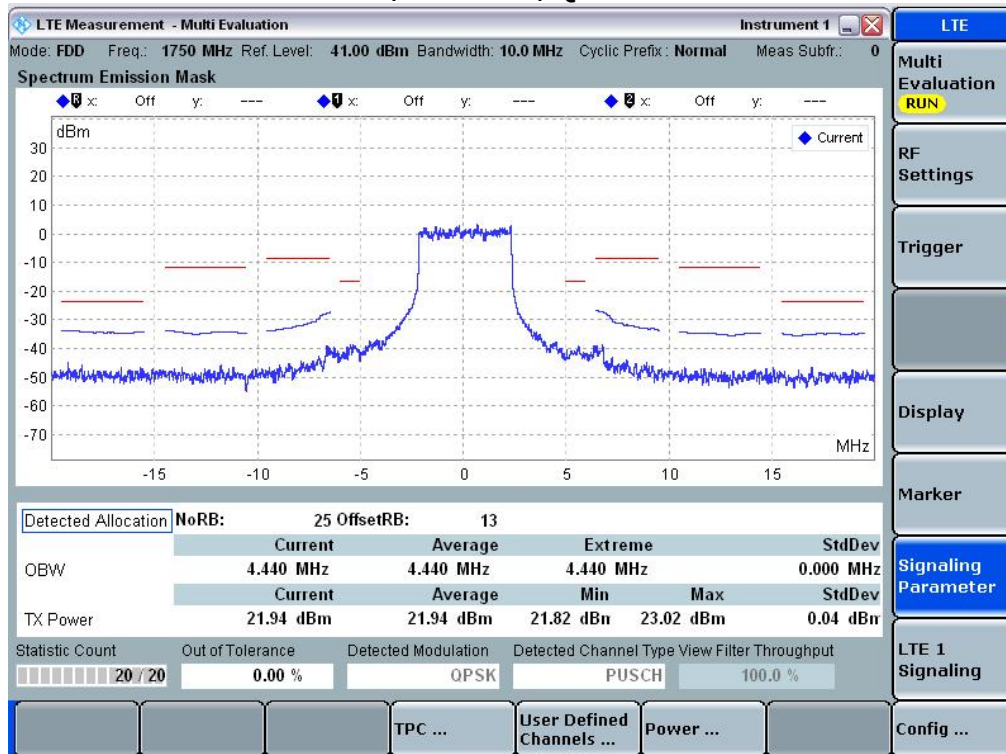
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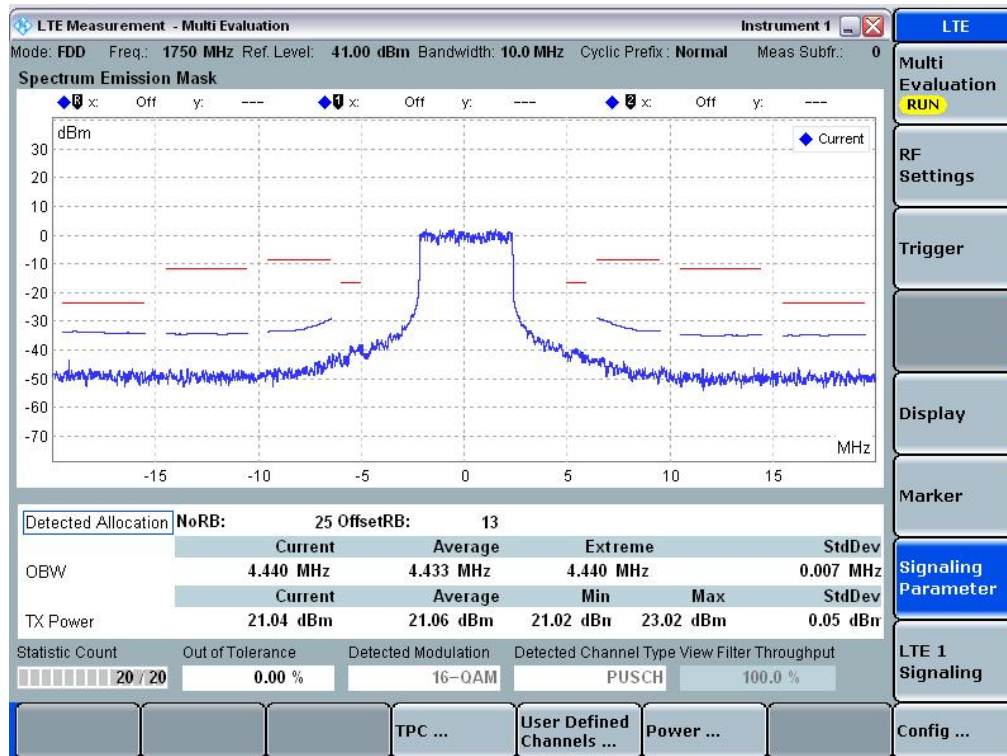
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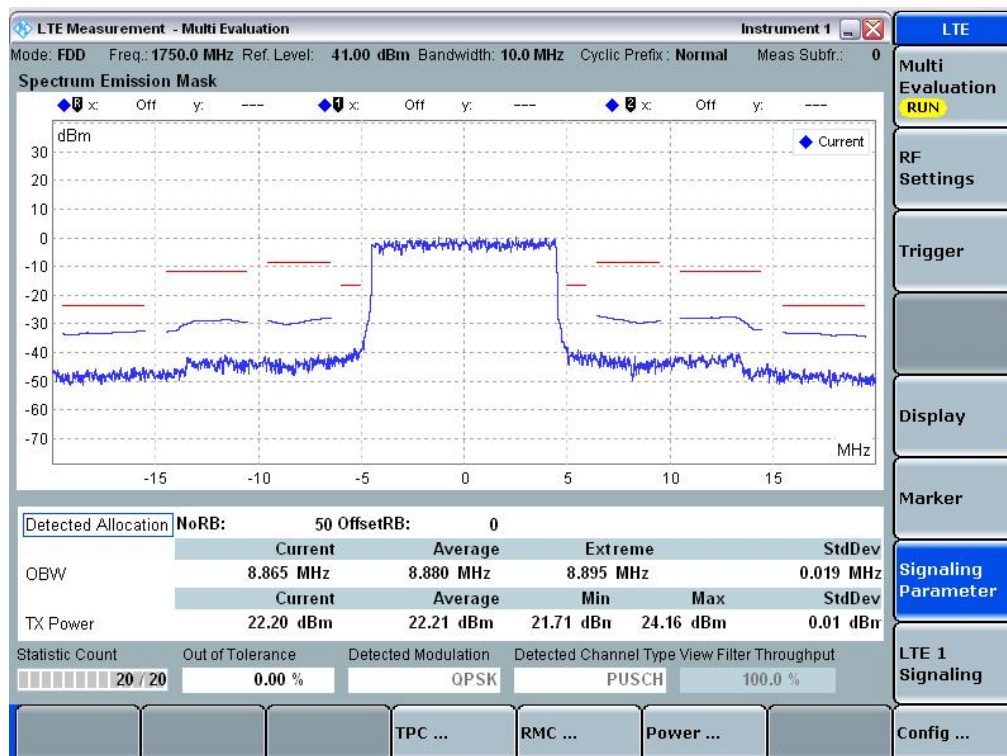
B4 10MHz 20350CH RB25, Offset13, QPSK Plot



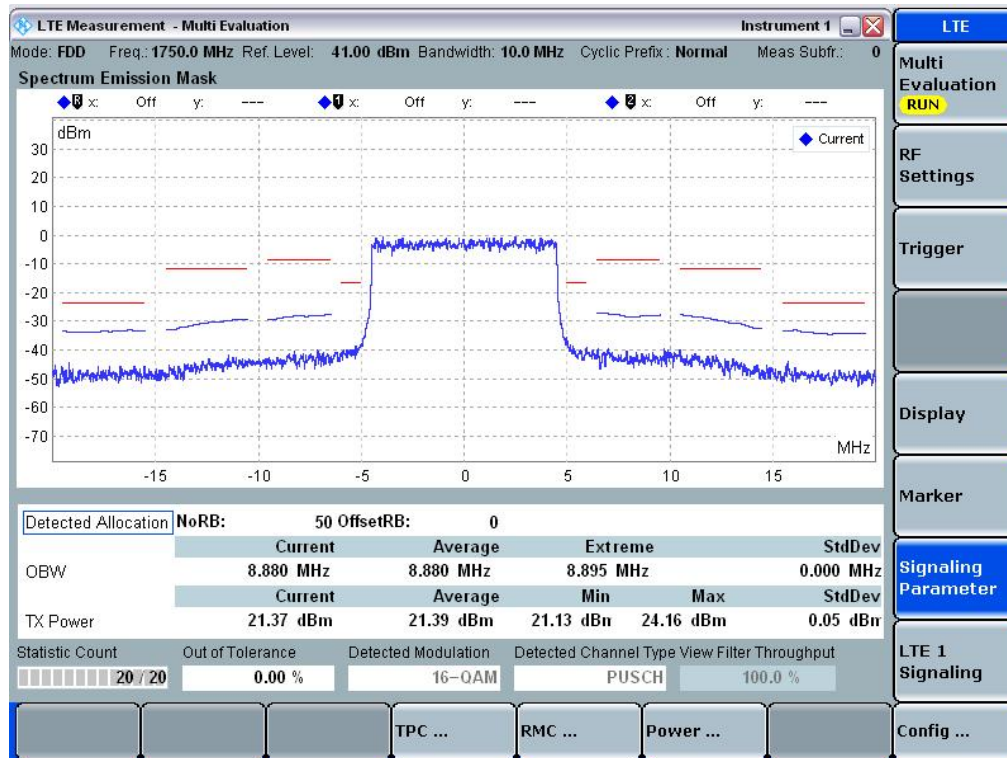
B4 10MHz 20350CH RB25, Offset13, 16QAM Plot



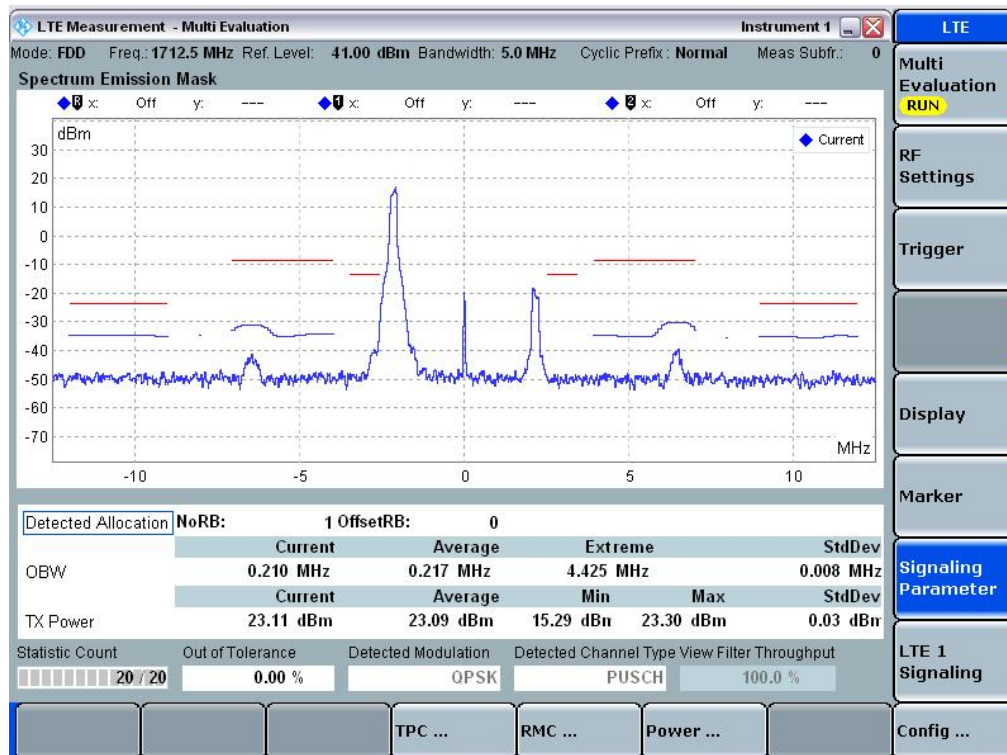
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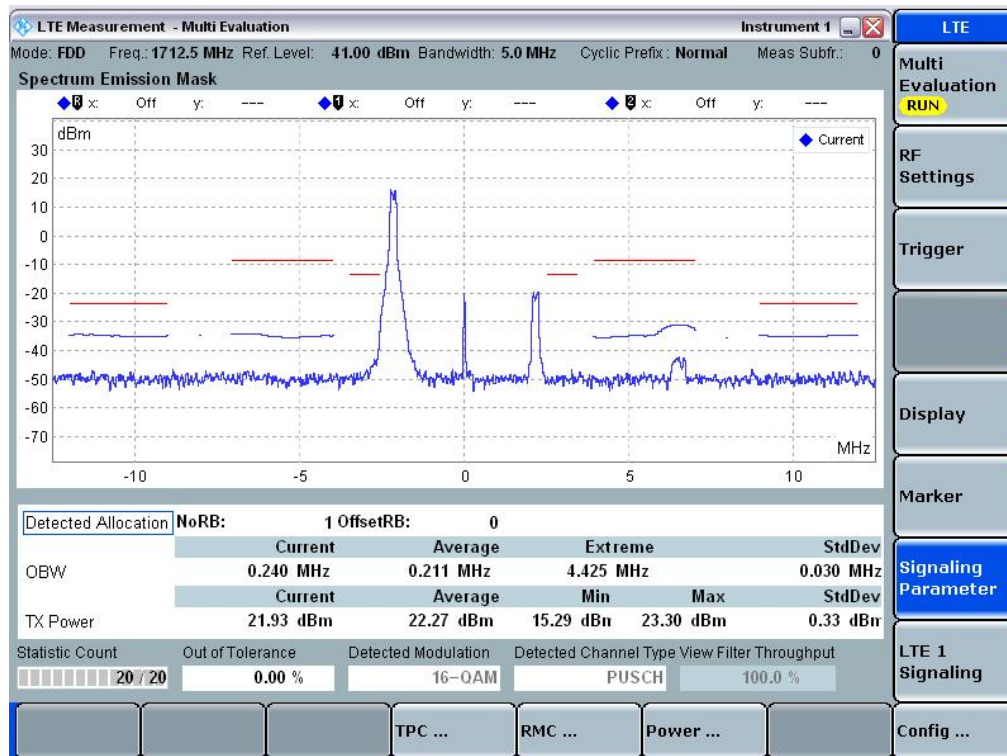
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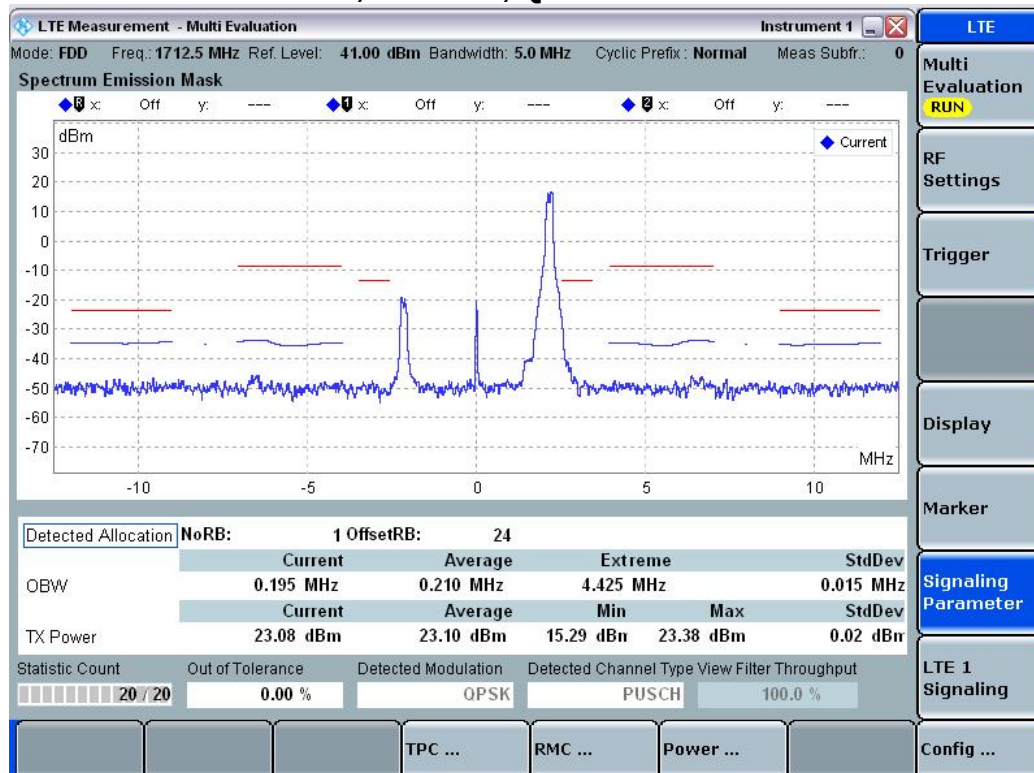
B4 5MHz 19975CH RB1, Offset0, QPSK Plot



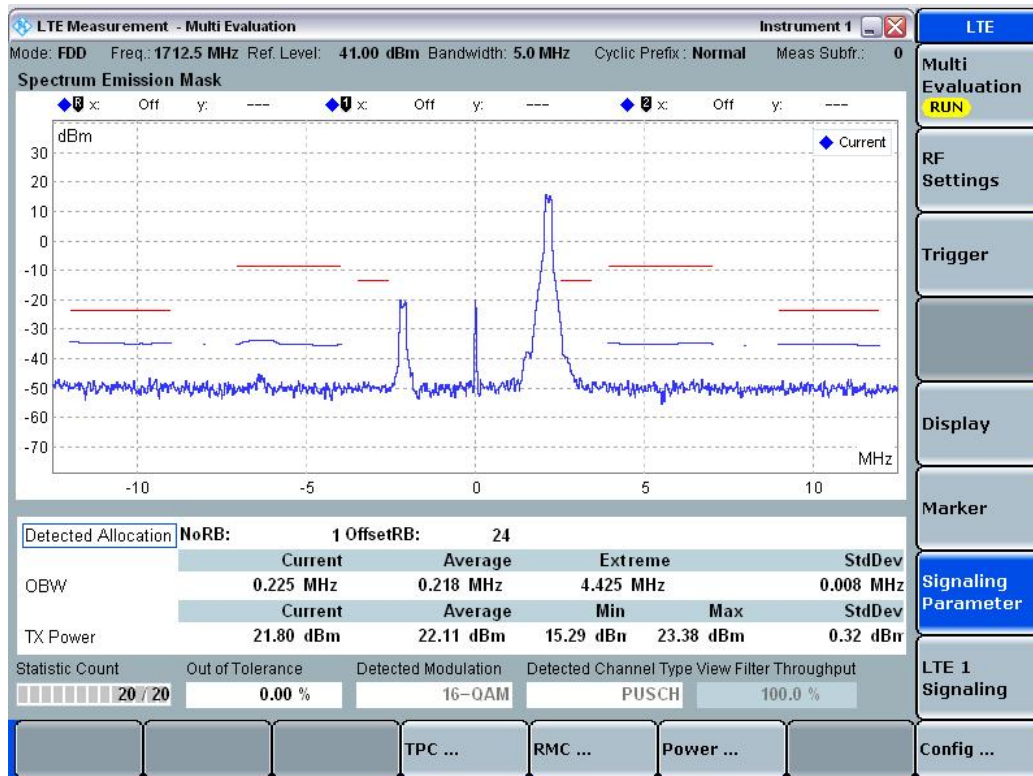
B4 5MHz 19975CH RB1, Offset0, 16QAM Plot



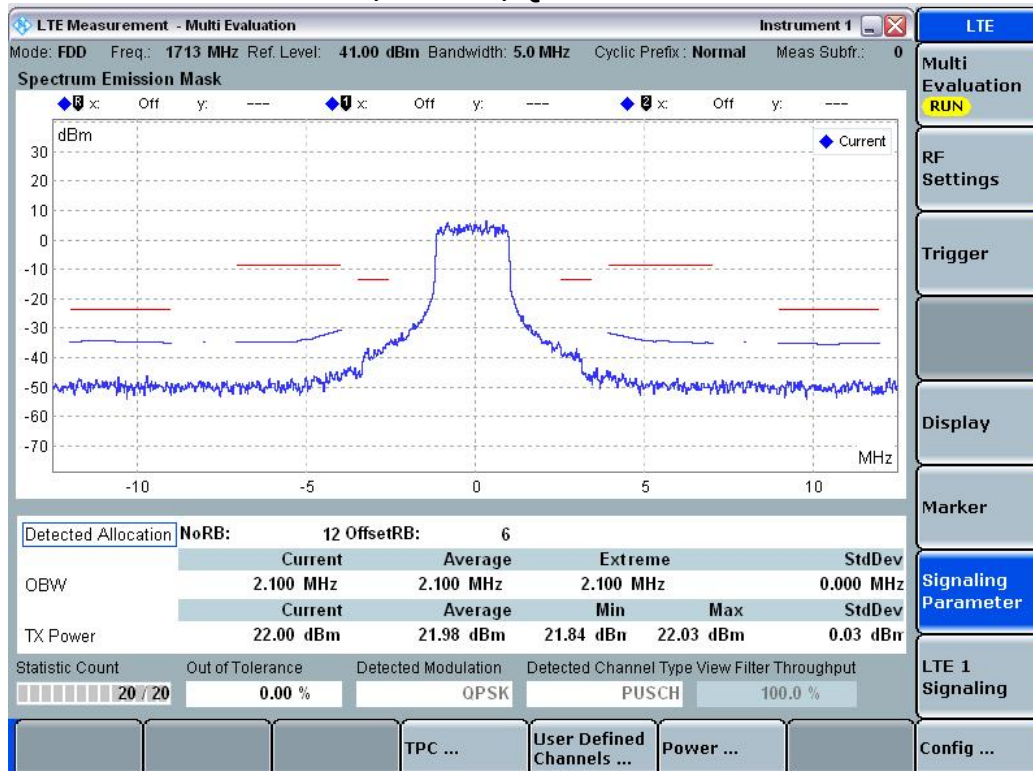
B4 5MHz 19975CH RB1, Offset24, QPSK Plot



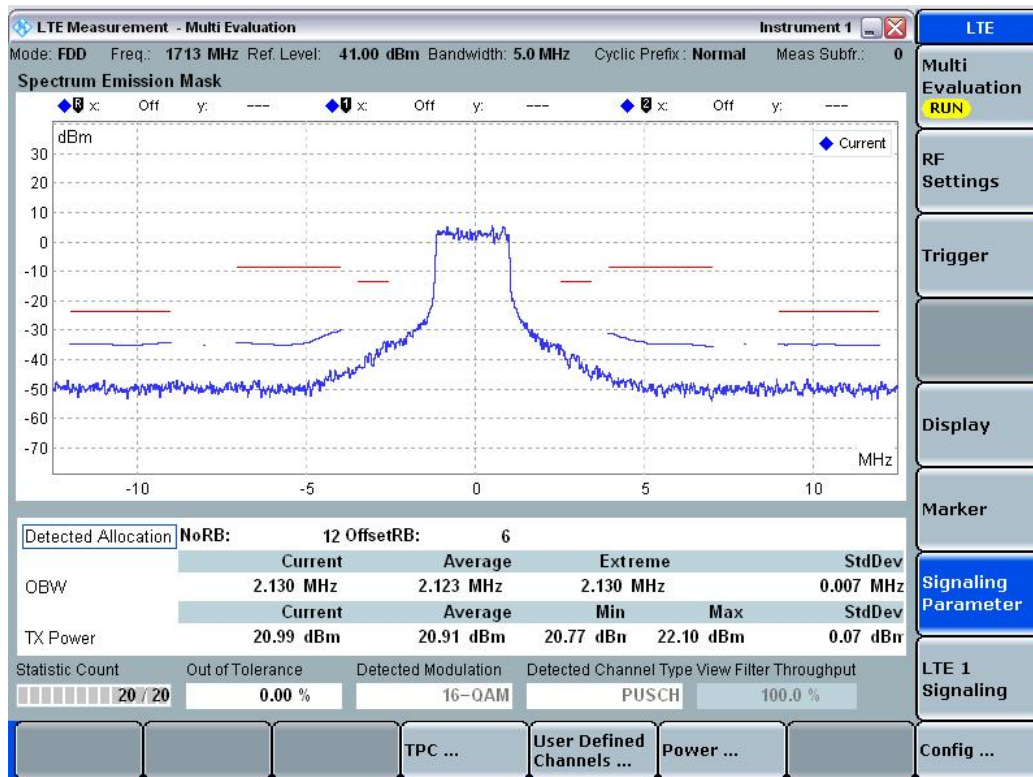
B4 5MHz 19975CH RB1, Offset24, 16QAM Plot



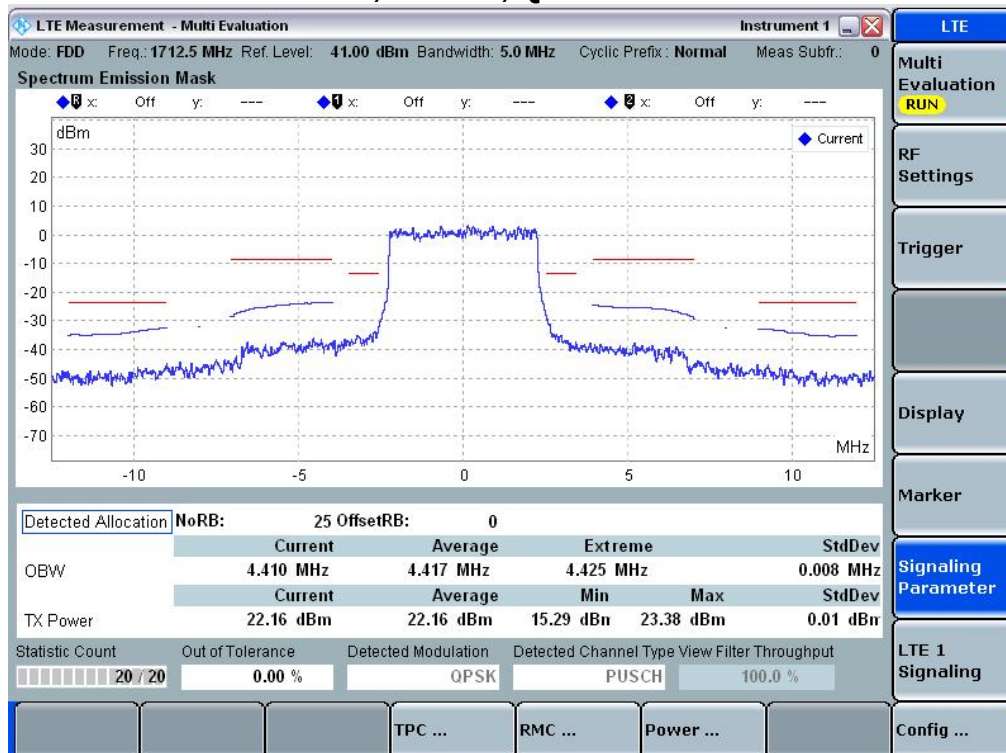
B4 5MHz 19975CH RB12, Offset6, QPSK Plot



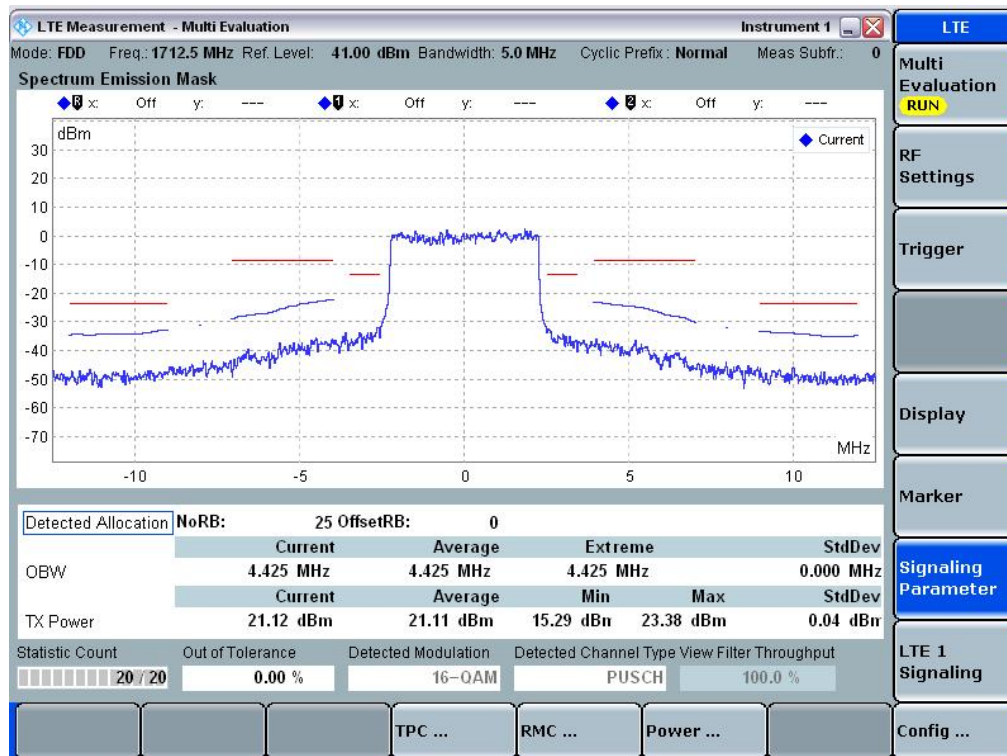
B4 5MHz 19975CH RB12, Offset6, 16QAM Plot



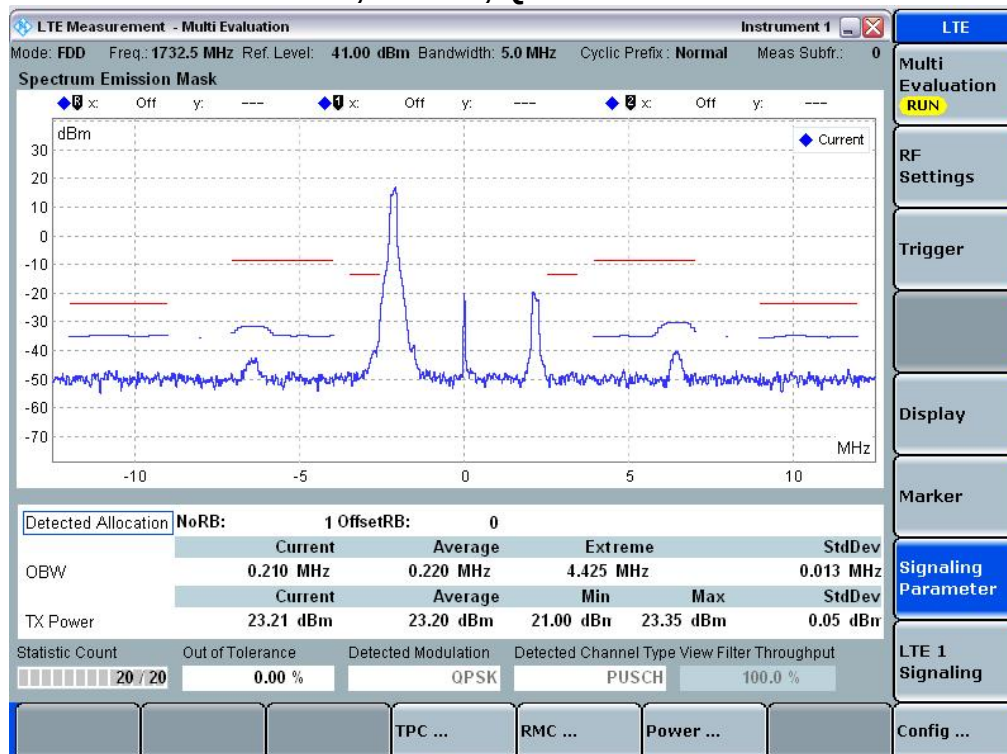
B4 5MHz 19975CH RB25, Offset0, QPSK Plot



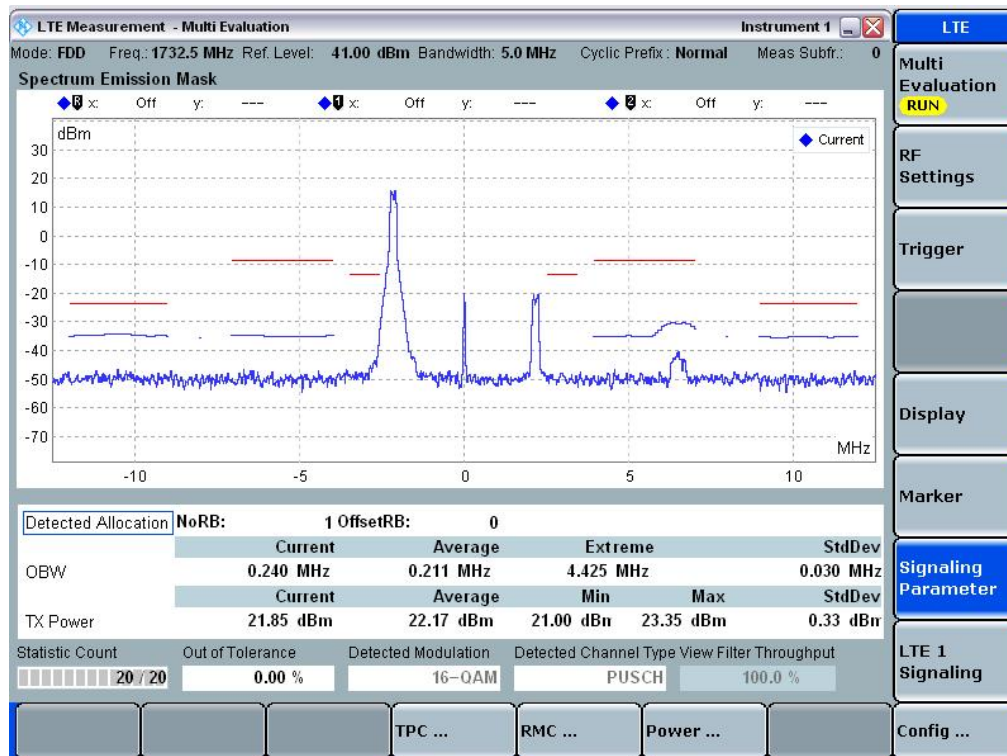
B4 5MHz 19975CH RB25, Offset0, 16QAM Plot



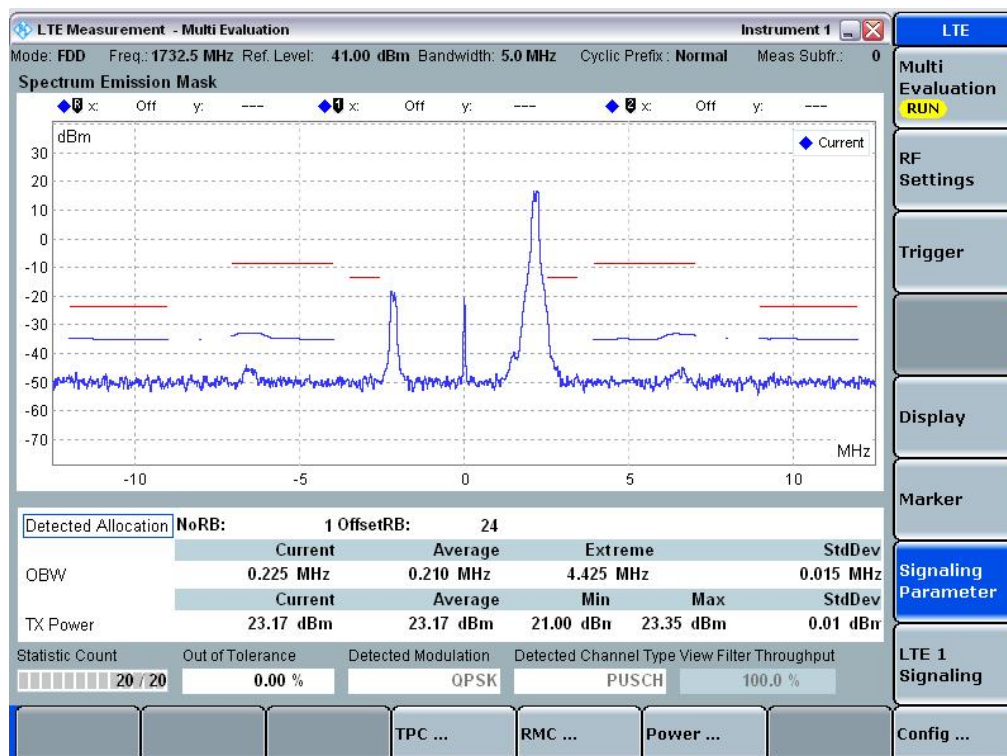
B4 5MHz 20175CH RB1, Offset0, QPSK Plot



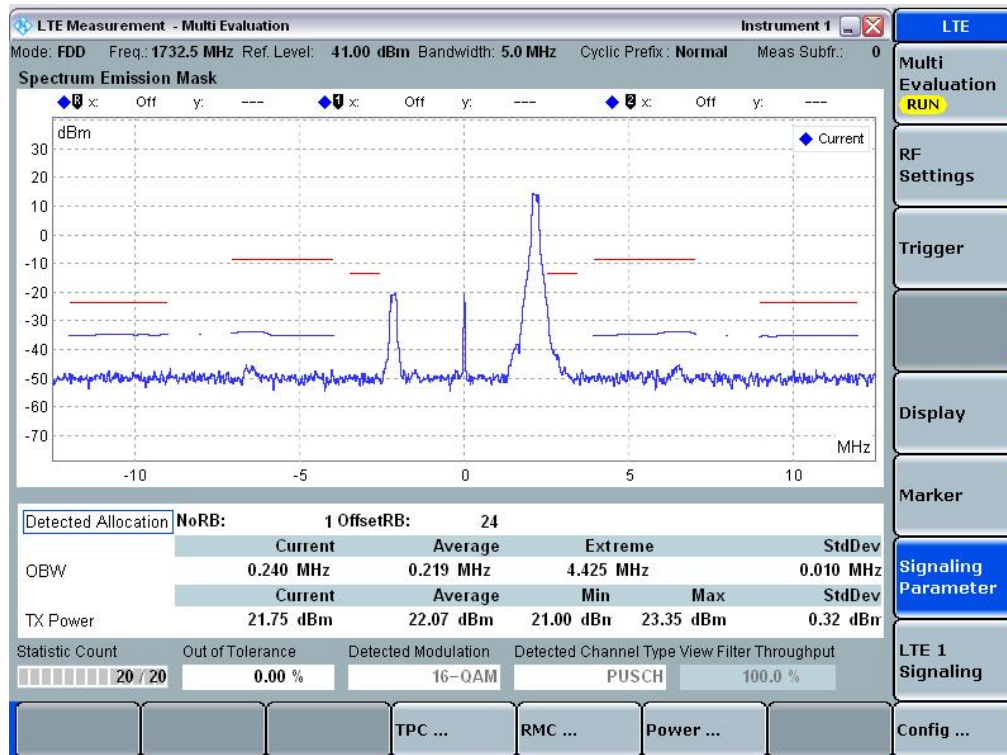
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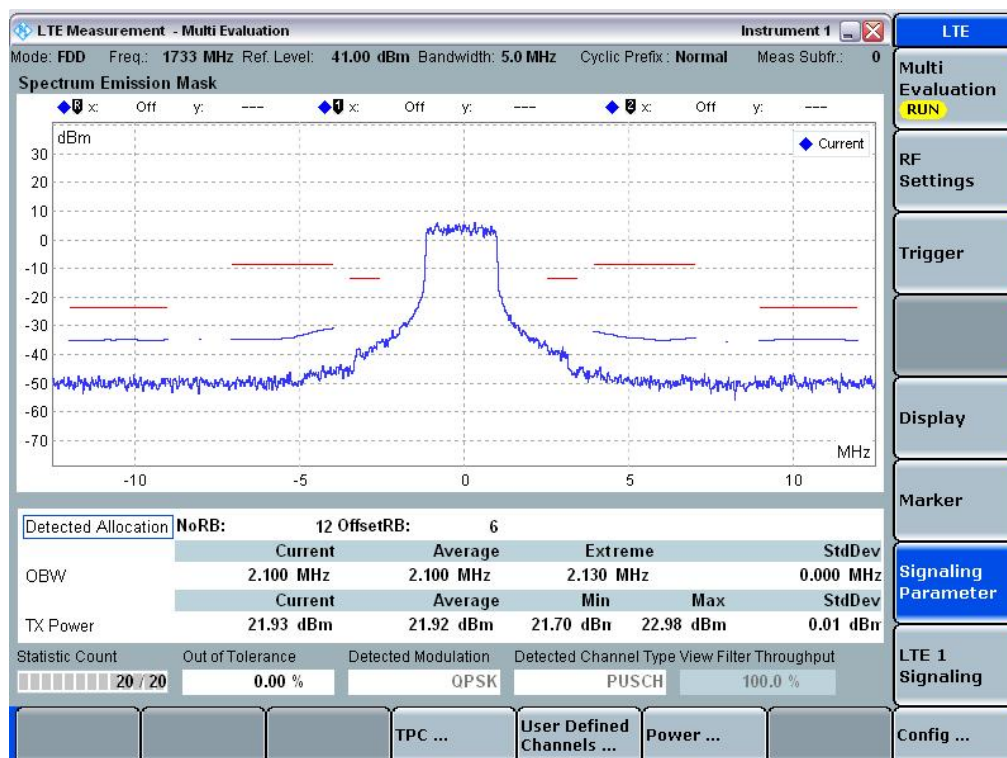
B4 5MHz 20175CH RB1, Offset24, QPSK Plot



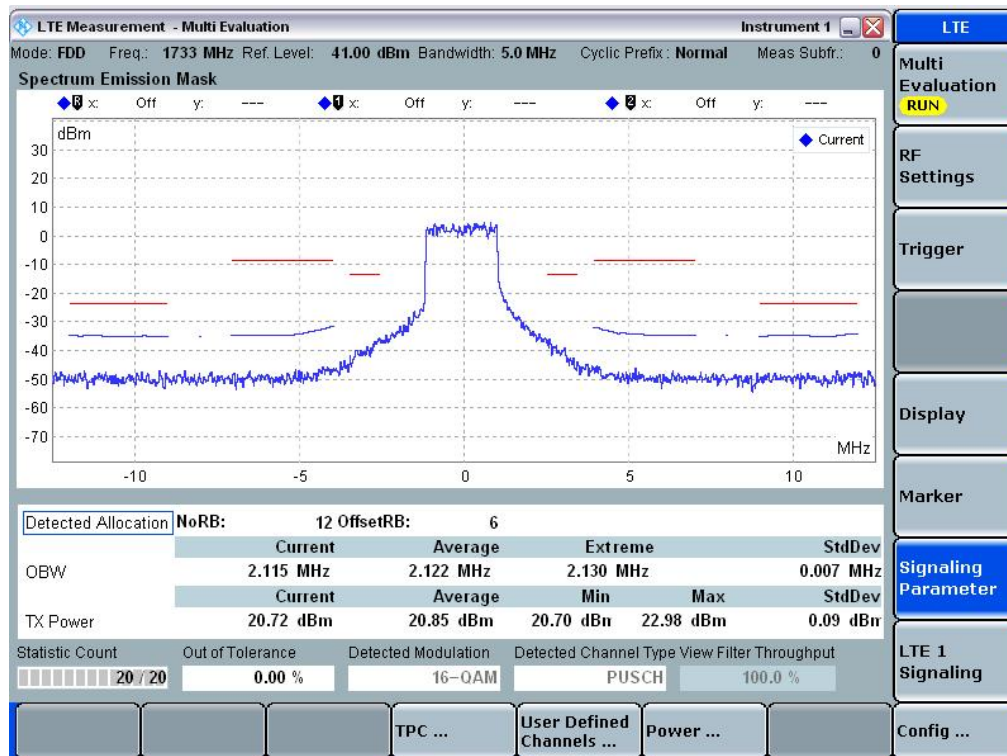
B4 5MHz 20175CH RB1, Offset24, 16QAM Plot



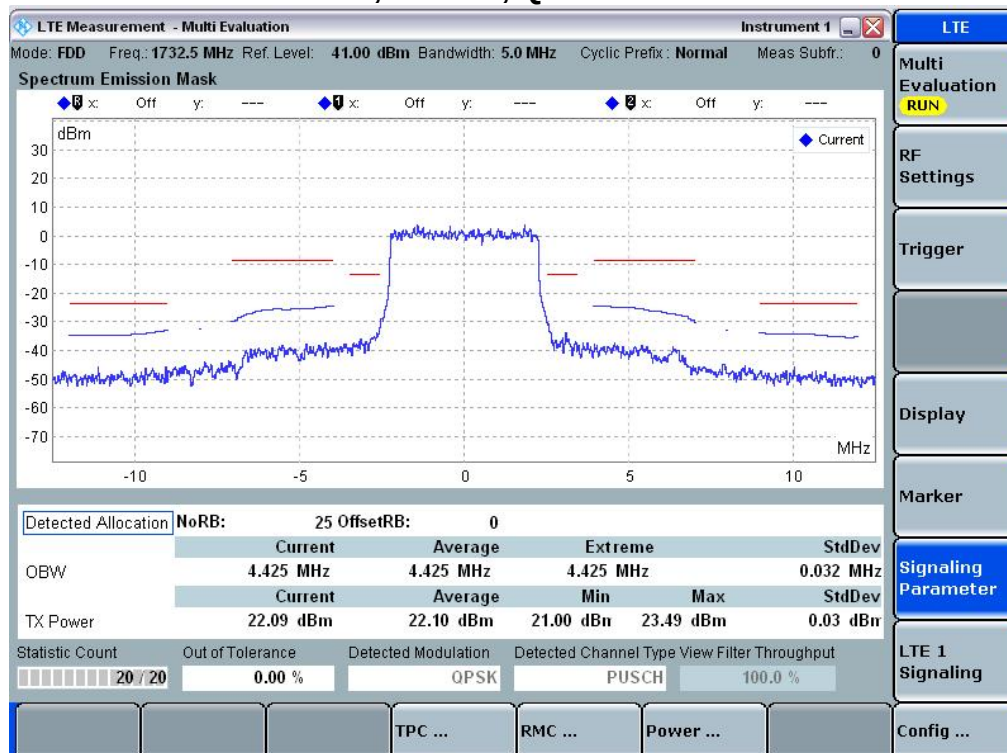
B4 5MHz 20175CH RB12, Offset6, QPSK Plot



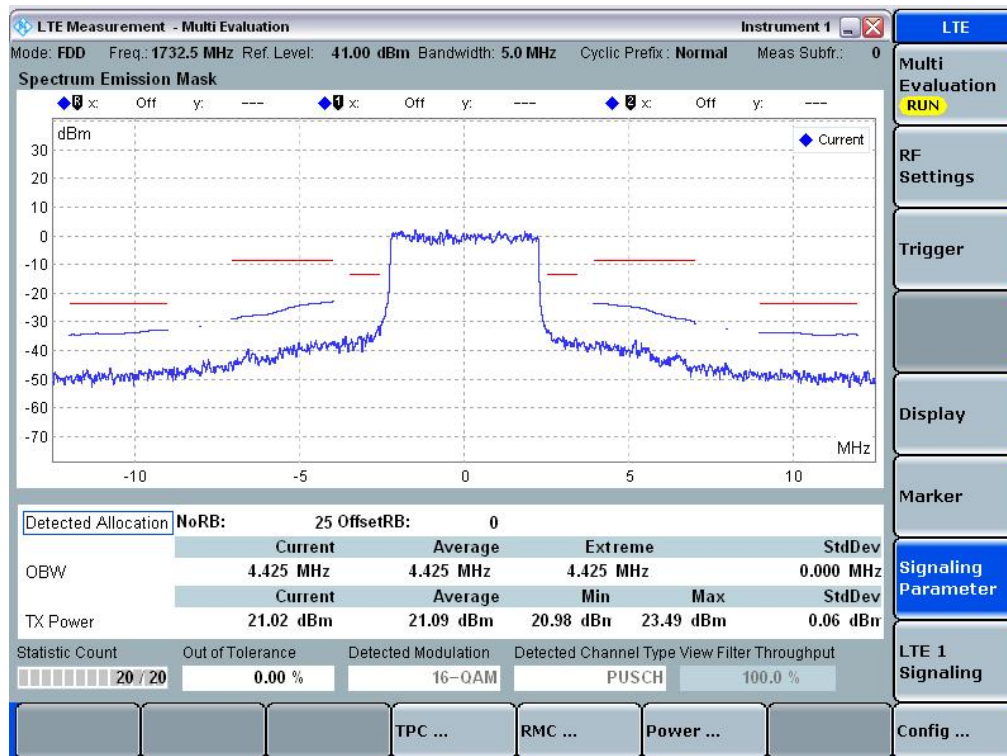
B4 5MHz 20175CH RB12, Offset6, 16QAM Plot



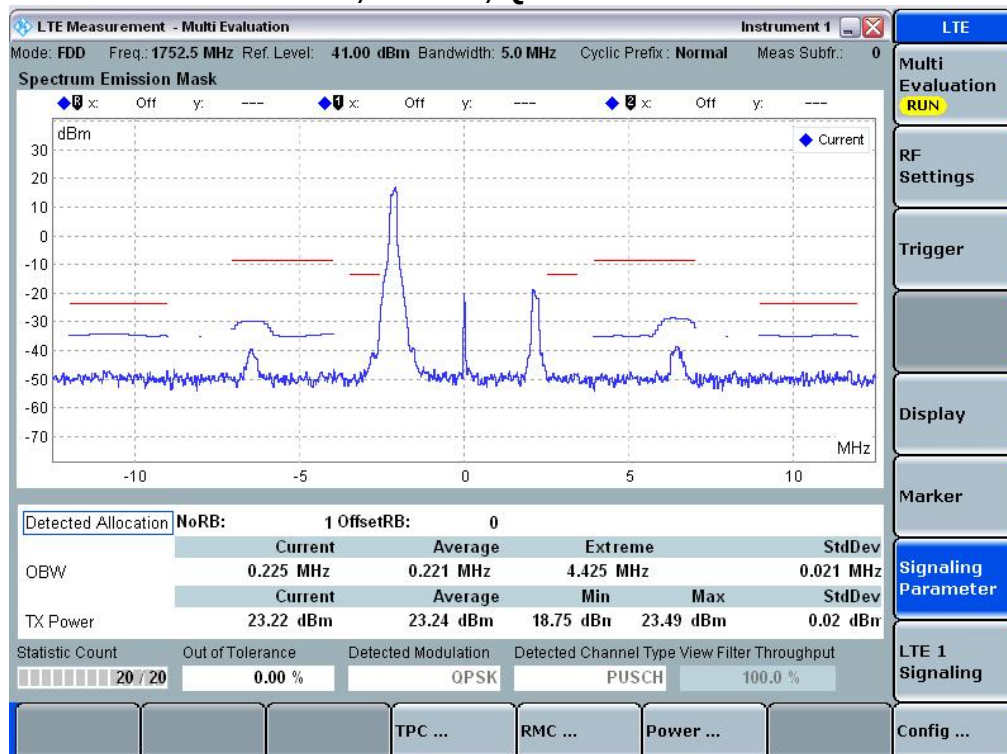
B4 5MHz 20175CH RB25, Offset0, QPSK Plot



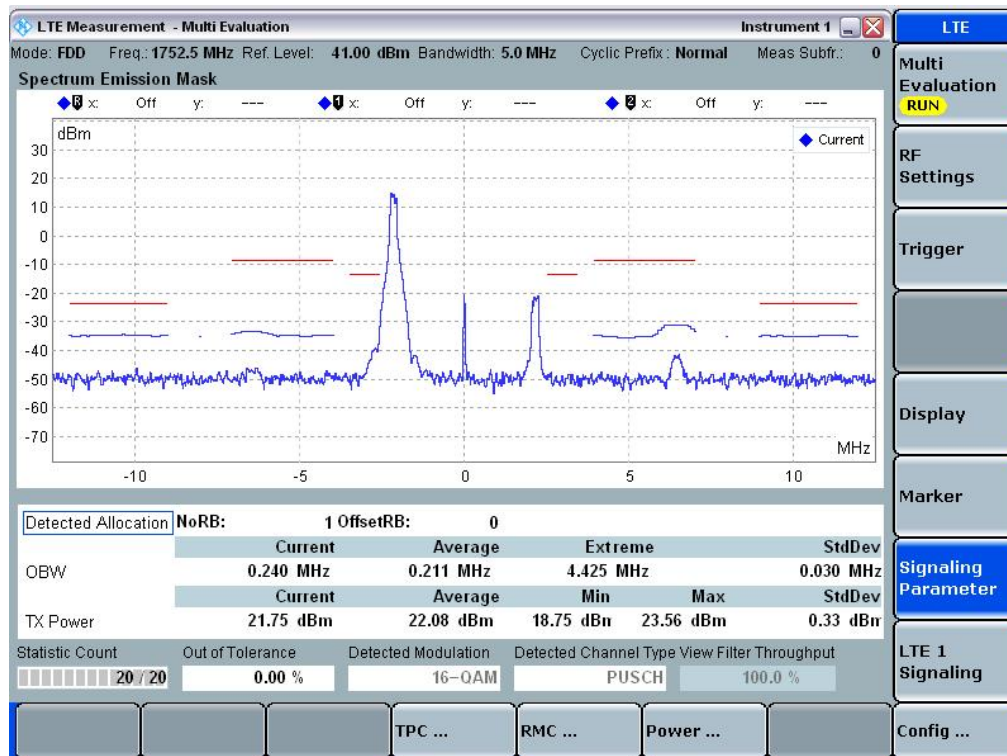
B4 5MHz 20175CH RB25, Offset0, 16QAM Plot



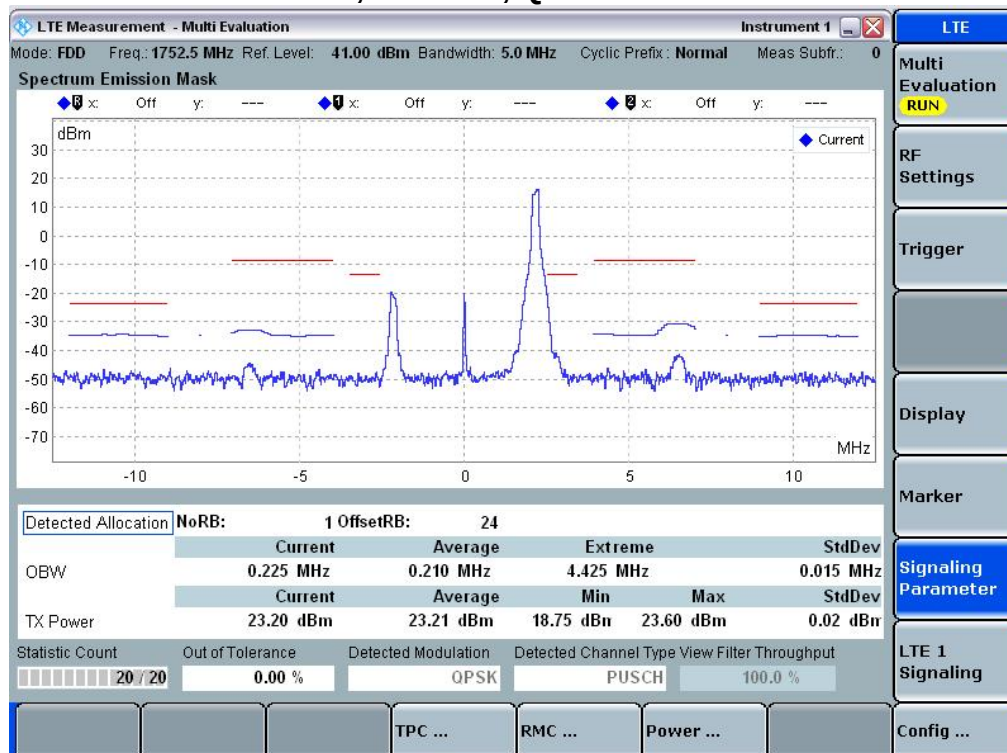
B4 5MHz 20375CH RB1, Offset0, QPSK Plot



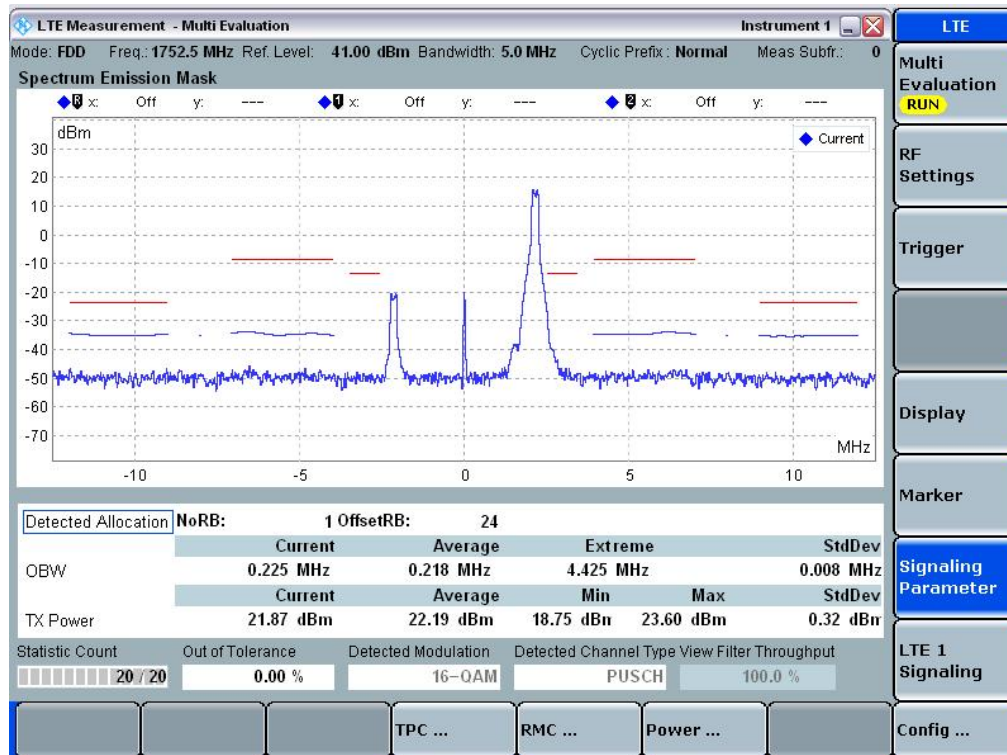
B4 5MHz 20375CH RB1, Offset0, 16QAM Plot



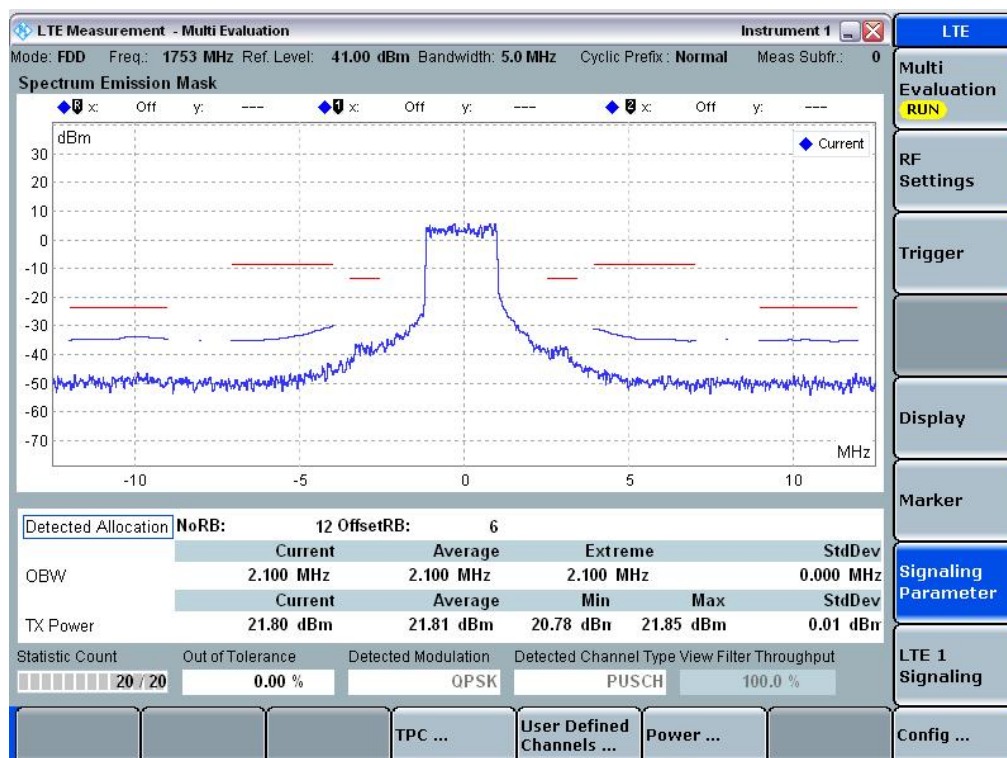
B4 5MHz 20375CH RB1, Offset24, QPSK Plot



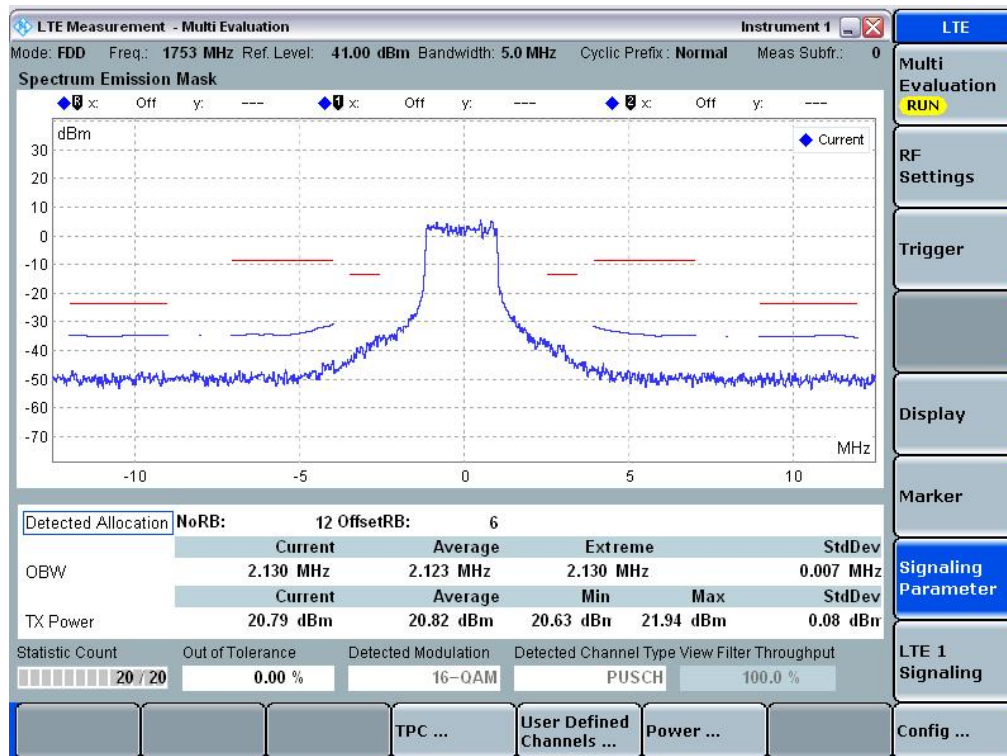
B4 5MHz 20375CH RB1, Offset24, 16QAM Plot



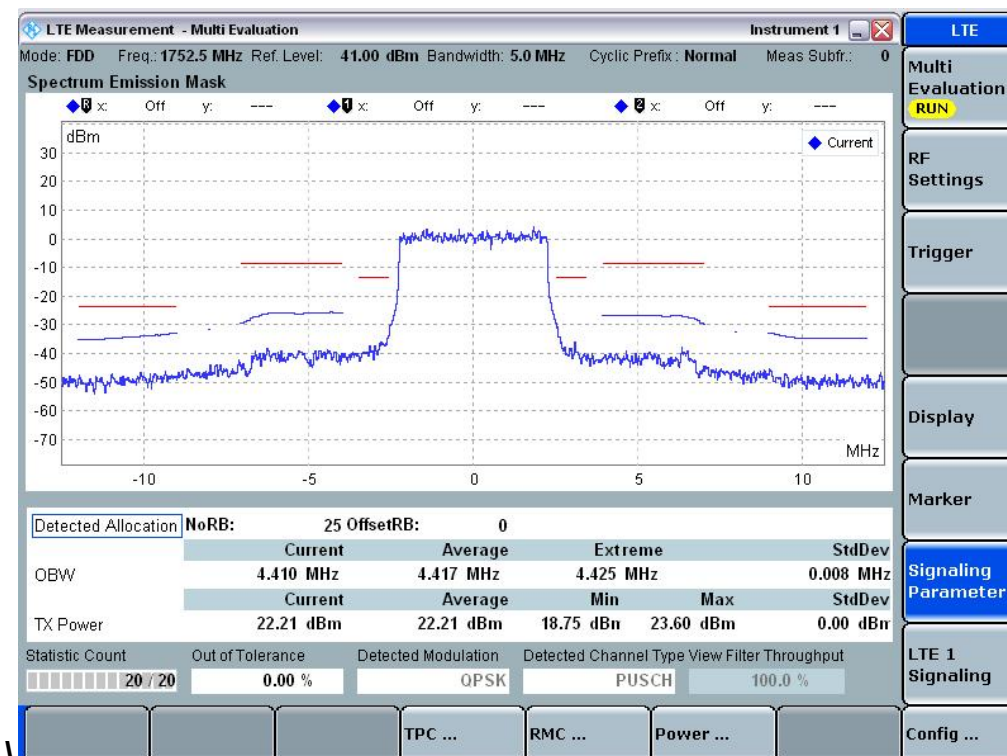
B4 5MHz 20375CH RB12, Offset6, QPSK Plot



B4 5MHz 20375CH RB12, Offset6, 16QAM Plot



B4 5MHz 20375CH RB25, Offset0, QPSK Plot



B4 5MHz 20375CH RB25, Offset0, 16QAM Plot

