



Hitron SMCD3GN3

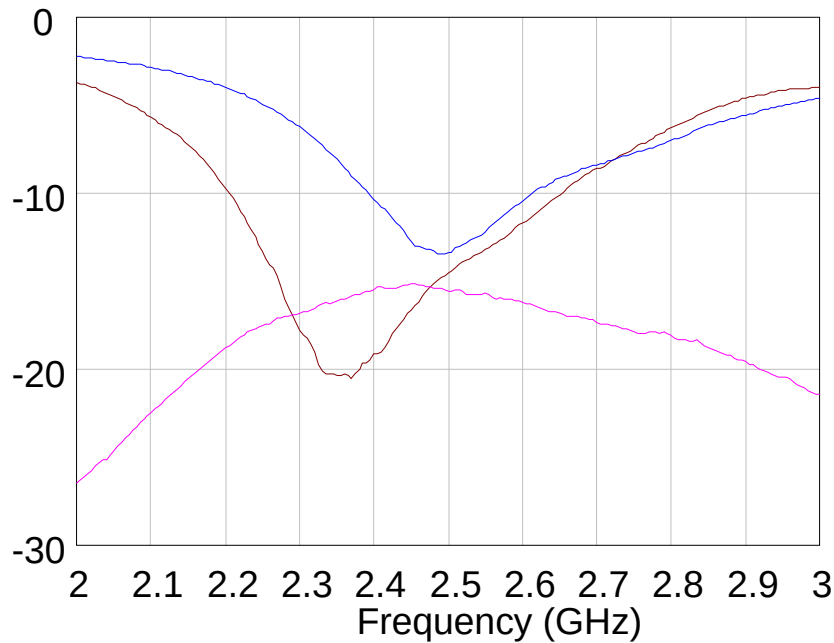
RTL8198+RTL8192

Ben Wang
March, 2011

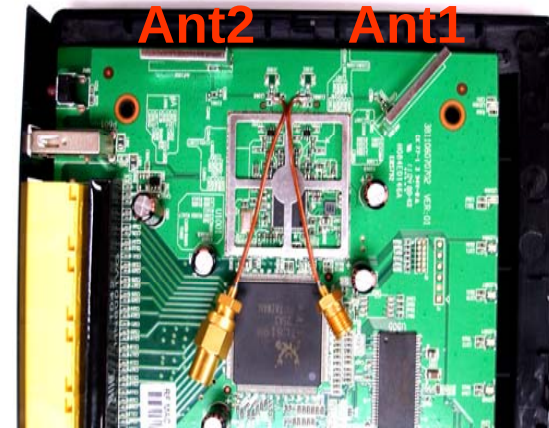




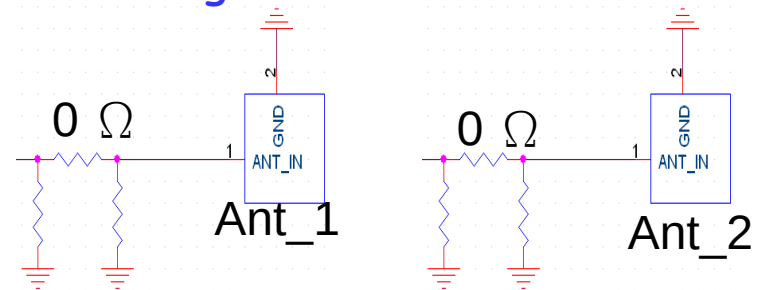
■ S-parameters — S11 — S22 — S12



■ Photo



■ Matching circuit





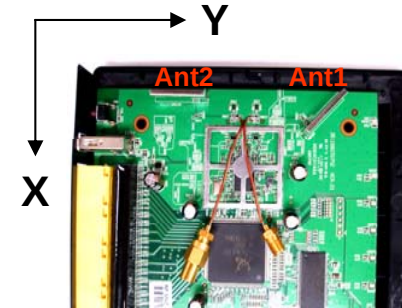
■ Test environment

Operating Freq. : 2450 MHz

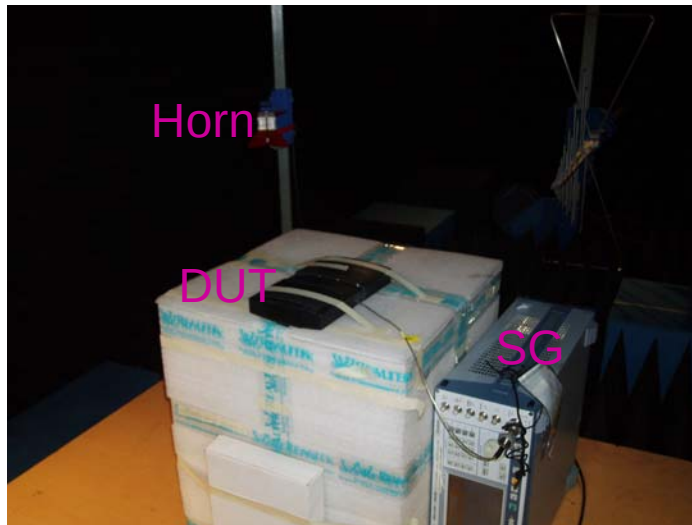
Test plane : XY-plane, YZ-plane

Input power : SG $P_{in} = 0$ dBm

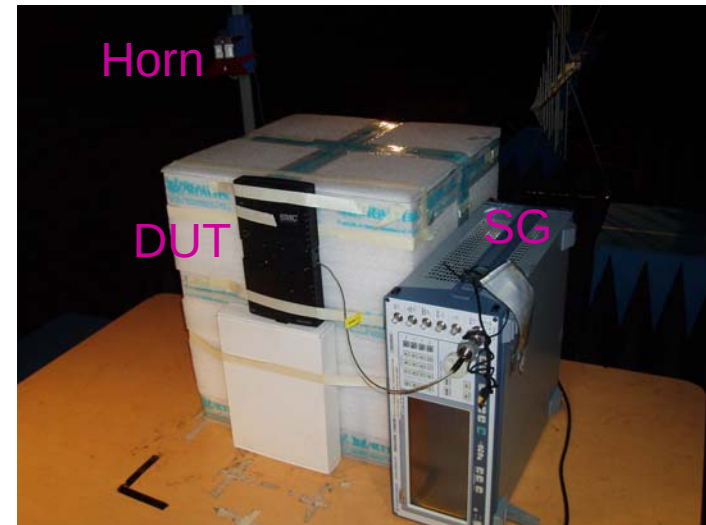
Place: RTK 4F chamber



■ XY-plane Test



■ YZ-plane Test





■ Measured Patterns @ 2.45GHz — E-phi — E-theta — E-total

