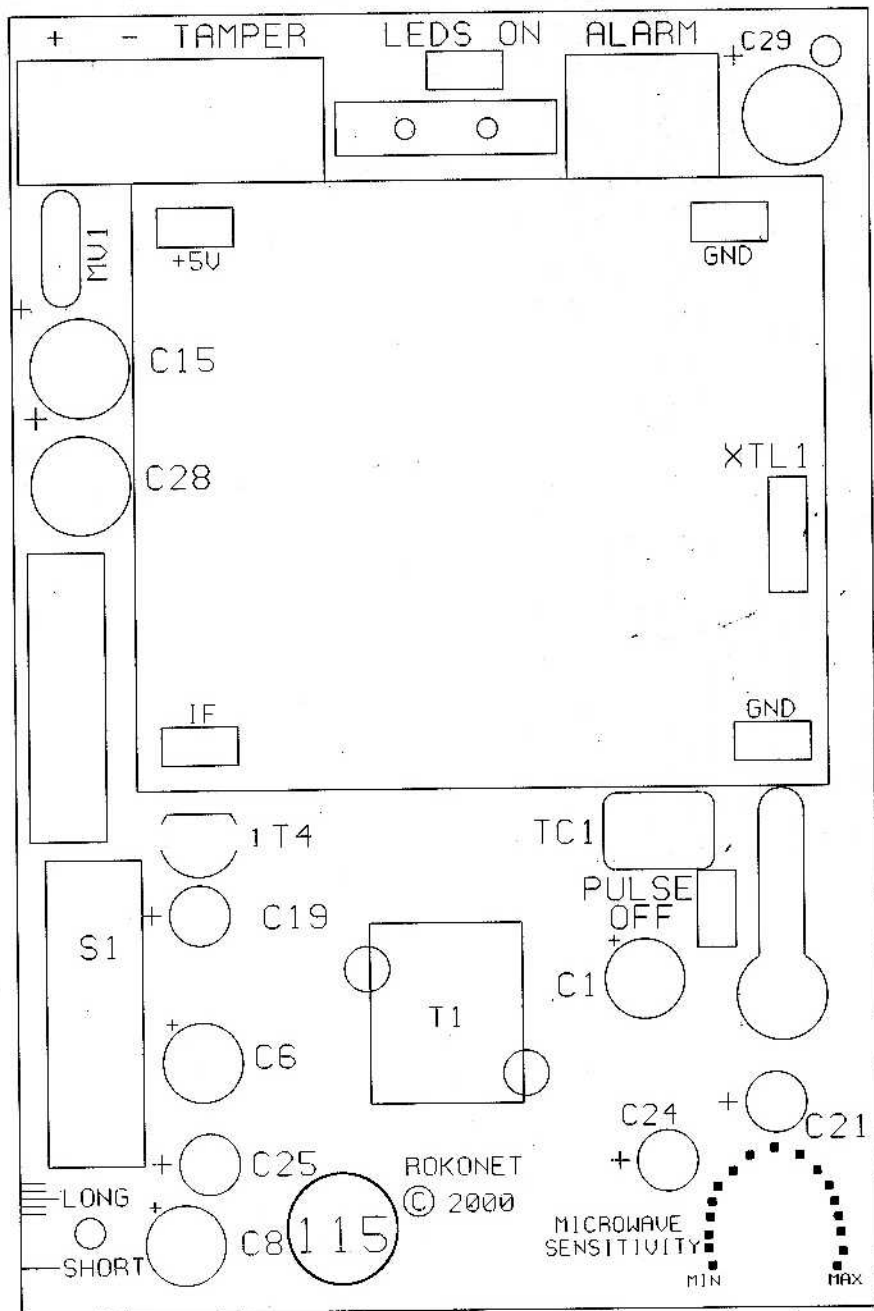
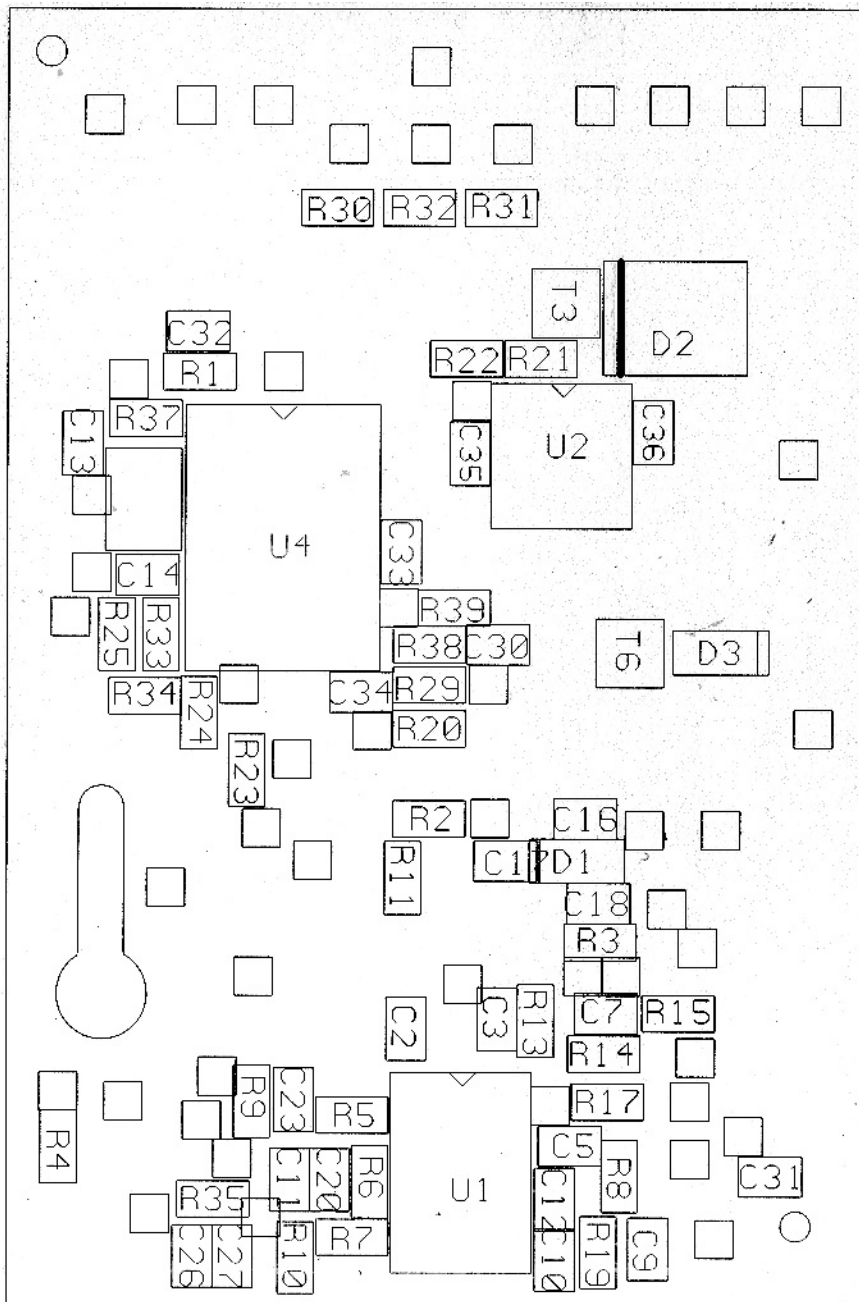


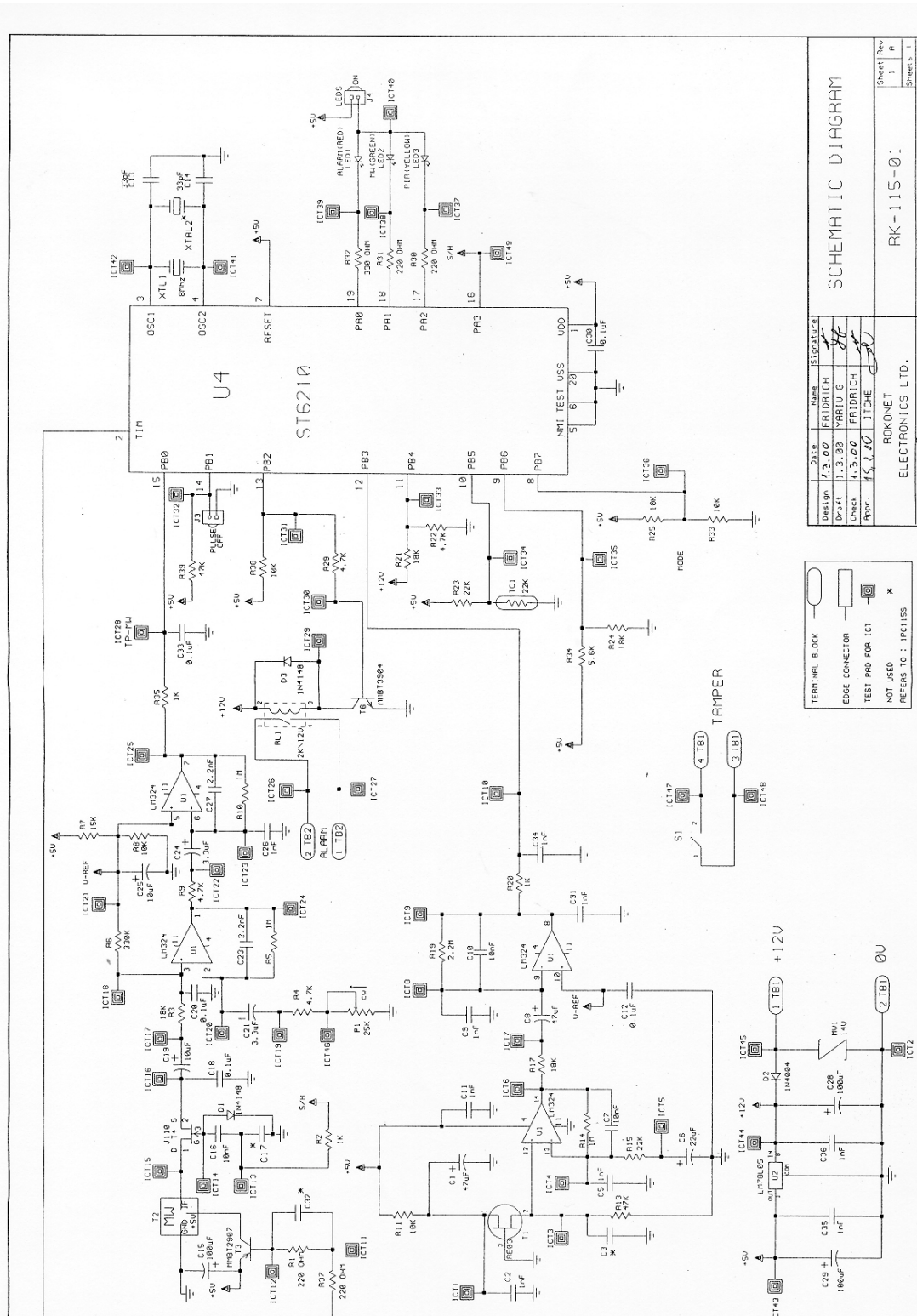
PCB – Component side:



PCB – Print side



PCB – Schematic diagram



Date	Name	Signature
1.3.00	FRIDRICH	<i>[Signature]</i>
1.3.00	VARLJU G	<i>[Signature]</i>
1.3.00	FRIDRICH	<i>[Signature]</i>
1.3.00	JITCHE	<i>[Signature]</i>

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Sheet Rev
1 a
1 a
Sheets 1

SCHEMATIC DIAGRAM

TERMINAL BLOCK
EDGE CONNECTOR
TEST PNT FOR ICI
NOT USED
REFERS TO : IPC1155

