FCC ID: JCKGN-AP101B

Operational Description

The NEC Point-M main board is using the IDT RC32332 Microprocessor, which is 32bits, and 133Mhz. About this CPU processor is using 2 * SDRAM interface 2M * 4Banks * 16 bits SDRAM. Totally is about 32M byte. The local bus interface is using 2M byte Flash RAM.
One PCI interface is using Via6105 Rhin fast Ethernet controller to RJ45 LAN port, And the other one PCI interface is using TI1410A, which are high performance PCI to PC card controller. The PCI bus supports Single PC card socket compliant with the wireless LAN card.

The wireless LAN card module is made up of the IEEE 802.11b MAC, Baseband, and radio components, PCMCIA interface, two mmcx connectors for external antenna.
It uses direct sequence spread spectrum (DSSS) technology and implements DBPSK, DQPSK, and CCK modulation. This provides a very robust radio channel and excellent receiver sensitivity.

With its PCMCIA Type-II extended form factor, low power consumption, power management and high-speed wireless data communication, it is ideally suited for integration into mobile and handheld platform.