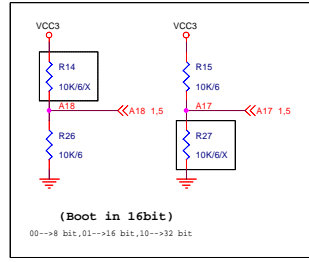
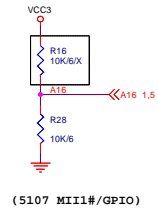


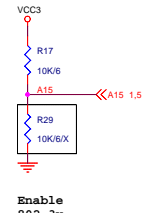
(TEST MODE) (Little Endian)



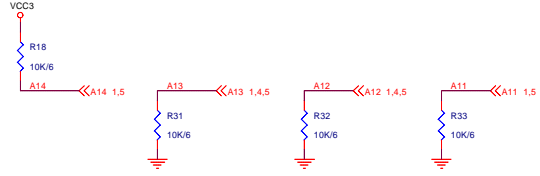
(Boot in 16bit)
00-->8 bit, 01-->16 bit, 10-->32 bit



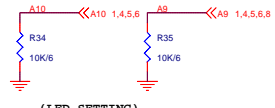
(5107 MII# / GPIO)



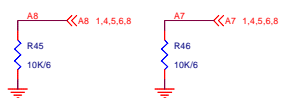
Enable 802.3x flow control



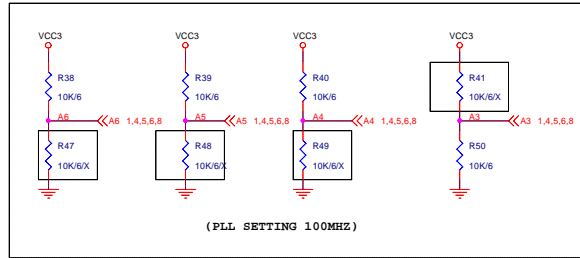
(BP1) (BP0)
All jam back-pressure (SW SETTING)
(COL_ABT) will not abort packet when meet 16 consecutive collisions



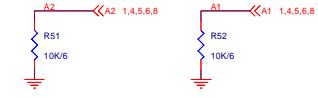
(LED SETTING)



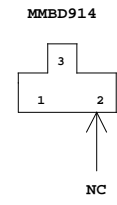
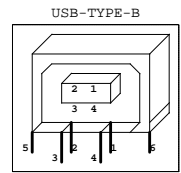
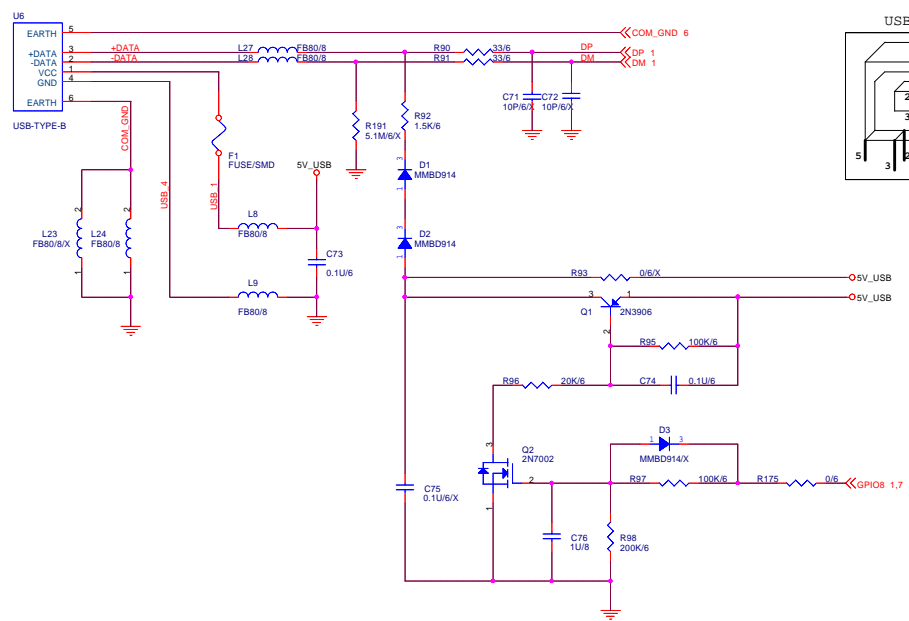
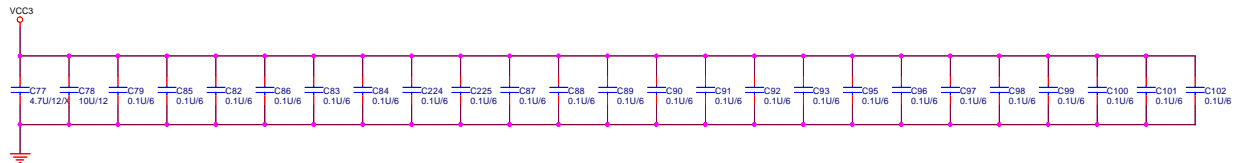
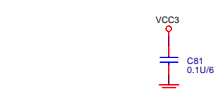
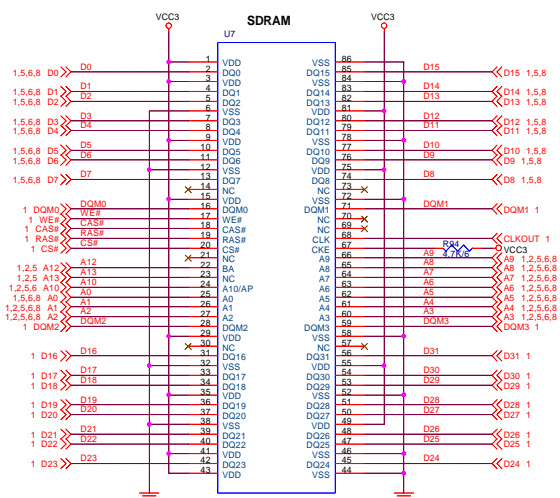
(ARM/USB OBSERVED MODE)



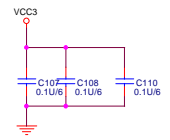
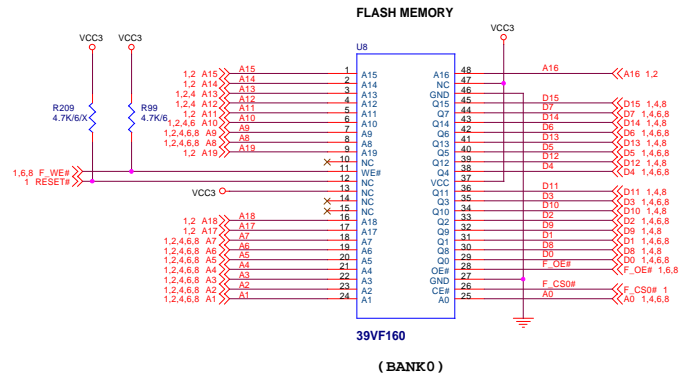
(PLL SETTING 100MHZ)

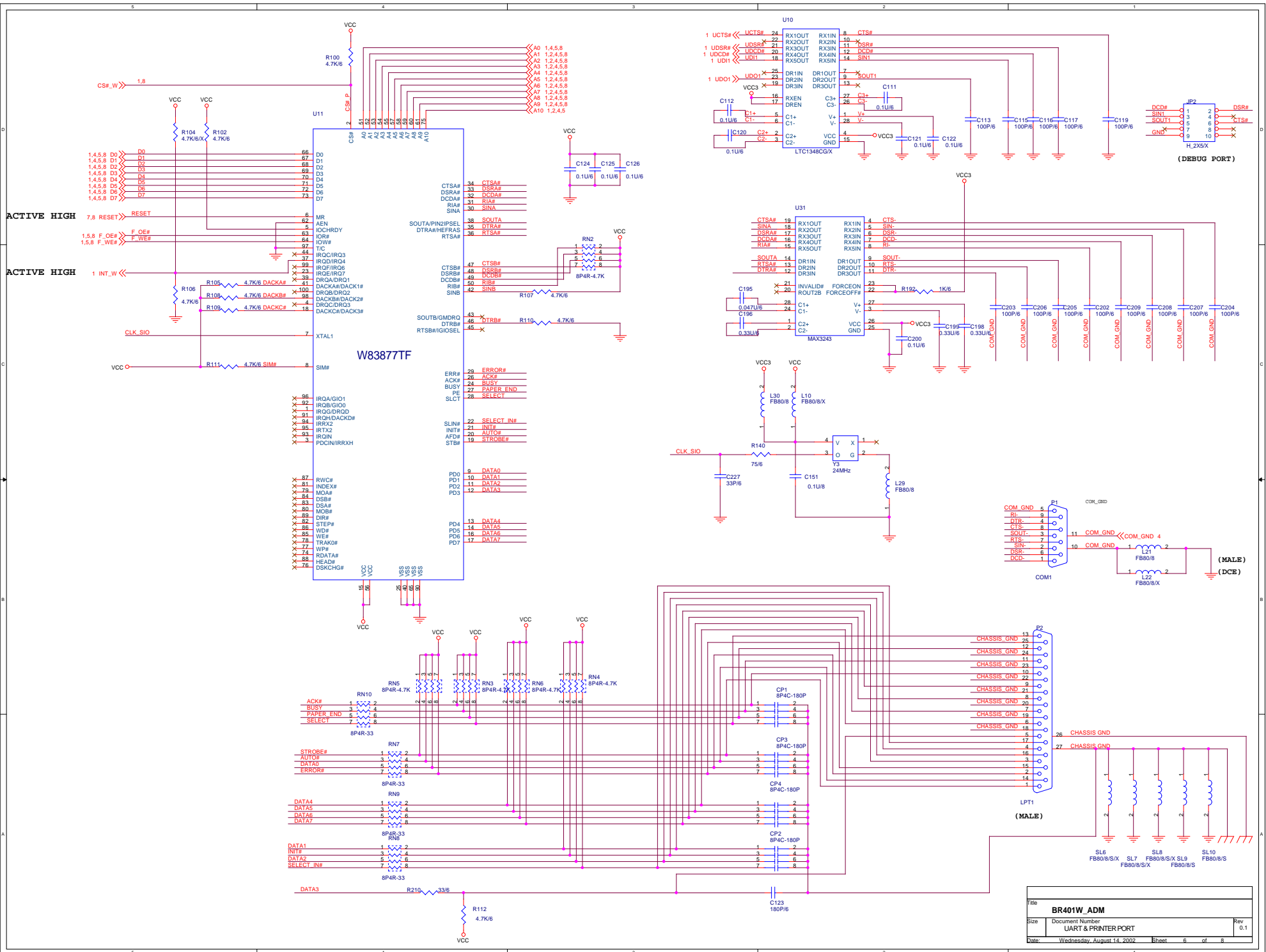


(USB CXVR BYPASS) (SIMULATION MODE)

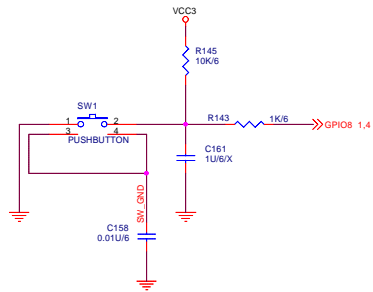


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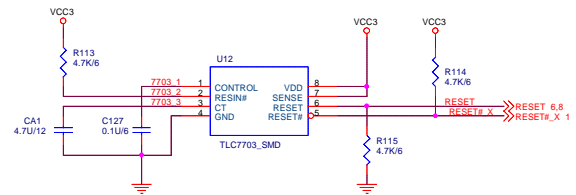




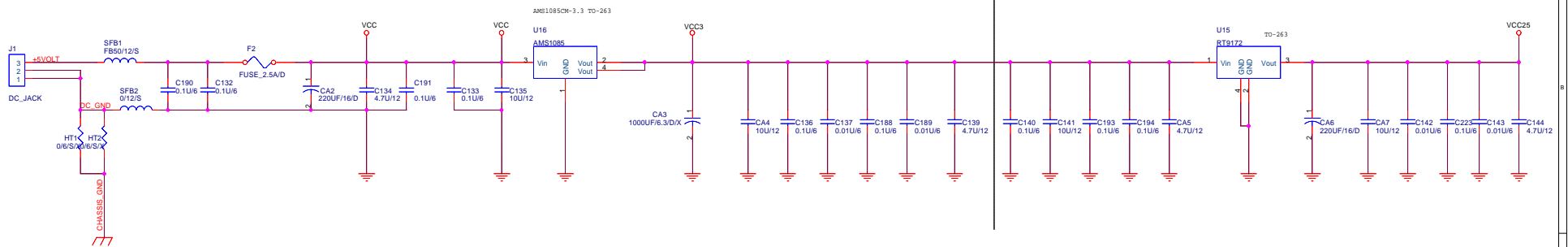
INIT



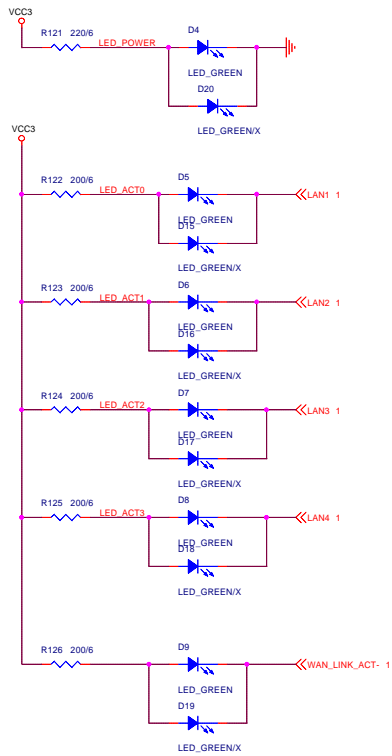
RESET



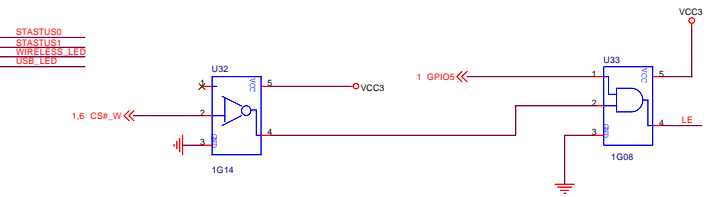
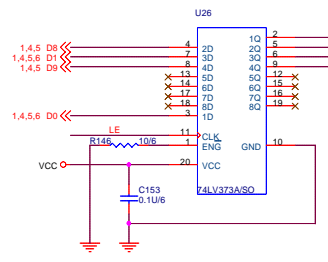
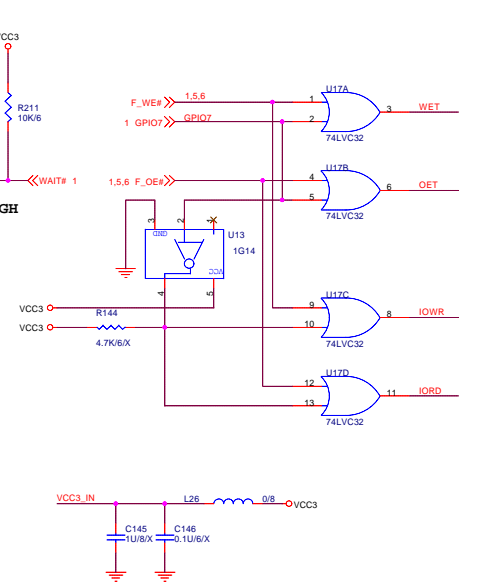
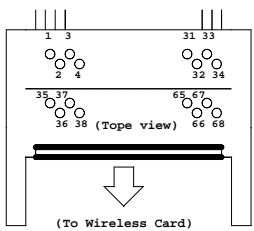
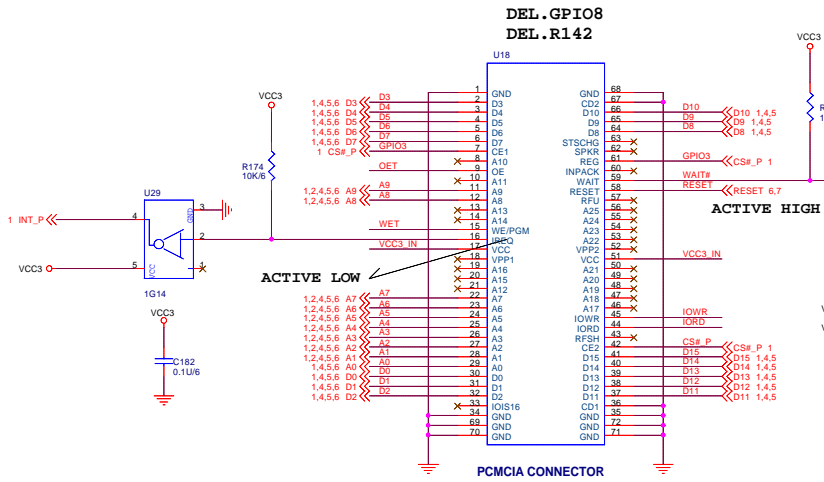
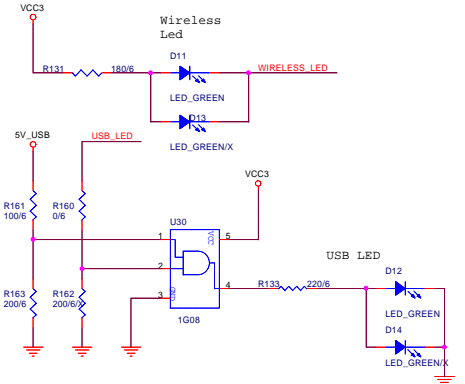
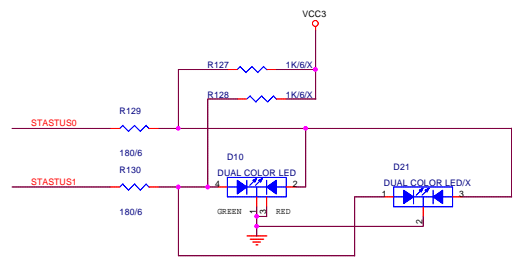
POWER



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STATUS LED



K1 X
K2 X
K3 X
K4 X
K5 X
K6 X
L1 X
L2 X
L3 X
L4 X

Title		
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