

Description of changes to Dual LAR (ID#ISEDLAR)

Class 2 Changes

There is no physical change that has to be made to allow the Dual LAR (ID# ISEDLAR) to operate at either 217MHz or 216.6645MHz. The frequency change is achieved by reloading the LAR firmware that will reprogram the PLL to the desired frequency.

Class 1Changes

The PCB was relayed out to provide better isolation between the digital and RF (analog) section. The microprocessor and memory devices were moved to the bottom side of the board and a shield is clipped over top to improve isolation.

As well some digital devices, the microprocessor and interface devices are now 3V devices instead of 5V. This change was made due to availability of parts.