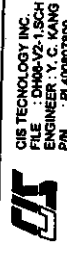
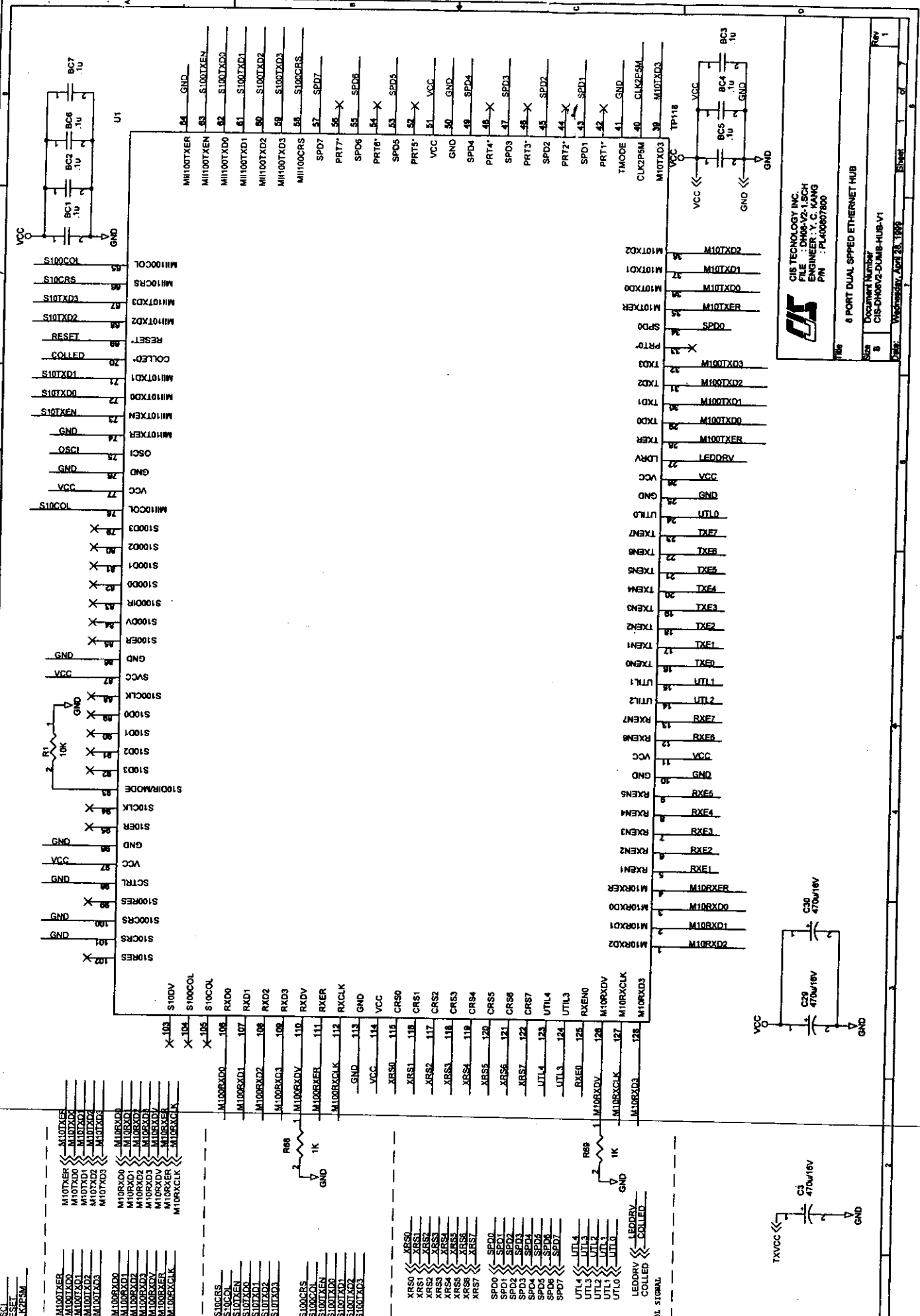


EXHIBIT D

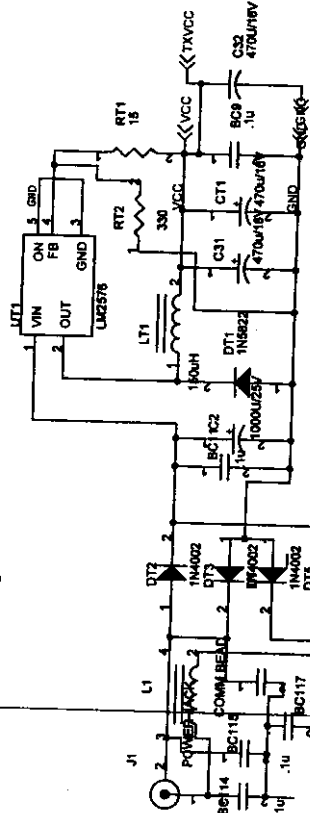
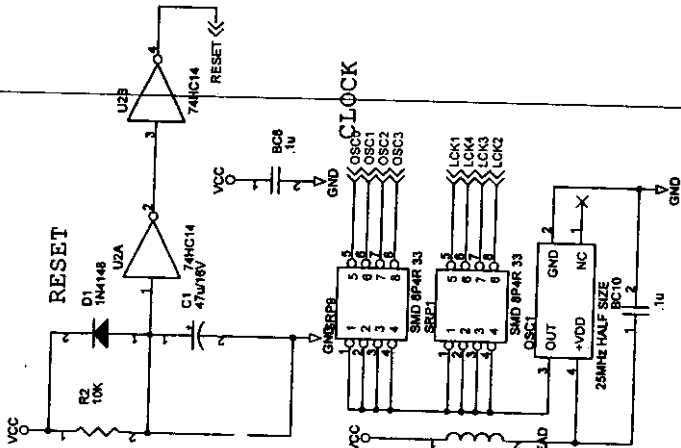
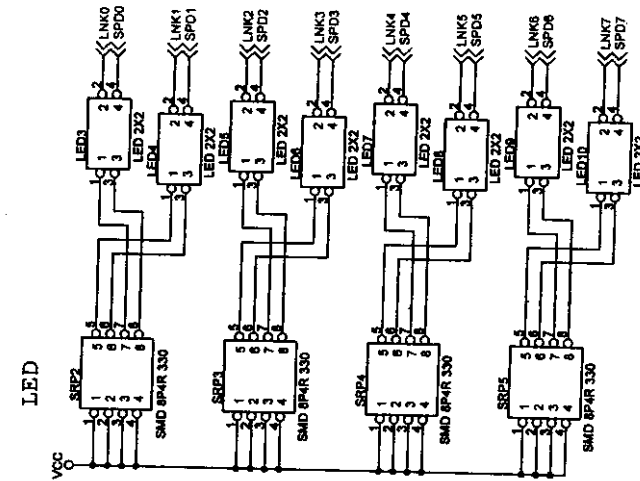
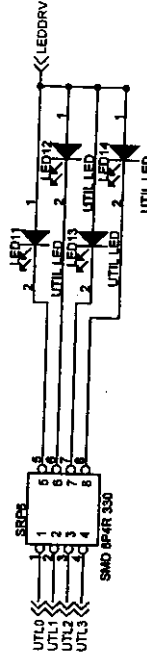
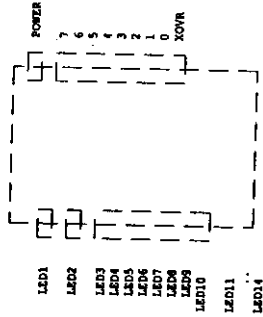
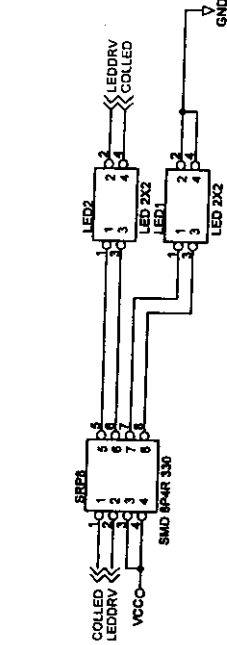
Circuit Diagram




CIS TECHNOLOGY INC.
 FILE : DMM-V2-1.SCH
 ENGINEER : Y. C. KANG
 PIN : PL400007800

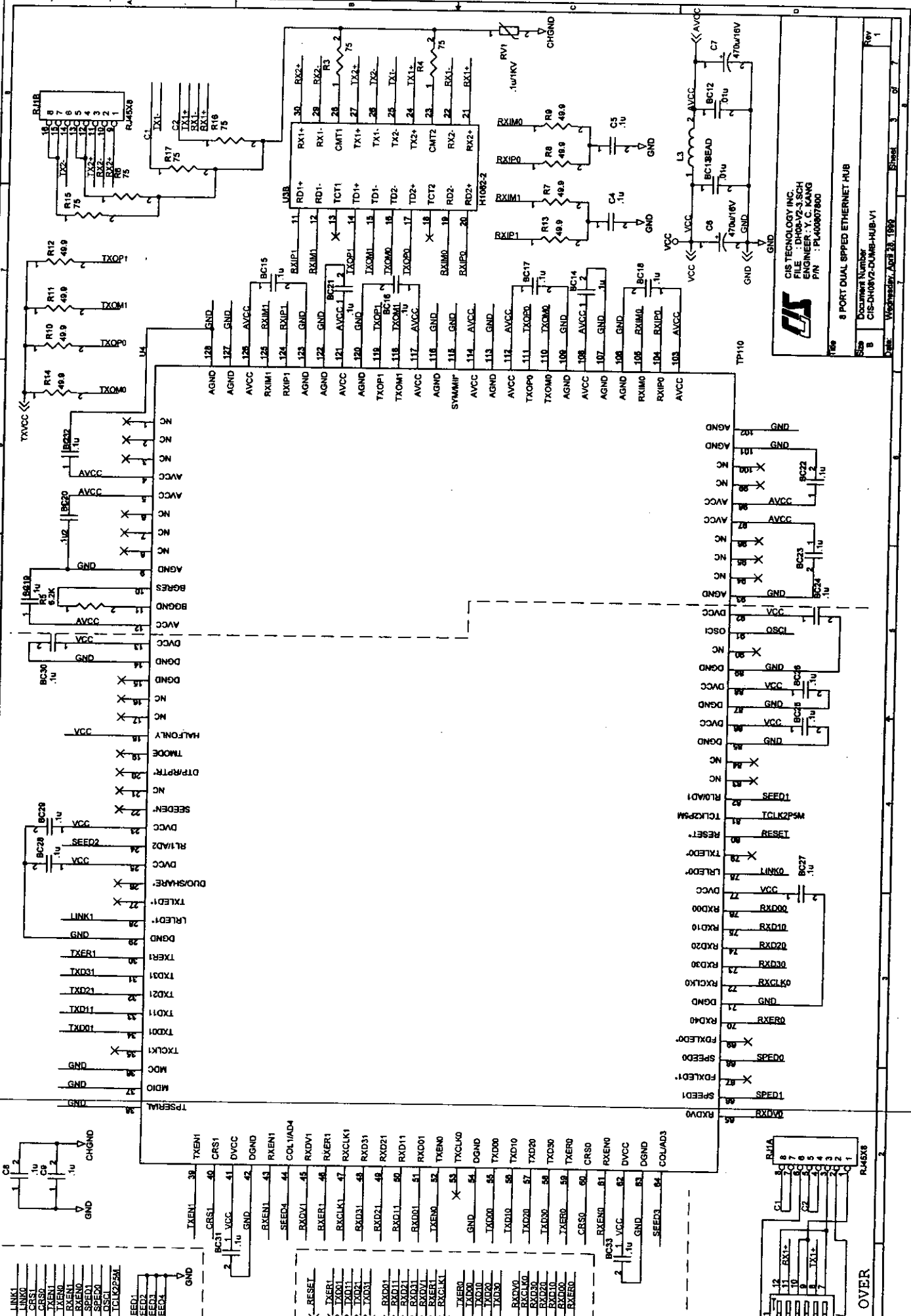
6 PORT DUAL SPEED ETHERNET HUB
 Document Number
 CIS-DMMV2-DUMB-HUB-V1
 Date: Wednesdays, April 28, 1999

Rev
 1




CIS TECHNOLOGY INC.
 FILE : DM252.SCH
 ENGINEER : Y. C. KANG
 P/N : PL400607800

Rev	1
Doc	7
Sheet	2 of 7
Proj	8 PORT DUAL SPPED ETHERNET HUB
Docu	Document Number
Part	CIS-DM252-COMB-HUB-V1
Date	Wednesday, April 28, 1993

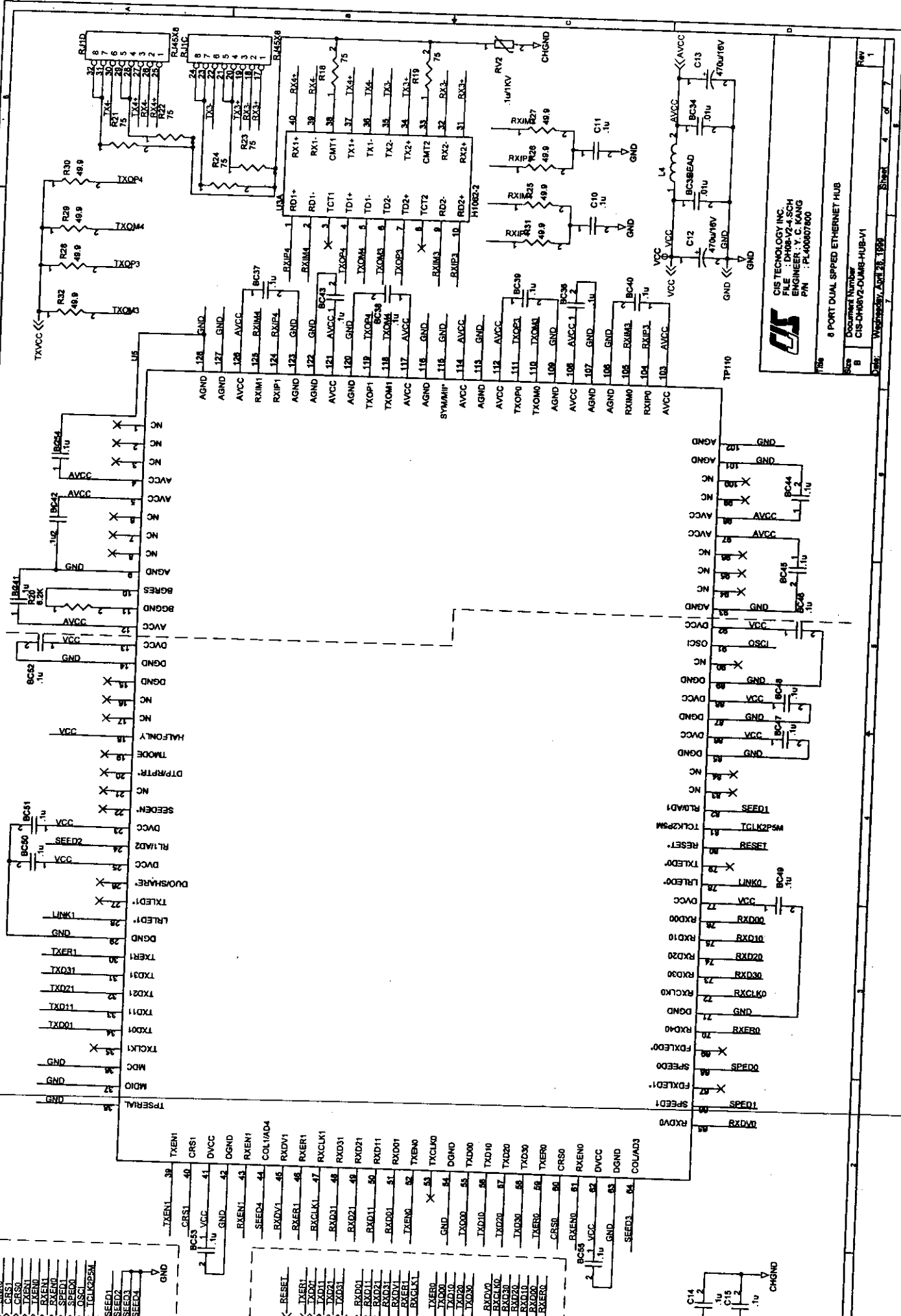



CIS TECHNOLOGY INC.
 FILE : DH08V2-3 SCH
 ENGINEER : Y. C. KANG
 P/N : PL40807800

8 PORT DUAL SPEED ETHERNET HUB

Doc# : _____
 Rev : 1

OVER

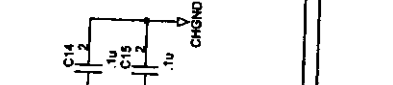
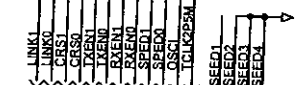


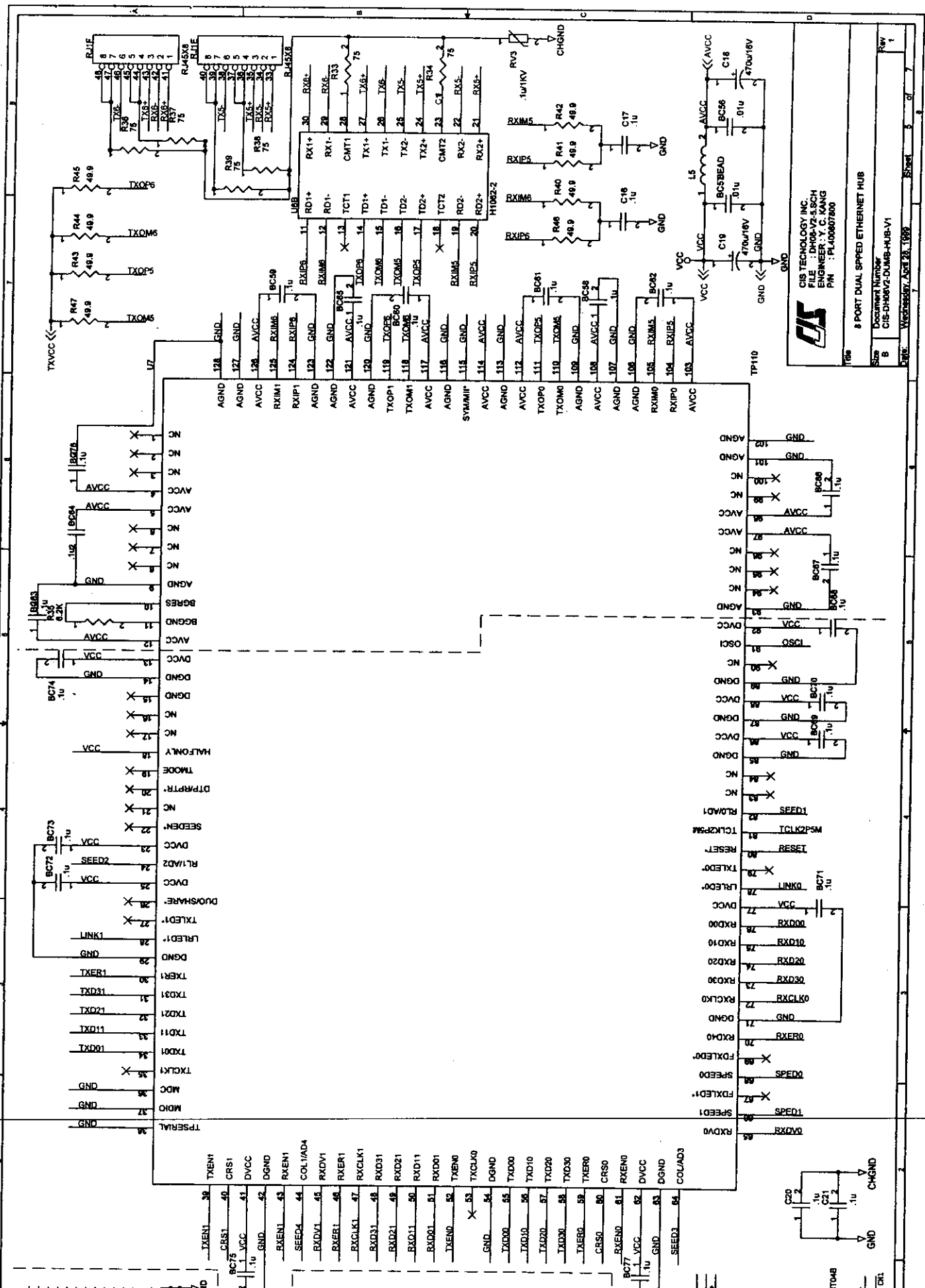



 CIS TECHNOLOGY INC.
 FILE : DHR8-V2-SCH
 ENGINEER : Y. C. KANG
 P/N : PL400007800

6 PORT DUAL SPEED ETHERNET HUB
 Document Number
 CIS-DHR8V2-DUMR-HUB-V1
 Date: Wednesday, April 26, 1995
 Sheet 4 of 7

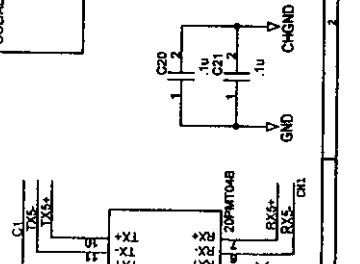
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CRS1	40	CRS1	40
VCC	41	DVCC	41
GND	42	DGND	42
RXEN1	43	RXEN1	43
SEED4	44	COL1AD4	44
RXDV1	45	RXDV1	45
RXER1	46	RXER1	46
RXCLK1	47	RXCLK1	47
RXD31	48	RXD31	48
RXD21	49	RXD21	49
RXD11	50	RXD11	50
RXD01	51	RXD01	51
TXEN0	52	TXEN0	52
TXCLK0	53	TXCLK0	53
DOND	54	DOND	54
TXD00	55	TXD00	55
TXD10	56	TXD10	56
TXD20	57	TXD20	57
TXD30	58	TXD30	58
TXD00	59	TXD00	59
TXER0	60	TXER0	60
CRS0	61	CRS0	61
RXEN0	62	RXEN0	62
VCC	63	DVCC	63
GND	64	DGND	64
SEED3	65	COL1AD3	65

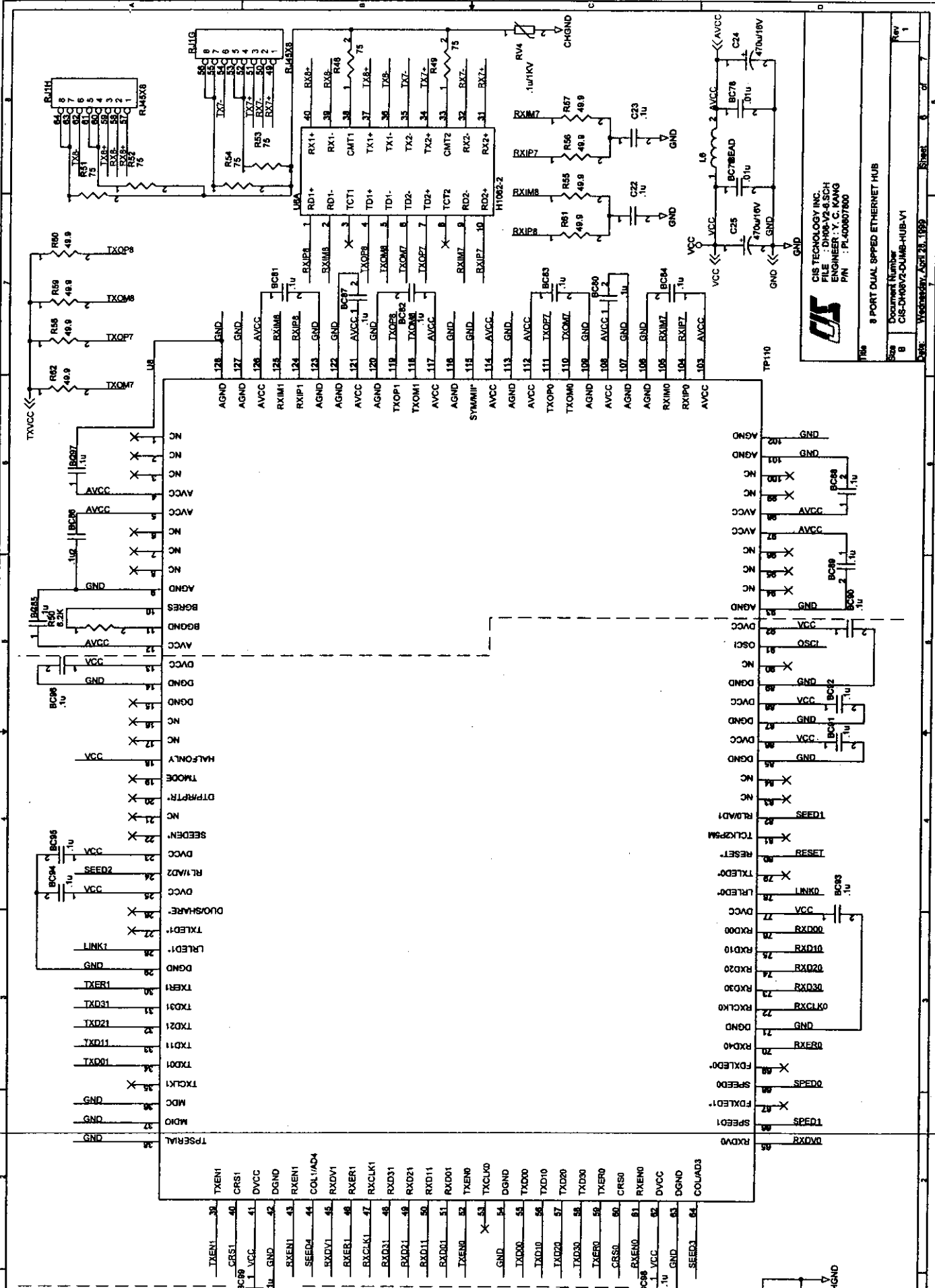






 CIS TECHNOLOGY INC
 FILE : DH0AV2LSCH
 ENGINEER : Y. C. KANG
 P/N : PL40060700
 8 PORT DUAL SPEED ETHERNET HUB
 Document Number : CIS-DH0AV2-DUM8-HUB-V1
 Rev : 1
 Date : Wednesday, April 28, 1999
 Sheet : 5 of 7



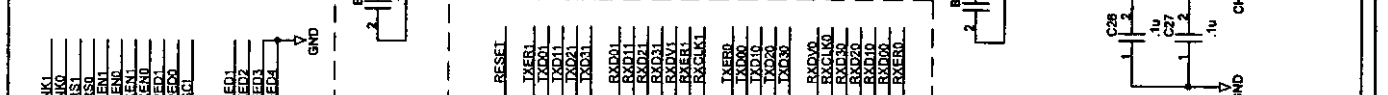


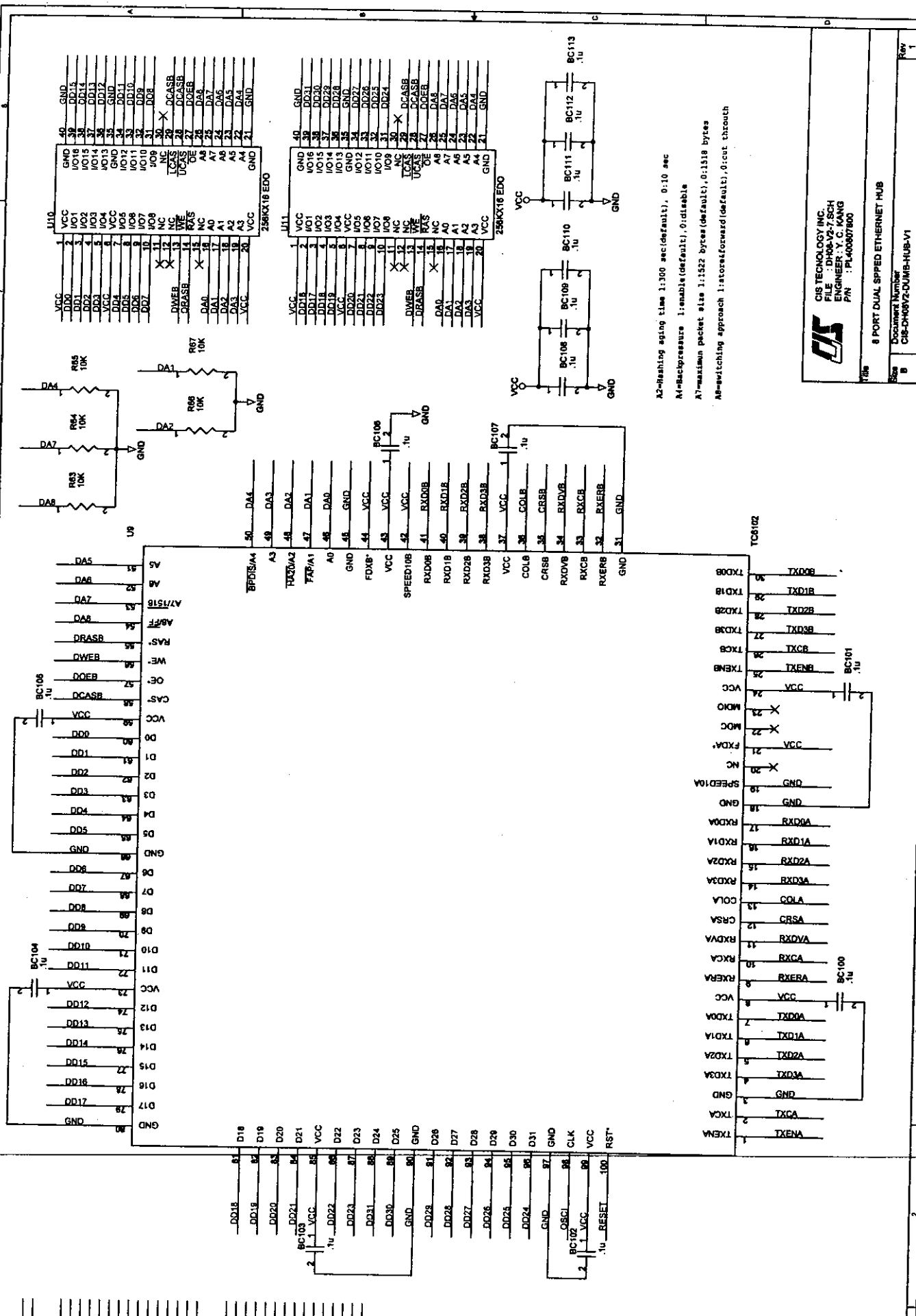
CIS TECHNOLOGY INC.
 FILE : DHR-V2-S.SCH
 ENGINEER : Y. C. KANG
 P/N : PL-400607800

8 PORT DUAL SPEED ETHERNET HUB

Document Number: CIS-DHRV2-DUIMB-HUB-V1
 Size: B
 Date: Wednesday, April 26, 1995

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 Rev: 1





X2=flashing aging time 1:300 sec(default), 0:10 sec
 A1=MacPressure 1:enable(default), 0:disable
 A7=maximum packet size 1:1522 bytes(default), 0:1518 bytes
 M=switching approach 1:storeforward(default), 0:cut through



CIS TECHNOLOGY INC.
 FILE : CIS-CH087-DUAL8-HUB-V1
 ENGINEER : Y. C. KANG
 P/N : PL400007800

8 PORT DUAL SPACED ETHERNET HUB
 Document Number
 CIS-CH087-DUAL8-HUB-V1
 Date: Wednesday, April 28, 1999

Rev 1