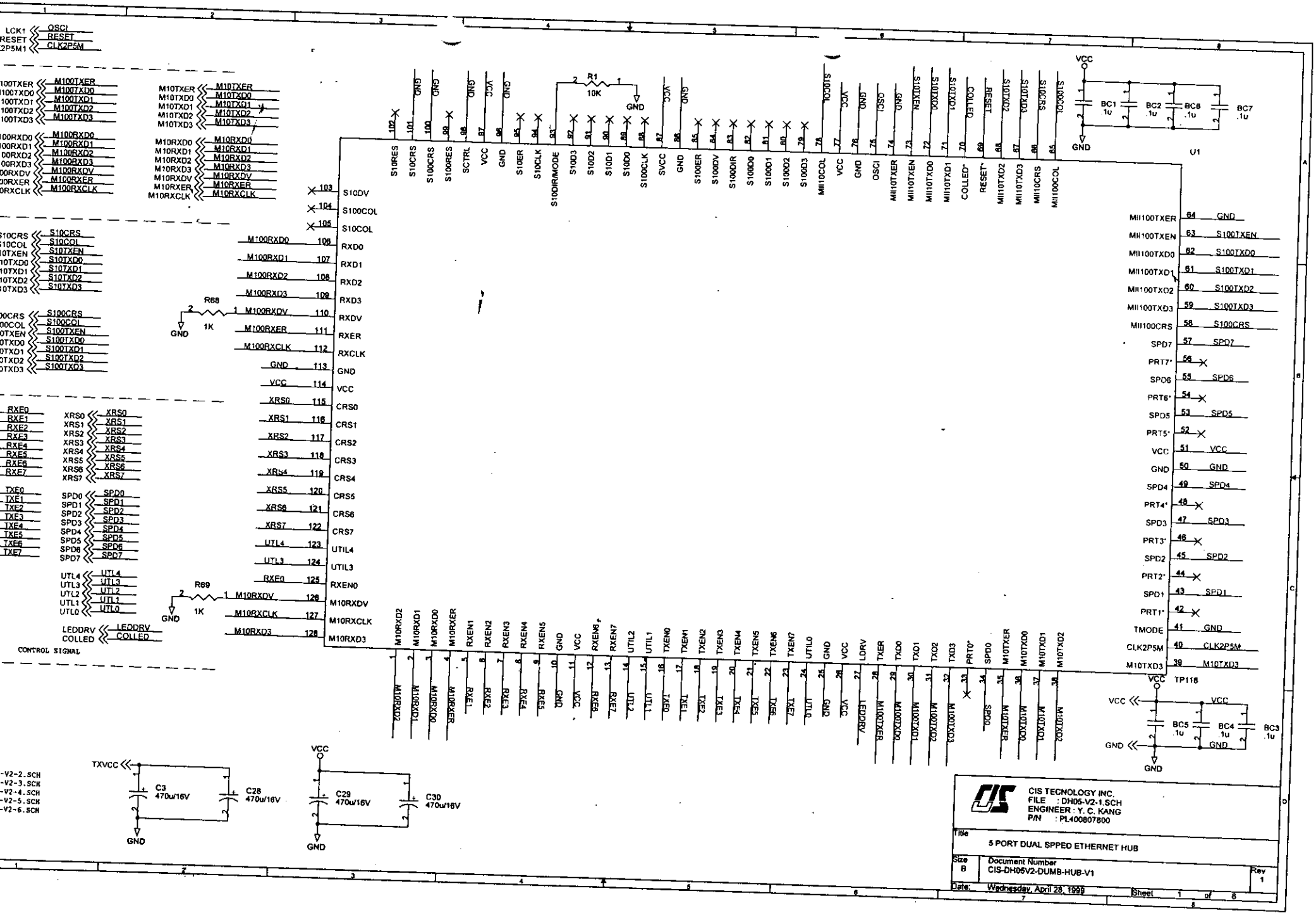
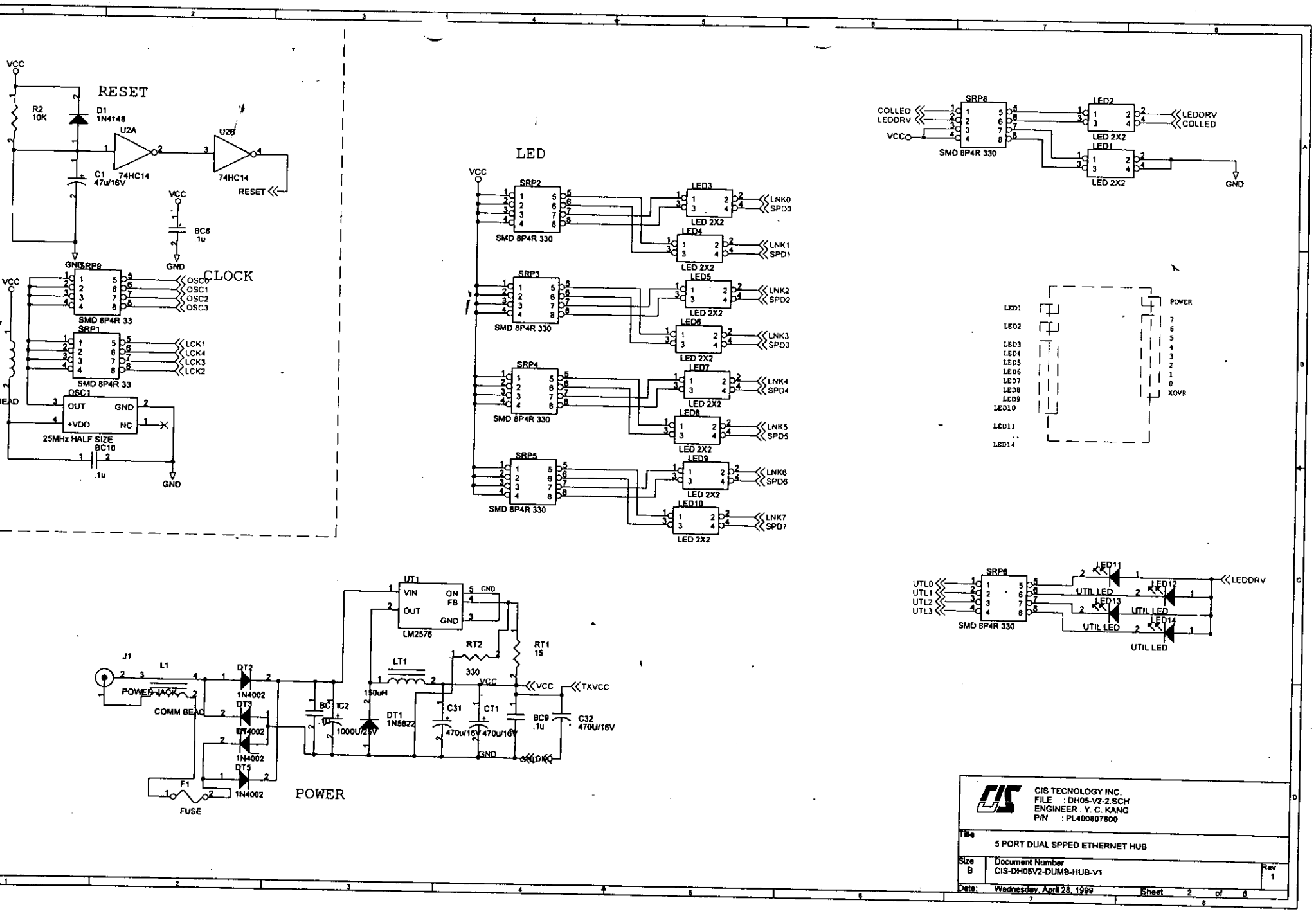
EXHIBIT D

Circuit Diagram


U1
R1 10K
R68 1K
R69 1K
BC1 .1u
BC2 .1u
BC6 .1u
BC7 .1u
BC5 .1u
BC4 .1u
BC3 .1u
C3 470u/16V
C28 470u/16V
C29 470u/16V
C30 470u/16V
TP116
CONTROL SIGNAL
CIS TECNOLOGY INC.
FILE : DH05-V2-1.SCH
ENGINEER : Y. C. KANG
P/N : PL400807600
Title
5 PORT DUAL SPPED ETHERNET HUB
Size
B
Document Number
CIS-DH05V2-DUMB-HUB-V1
Rev
1
Date: Wednesday, April 28, 1999
Sheet 1 of 6

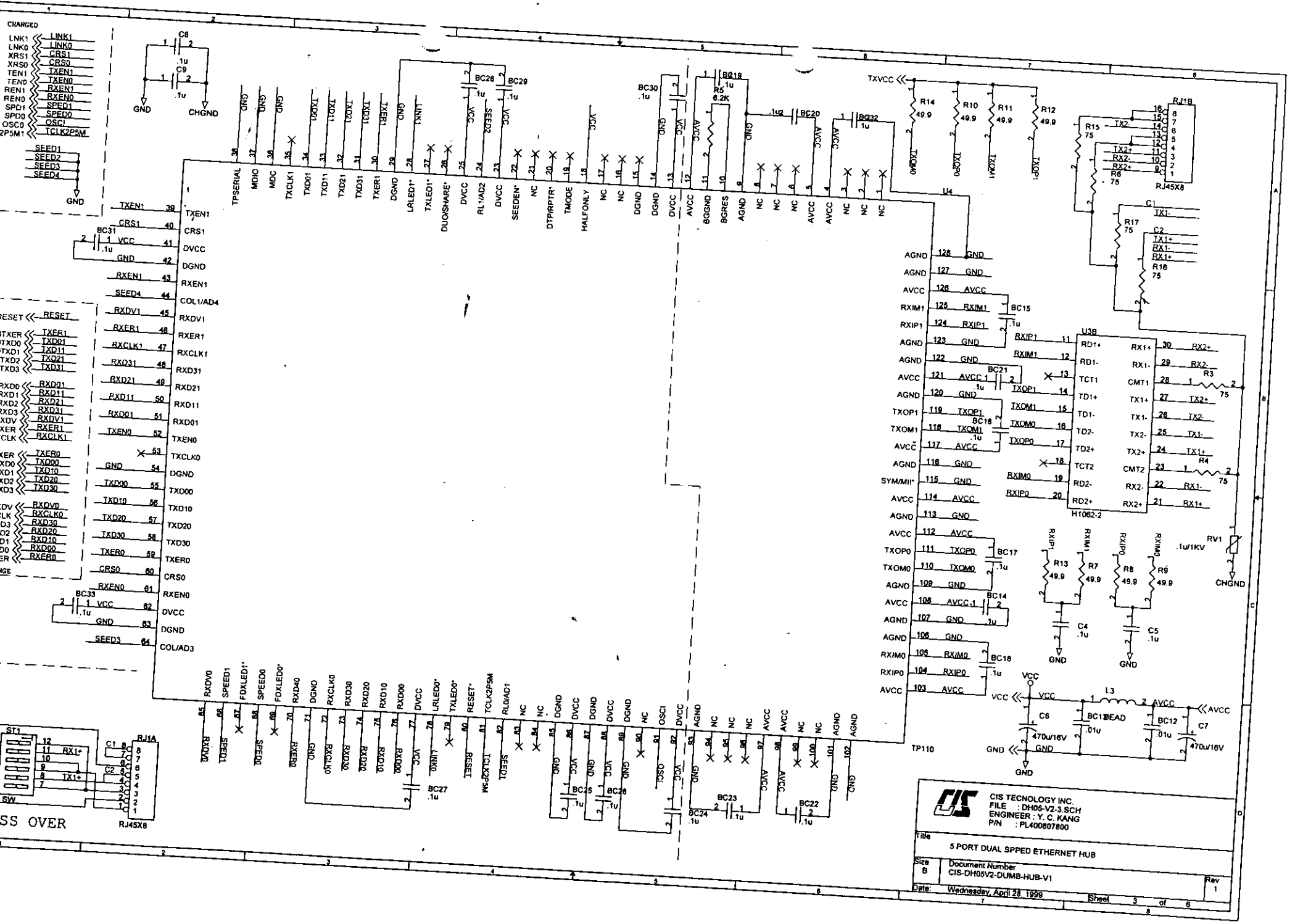


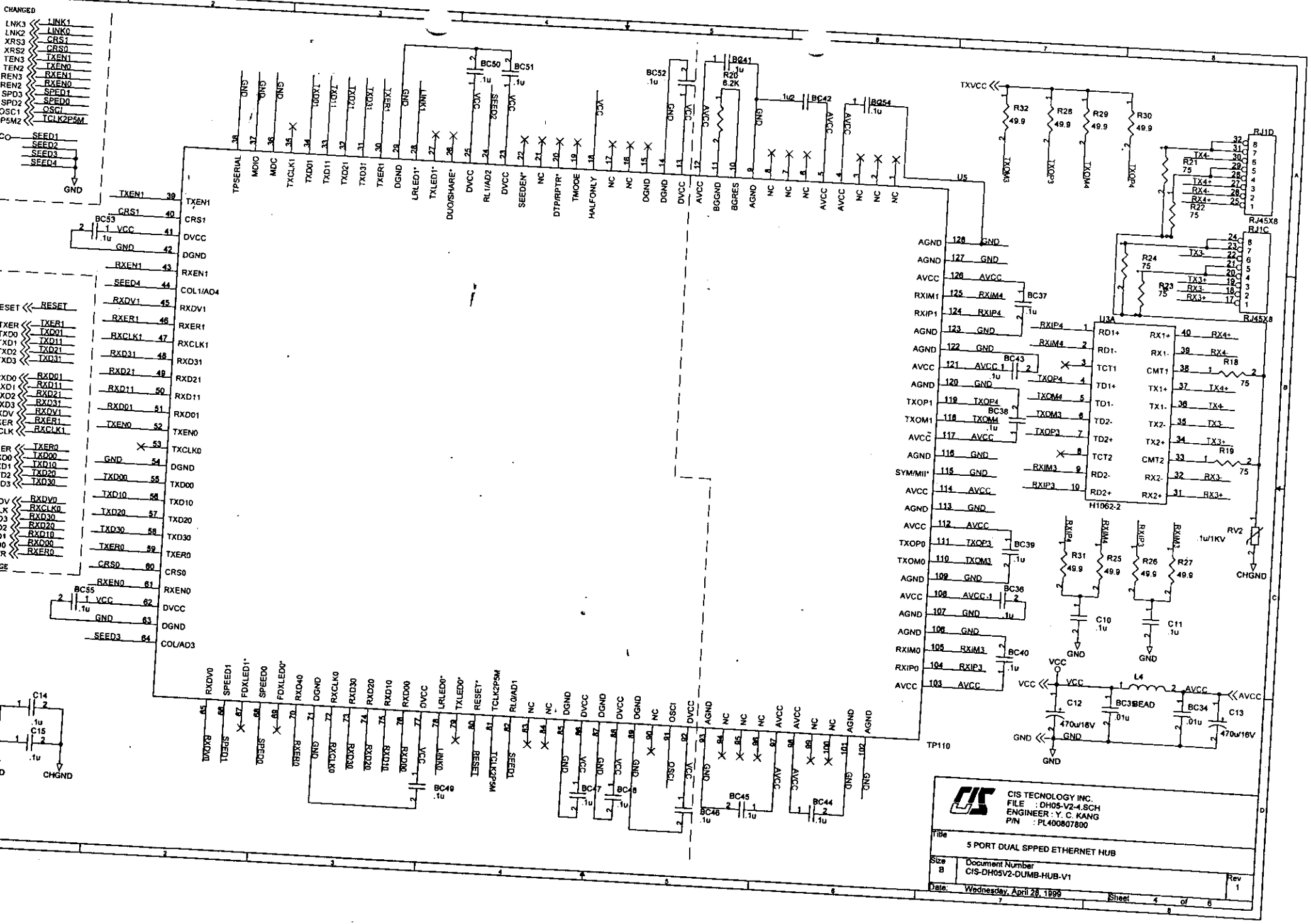
CIS TECNOLOGY INC.
FILE : DH05-V2-2.SCH
ENGINEER : Y. C. KANG
P/N : PL400807600

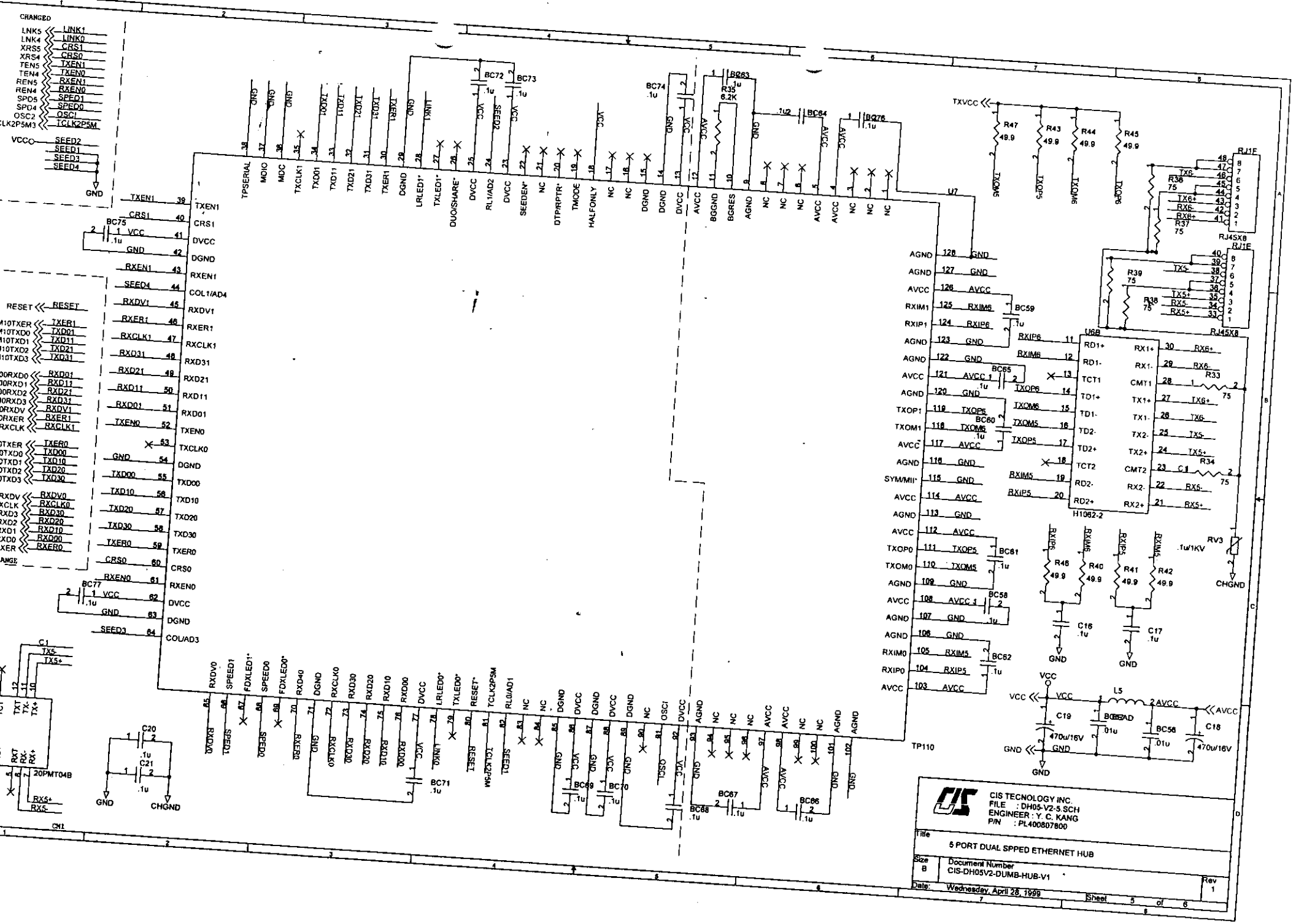
Title: 5 PORT DUAL SPPED ETHERNET HUB

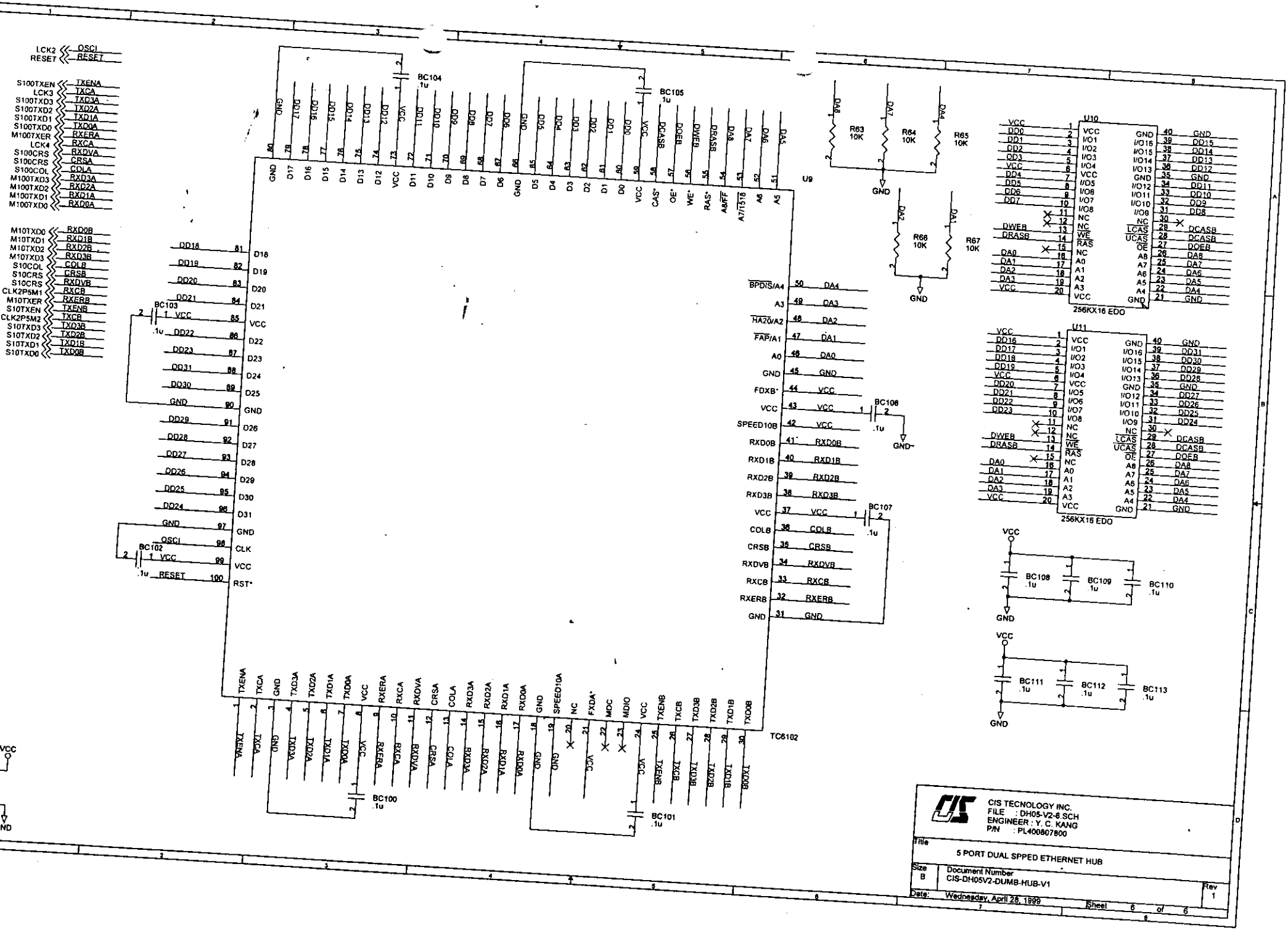
Size: B | Document Number: CIS-DH05V2-DUM9-HUB-V1 | Rev: 1

Date: Wednesday, April 28, 1999 | Sheet 2 of 6


CIS TECNOLOGY INC.
FILE : DH05-V2-3.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800
Title
5 PORT DUAL SPPED ETHERNET HUB
Size
B
Document Number
CIS-DH05V2-DUMB-HUB-V1
Rev
1
Date: Wednesday, April 28, 1999
Sheet 3 of 6
U4
TP110
U3B
H1062-2
RJ45X8
RJ1A
RJ1B
ST1
SS OVER


CIS TECNOLOGY INC.
FILE : DH05-V2-4.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800
Title
5 PORT DUAL SPPED ETHERNET HUB
Size B
Document Number
CIS-DH05V2-DUMB-HUB-V1
Rev 1
Date: Wednesday, April 28, 1999
Sheet 4 of 6
U5
U3A
H1062-2
RJ45X8
RJ1D
RJ1C
TP110
CHGND
TXVCC
VCC
AVCC
GND


CIS TECNOLOGY INC.
FILE : DH05-V2-5.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800
Title
5 PORT DUAL SPPED ETHERNET HUB
Size
B
Document Number
CIS-DH05V2-DUMB-HUB-V1
Rev
1
Date: Wednesday, April 28, 1999
Sheet 5 of 6
U7
TP110
U6B
H1062-2
RJ45X8
RJ1F
RJ1E
RJ45X8
20PMT04B
CH1
CHGND
GND
VCC
AVCC
TXVCC


U9
TC6102
U10
256KX16 EDO
U11
256KX16 EDO
R63 10K
R64 10K
R65 10K
R66 10K
R67 10K
BC100 .1u
BC101 .1u
BC102 .1u
BC103 .1u
BC104 .1u
BC105 .1u
BC106 .1u
BC107 .1u
BC108 .1u
BC109 .1u
BC110 .1u
BC111 .1u
BC112 .1u
BC113 .1u
CIS TECNOLOGY INC.
FILE : DH05-V2-6.SCH
ENGINEER : Y. C. KANG
P/N : PL400807800
Title
5 PORT DUAL SPPED ETHERNET HUB
Size B
Document Number
CIS-DH05V2-DUMB-HUB-V1
Rev 1
Date: Wednesday, April 28, 1999
Sheet 6 of 6