

APPENDIX REPORT

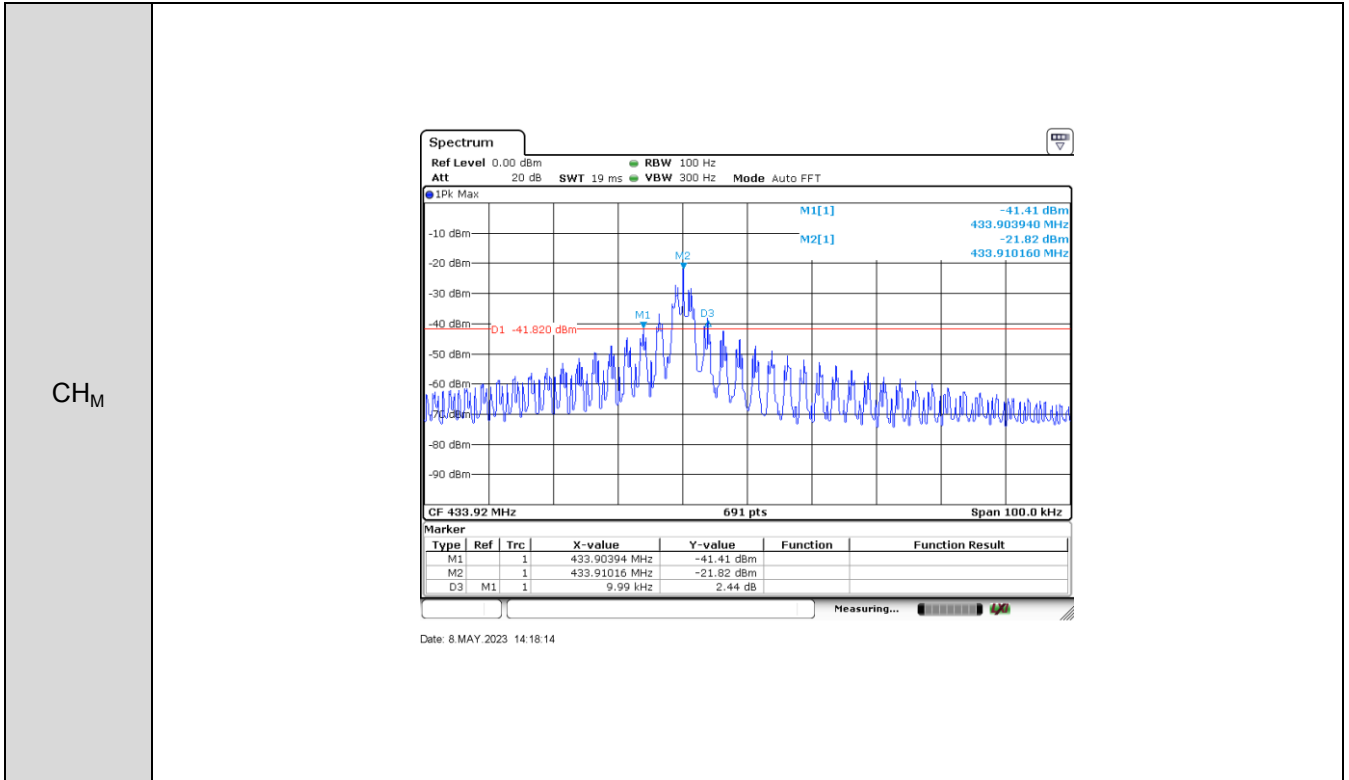
Project No.	SHT2303030201EW		
Test sample No.	YPHT23030302001	Model No.	K1131-A1
Start test date	2023-05-08	Finish date	2023-05-08
Temperature	24.8℃	Humidity	63%
Test Engineer	Xiaoqin Li	Auditor	Xiaodong Zheo

Appendix clause	Test item	Result
A	20dB Bandwidth	PASS
B	99% Occupied Bandwidth	PASS
C	Deactivation Time	PASS
D	Duty Cycle Corrected Factor	PASS

Appendix A: 20dB bandwidth

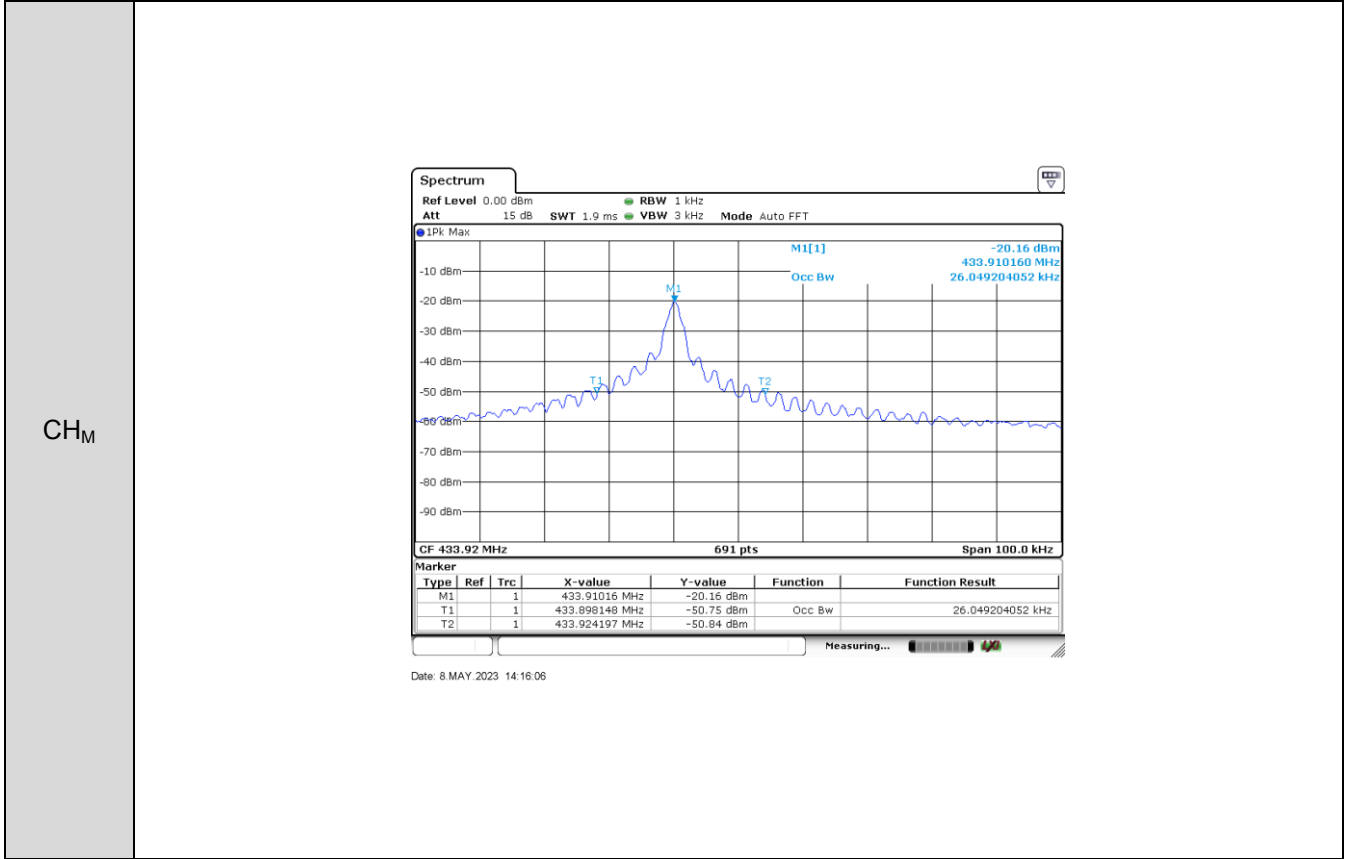
Test Channel	20dB Bandwidth (kHz)	Limit (kHz)	Result
CH _M	9.99	1084.8	Pass

NOTE:Limit=0.25%*Center Frequency=1084.8 kHz



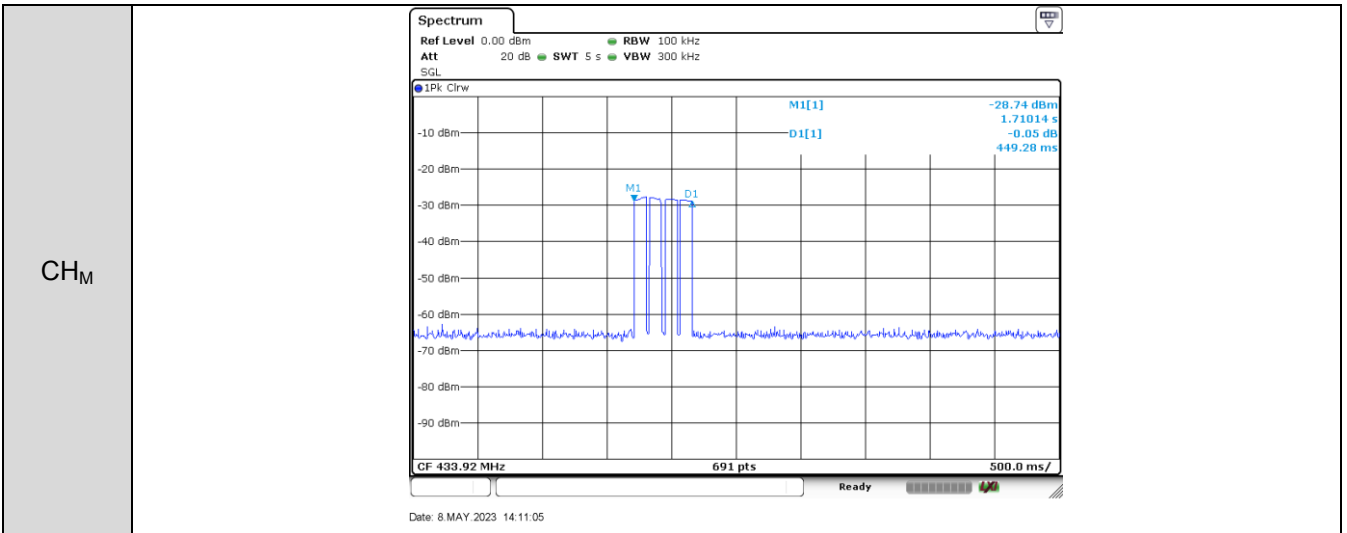
Appendix B: 99% Occupied Bandwidth

Test Channel	99% Occupied Bandwidth (kHz)	Limit (kHz)	Result
CH _M	26.05	-	Pass



Appendix C: Deactivation Time

Transmission time (second)	Limit (second)	Result
0.45	5	Pass

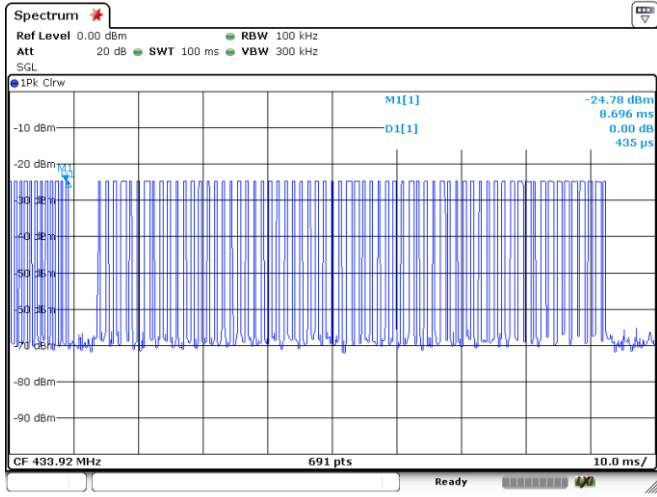
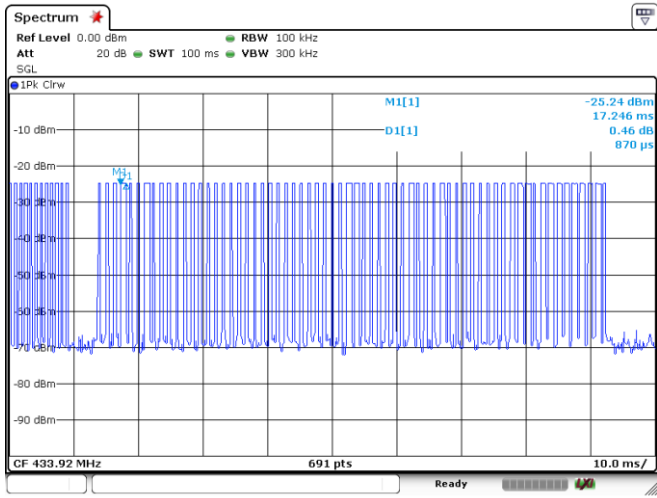
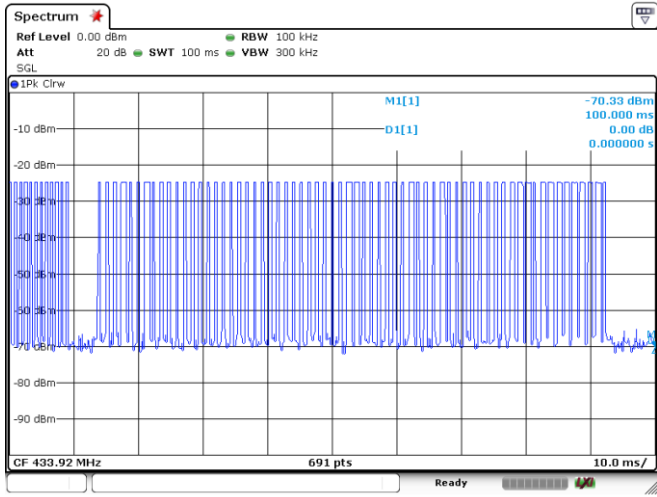


Appendix D: Duty Cycle Corrected Factor

T _{ON1} (ms) :	0.435
T _{ON1} number	62
T _{ON2} (ms) :	0.87
T _{ON2} number	16
Period (ms) :	100
Duty Cycle :	40.89%
Duty Cycle Corrected Factor:	-7.77

Note:

- 1) Duty cycle= $T_{ON1} * T_{ON1 \text{ number}} + T_{ON2} * T_{ON2 \text{ number}} / T_{period}$
- 2) Duty Cycle Corrected Factor/DCCF= $20 * \log (\text{Duty Cycle})$

<p>T_{ON1} (ms) :</p>	 <p>Ref Level 0.00 dBm RBW 100 kHz Att 20 dB SWT 100 ms VBW 300 kHz SGL 1Pk Clrv M1[1] -24.78 dBm D1[1] 8.696 ms 0.00 dB 435 μs CF 433.92 MHz 691 pts 10.0 ms/ Ready Date: 8 MAY 2023 14:12:56</p>
<p>T_{ON2} (ms) :</p>	 <p>Ref Level 0.00 dBm RBW 100 kHz Att 20 dB SWT 100 ms VBW 300 kHz SGL 1Pk Clrv M1[1] -25.24 dBm D1[1] 17.246 ms 0.46 dB 870 μs CF 433.92 MHz 691 pts 10.0 ms/ Ready Date: 8 MAY 2023 14:13:16</p>
<p>Period (ms) :</p>	 <p>Ref Level 0.00 dBm RBW 100 kHz Att 20 dB SWT 100 ms VBW 300 kHz SGL 1Pk Clrv M1[1] -70.33 dBm D1[1] 100.000 ms 0.00 dB 0.000000 s CF 433.92 MHz 691 pts 10.0 ms/ Ready Date: 8 MAY 2023 14:12:24</p>

-----End of Report-----