



MOTOROLA

Cellular Infrastructure Group

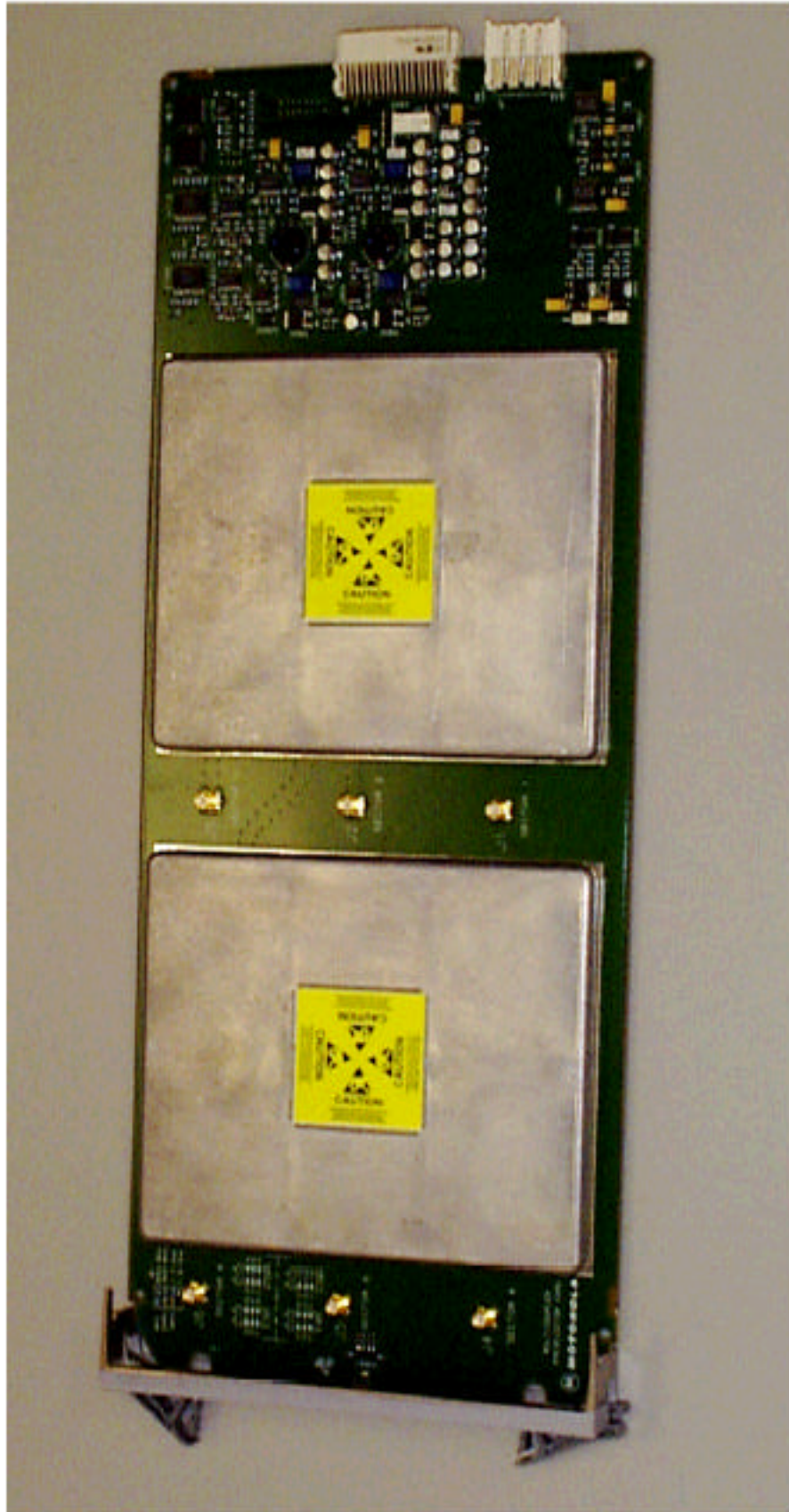
APPLICANT: MOTOROLA

TRANSCEIVER TYPE: IHET5YP1

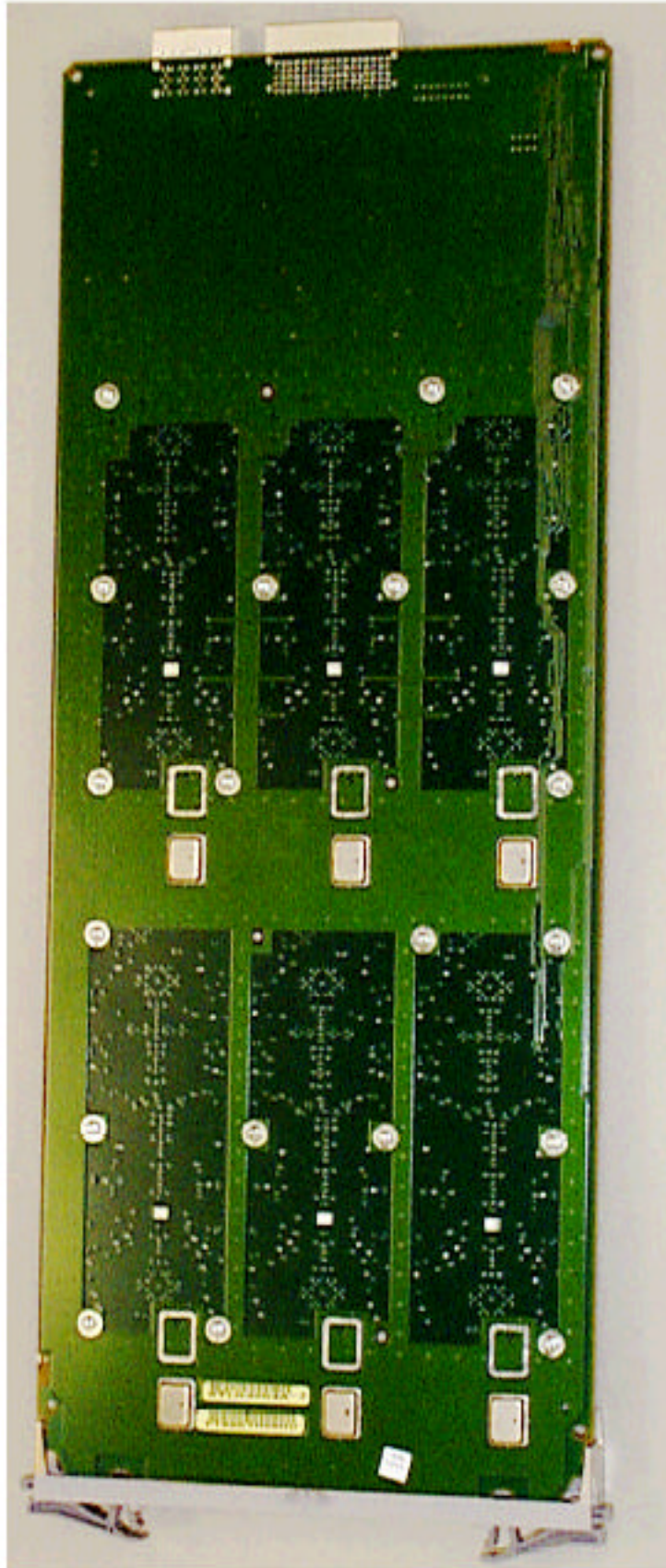
SC4812T @800 MHz CDMA BTS Frame

INTERNAL PHOTOS EXHIBIT INDEX

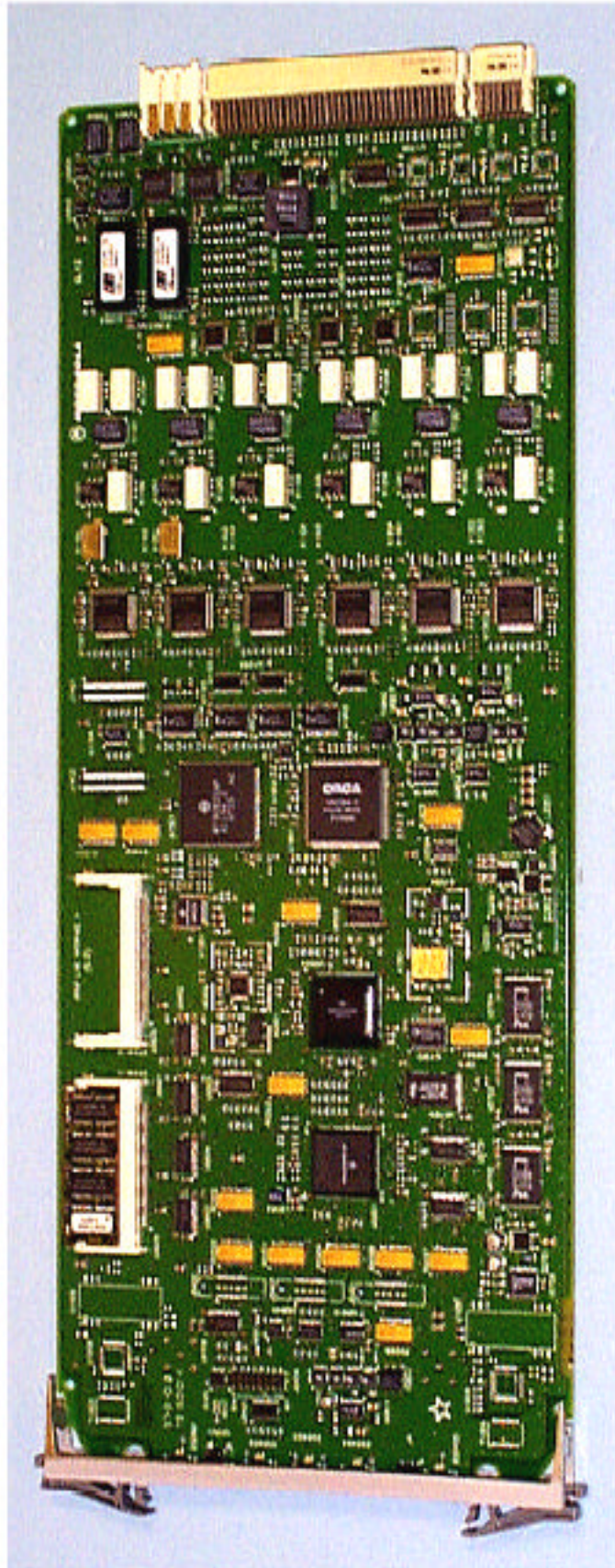
<u>NO.</u>	<u>PHOTOGRAPH</u>
01	MPC Front
02	MPC Back
03	GLI2 Front
04	GLI2 Back
05	BBX2 Front with Cover
06	BBX2 Front without Cover
07	BBX2 Back
08	MCC x 24E Front
09	MCC x 24E Back
10	CCD Front
11	CCD Back
12	CCD Front with Cover
13	CSM1 Front
14	CSM1 Back
15	CSM2 Front
16	CSM2 Back
17	LPA Front
18	LPA Back
19	LPA without Cover
20	Power Supply Front
21	Power Supply Back
22	CIO Front
23	CIO Back
24	Switch Card Front
25	Switch Card Back
26	AMR Front
27	AMR Back
28	HSO Front
29	HSO Back
30	SPAN I/O Front
31	SPAN I/O Back
32	SITE I/O Front
33	SITE I/O Back
34	RGD Front
35	RGD Back
36	LFR Front
37	LFR Back
38	DBPBBX Front with Cover
39	DBPBBX Front without Cover
40	DBPBBX Back



**IHET5YP1
SC4812T 800 MHz
MPC Front**



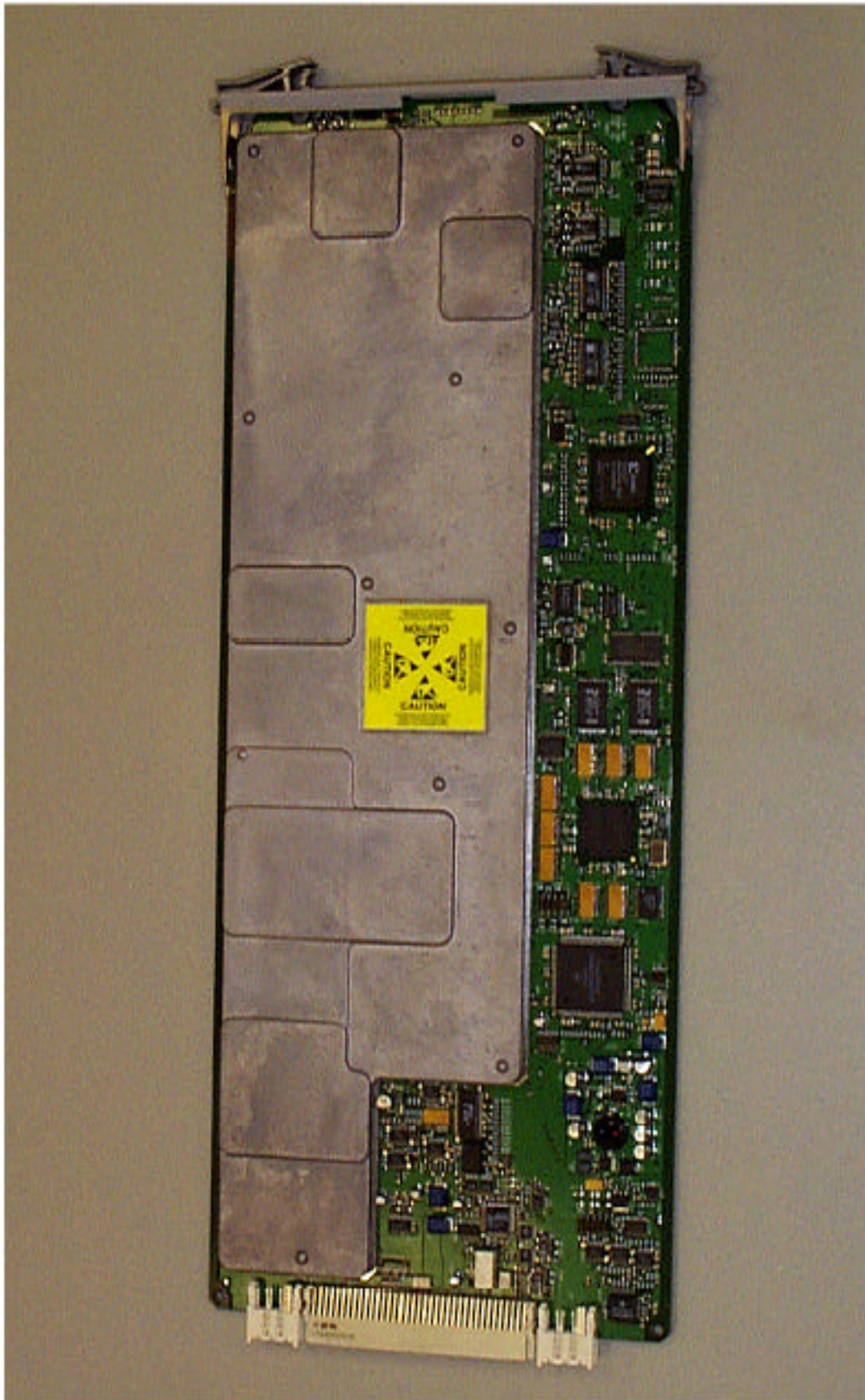
IHET5YP1
SC4812T 800 MHz
MPC Back



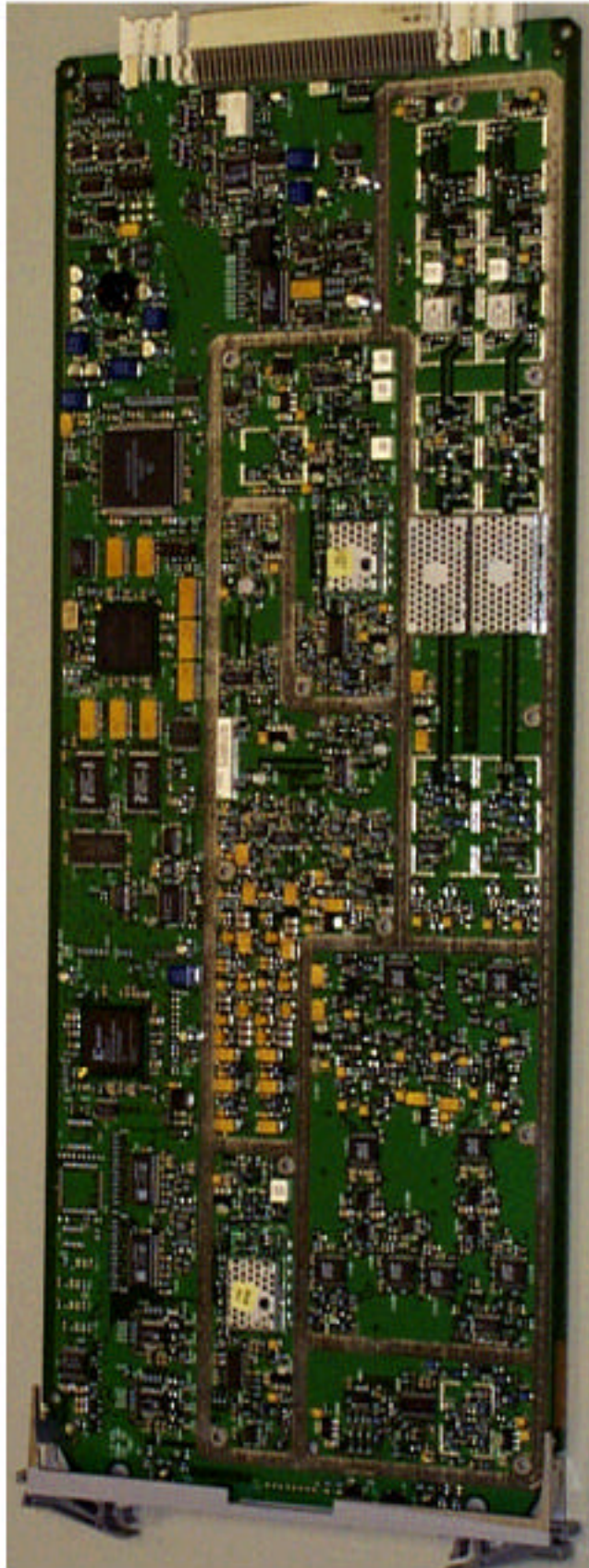
**IHET5YP1
SC4812T 800 MHz
GLI2 Front**



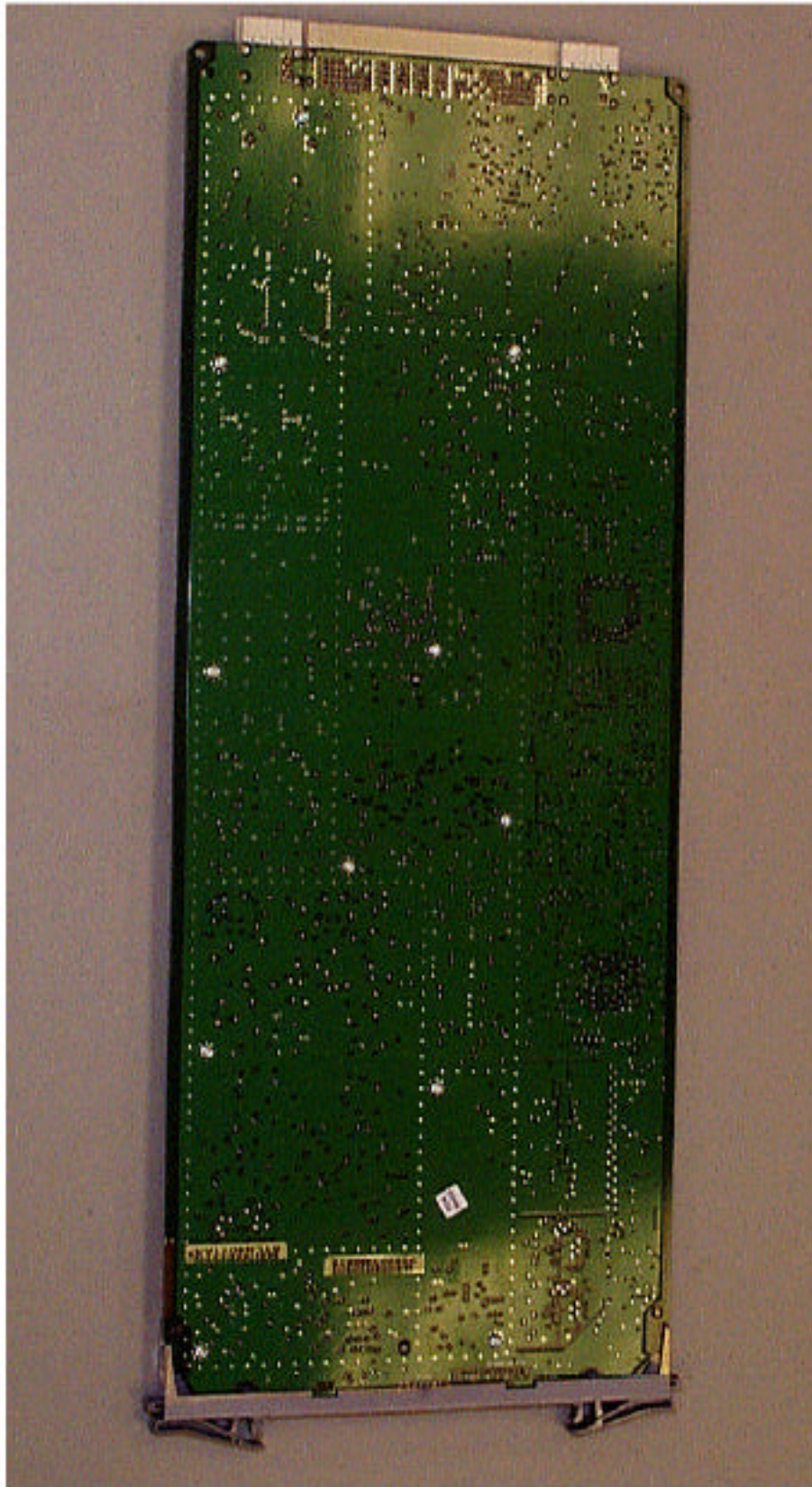
**IHET5YP1
SC4812T 800 MHz
GLI2 Back**



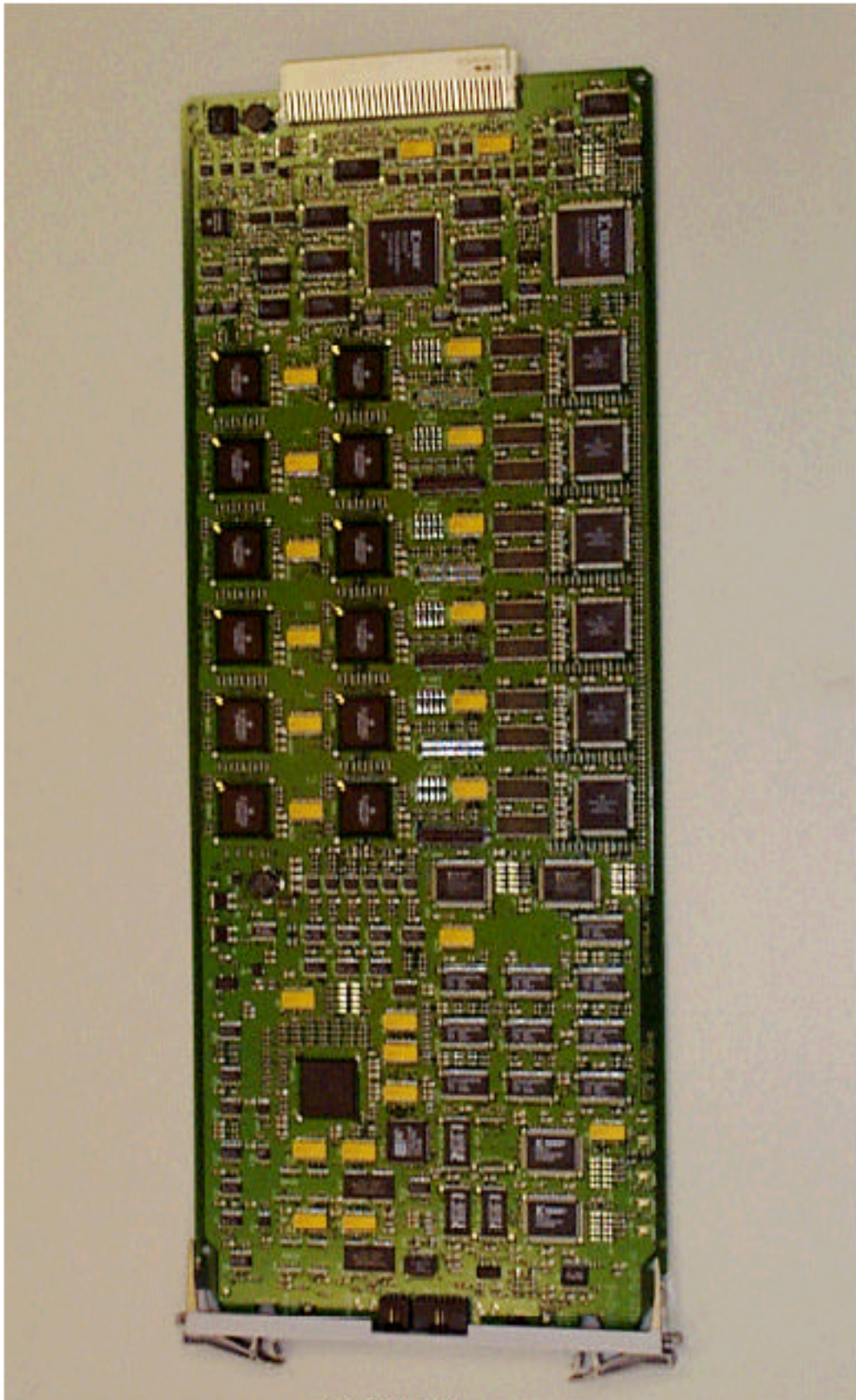
**IHET5YP1
SC4812T 800 MHz
BBX2 with Cover**



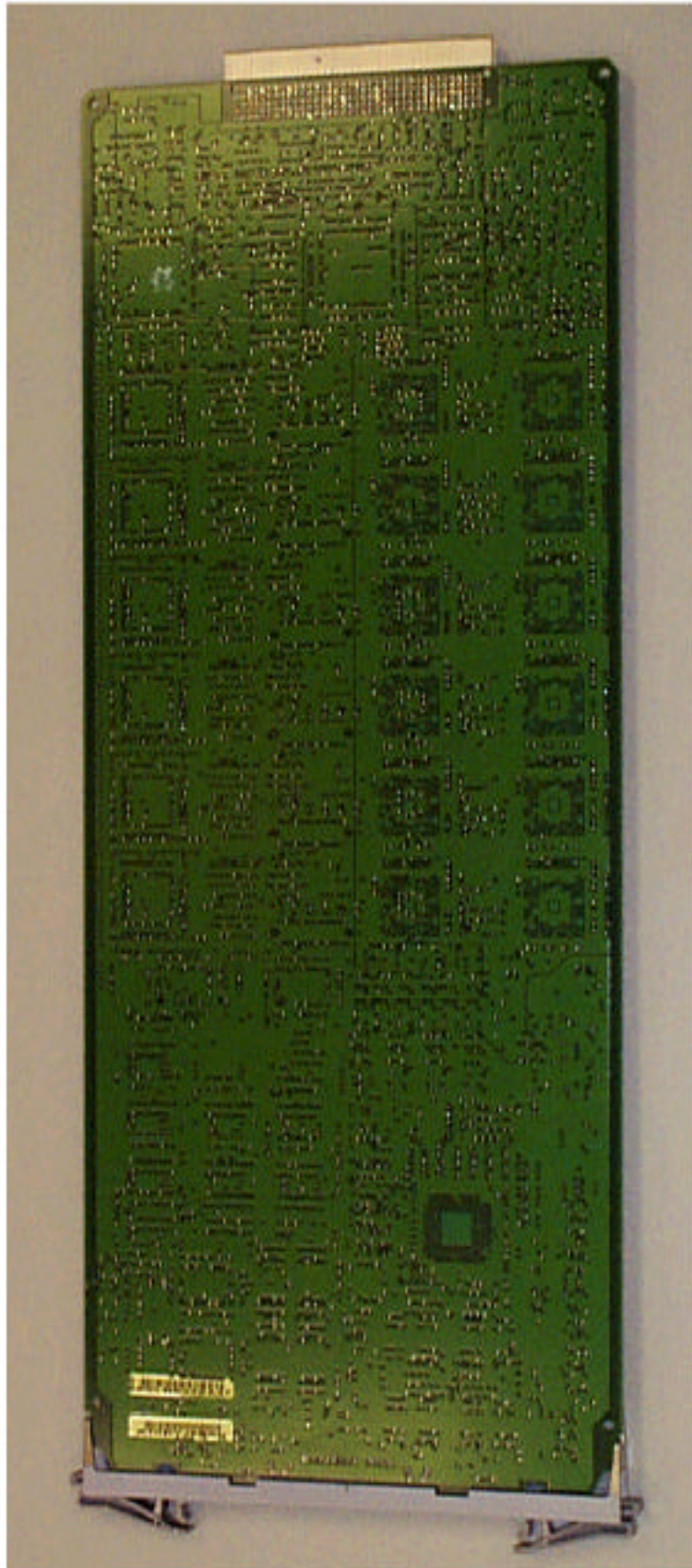
**IHET5YP1
SC4812T 800 MHz
BBX2 without Cover**



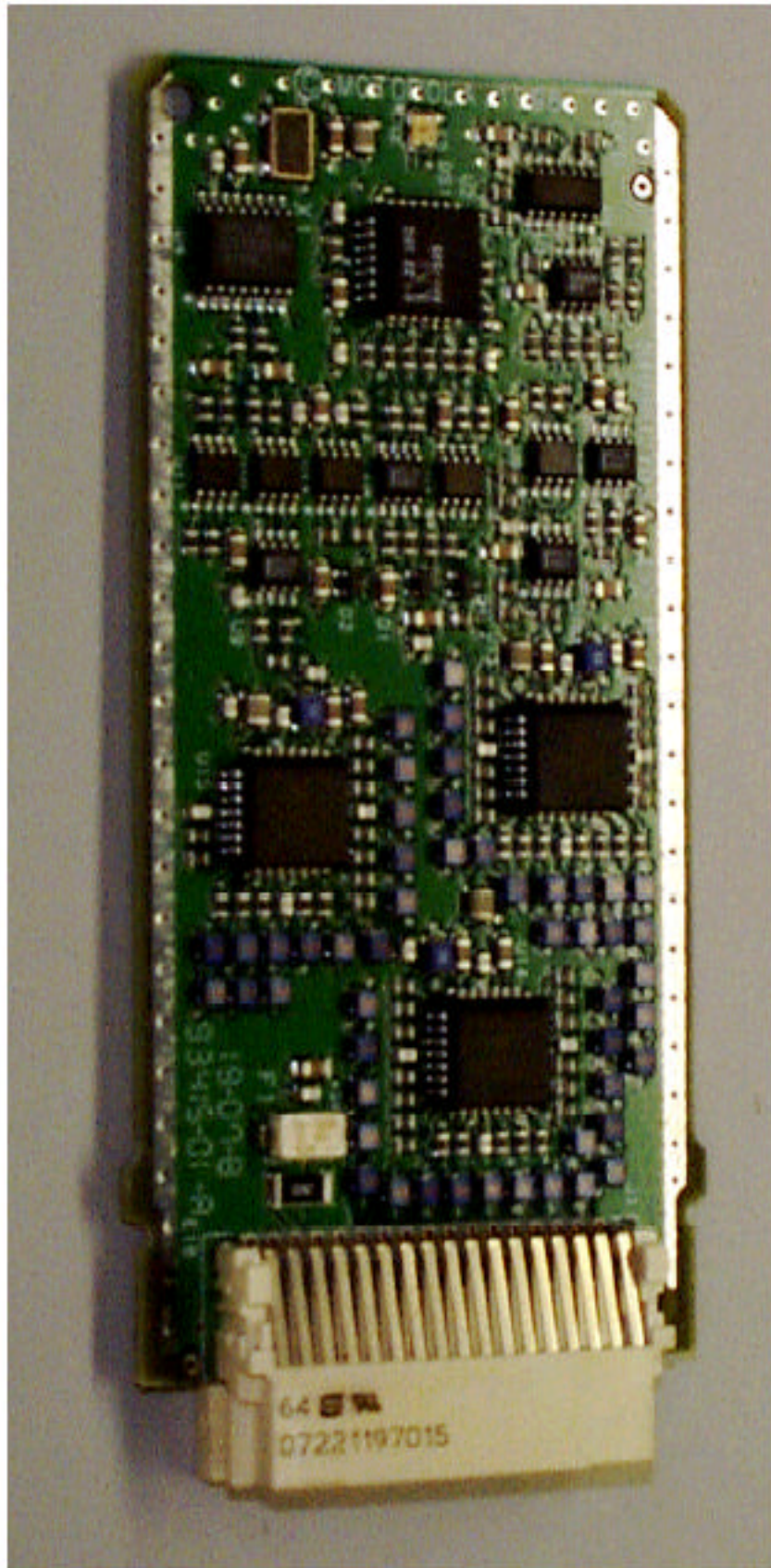
**IHET5YP1
SC4812T 800 MHz
BBX2 Back**



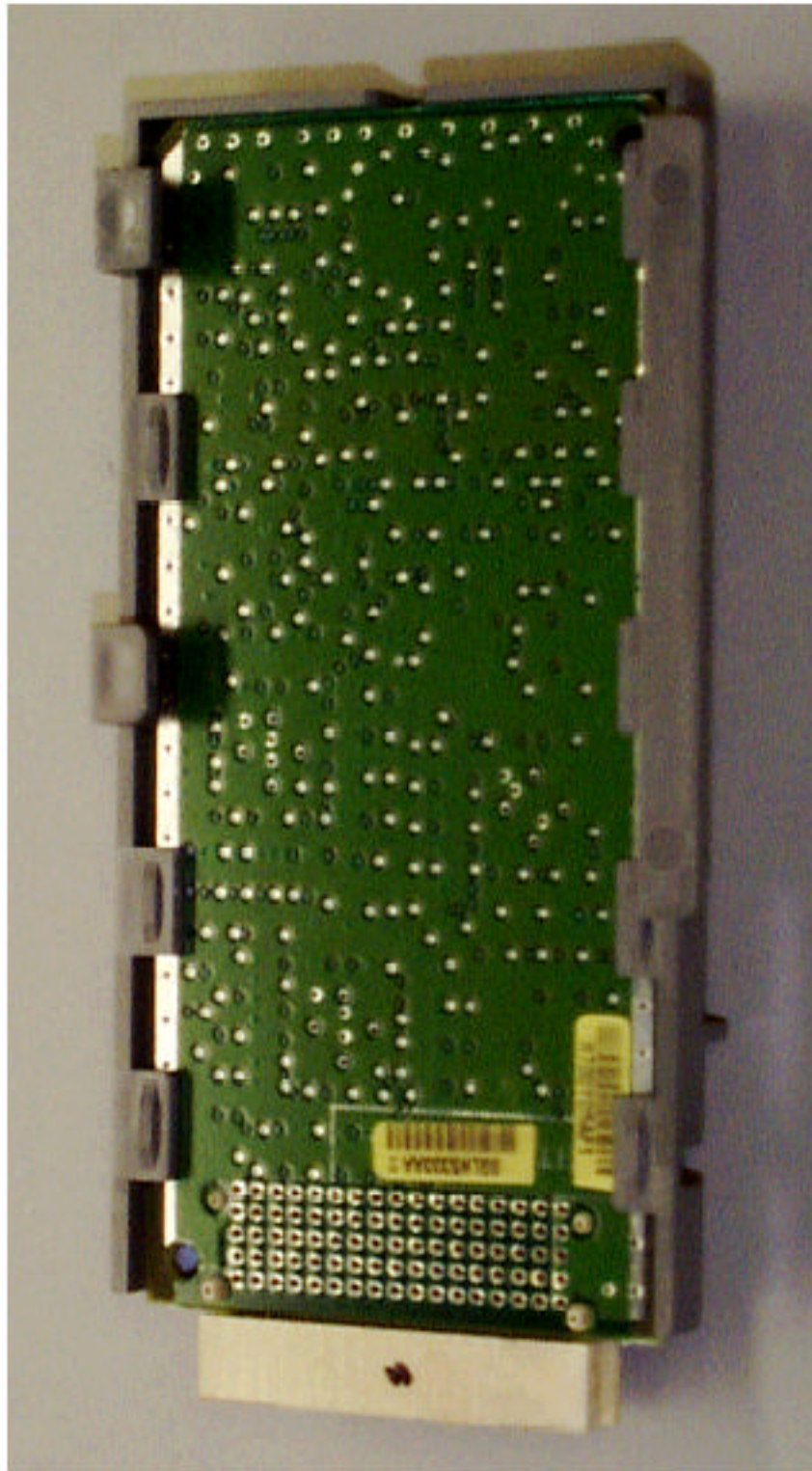
**IHET5YP1
SC4812T 800 MHz
MCC x 24E Front**



IHET5YP1
SC4812T 800 MHz
MCC x 24E Back



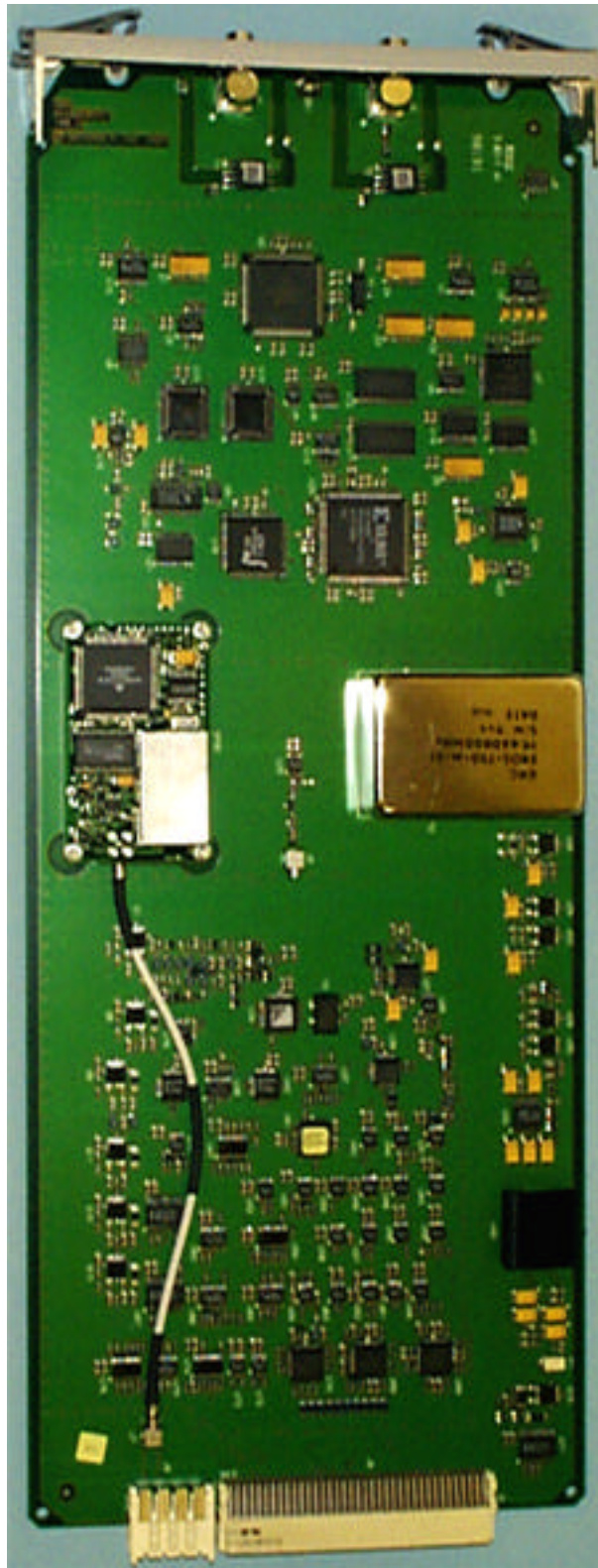
**IHET5YP1
SC4812T 800 MHz
CCD Front**



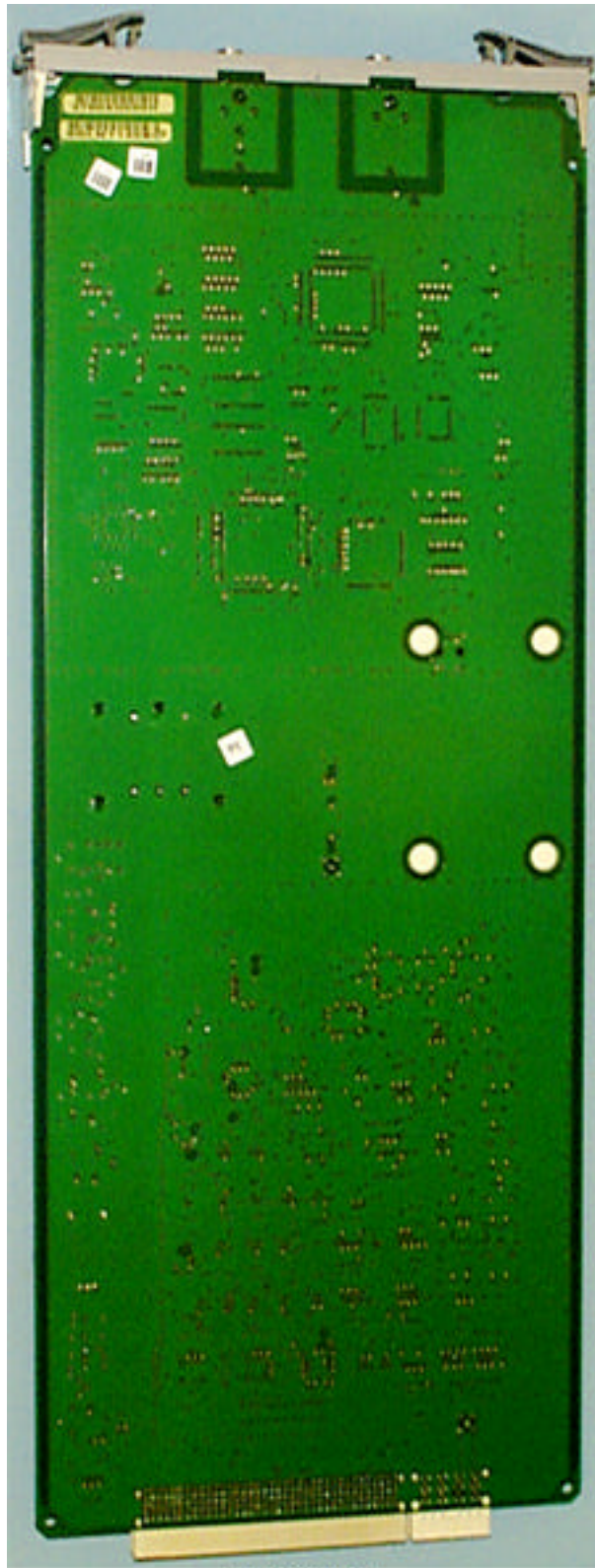
**IHET5YP1
SC4812T 800 MHz
CCD Back**



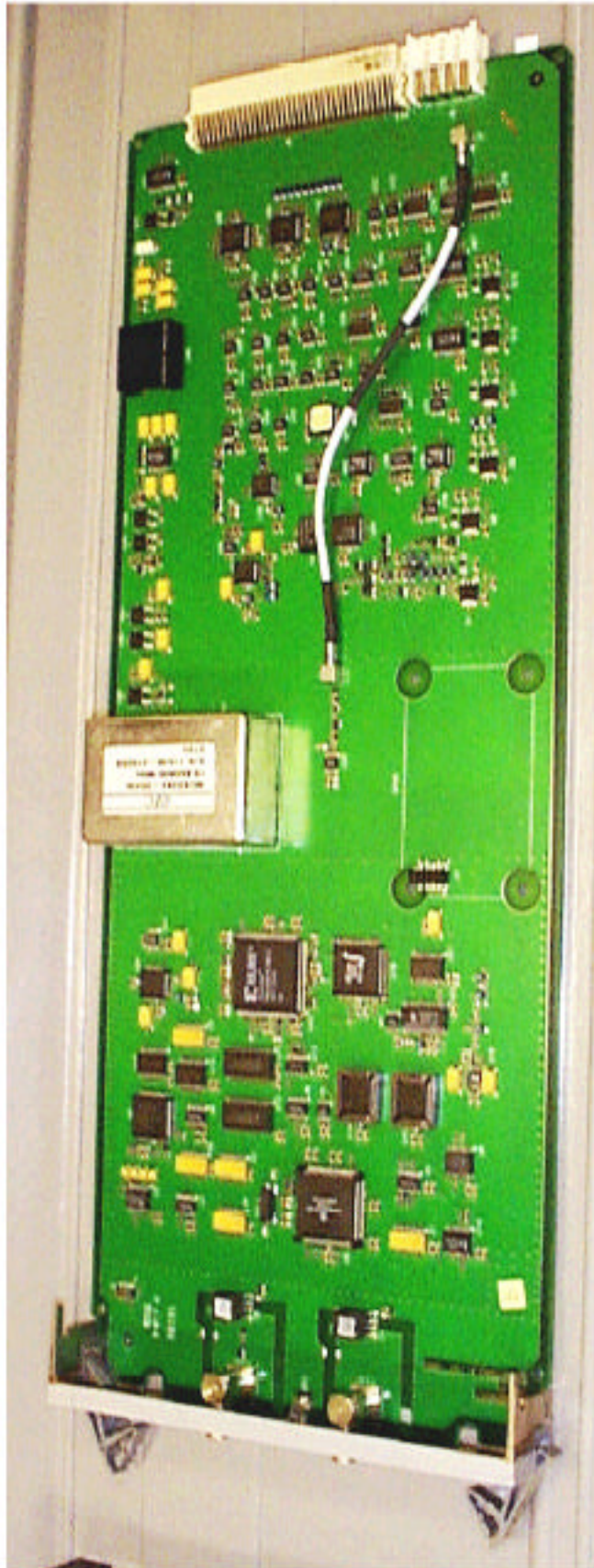
**IHETSYP1
SC4812T 800 MHz
CCD Front with Cover**



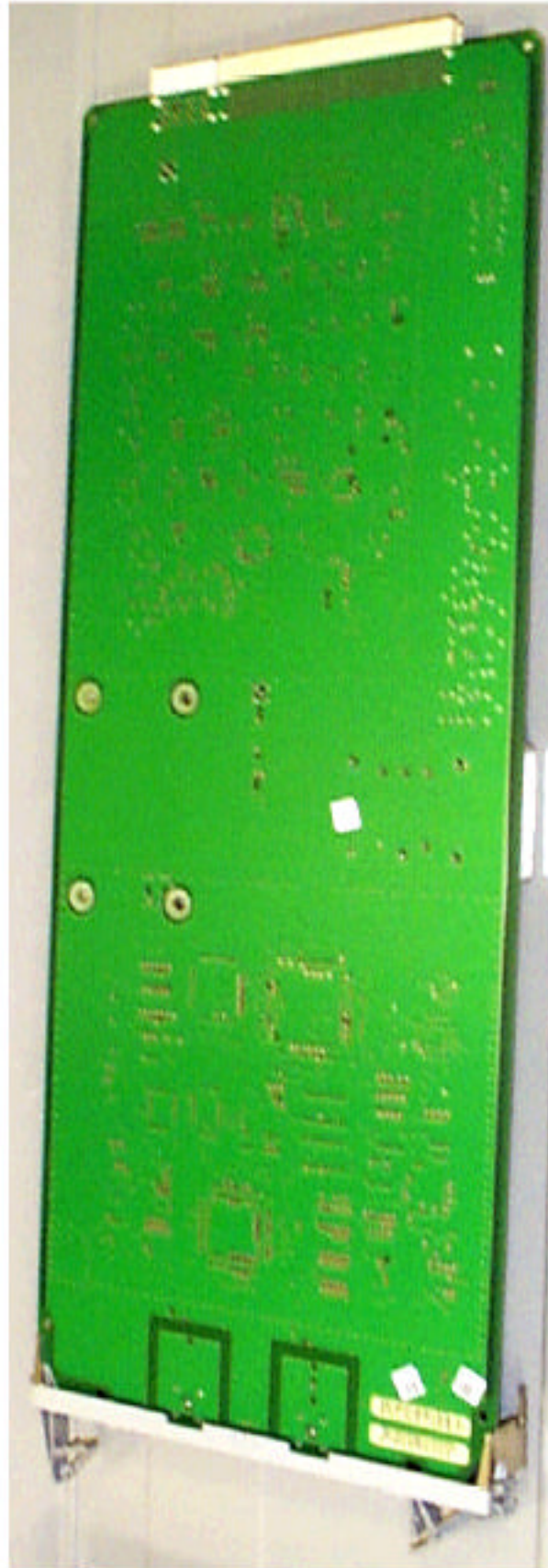
**IHET5YP1
SC4812T 800 MHz
CSM1 Front**



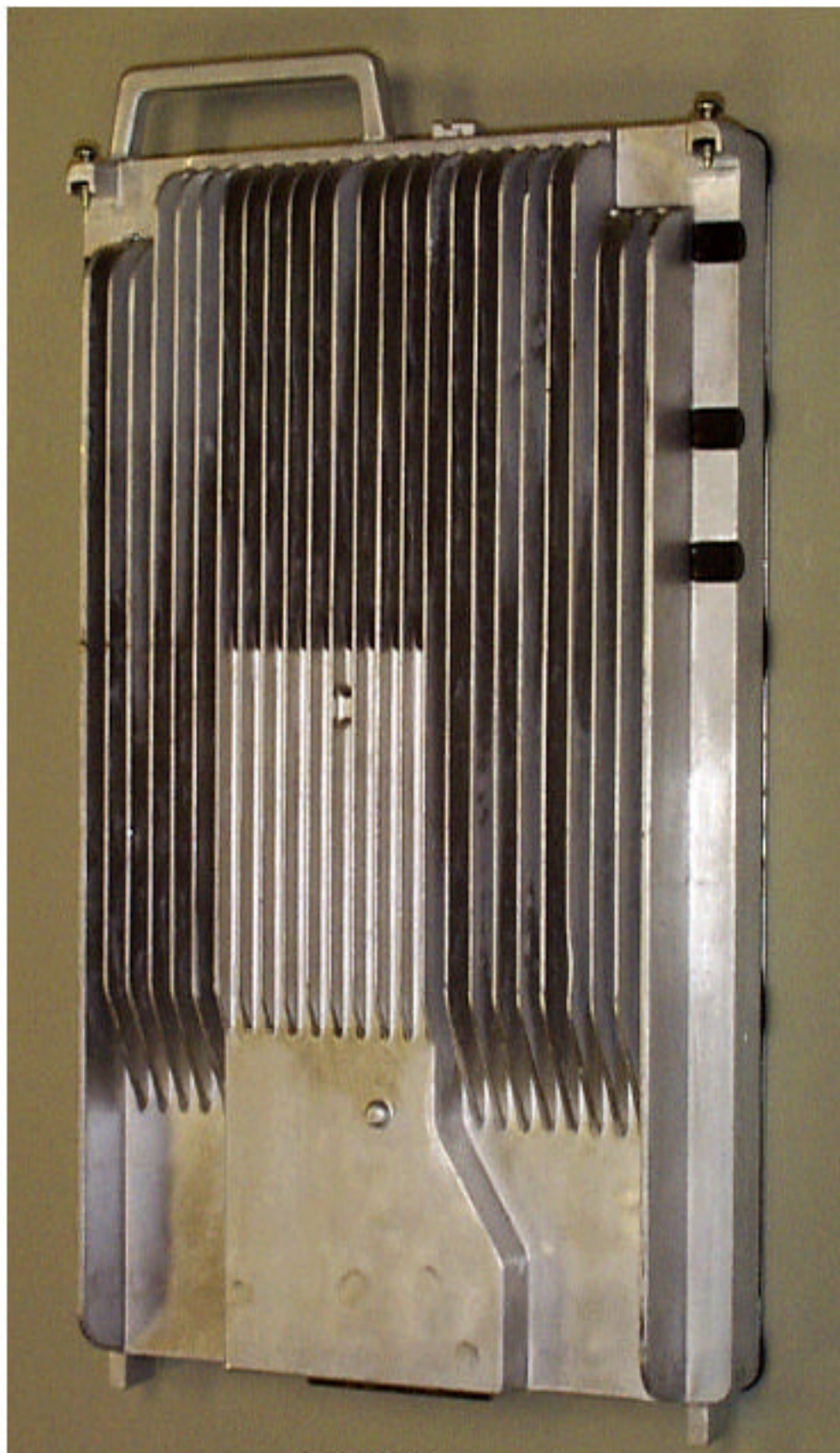
**IHET5YP1
SC4812T 800 MHz
CSM1 Back**



IHET5YP1
SC4812T 800 MHz
CSM2 Front



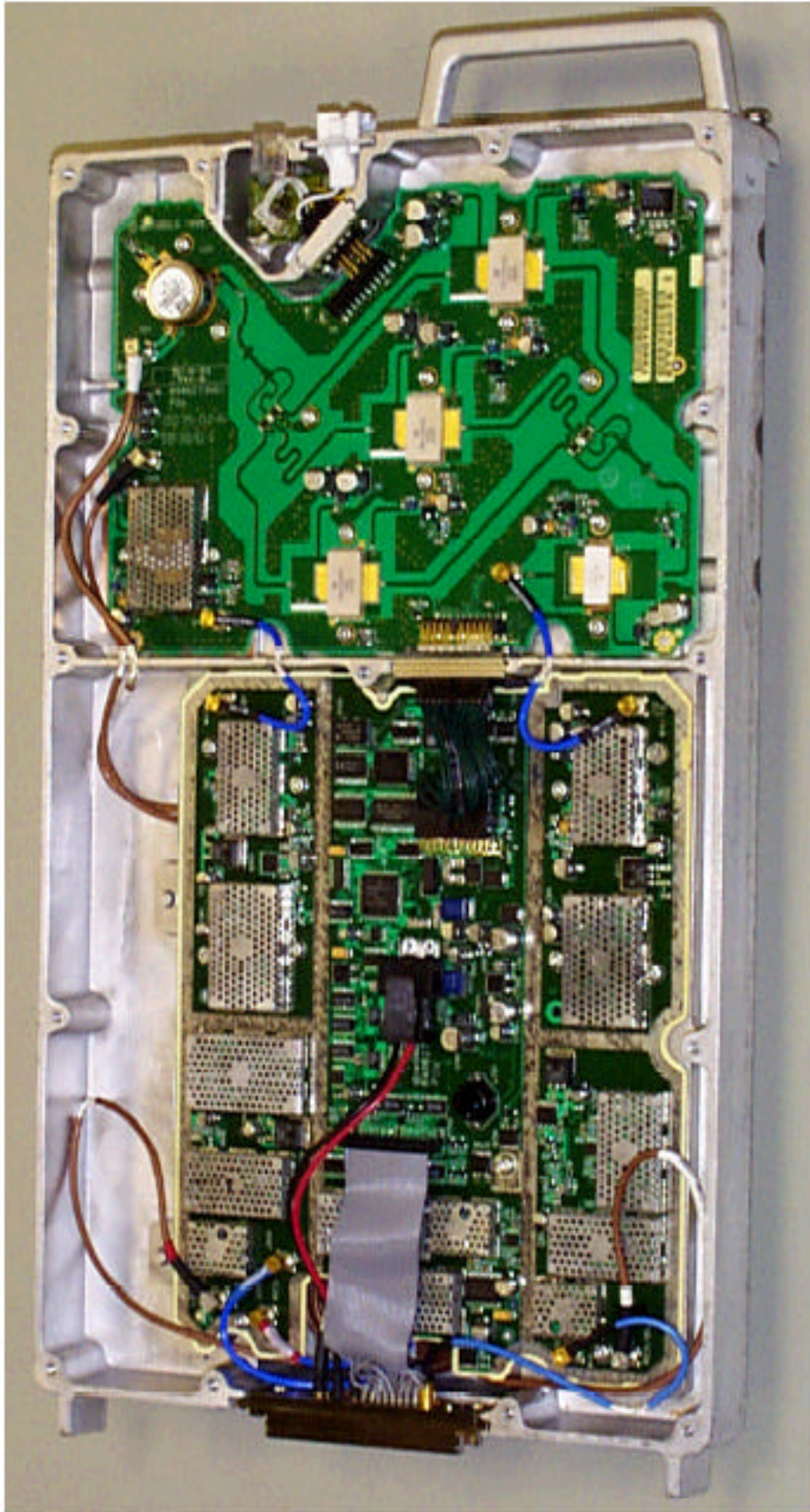
**IHET5YP1
SC4812T 800 MHz
CSM2 Back**



**IHET5YP1
SC4812T 800 MHz
LPA Front**



**IHET5YP1
SC4812T 800 MHz
LPA Back**



IHET5YP1
SC4812T 800 MHz
LPA without Cover