

# Motorola Mobility LLC

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## Variation Attestation

To Federal Communications commission:

We hereby declares

Initial Model: XT1924-4/XT1924-5, FCC ID: IHDT56XA5

Variant Model: XT1924-3, FCC ID: IHDT56XA6

XT1924-3 is a variant version of XT1924-4/XT1924-5, which was granted on Feb. 22, 2018. All the circuit and PCB layout of XT1924-4/XT1924-5 and XT1924-3 were identical, some difference of population/depoulation to enable support of different cellular bands via software. The Chipset of XT1924-4/XT1924-5 is MSM8917, While the Chipset of XT1924-3 is MSM8937. The MSM8917 and MSM8937 have the same RF Character and parameter. And also the differences between MSM8917 and MSM8937 are as bellows:

- (i) The package name of MSM8937 and MSM8917 is different;
- (ii) The core of MSM 8937 and MSM 8917 is different;
- (iii) The memory size of MSM8937 and MSM8917 is different;
- (iv) The Graphics engine of MSM8937 and MSM8917 is different

|                           | MSM8937   | MSM8917   |
|---------------------------|---|---|
| <b>Process technology</b> | 28 nm   | 28 nm   |
| <b>Package</b>            | 727 NSP: 14 × 12 × 0.91 mm; 0.4 mm pitch  | 720 NSP: 14 × 12 × 0.91 mm; 0.4 mm pitch  |
| <b>Core</b>               | Octa core 1.4 GHz ARM Cortex-A53 64-bit application processors with 512 kB L2 cache | Quad core 1.2 GHz ARM Cortex-A53 64-bit application processors with 512 kB L2 cache |
| <b>Memory</b>             | LPDDR3 SDRAM; 32-bit wide; up to 800 MHz<br>Discrete (non-POP) package              | LPDDR3 SDRAM; 32-bit wide; up to 667 MHz<br>Discrete (non-POP) package              |
| <b>Modem</b>              | QDSP6 v56 core<br>768 kB L2 cache<br>• DSDS<br>• LTE 2 × 10 MHz CA                  | QDSP6 v56 core<br>768 kB L2 cache<br>• DSDS<br>• LTE 2 × 10 MHz CA                  |
| <b>RF</b>                 | WTR2965 (LTE) paired with another WTR2965 for CA                                    | WTR2965 (LTE) paired with another WTR2965 for CA                                    |
| <b>Graphics engine</b>    | Qualcomm Adreno 505; up to 450 MHz  | Qualcomm Adreno 308; up to 500 MHz  |
| <b>Graphics memory</b>    | 128 kB unified SRAM pool on-chip memory (GMEM)                                      | 128 kB unified SRAM pool on-chip memory (GMEM)                                      |
| <b>SLIMbus</b>            | One port SLIMbus interface to WCD9326 (optional)                                    | One port SLIMbus interface to WCD9326 (optional)                                    |

Based on the similarities between these devices,

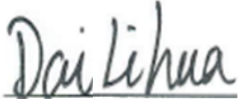
FCC WWAN RF reports of GSM 850/1900, WCDMA850/1900, LTE B5/7 issued by Sporton International Inc. for model: XT1924-4/XT1924-5 remain representative of model: XT1924-3 demonstrates compliance for Part 22/24/27 standard.

In addition, the XT1924-3 is also re-tested to ensure the compliance is kept:

- (i) FCC Part 15B is full test;
- (ii) FCC Part27M(LTE B38/41) and FCC Part27D(LTE B40) are full test for additional Bands from XT1924-3;

(iii) FCC Part2.1093 (LTE B38/40/41) is full test for additional Bands from XT1924-3;

Sincerely,

A handwritten signature in cursive script that reads "Dailihua". The signature is written in black ink and is positioned above a thin horizontal line.

Dailihua  
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