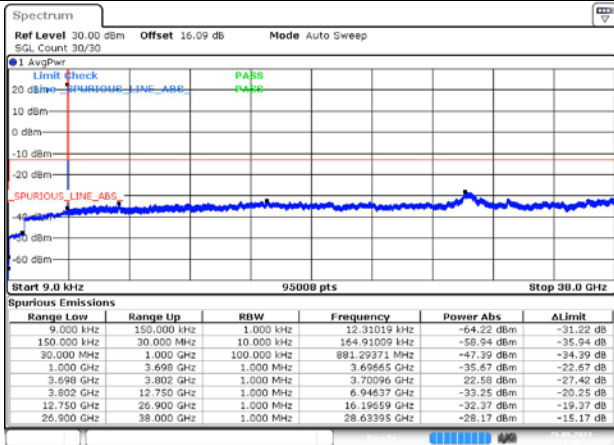


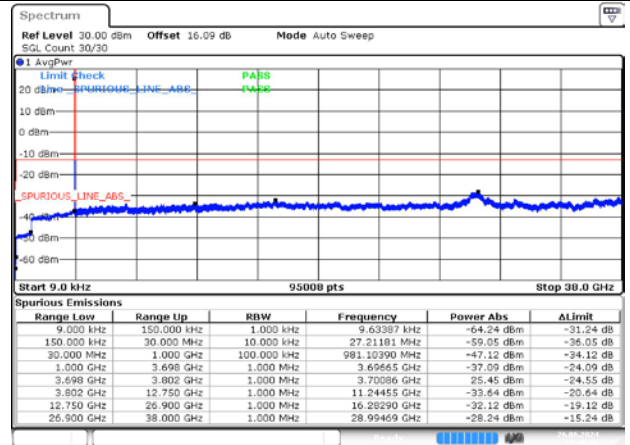
DC_7A_n78A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



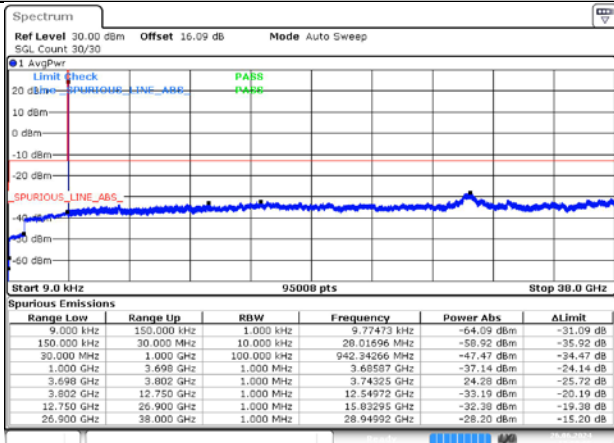
Date: 26 JUN 2024 17:49:11

LCH / DFT-QPSK / Edge_1RB_Left



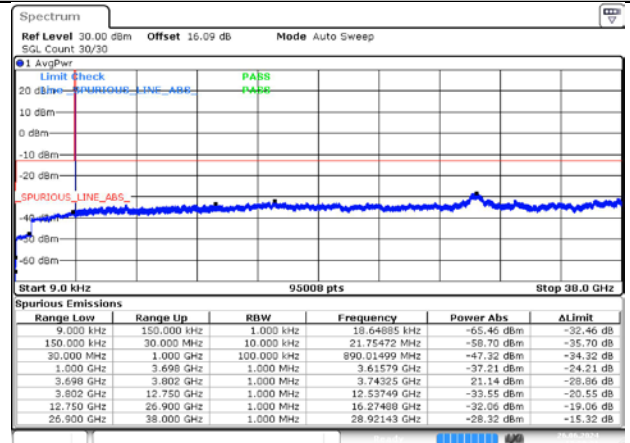
Date: 26 JUN 2024 17:49:37

MCH / DFT-Pi2BPSK / Edge_1RB_Left



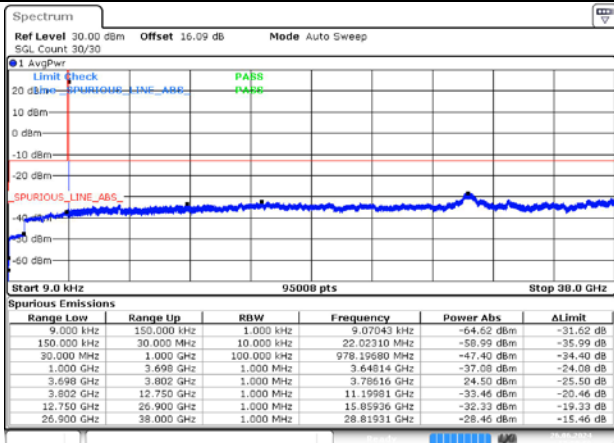
Date: 26 JUN 2024 17:50:05

MCH / DFT-QPSK / Edge_1RB_Left



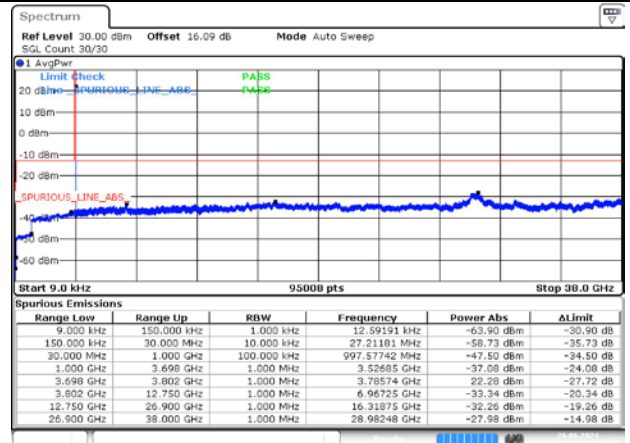
Date: 26 JUN 2024 17:50:31

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 17:50:58

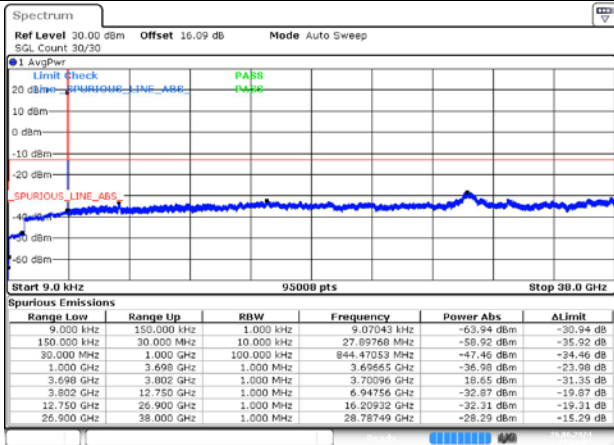
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 17:51:24

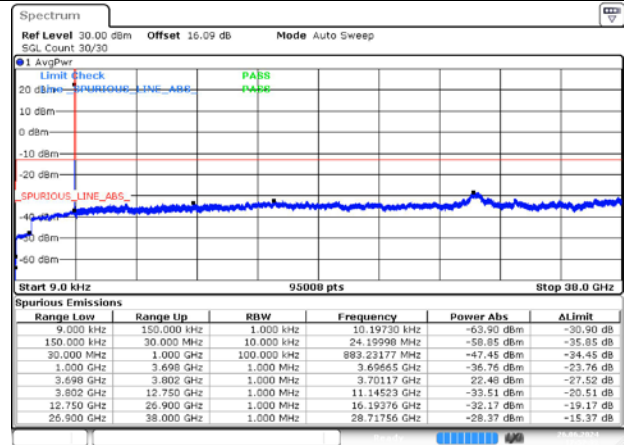
DC_7A_n78A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



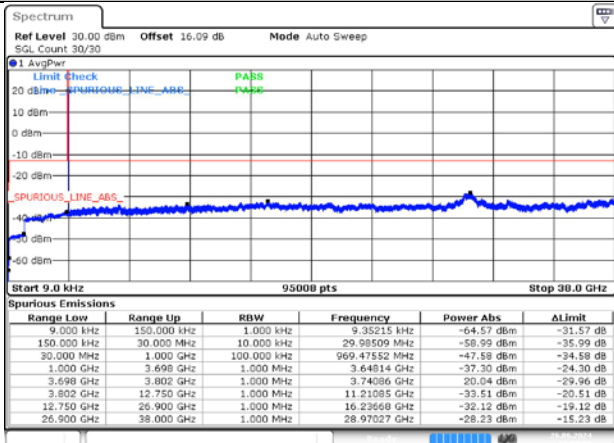
Date: 26 JUN 2024 17:52:05

LCH / DFT-QPSK / Edge_1RB_Left



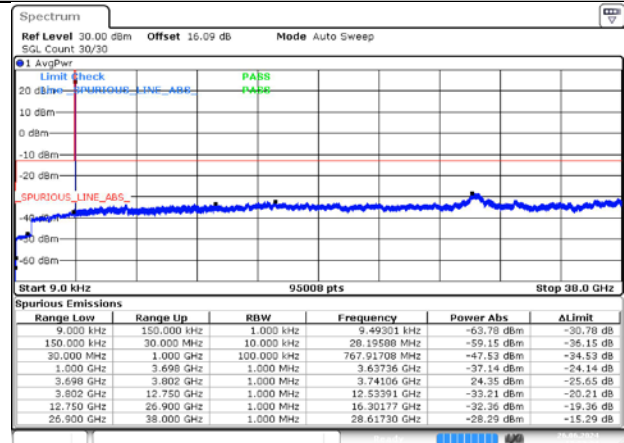
Date: 26 JUN 2024 17:52:31

MCH / DFT-Pi2BPSK / Edge_1RB_Left



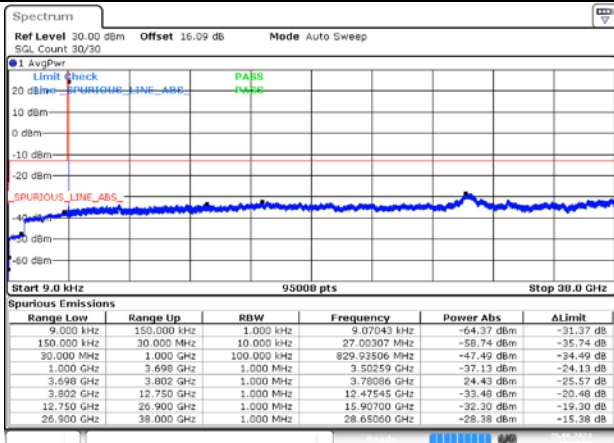
Date: 26 JUN 2024 17:52:59

MCH / DFT-QPSK / Edge_1RB_Left



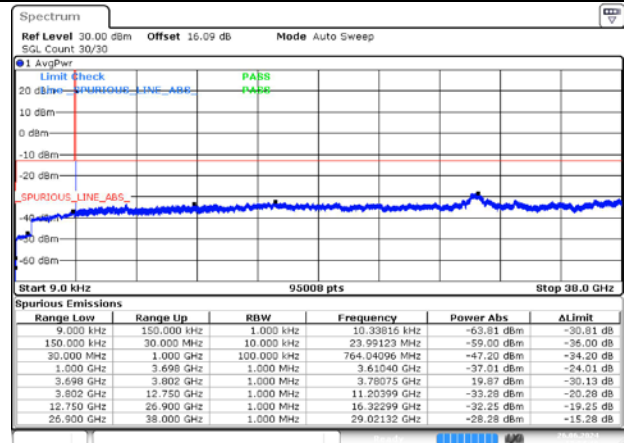
Date: 26 JUN 2024 17:53:25

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 17:53:52

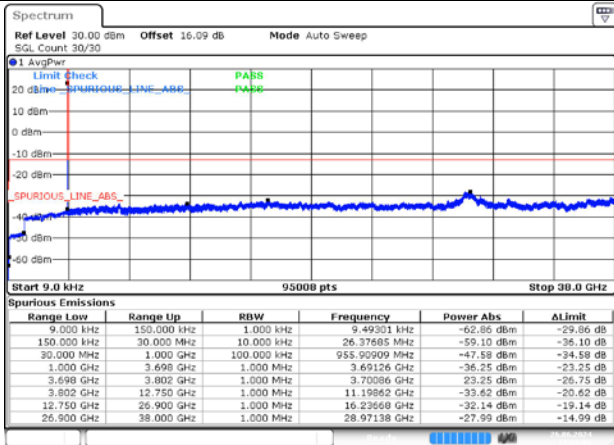
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 17:54:18

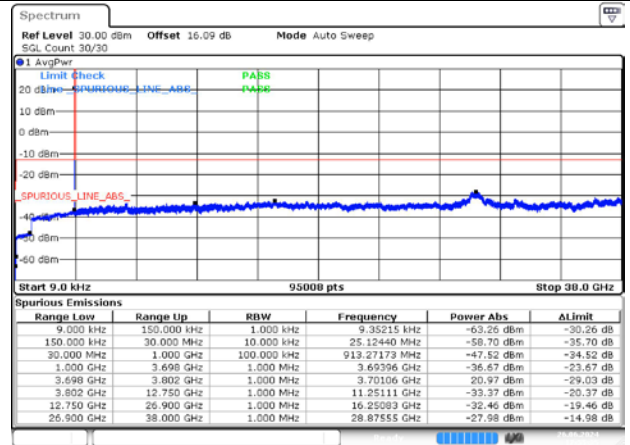
DC_7A_n78A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



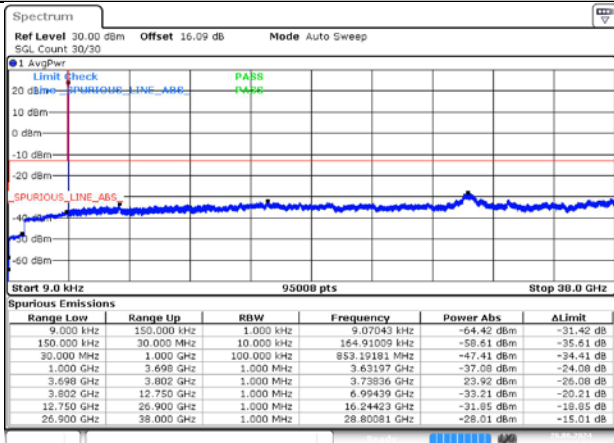
Date: 26 JUN 2024 17:54:59

LCH / DFT-QPSK / Edge_1RB_Left



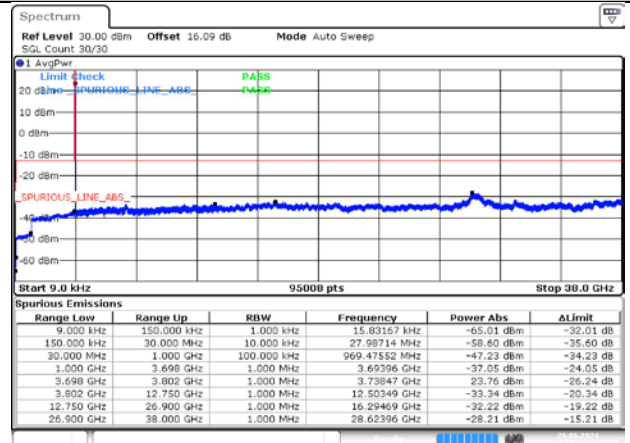
Date: 26 JUN 2024 17:55:25

MCH / DFT-Pi2BPSK / Edge_1RB_Left



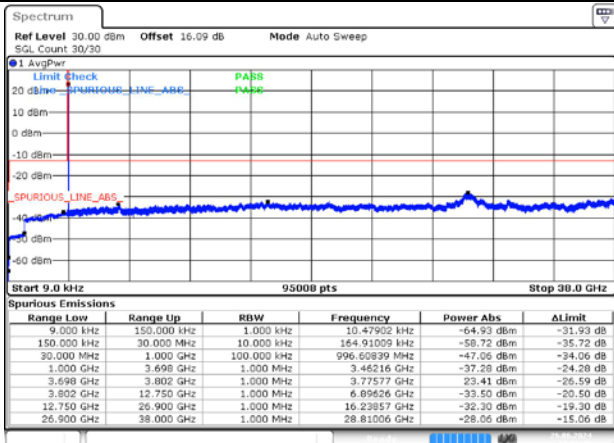
Date: 26 JUN 2024 17:55:53

MCH / DFT-QPSK / Edge_1RB_Left



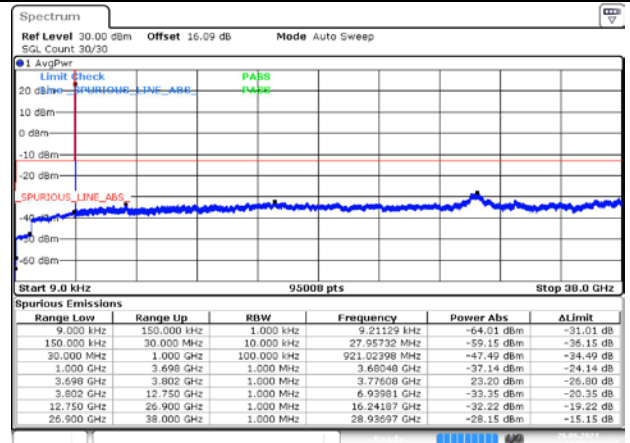
Date: 26 JUN 2024 17:56:19

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 17:56:48

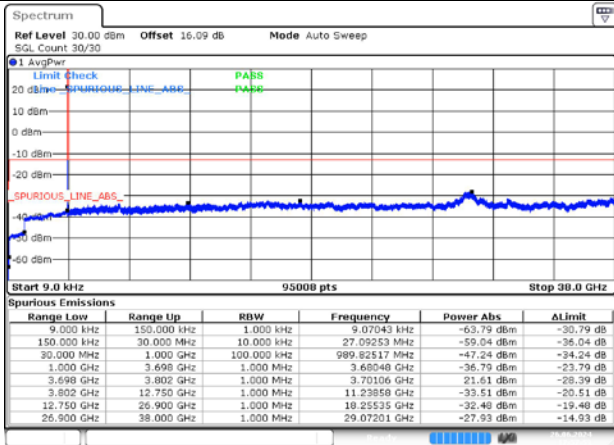
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 17:57:12

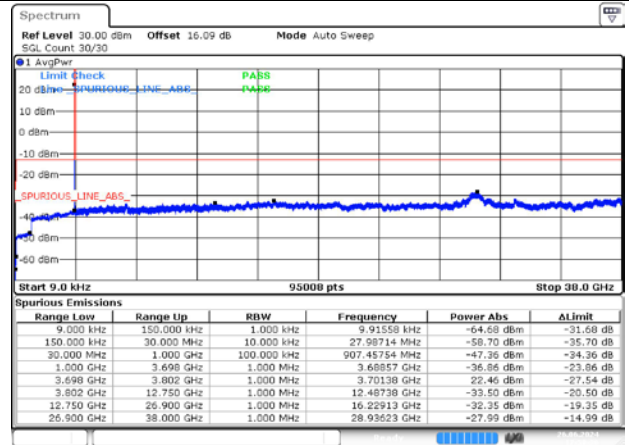
DC_7A_n78A / 30KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



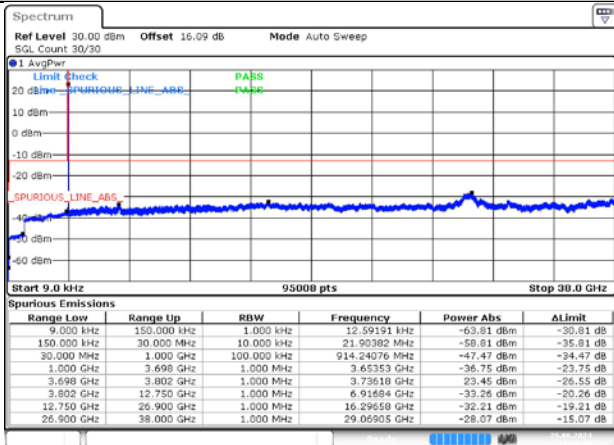
Date: 26 JUN 2024 17:57:53

LCH / DFT-QPSK / Edge_1RB_Left



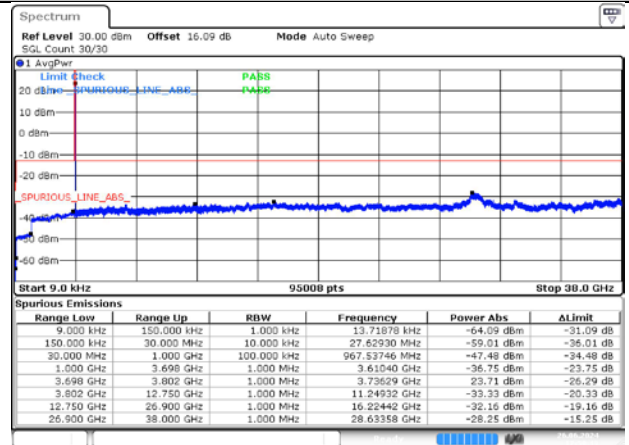
Date: 26 JUN 2024 17:58:20

MCH / DFT-Pi2BPSK / Edge_1RB_Left



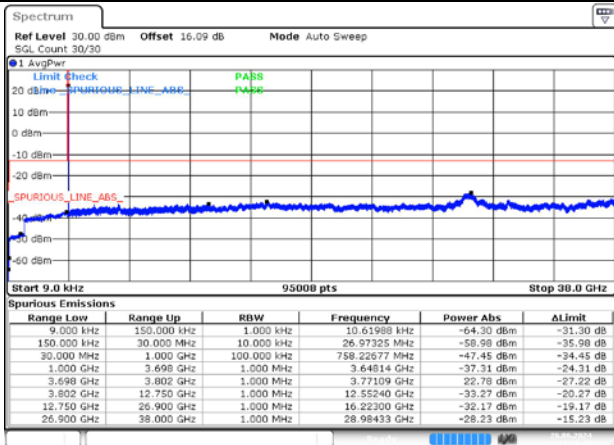
Date: 26 JUN 2024 17:58:47

MCH / DFT-QPSK / Edge_1RB_Left



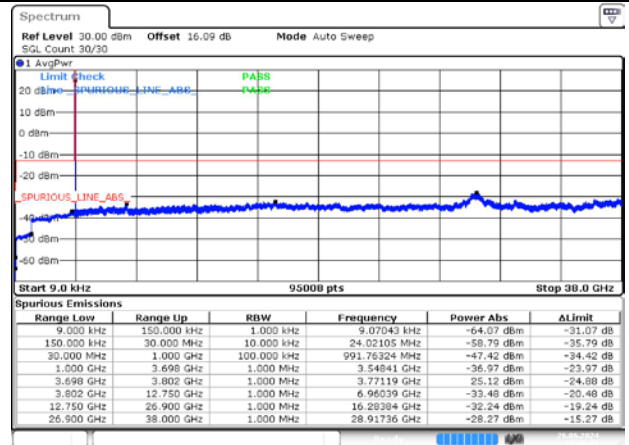
Date: 26 JUN 2024 17:59:13

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 17:59:41

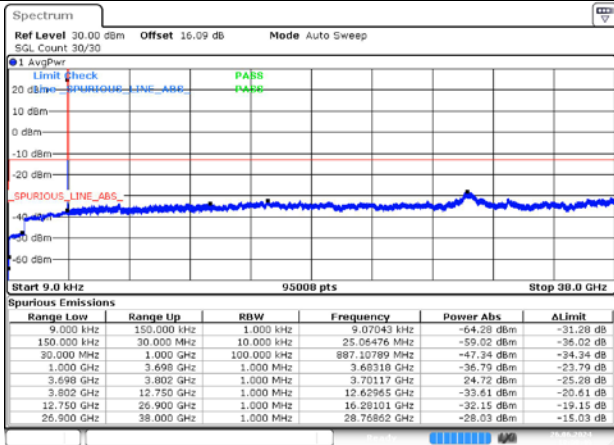
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:00:07

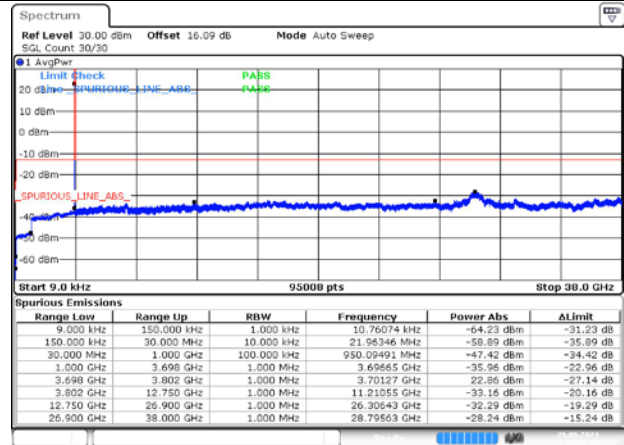
DC_7A_n78A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



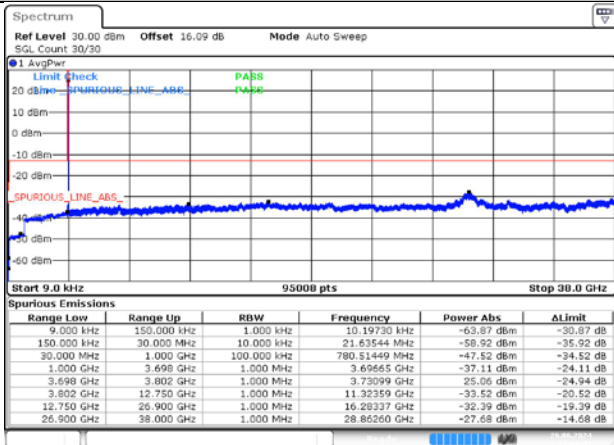
Date: 26 JUN 2024 18:00:48

LCH / DFT-QPSK / Edge_1RB_Left



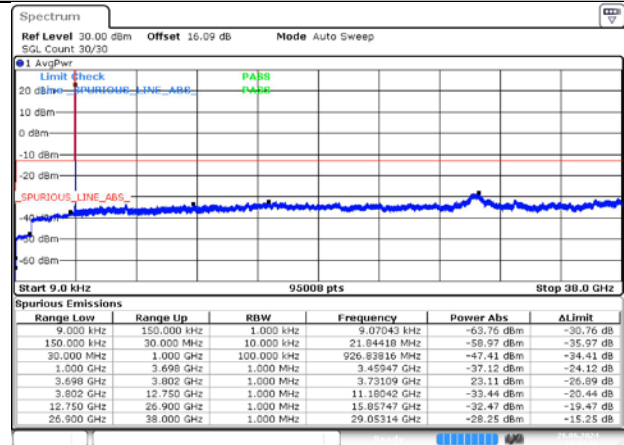
Date: 26 JUN 2024 18:01:14

MCH / DFT-Pi2BPSK / Edge_1RB_Left



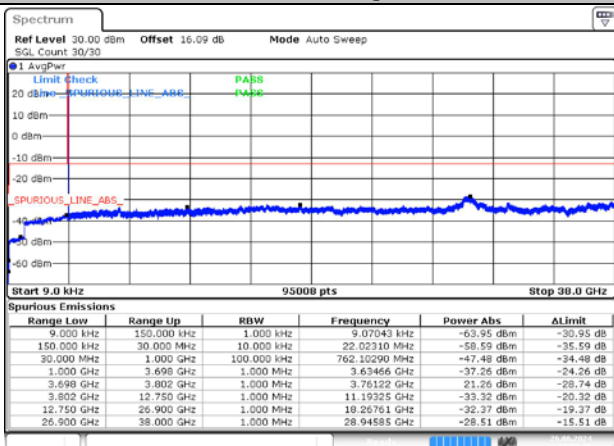
Date: 26 JUN 2024 18:01:42

MCH / DFT-QPSK / Edge_1RB_Left



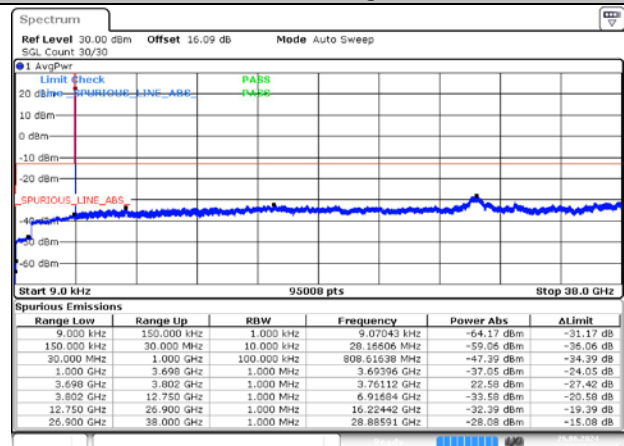
Date: 26 JUN 2024 18:02:08

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:02:35

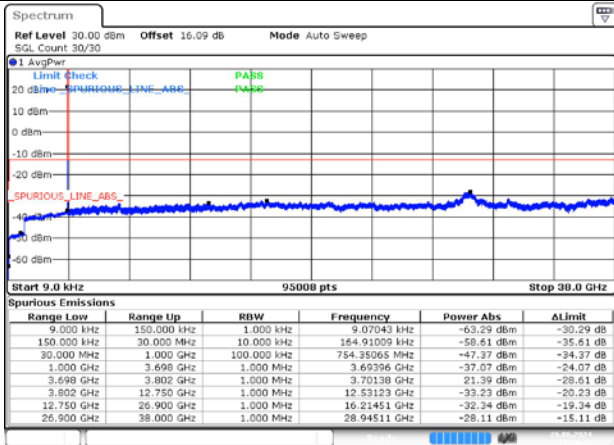
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:03:01

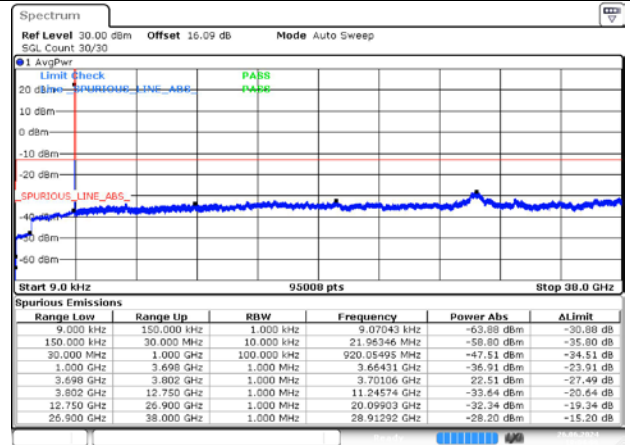
DC_7A_n78A / 30KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



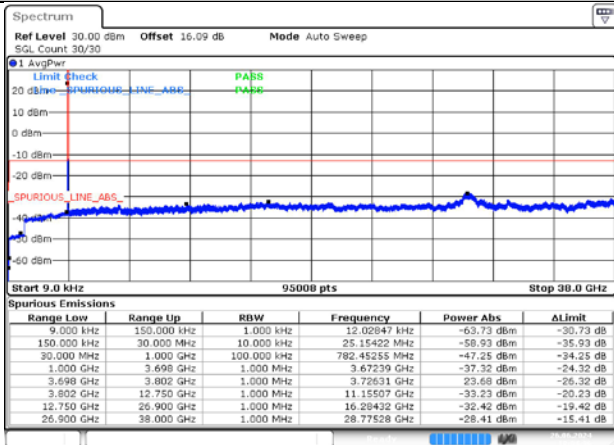
Date: 26 JUN 2024 18:03:42

LCH / DFT-QPSK / Edge_1RB_Left



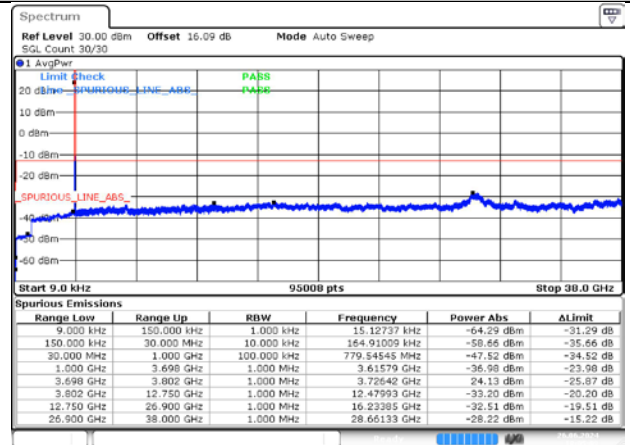
Date: 26 JUN 2024 18:04:08

MCH / DFT-Pi2BPSK / Edge_1RB_Left



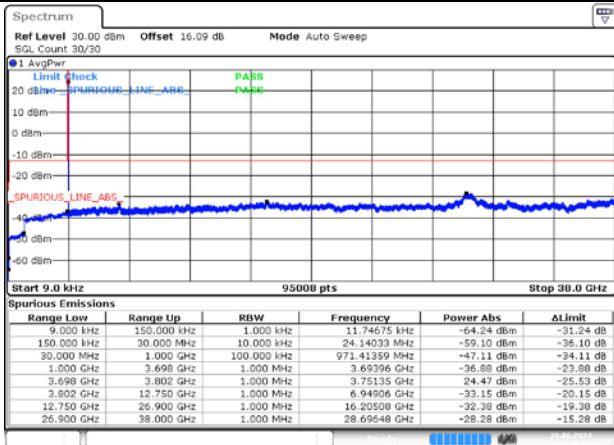
Date: 26 JUN 2024 18:04:35

MCH / DFT-QPSK / Edge_1RB_Left



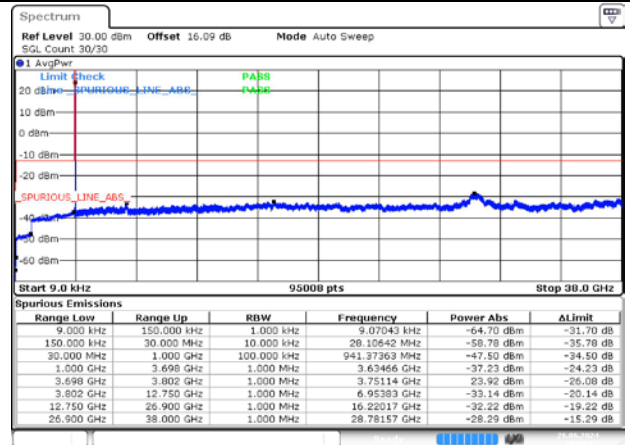
Date: 26 JUN 2024 18:05:02

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:05:29

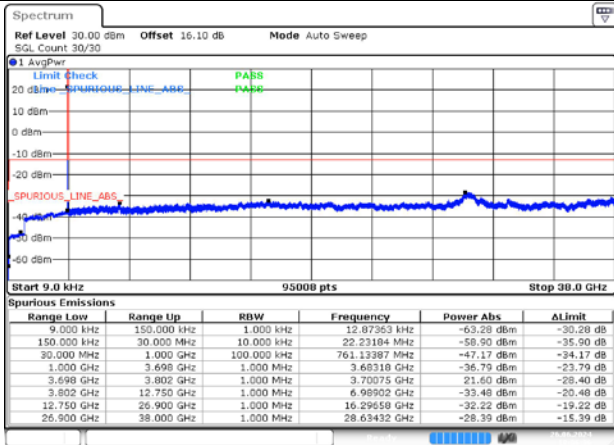
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:05:55

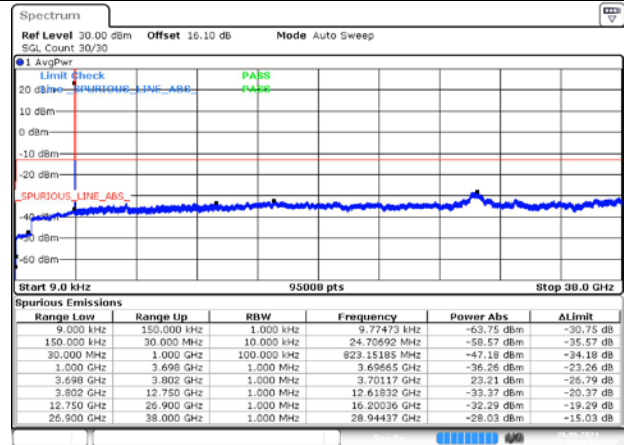
DC_7A_n78A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



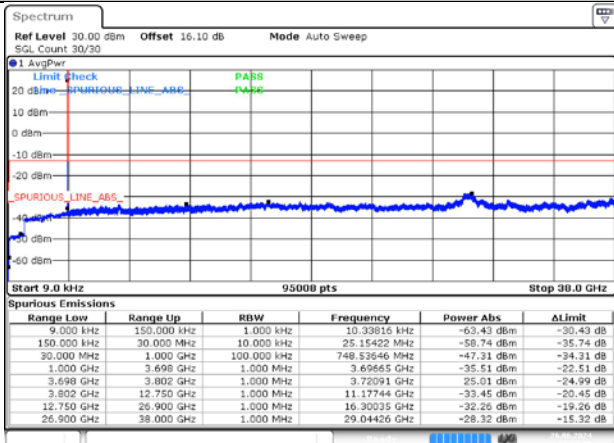
Date: 26 JUN 2024 18:06:38

LCH / DFT-QPSK / Edge_1RB_Left



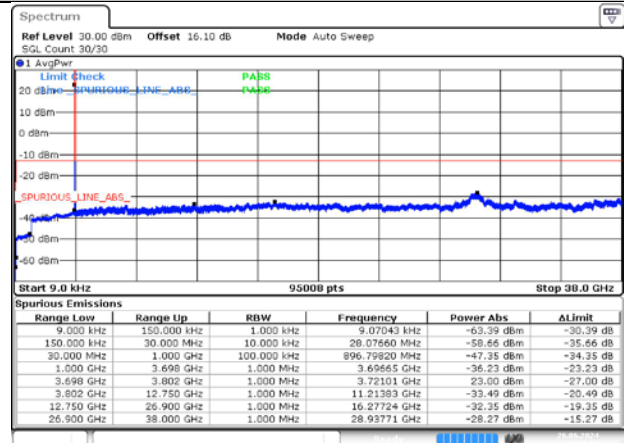
Date: 26 JUN 2024 18:07:02

MCH / DFT-Pi2BPSK / Edge_1RB_Left



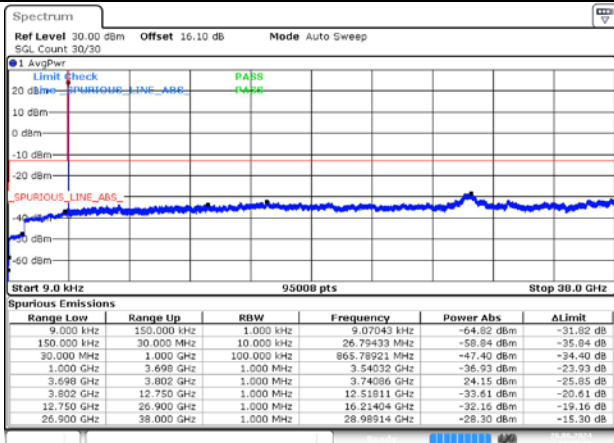
Date: 26 JUN 2024 18:07:29

MCH / DFT-QPSK / Edge_1RB_Left



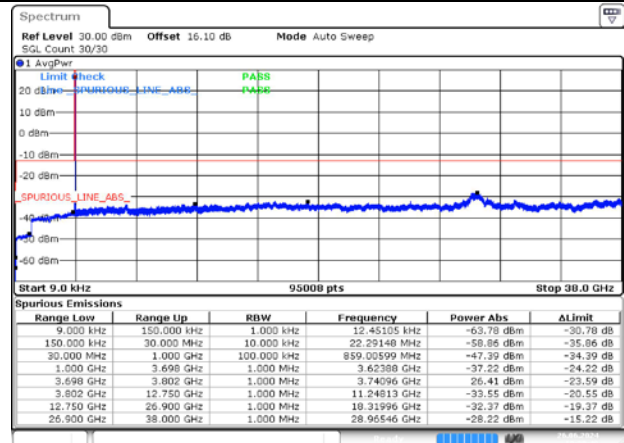
Date: 26 JUN 2024 18:07:56

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:08:23

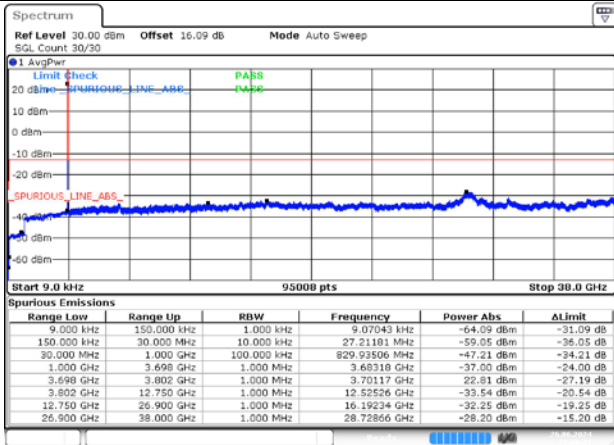
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:08:49

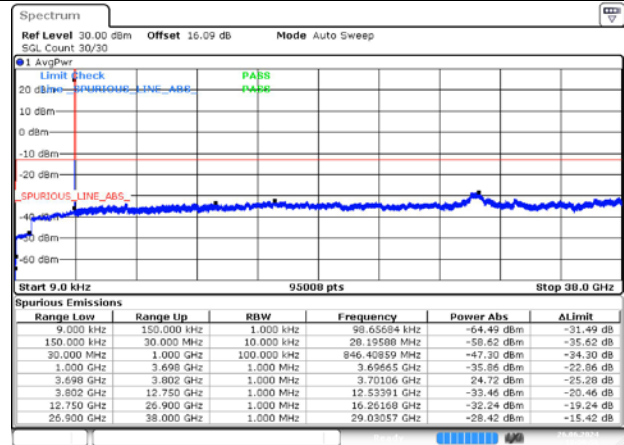
DC_7A_n78A / 30KHz / 70MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



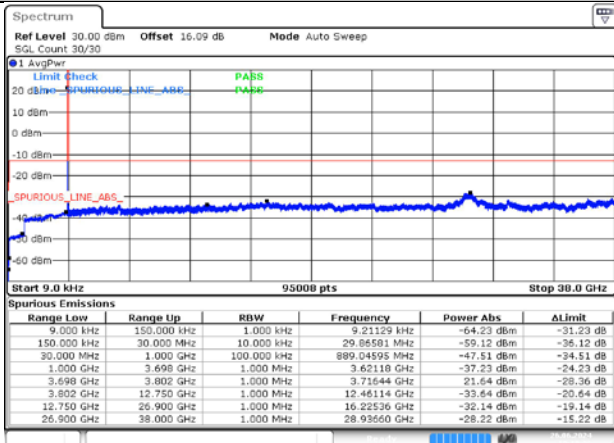
Date: 26 JUN 2024 18:09:31

LCH / DFT-QPSK / Edge_1RB_Left



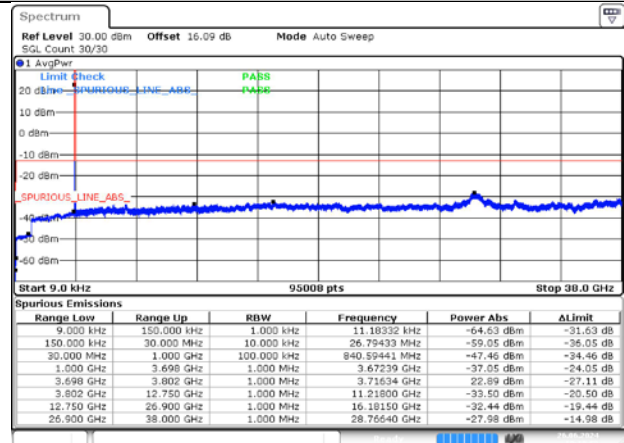
Date: 26 JUN 2024 18:09:58

MCH / DFT-Pi2BPSK / Edge_1RB_Left



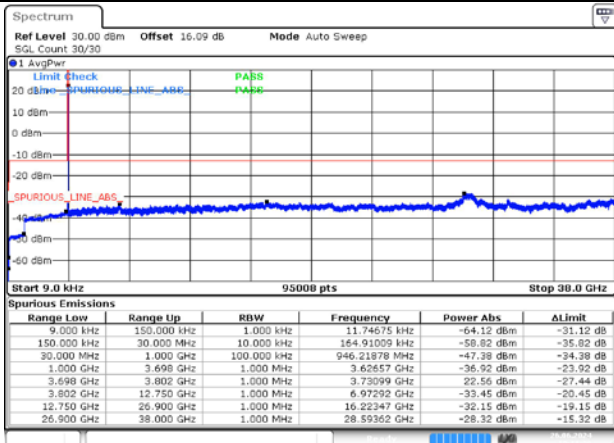
Date: 26 JUN 2024 18:10:28

MCH / DFT-QPSK / Edge_1RB_Left



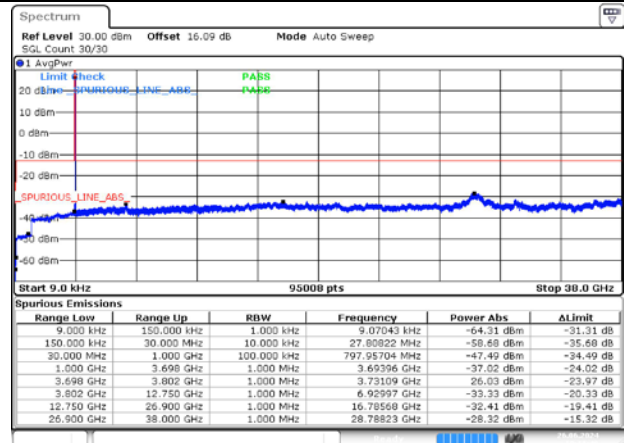
Date: 26 JUN 2024 18:10:53

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:11:21

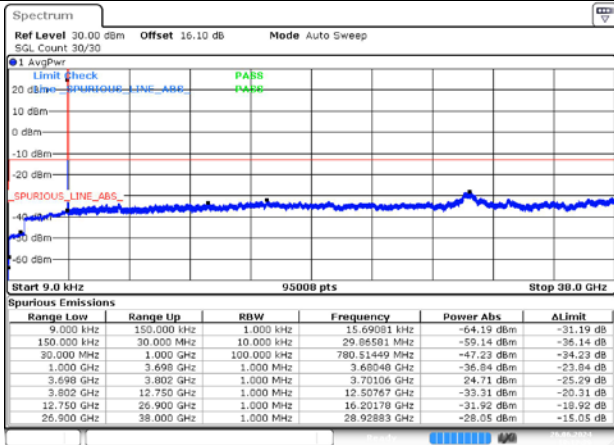
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:11:47

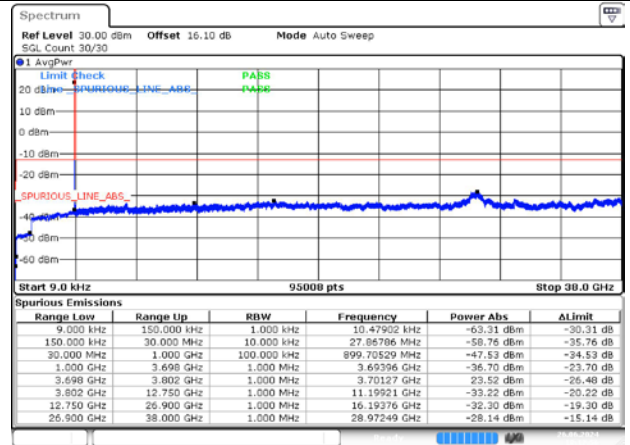
DC_7A_n78A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



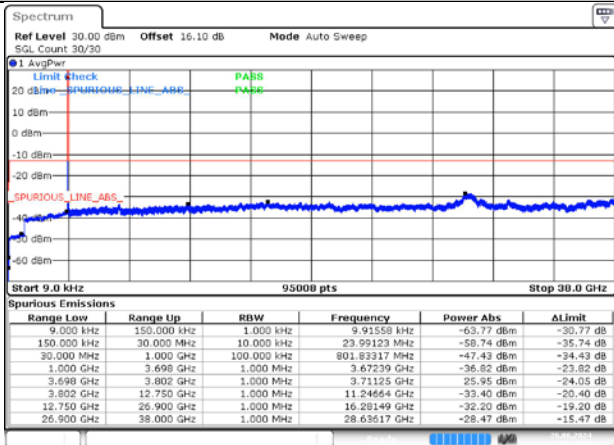
Date: 26 JUN 2024 18:12:29

LCH / DFT-QPSK / Edge_1RB_Left



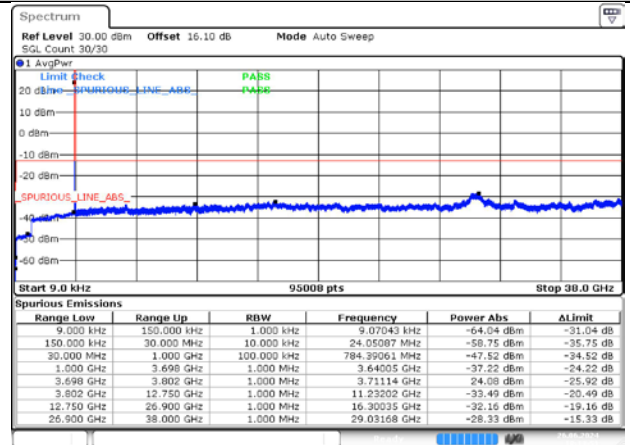
Date: 26 JUN 2024 18:12:56

MCH / DFT-Pi2BPSK / Edge_1RB_Left



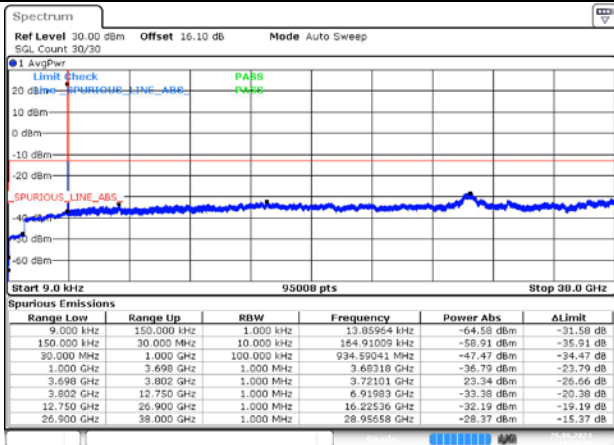
Date: 26 JUN 2024 18:13:24

MCH / DFT-QPSK / Edge_1RB_Left



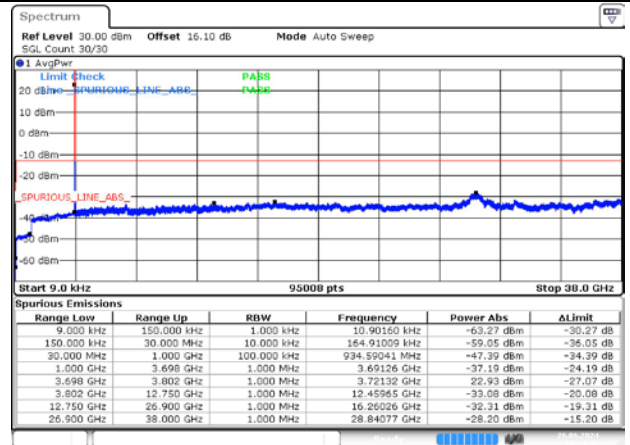
Date: 26 JUN 2024 18:13:51

HCH / DFT-Pi2BPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:14:19

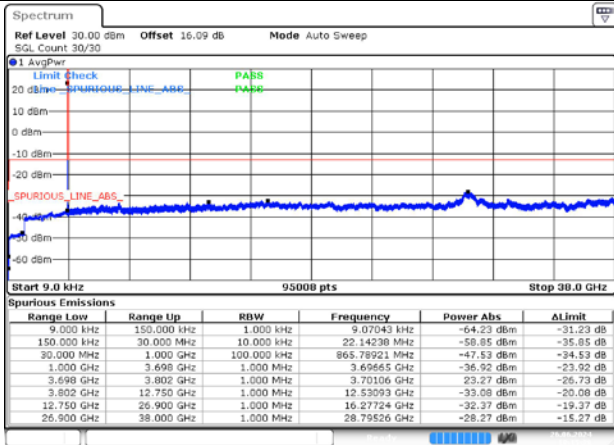
HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:14:46

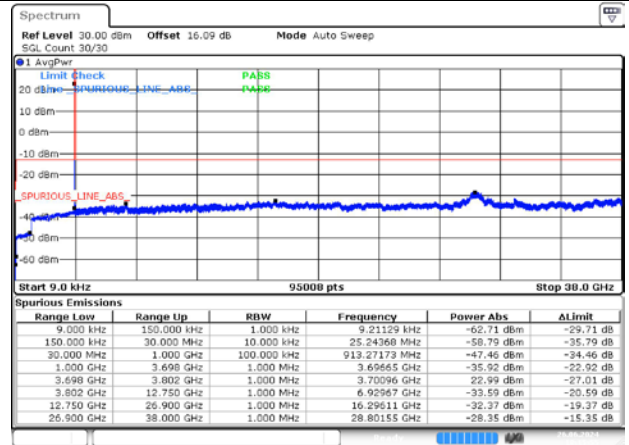
DC_7A_n78A / 30KHz / 90MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



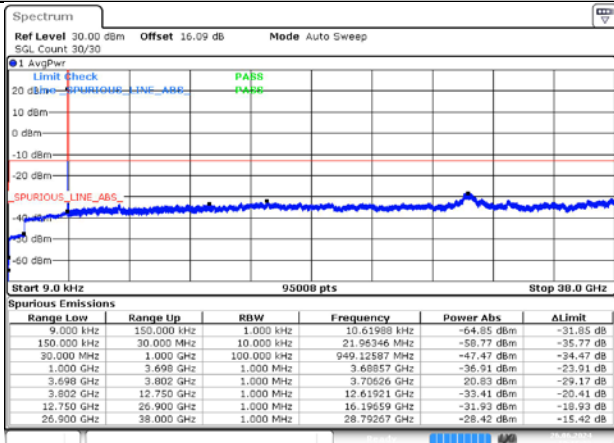
Date: 26 JUN 2024 18:15:27

LCH / DFT-QPSK / Edge_1RB_Left



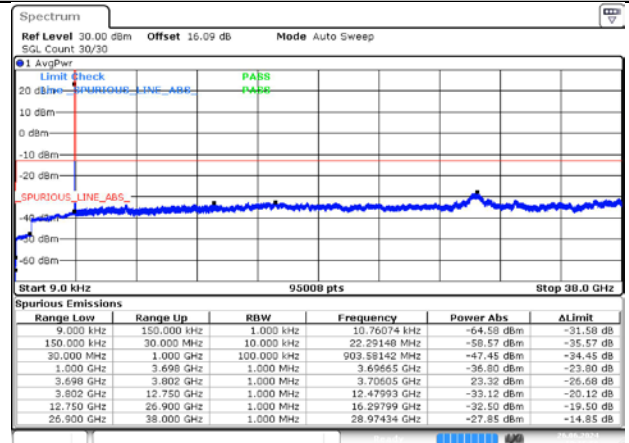
Date: 26 JUN 2024 18:15:54

MCH / DFT-Pi2BPSK / Edge_1RB_Left



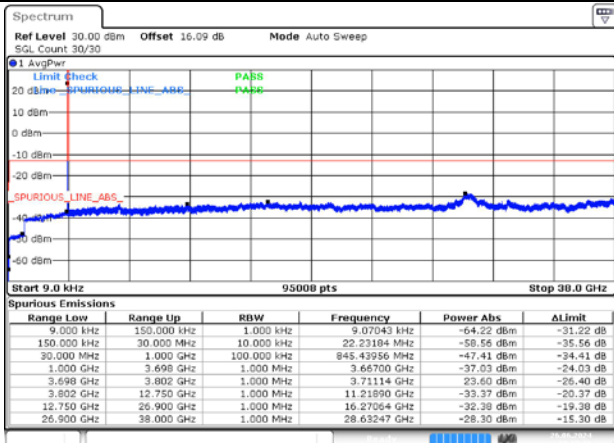
Date: 26 JUN 2024 18:16:22

MCH / DFT-QPSK / Edge_1RB_Left



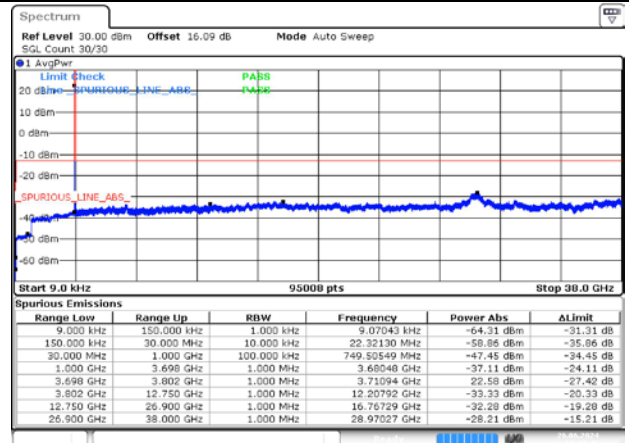
Date: 26 JUN 2024 18:16:49

HCH / DFT-Pi2BPSK / Edge_1RB_Left

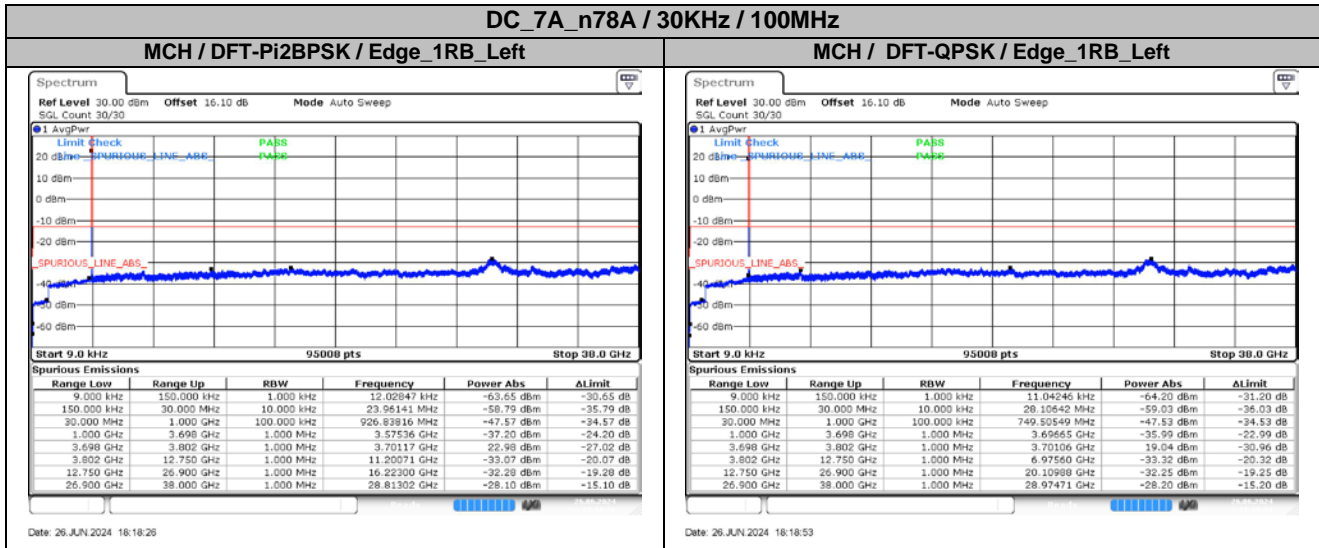


Date: 26 JUN 2024 18:17:17

HCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 18:17:44



7. Frequency Stability

7.1. Test Results

7.1.1. Frequency Error Vs Voltage

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	LV	-11.60	-0.003093	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	NV	-8.90	-0.002373	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	HV	-12.00	-0.003200	Pass

7.1.2. Frequency Error Vs Temperature

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-30°C	NV	-10.10	-0.002693	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-20°C	NV	-14.50	-0.003867	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-10°C	NV	-4.60	-0.001227	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	0°C	NV	-13.60	-0.003627	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	10°C	NV	-9.70	-0.002587	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	20°C	NV	-13.90	-0.003707	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	30°C	NV	-15.00	-0.004000	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	40°C	NV	-9.40	-0.002507	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	50°C	NV	-11.50	-0.003067	Pass

The End