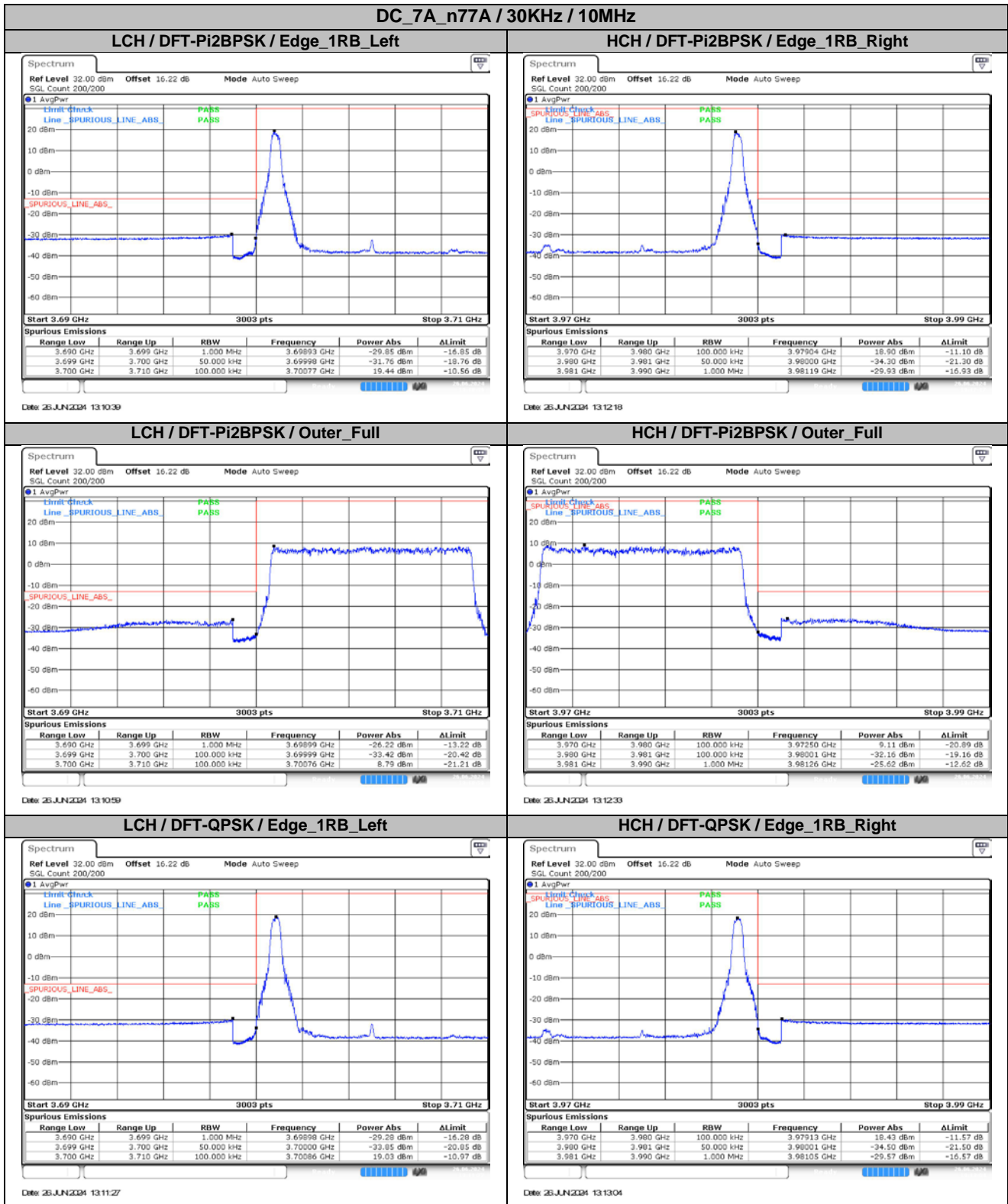
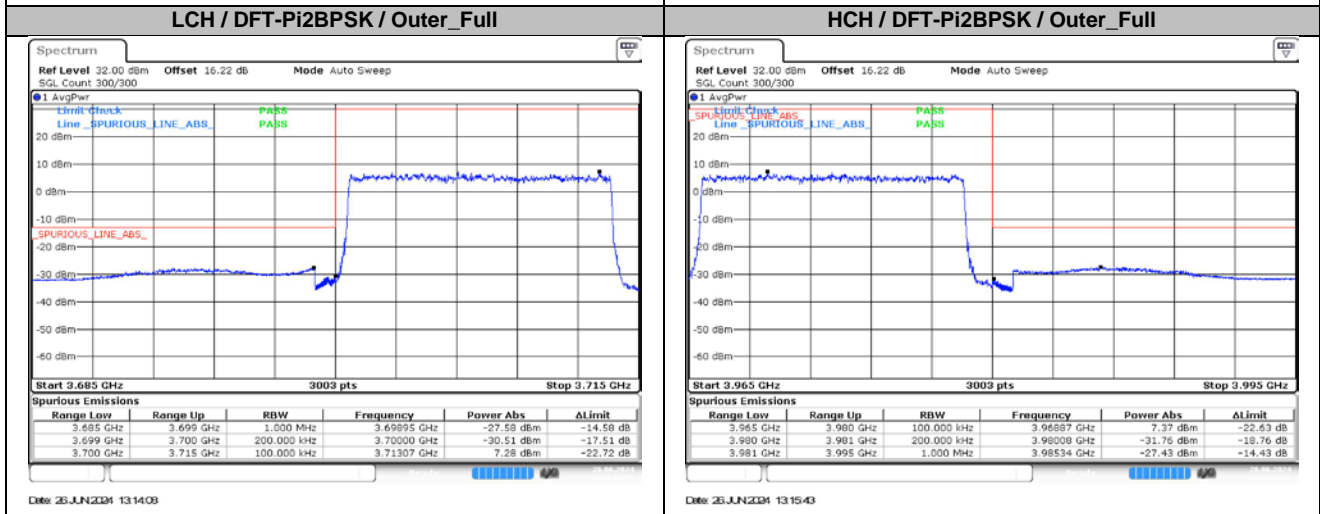
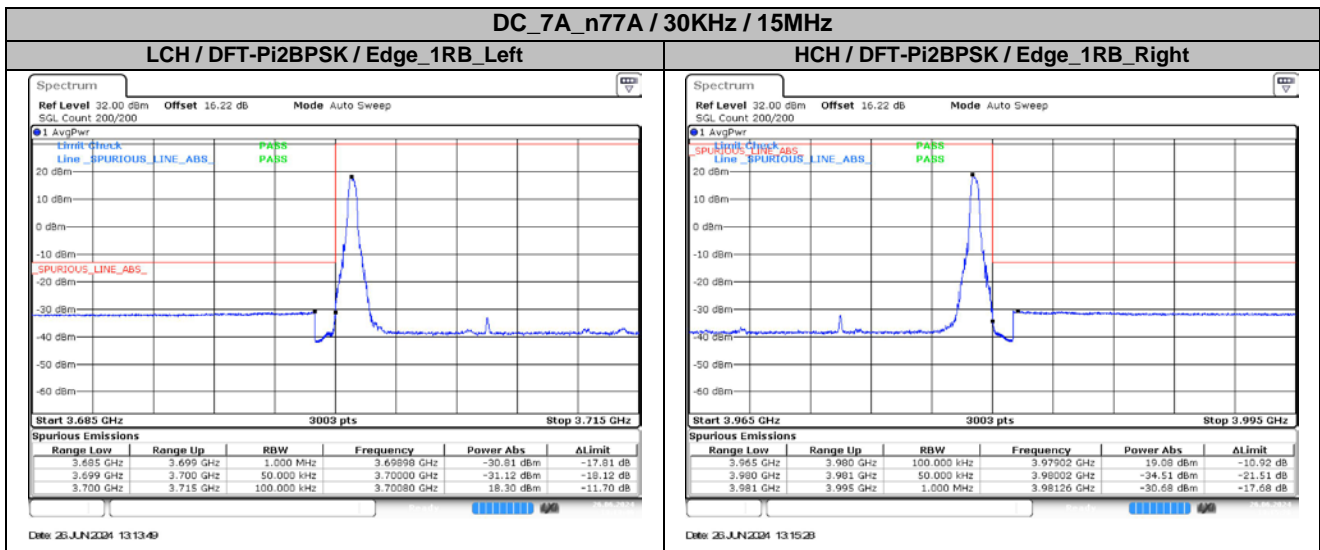
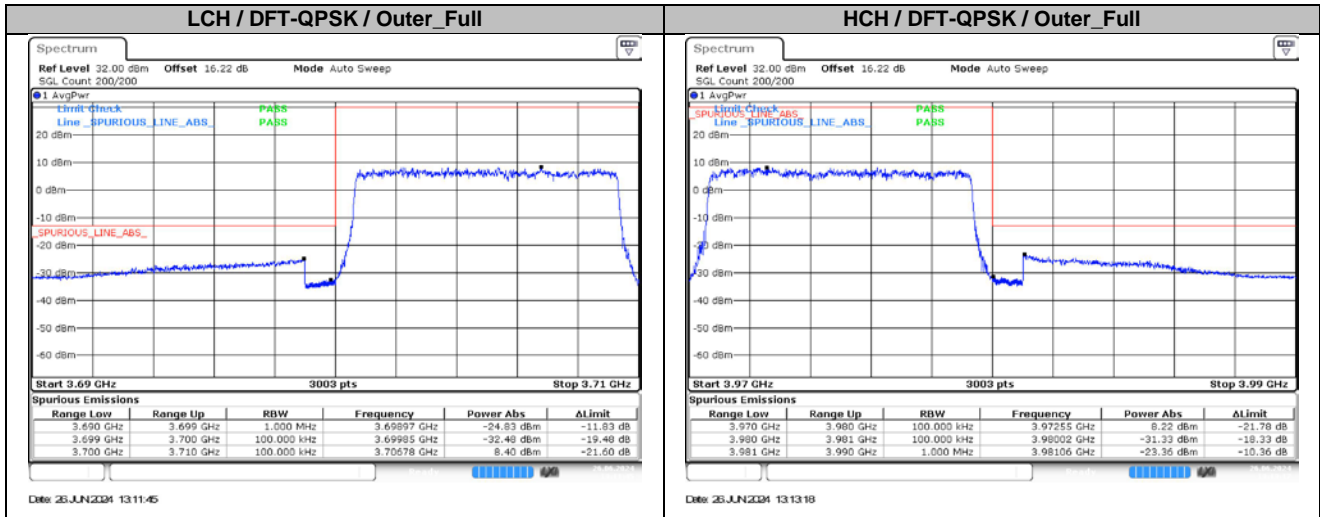
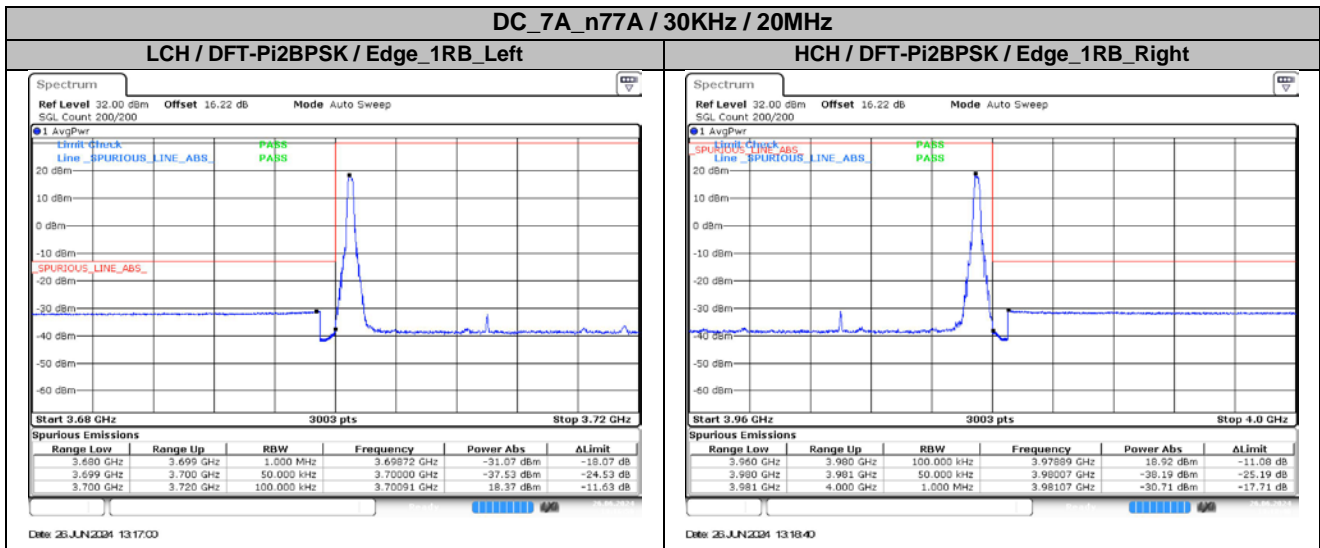
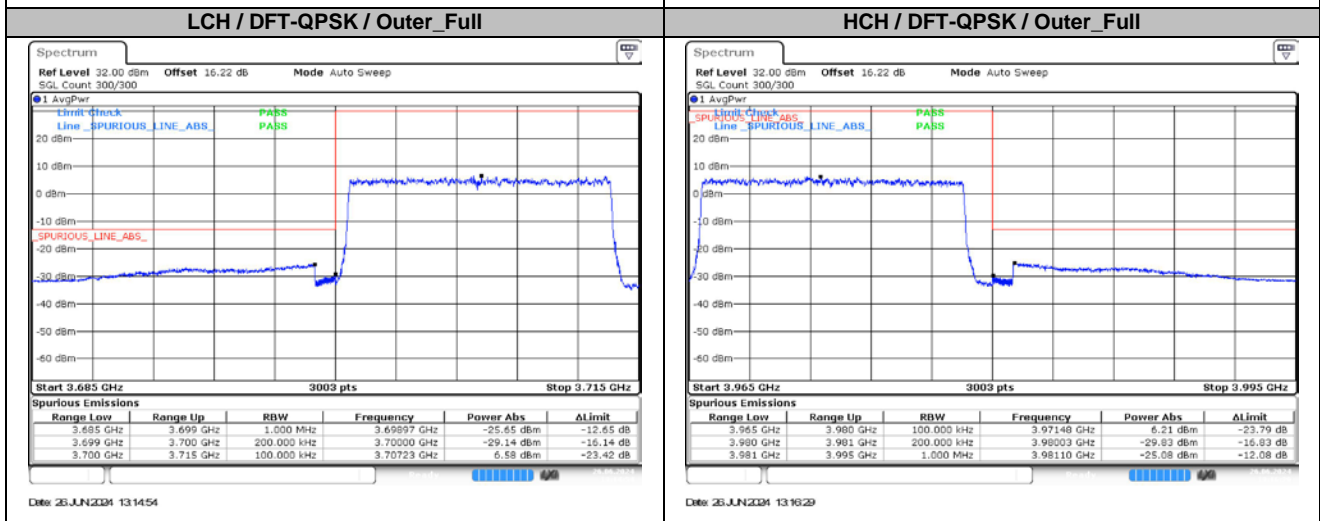
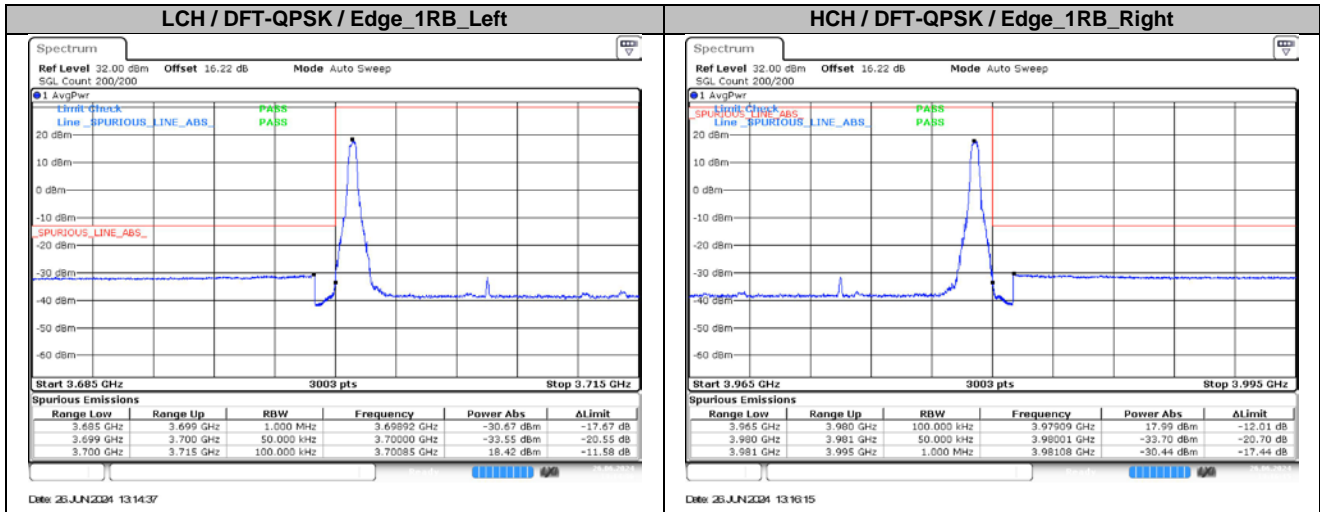


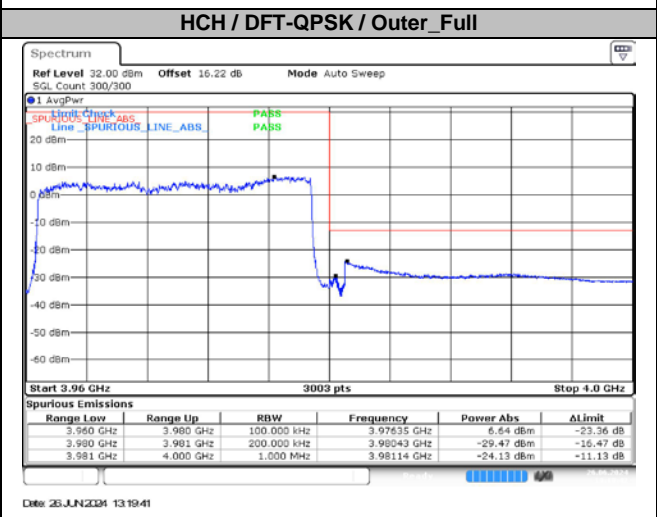
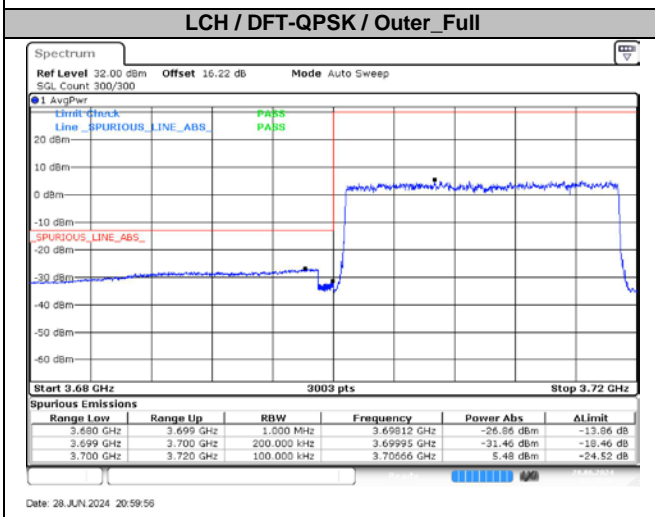
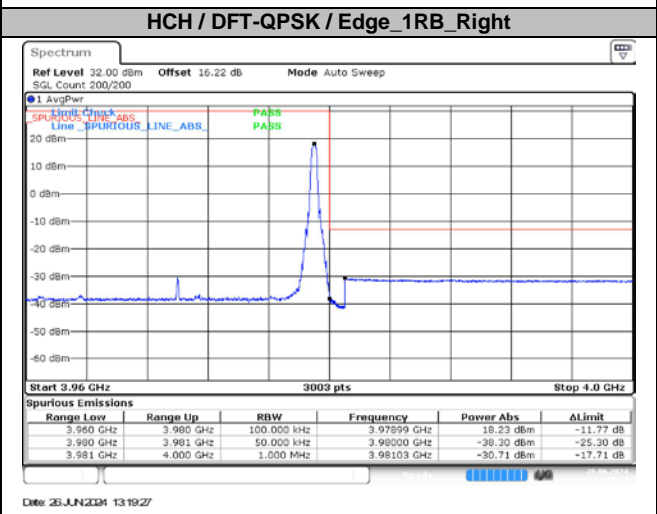
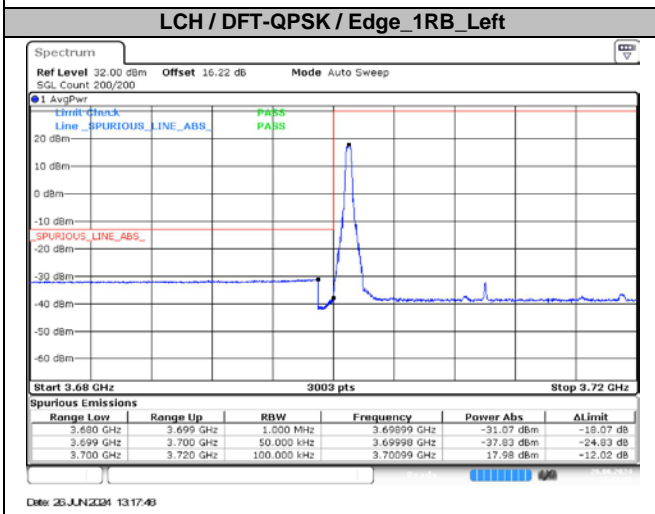
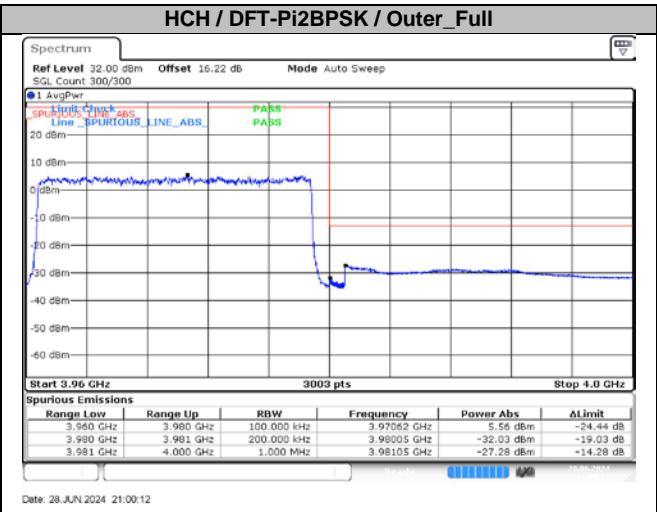
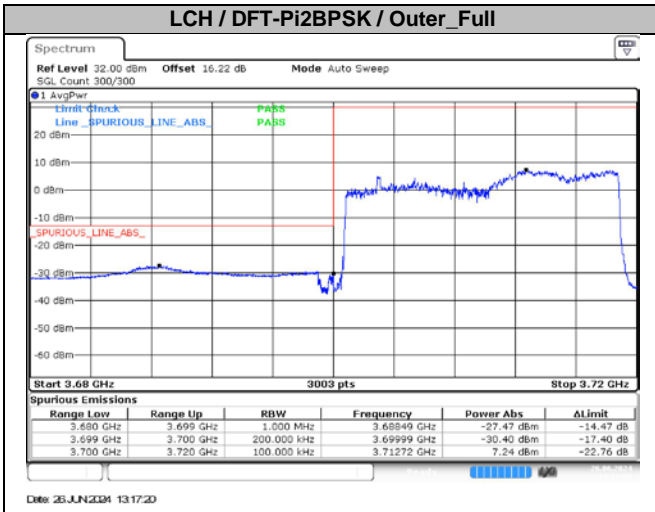
5. Conducted Band Edges

5.1. Test Plots



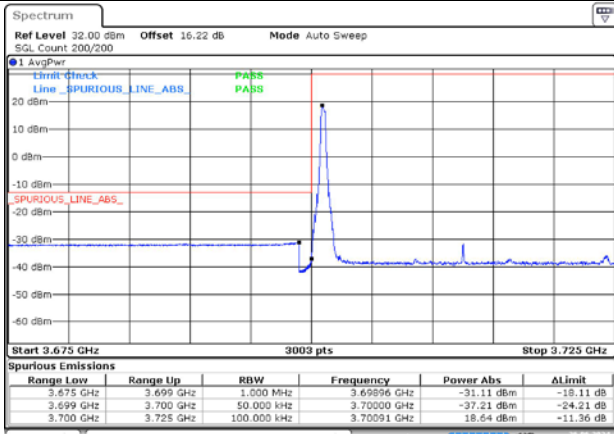






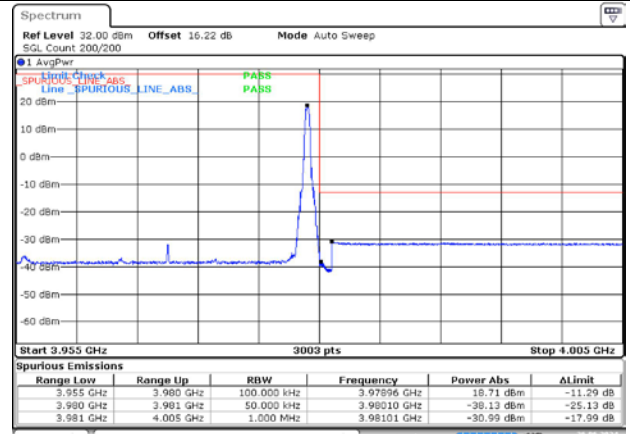
DC_7A_n77A / 30KHz / 25MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



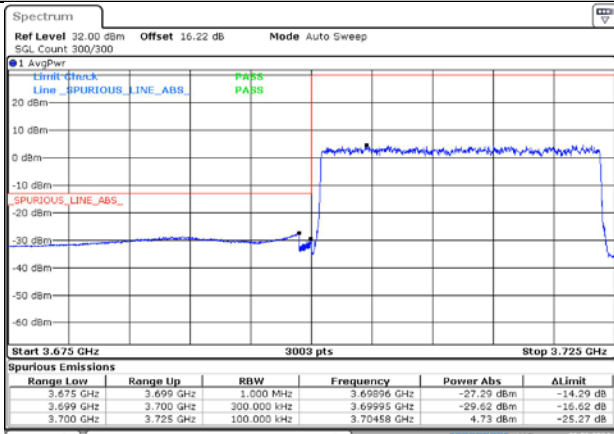
Date: 26 JUN 2024 13:20:13

HCH / DFT-Pi2BPSK / Edge_1RB_Right



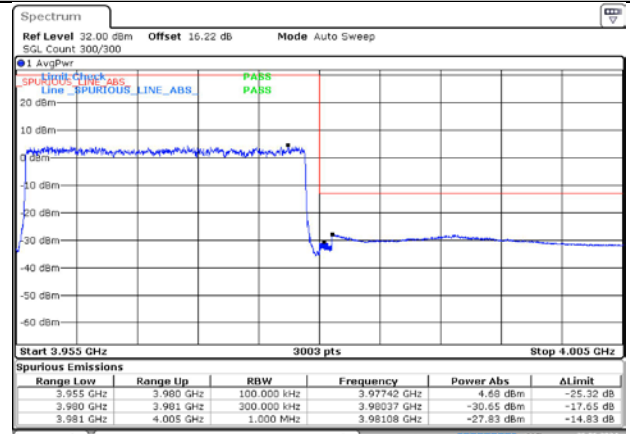
Date: 26 JUN 2024 13:21:54

LCH / DFT-Pi2BPSK / Outer_Full



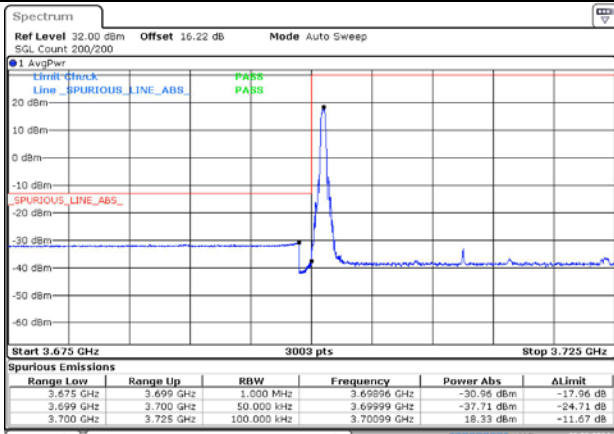
Date: 26 JUN 2024 13:20:33

HCH / DFT-Pi2BPSK / Outer_Full



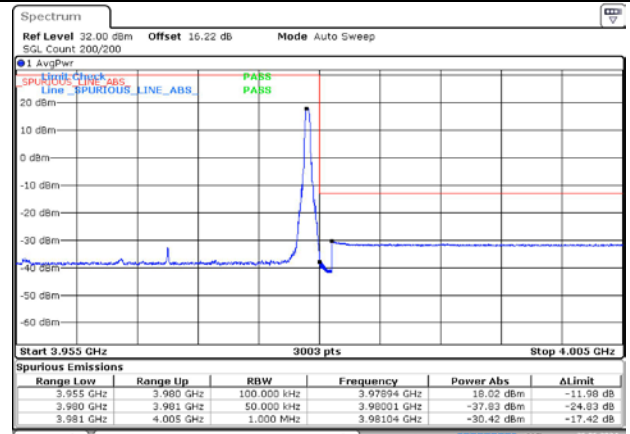
Date: 26 JUN 2024 13:22:03

LCH / DFT-QPSK / Edge_1RB_Left

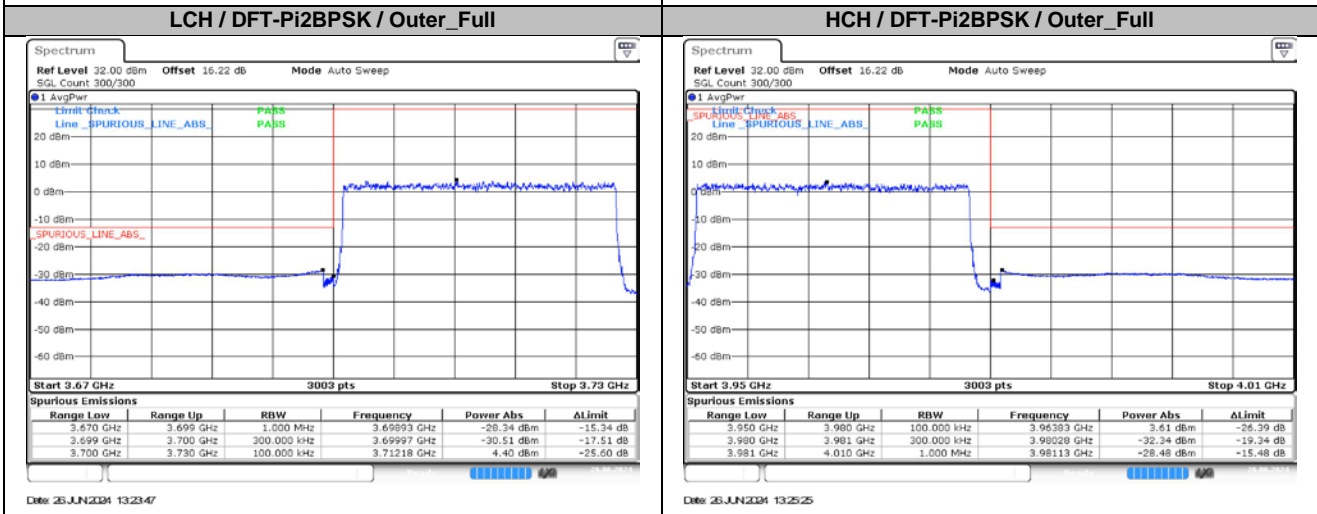
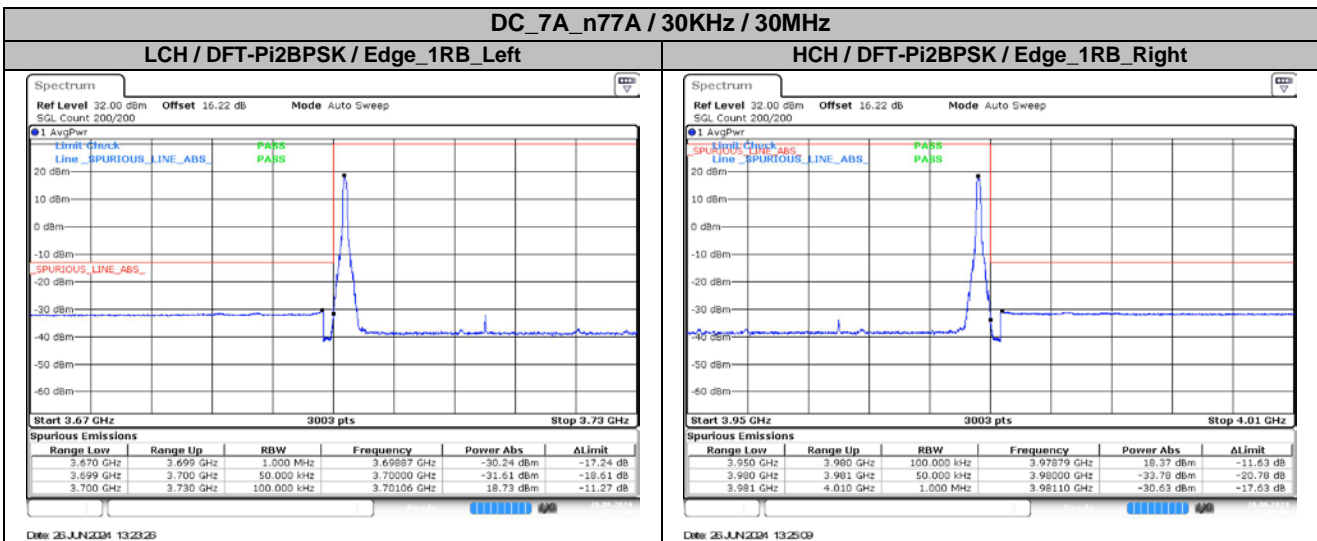
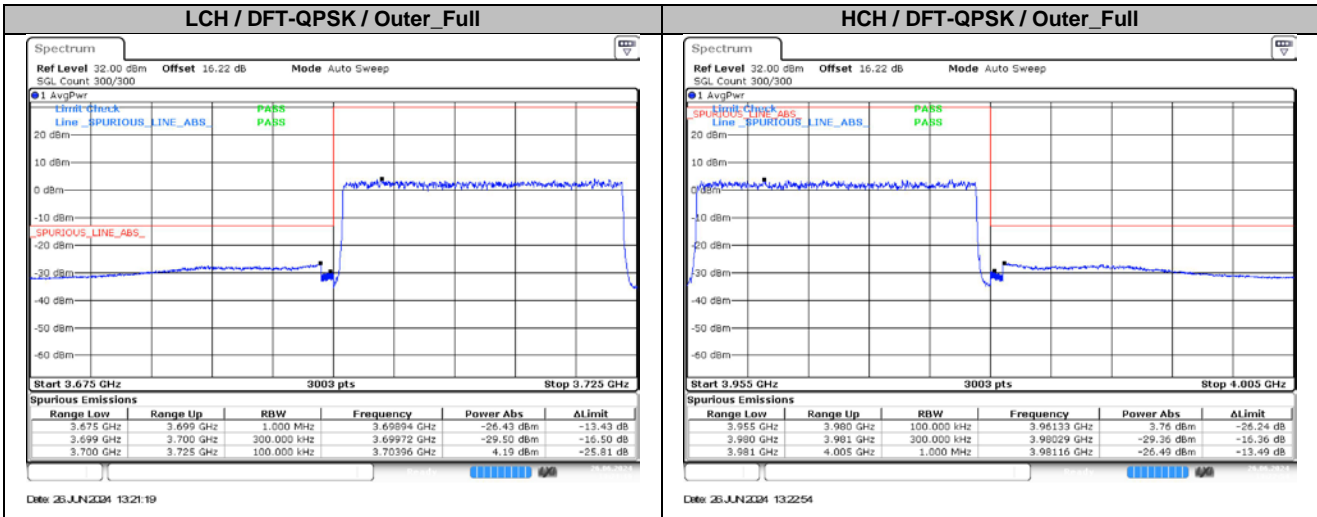


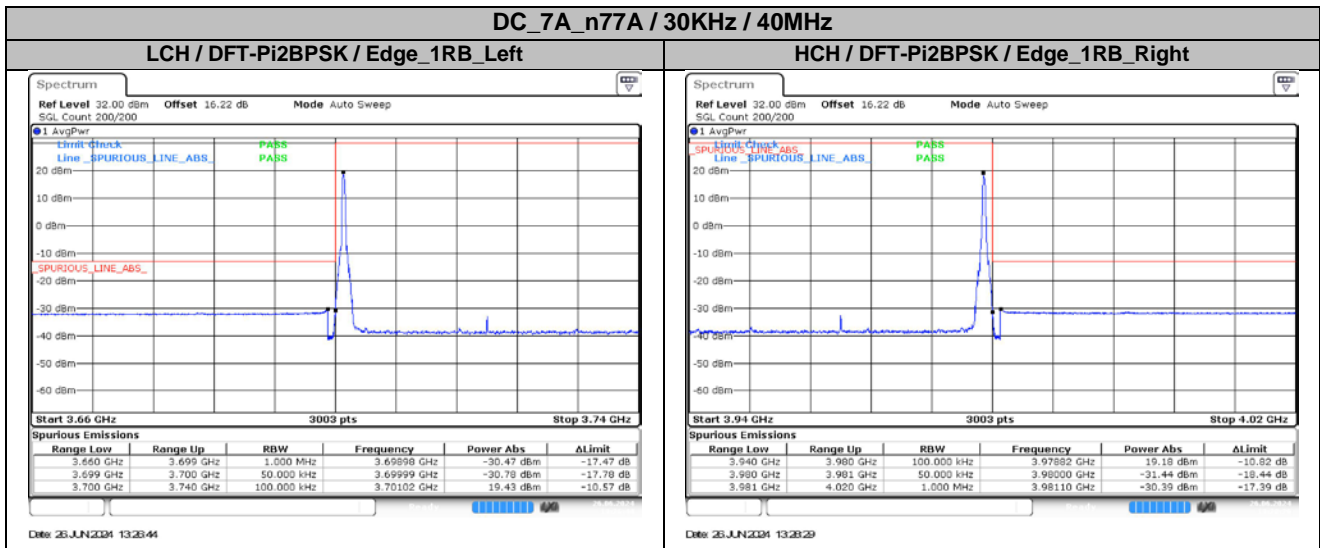
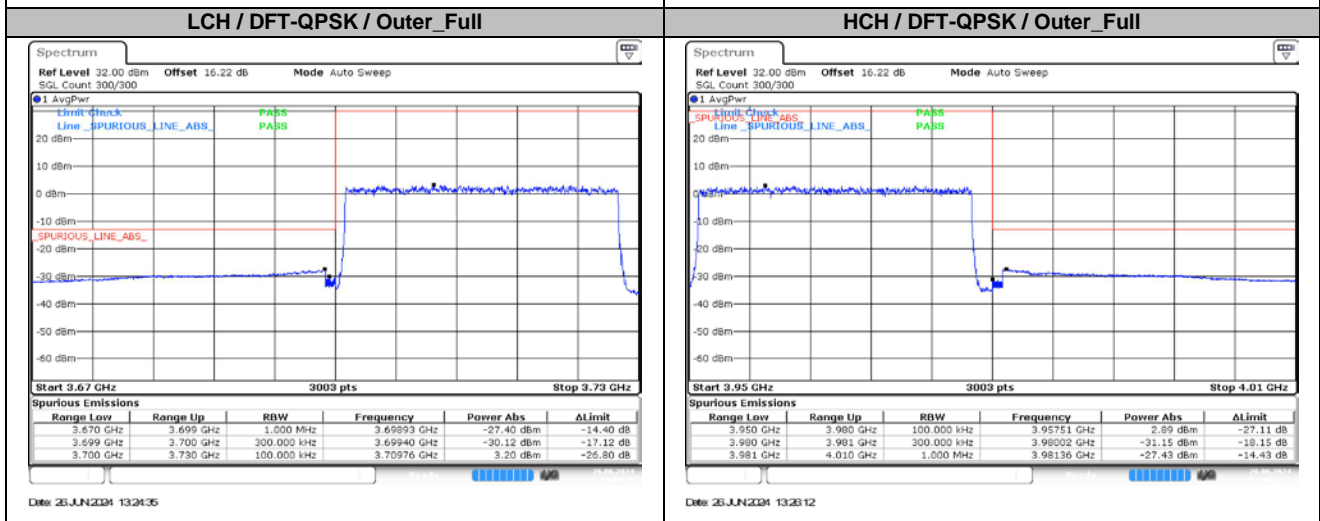
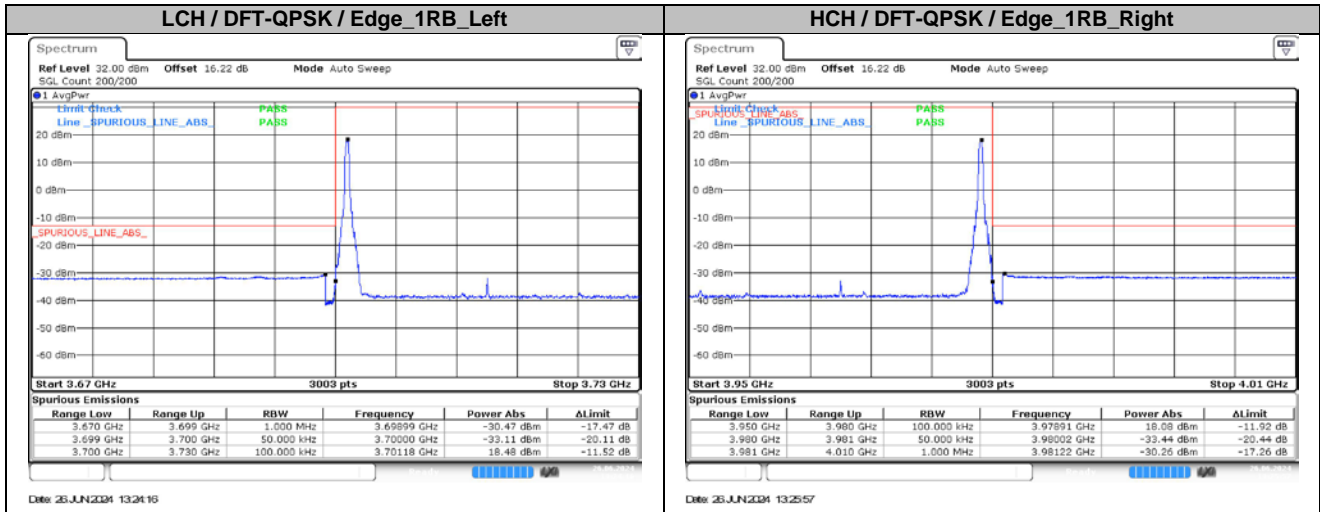
Date: 26 JUN 2024 13:21:02

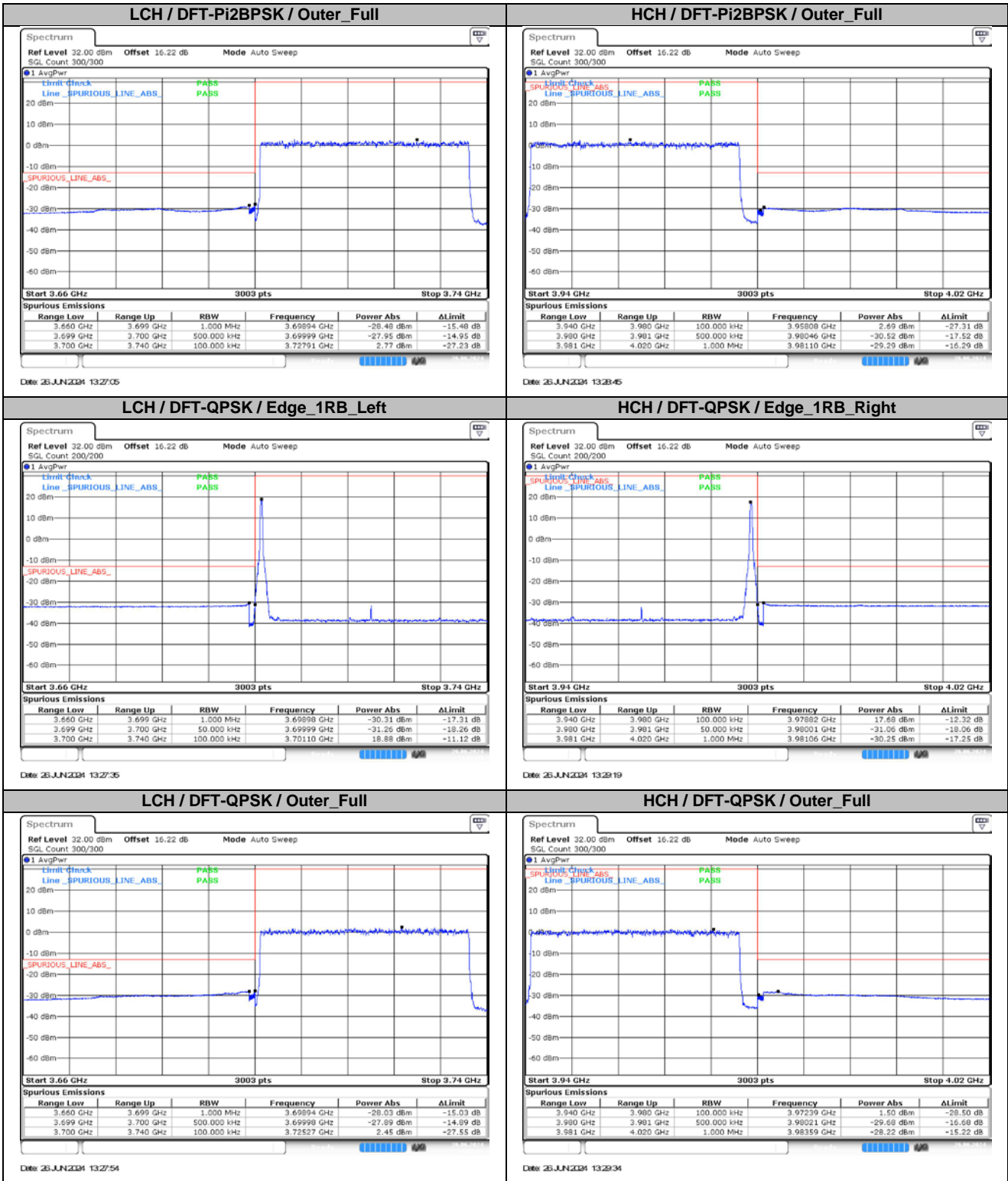
HCH / DFT-QPSK / Edge_1RB_Right

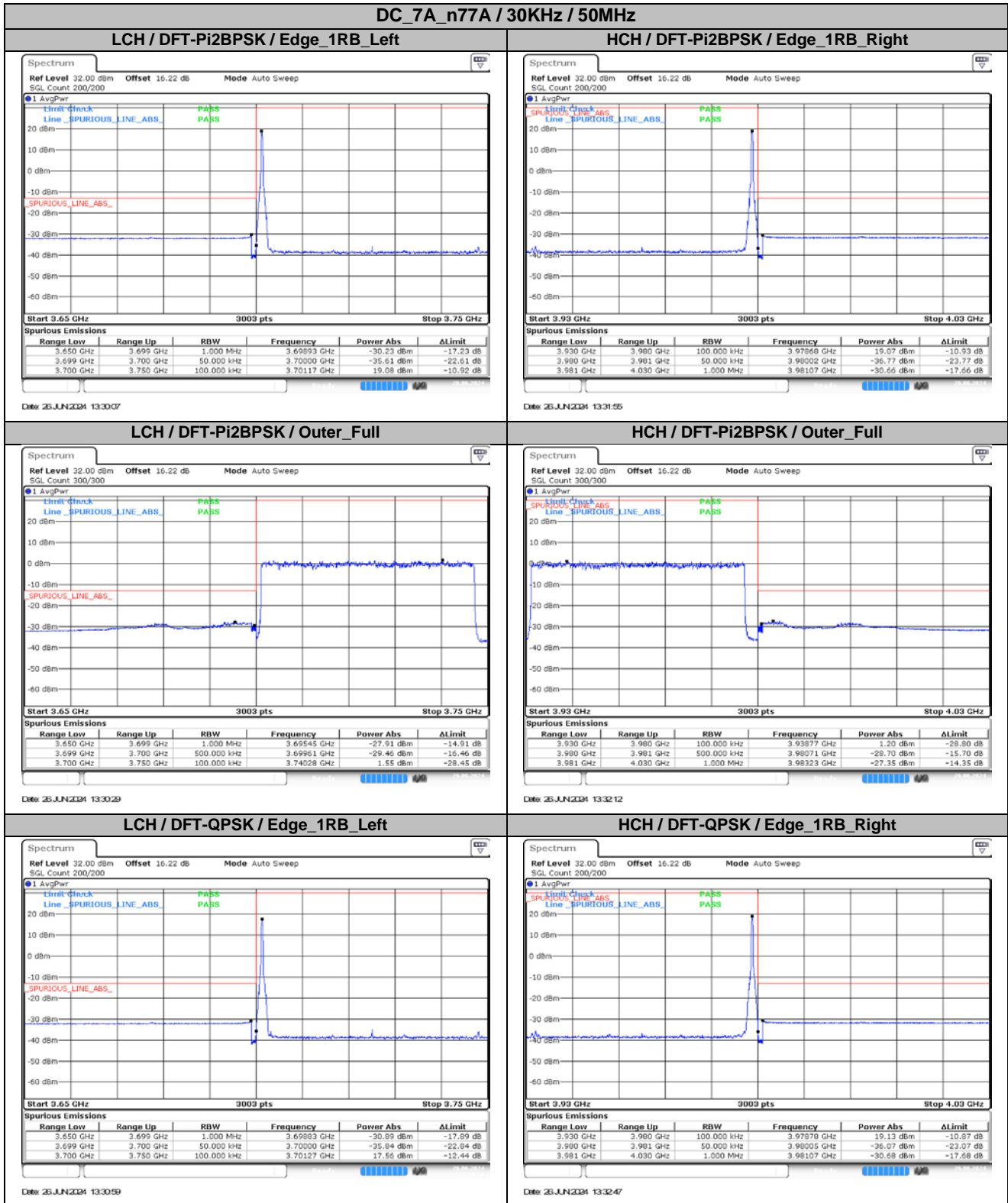


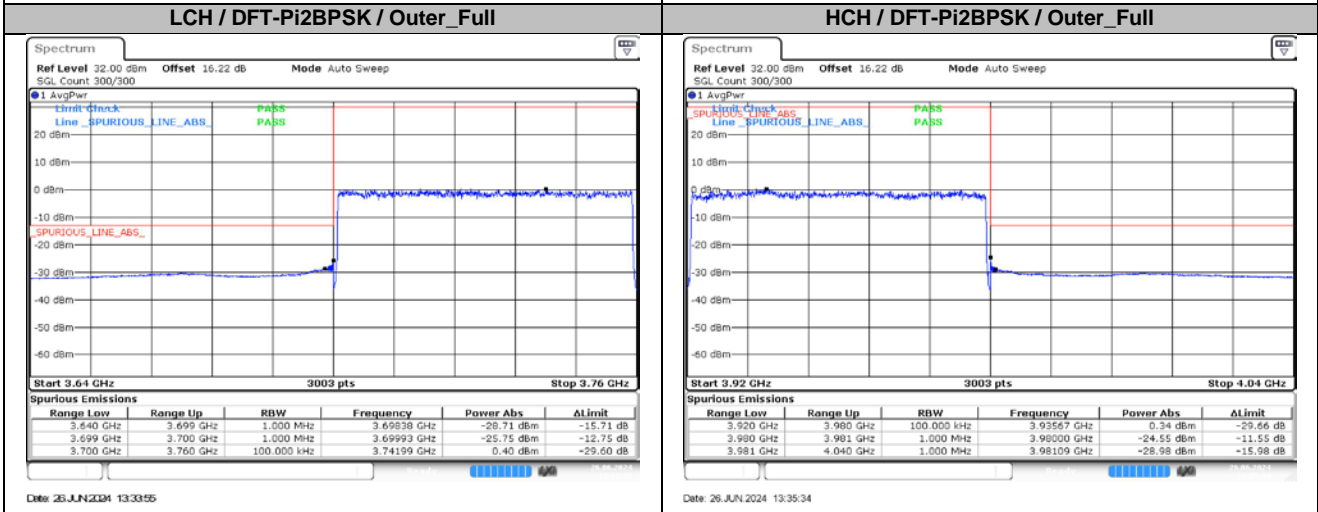
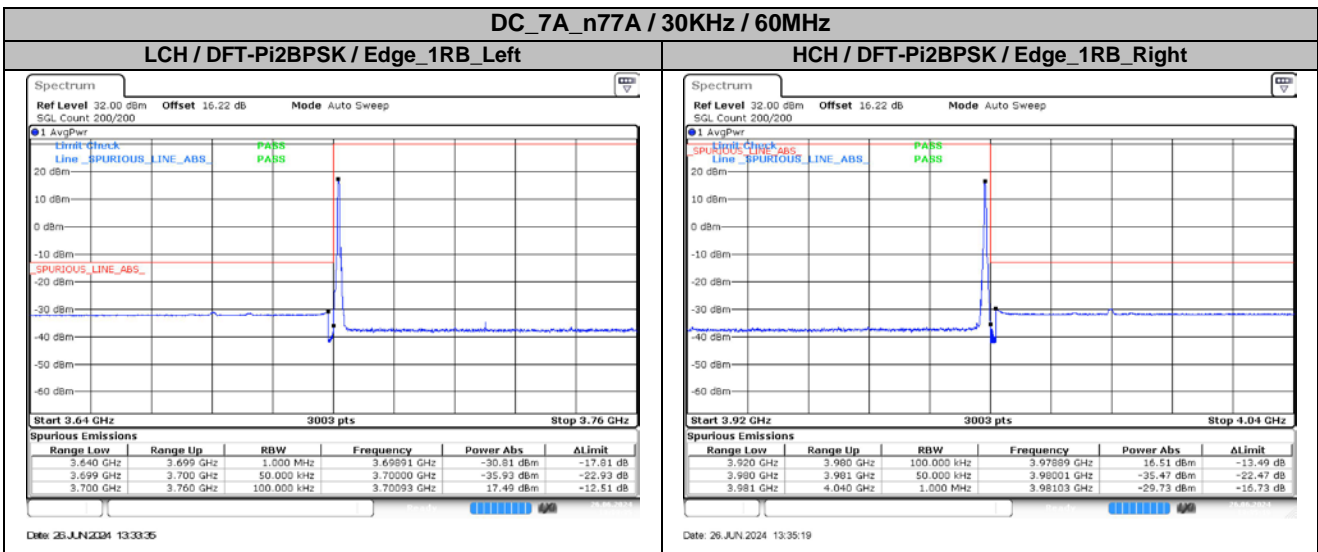
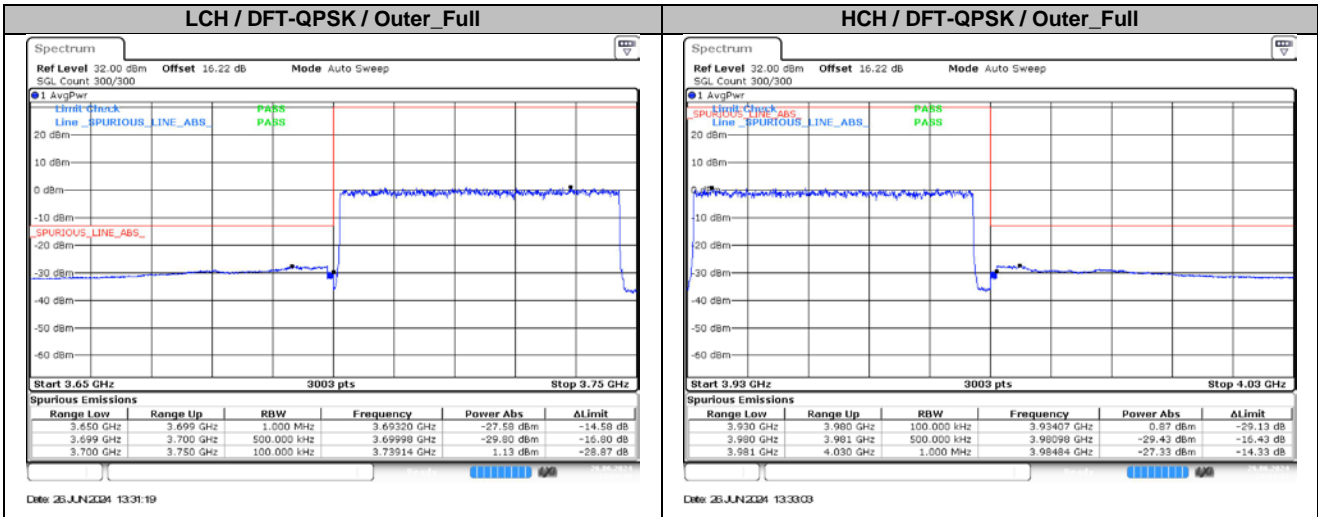
Date: 26 JUN 2024 13:22:40

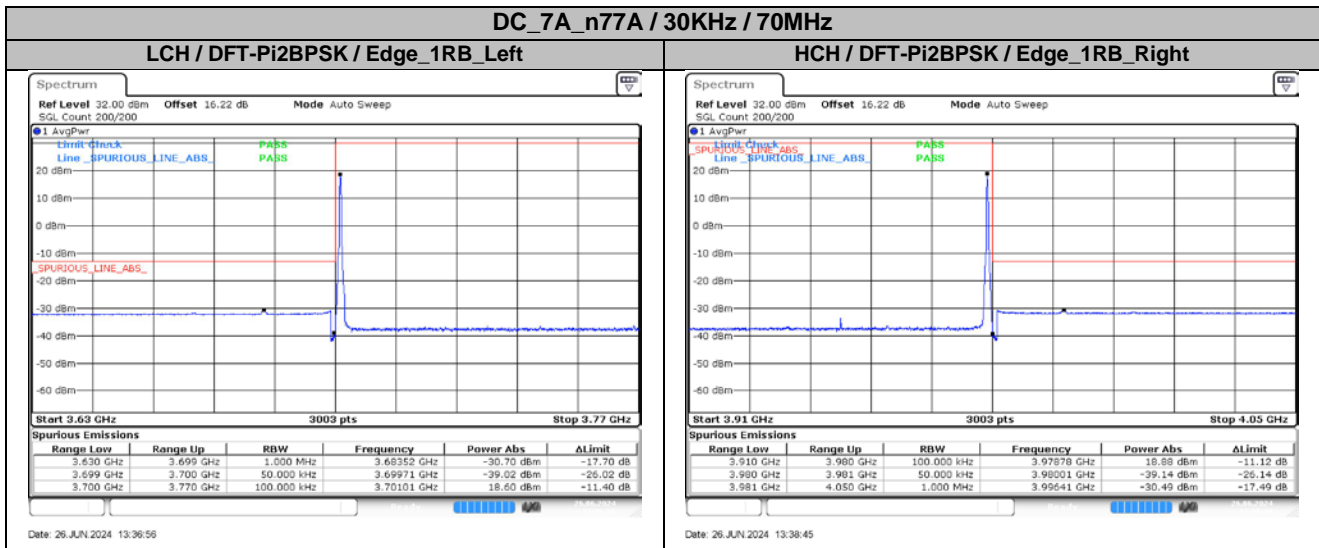
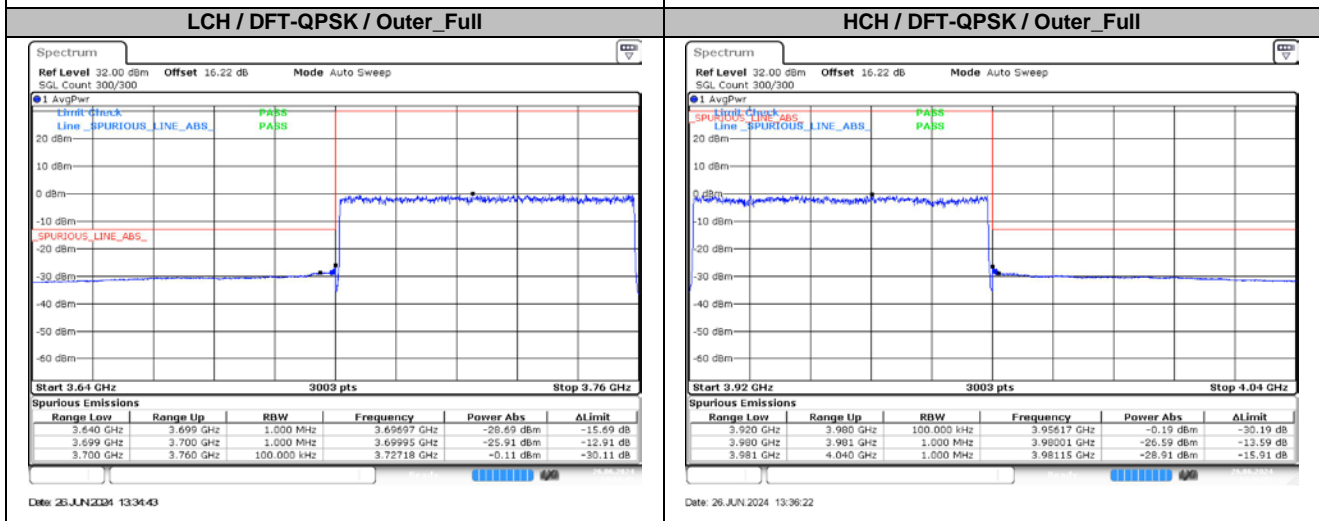
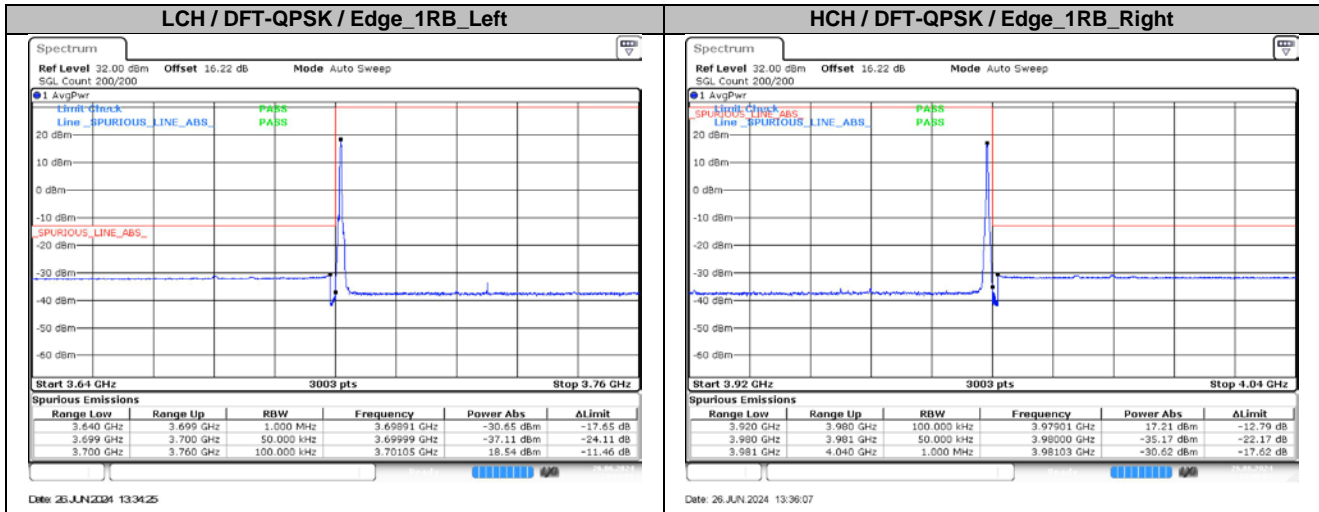


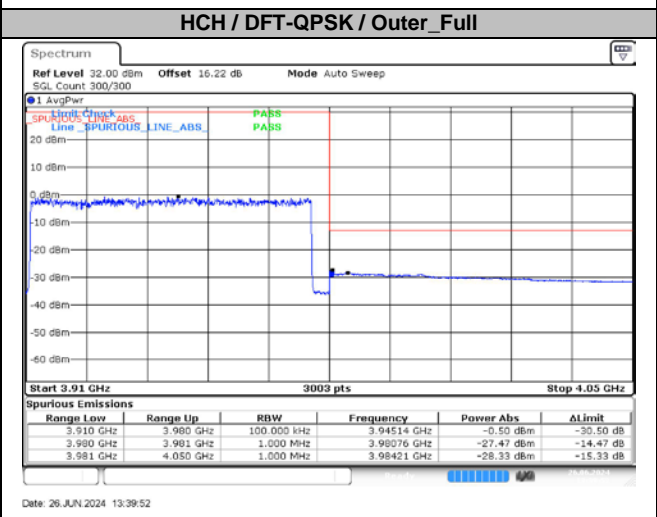
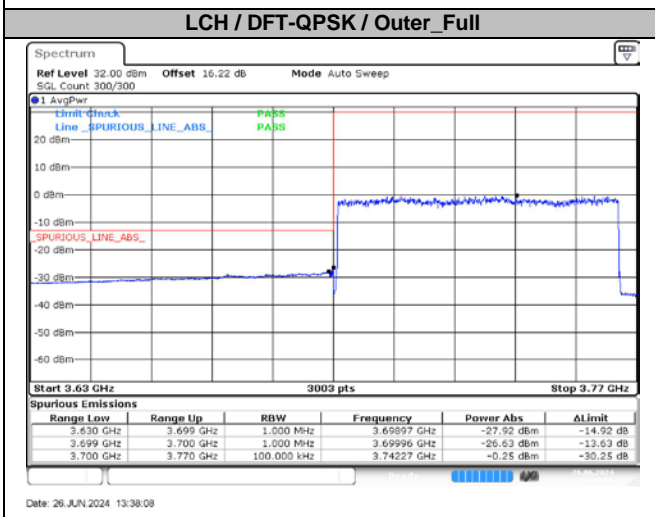
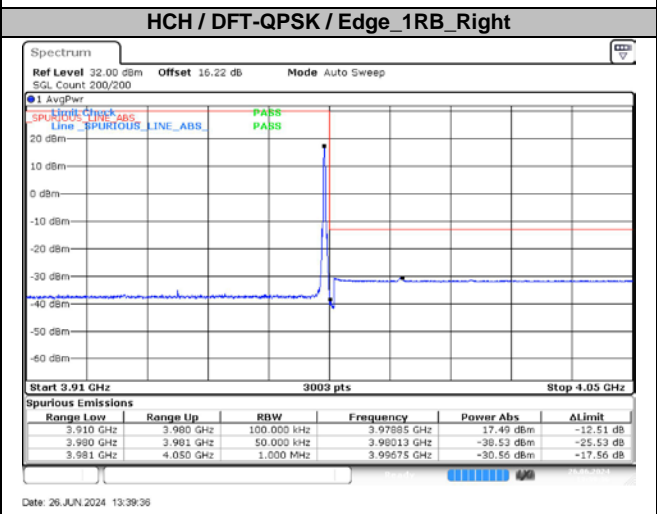
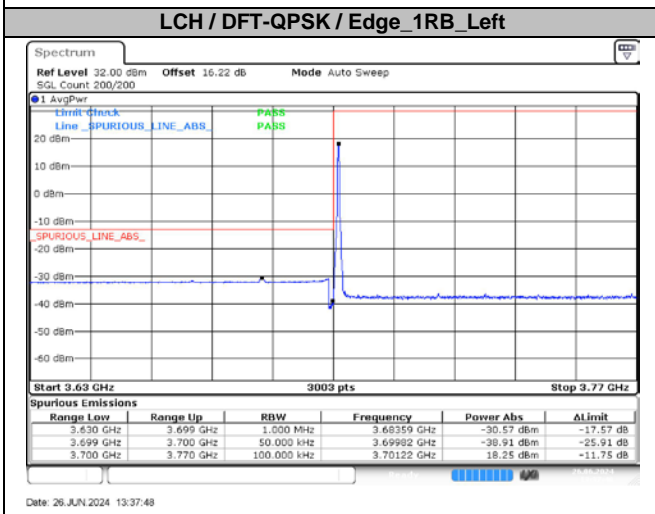
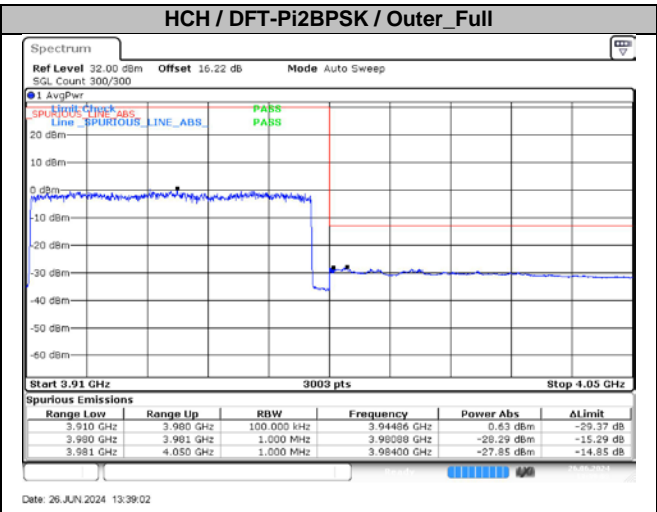
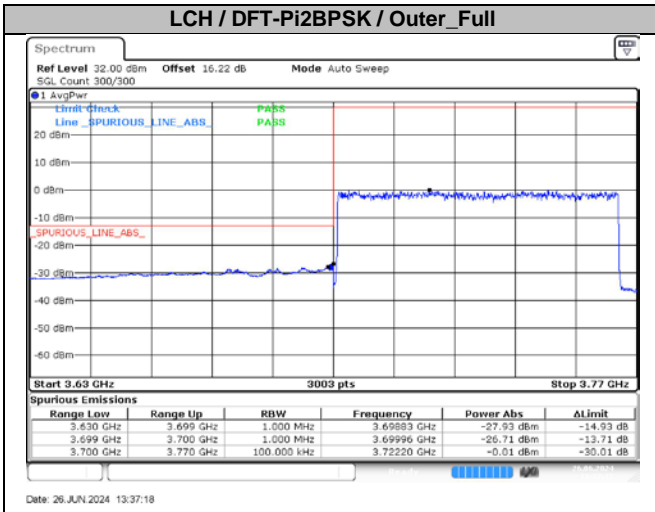






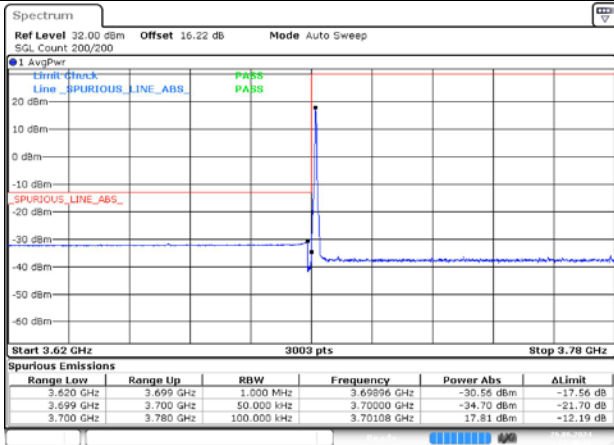






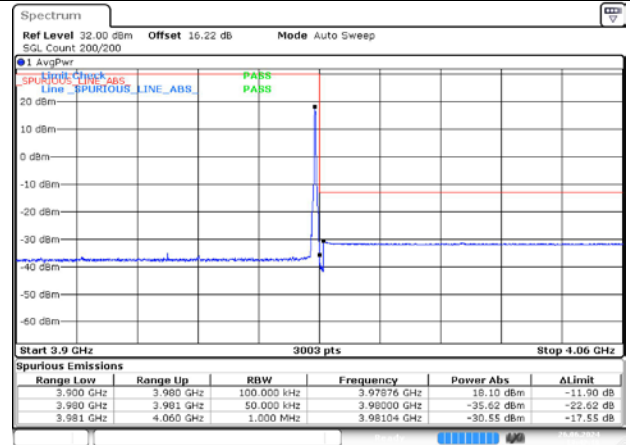
DC_7A_n77A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



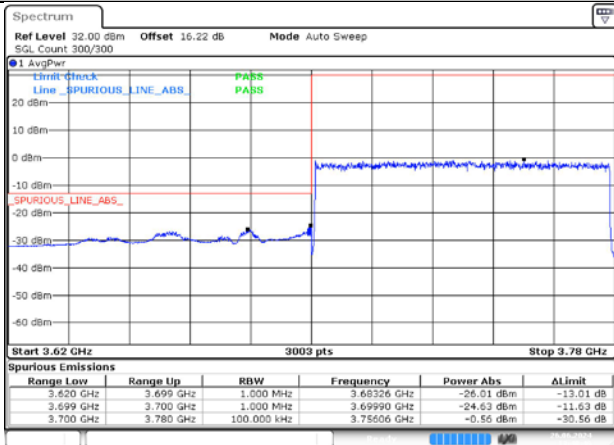
Date: 26 JUN 2024 13:49:29

HCH / DFT-Pi2BPSK / Edge_1RB_Right



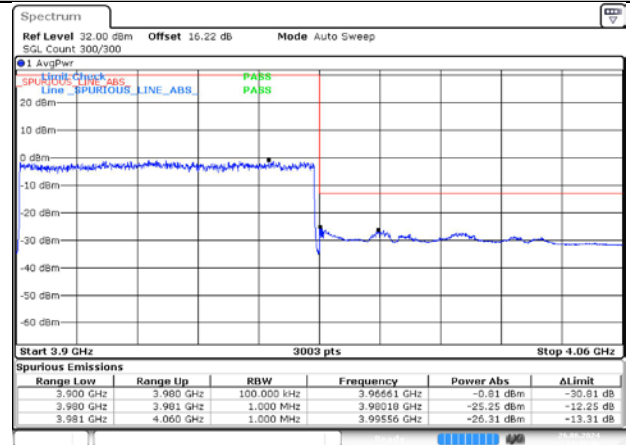
Date: 26 JUN 2024 13:42:18

LCH / DFT-Pi2BPSK / Outer_Full



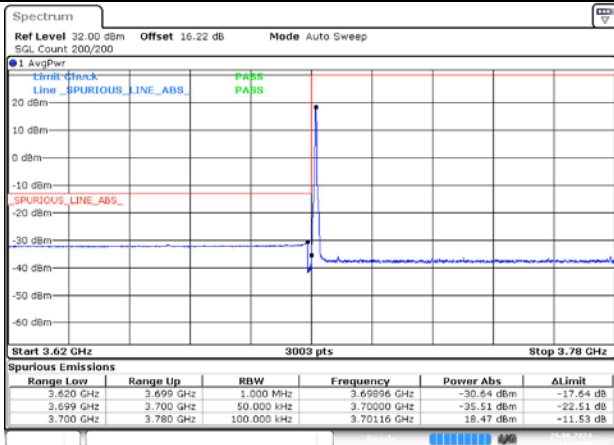
Date: 26 JUN 2024 13:49:49

HCH / DFT-Pi2BPSK / Outer_Full



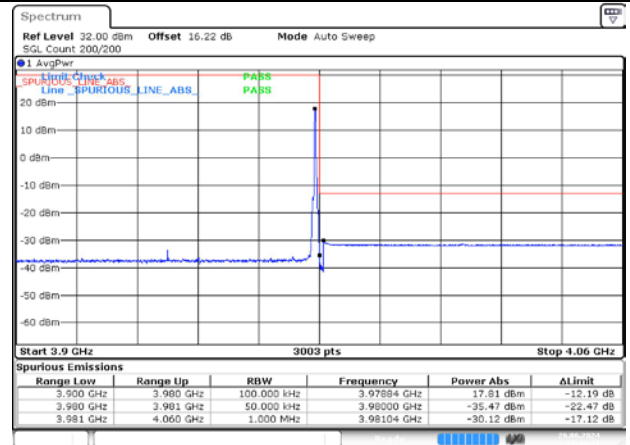
Date: 26 JUN 2024 13:42:36

LCH / DFT-QPSK / Edge_1RB_Left



Date: 26 JUN 2024 13:41:20

HCH / DFT-QPSK / Edge_1RB_Right



Date: 26 JUN 2024 13:43:11

