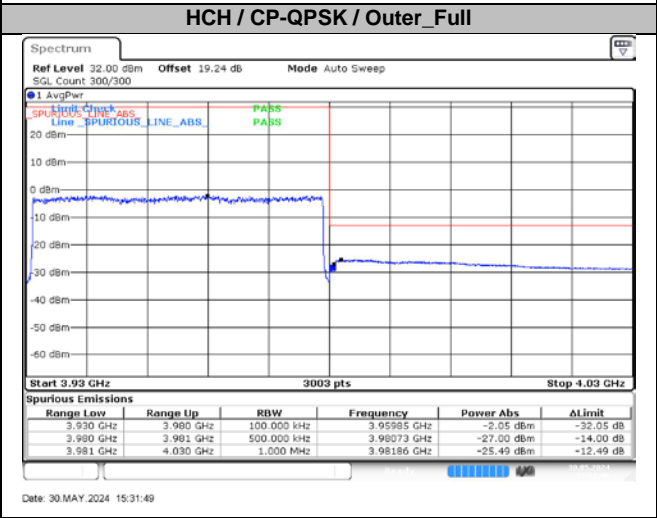
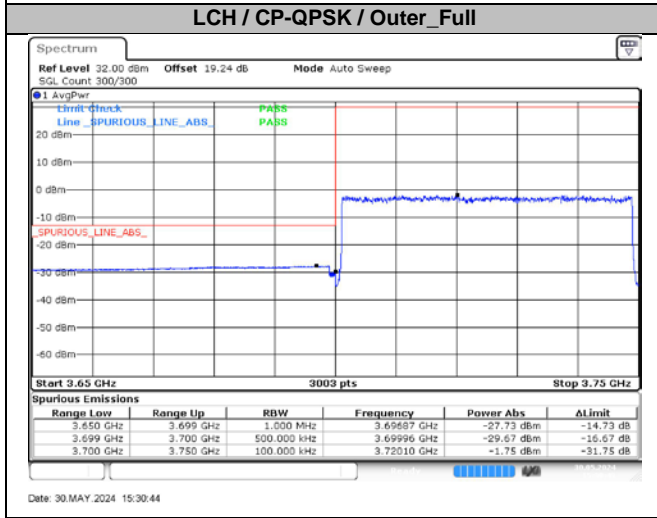
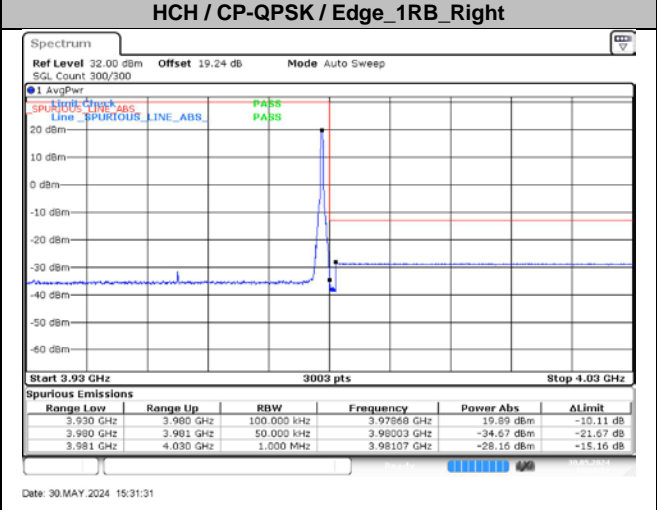
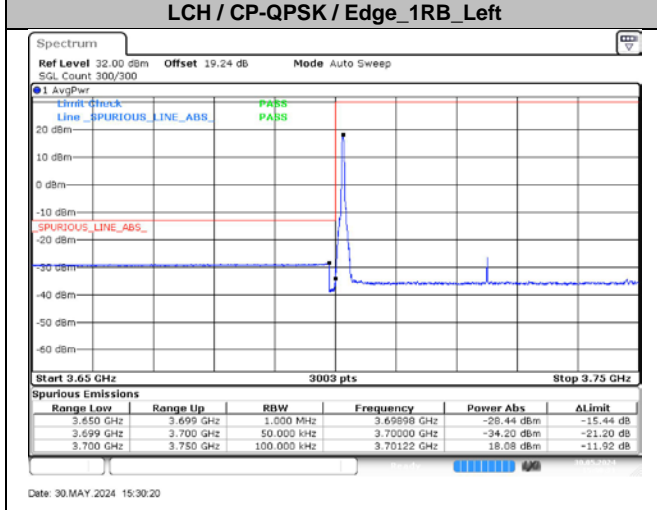
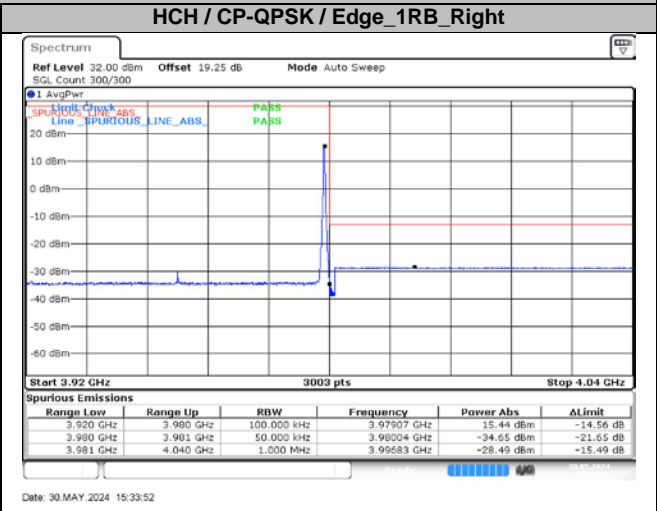
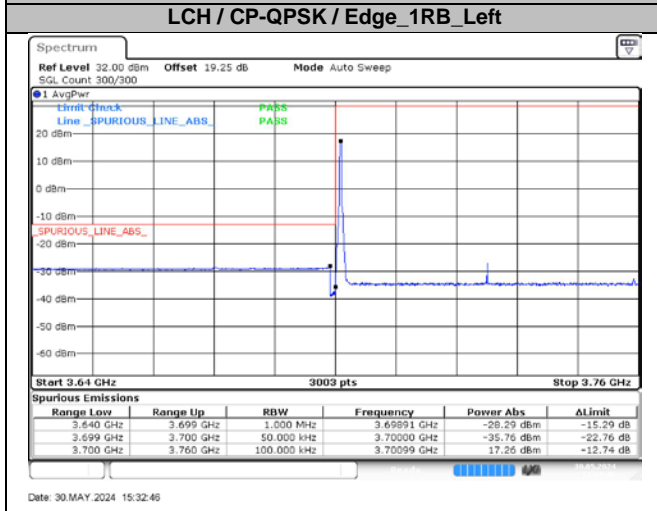
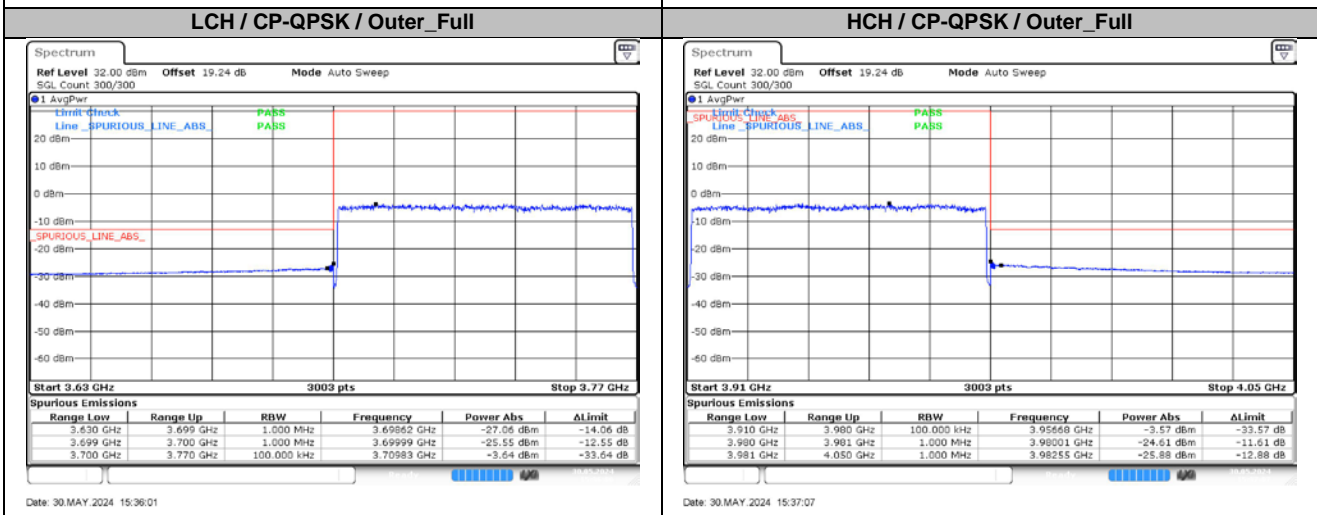
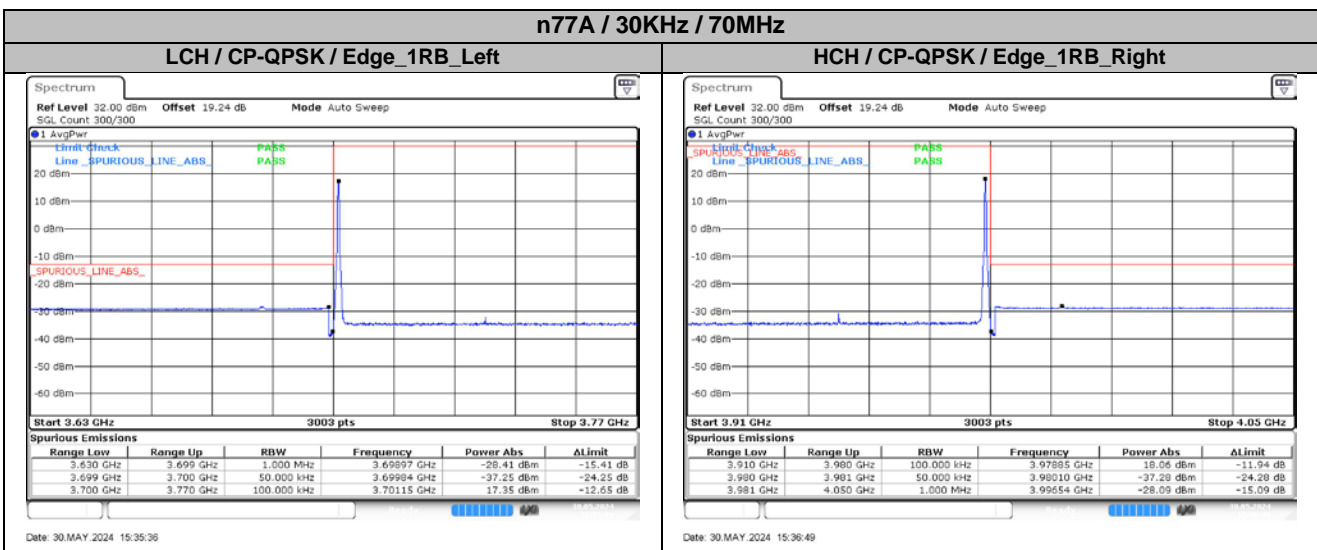
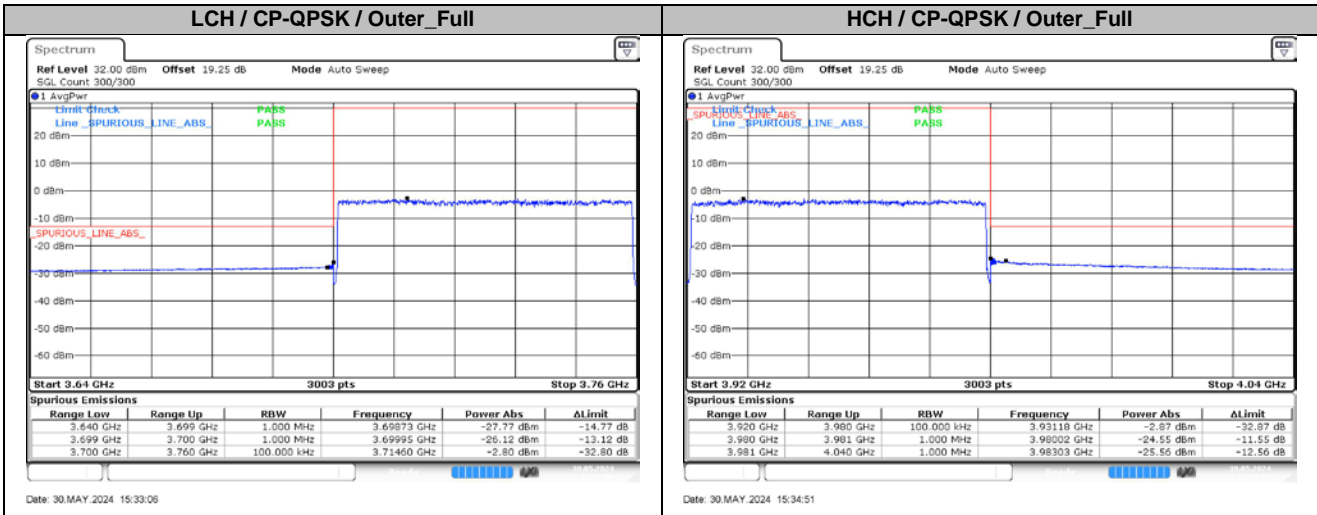


**n77A / 30KHz / 50MHz**



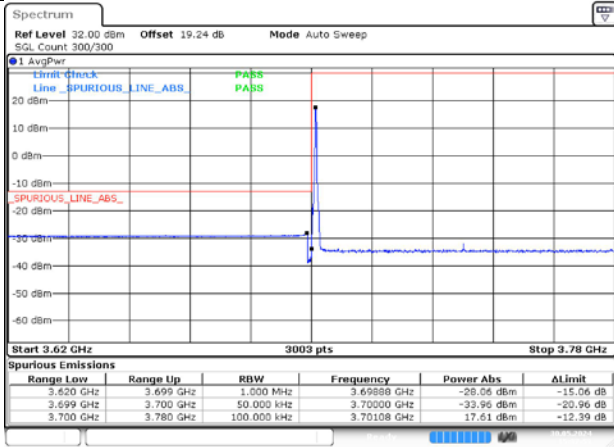
**n77A / 30KHz / 60MHz**





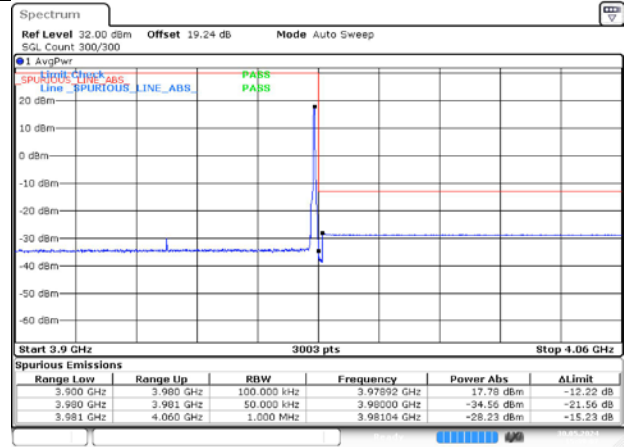
n77A / 30KHz / 80MHz

LCH / CP-QPSK / Edge\_1RB\_Left



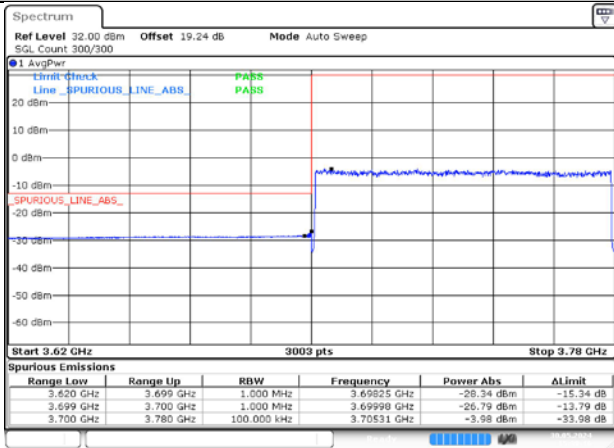
Date: 30 MAY 2024 15:37:52

HCH / CP-QPSK / Edge\_1RB\_Right



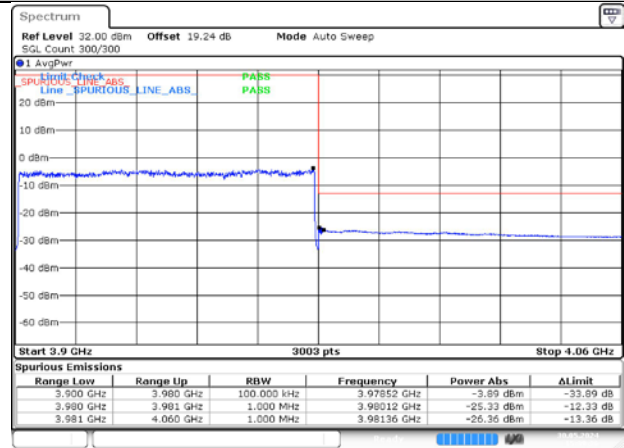
Date: 30 MAY 2024 15:39:07

LCH / CP-QPSK / Outer\_Full



Date: 30 MAY 2024 15:38:16

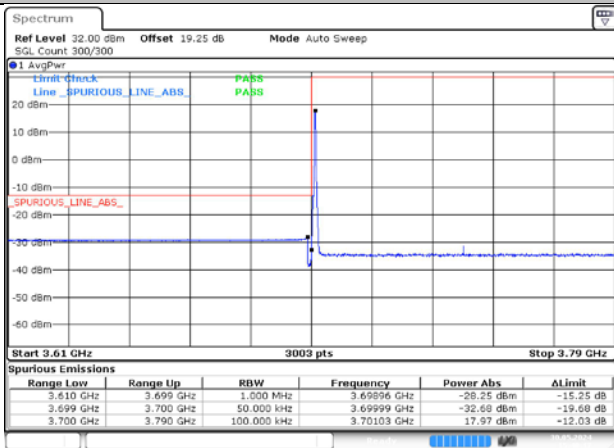
HCH / CP-QPSK / Outer\_Full



Date: 30 MAY 2024 15:39:26

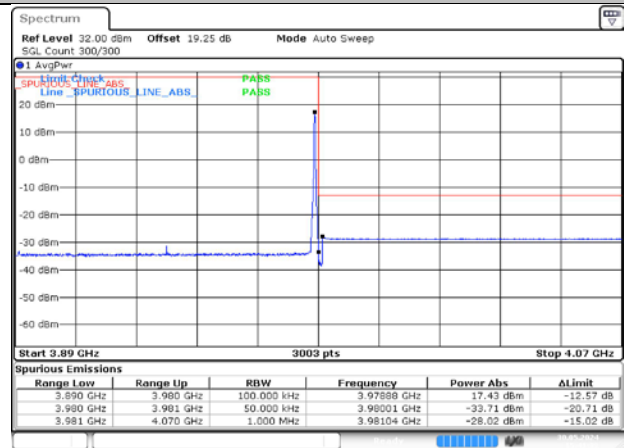
n77A / 30KHz / 90MHz

LCH / CP-QPSK / Edge\_1RB\_Left

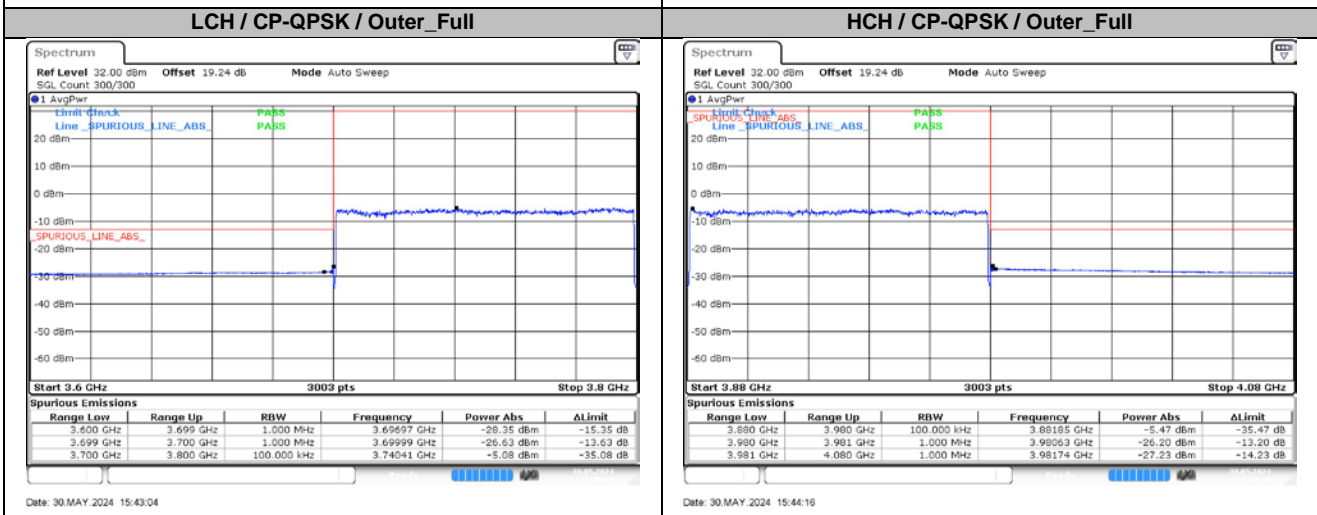
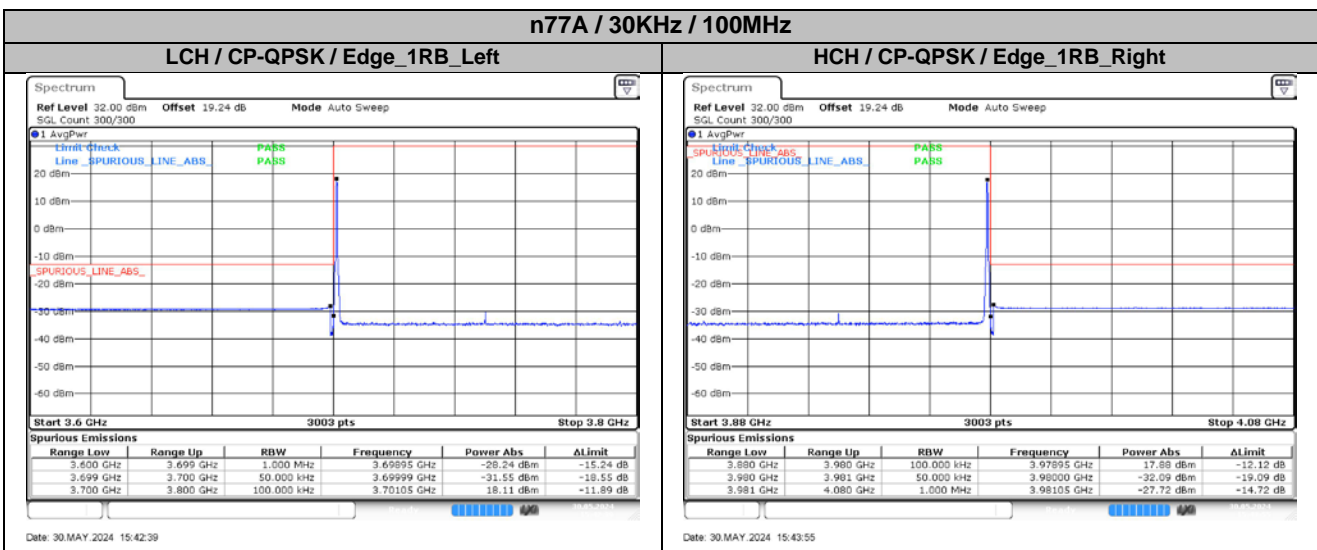
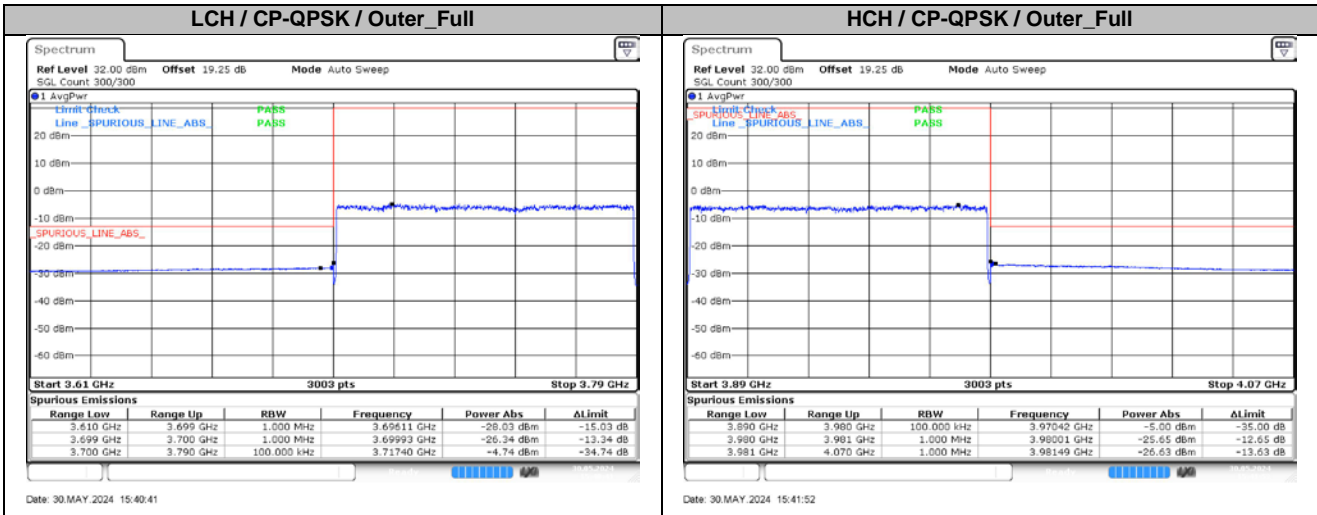


Date: 30 MAY 2024 15:40:17

HCH / CP-QPSK / Edge\_1RB\_Right



Date: 30 MAY 2024 15:41:32



## 6. Conducted Spurious Emission

### 6.1. Test Plots

