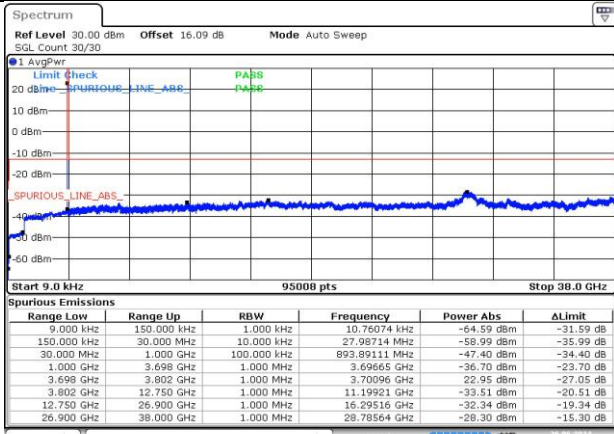


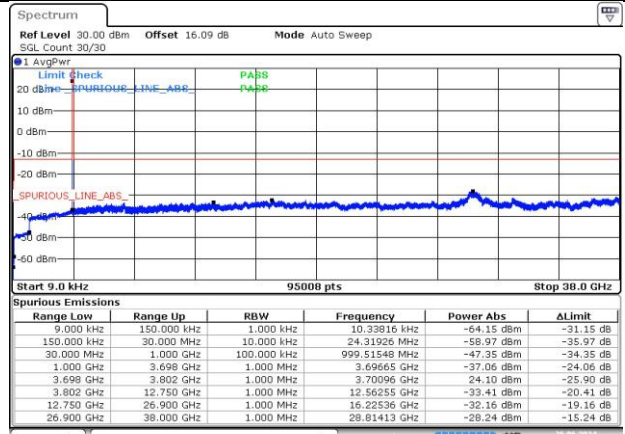
DC\_4A\_n78A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



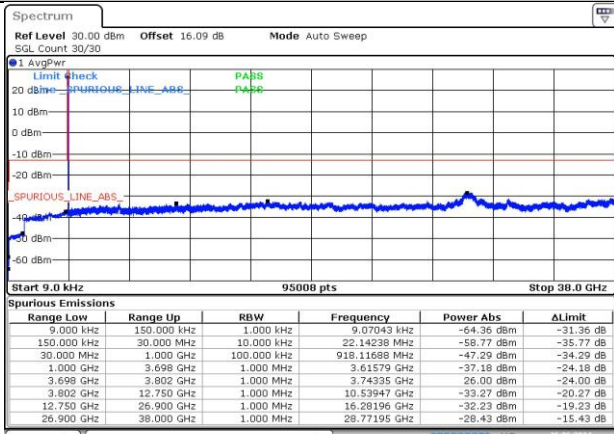
Date: 26 JUN 2024 16:08:25

LCH / DFT-QPSK / Edge\_1RB\_Left



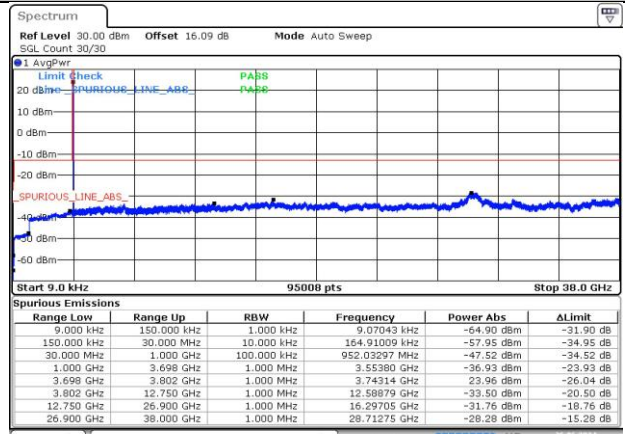
Date: 26 JUN 2024 16:08:53

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



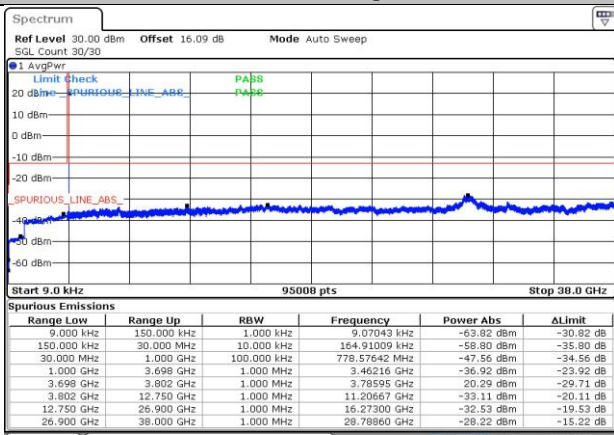
Date: 26 JUN 2024 16:09:20

MCH / DFT-QPSK / Edge\_1RB\_Left



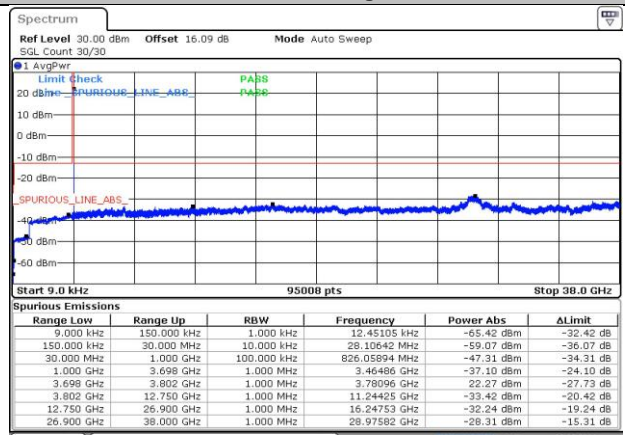
Date: 26 JUN 2024 16:09:47

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:10:14

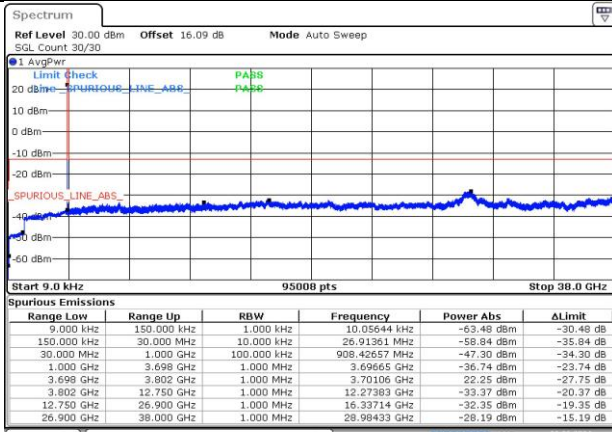
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:13:37

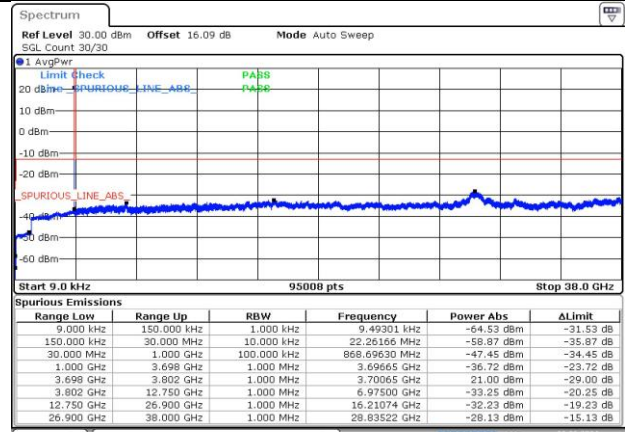
DC\_4A\_n78A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



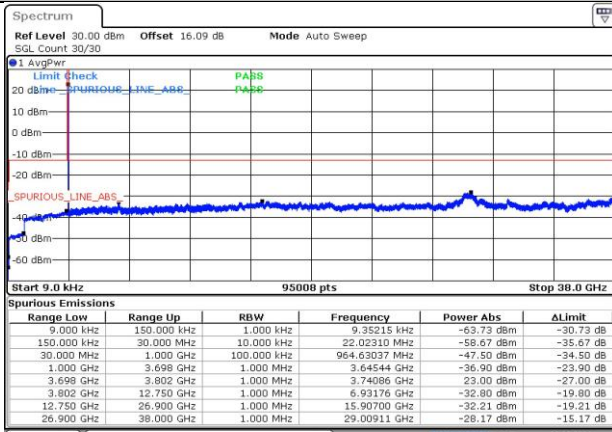
Date: 26 JUN 2024 16:11:22

LCH / DFT-QPSK / Edge\_1RB\_Left



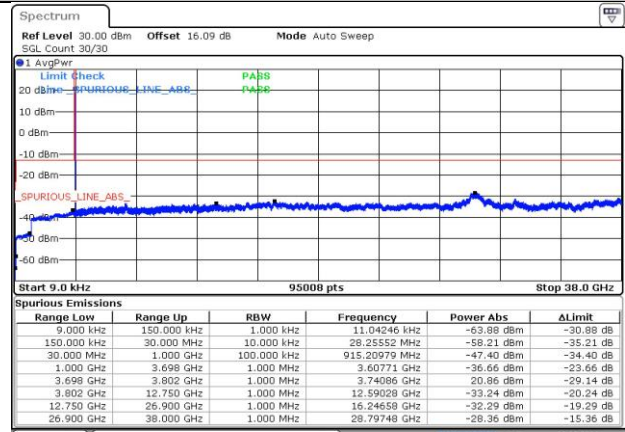
Date: 26 JUN 2024 16:11:49

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



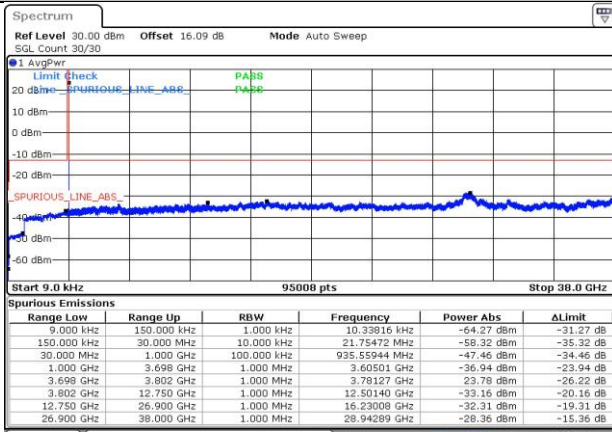
Date: 26 JUN 2024 16:12:17

MCH / DFT-QPSK / Edge\_1RB\_Left



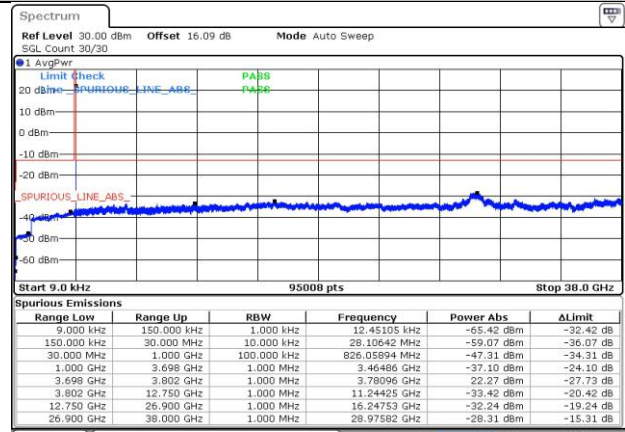
Date: 26 JUN 2024 16:12:43

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left

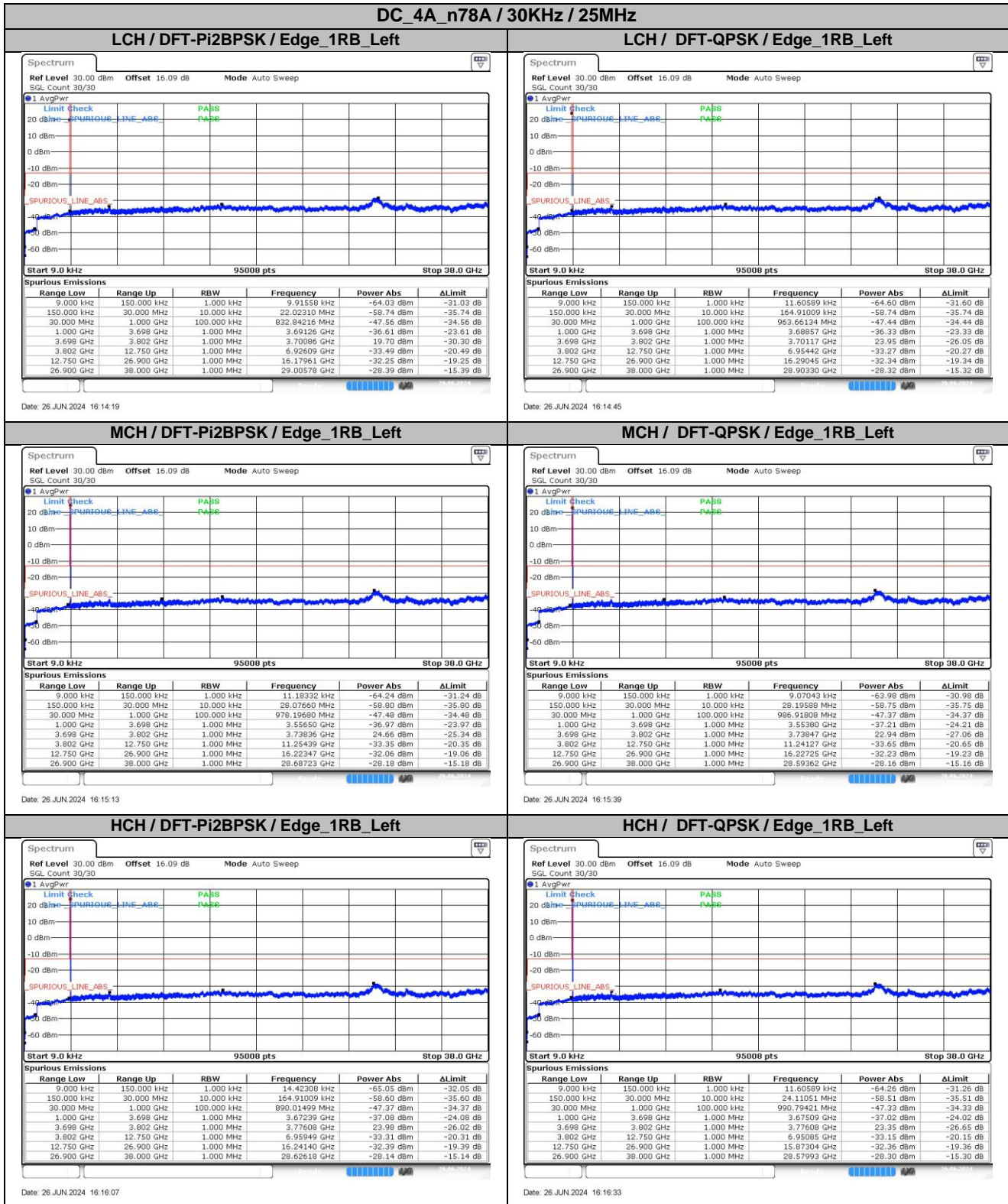


Date: 26 JUN 2024 16:13:11

HCH / DFT-QPSK / Edge\_1RB\_Left

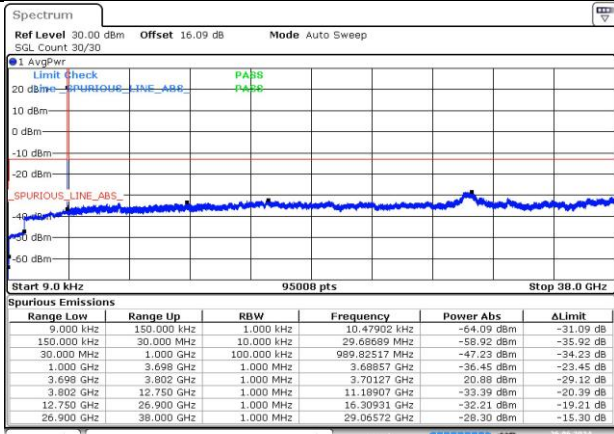


Date: 26 JUN 2024 16:13:37



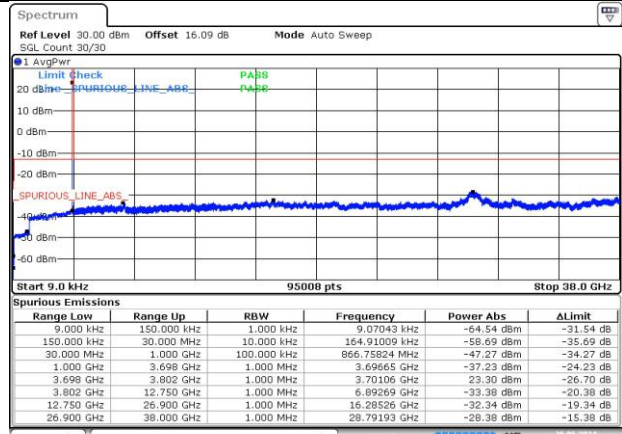
DC\_4A\_n78A / 30KHz / 30MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



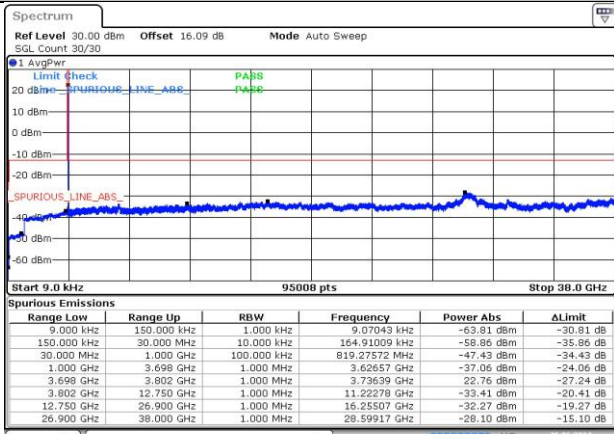
Date: 26 JUN 2024 16:17:14

LCH / DFT-QPSK / Edge\_1RB\_Left



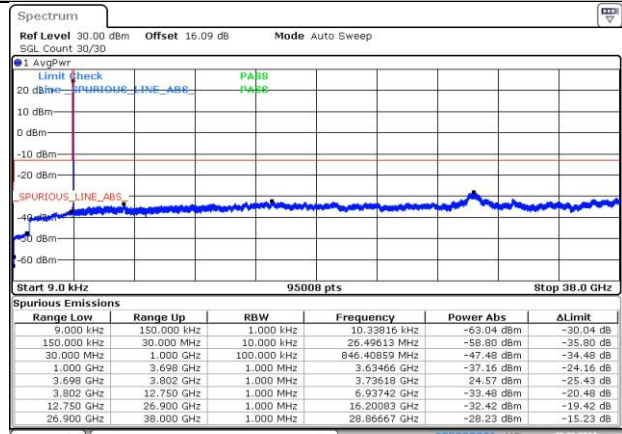
Date: 26 JUN 2024 16:17:41

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



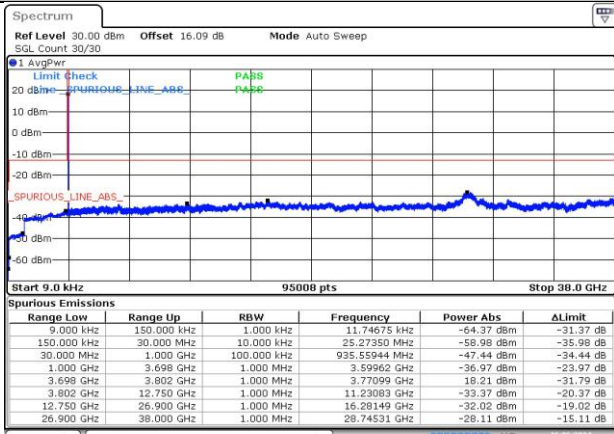
Date: 26 JUN 2024 16:18:08

MCH / DFT-QPSK / Edge\_1RB\_Left



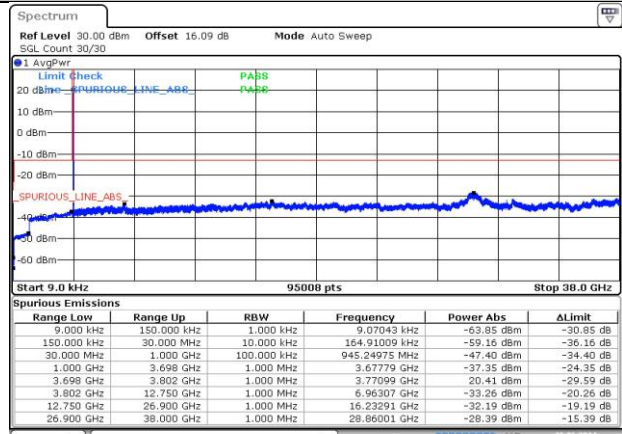
Date: 26 JUN 2024 16:18:35

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:19:02

HCH / DFT-QPSK / Edge\_1RB\_Left

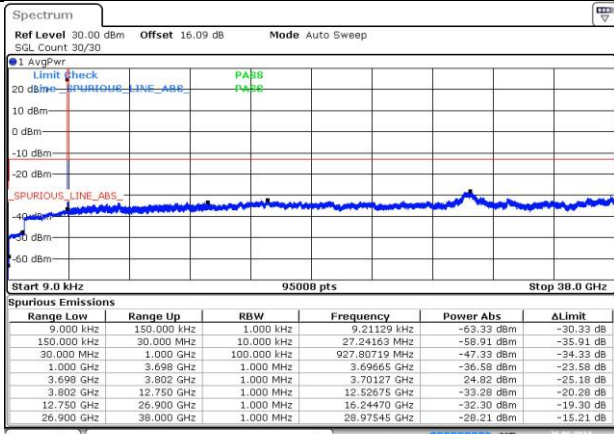


Date: 26 JUN 2024 16:19:29



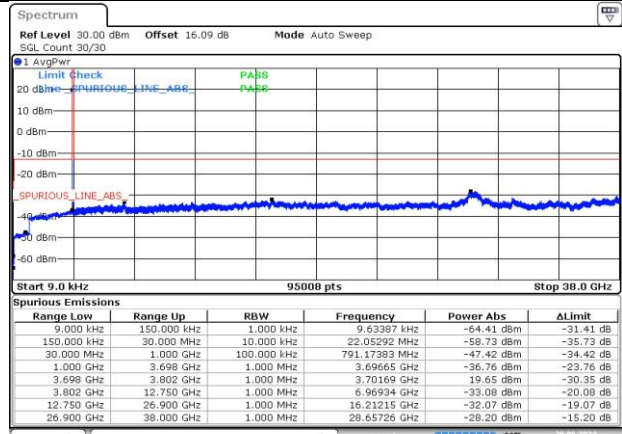
DC\_4A\_n78A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



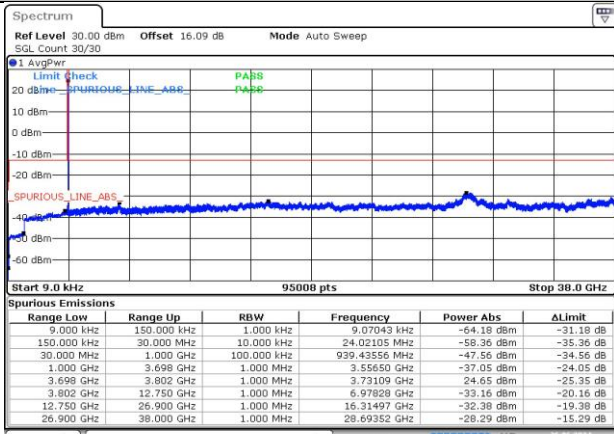
Date: 26 JUN 2024 16:20:10

LCH / DFT-QPSK / Edge\_1RB\_Left



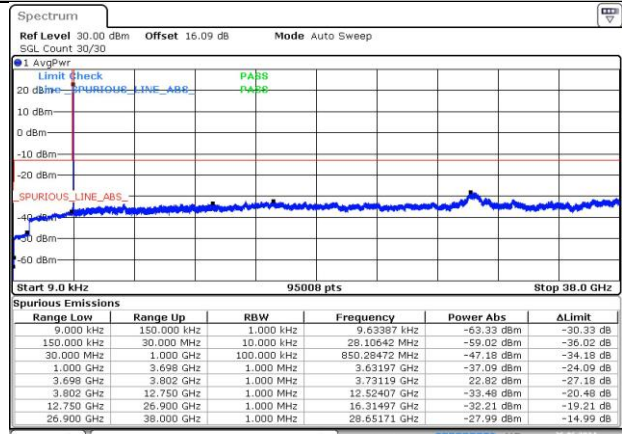
Date: 26 JUN 2024 16:20:37

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



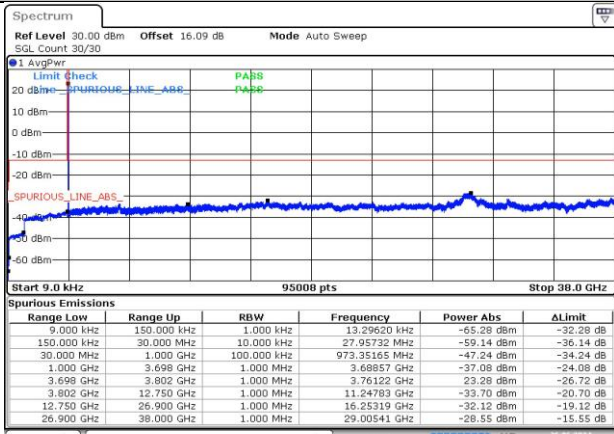
Date: 26 JUN 2024 16:21:04

MCH / DFT-QPSK / Edge\_1RB\_Left



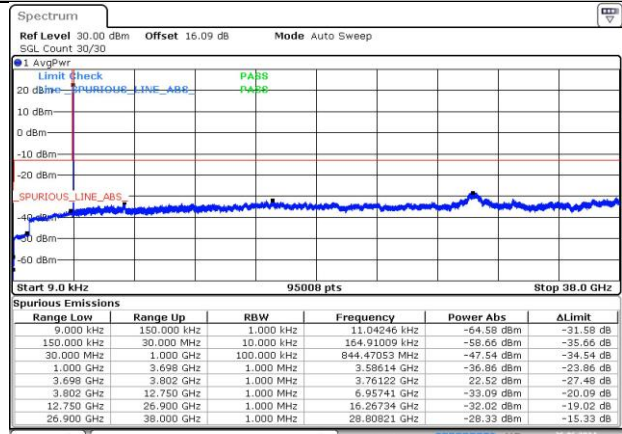
Date: 26 JUN 2024 16:21:31

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:21:58

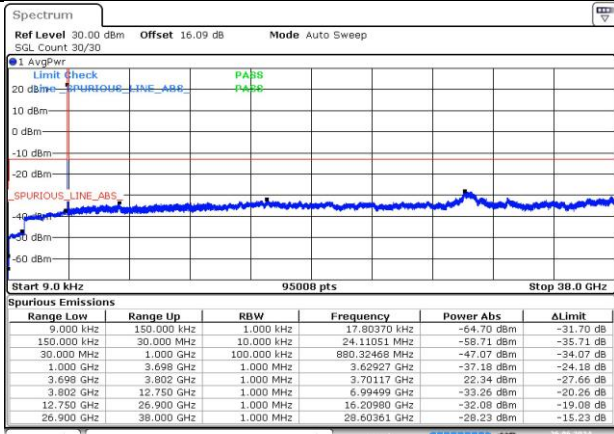
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:22:25

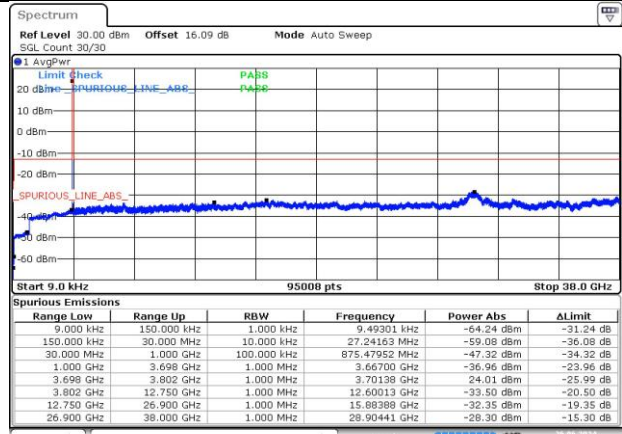
DC\_4A\_n78A / 30KHz / 50MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



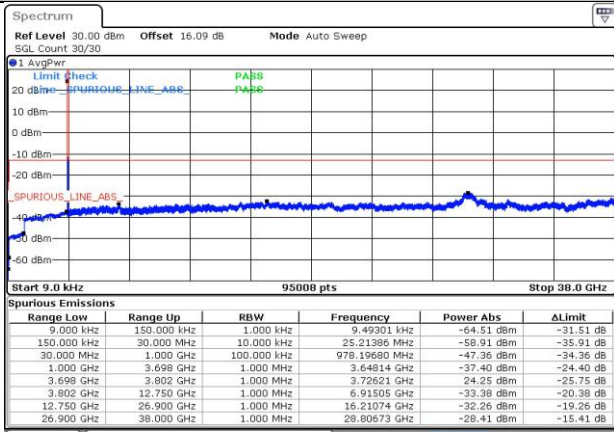
Date: 26 JUN 2024 16:23:05

LCH / DFT-QPSK / Edge\_1RB\_Left



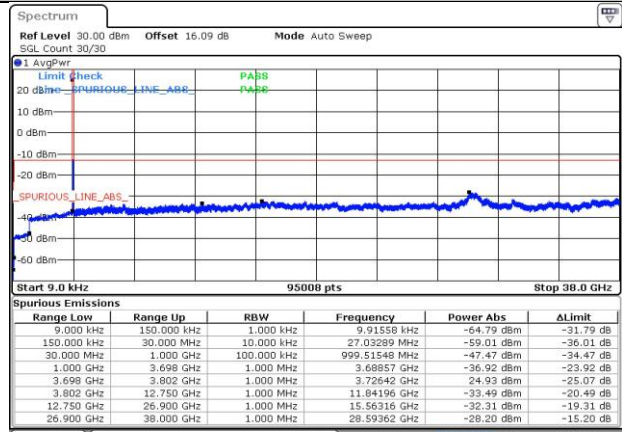
Date: 26 JUN 2024 16:23:33

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



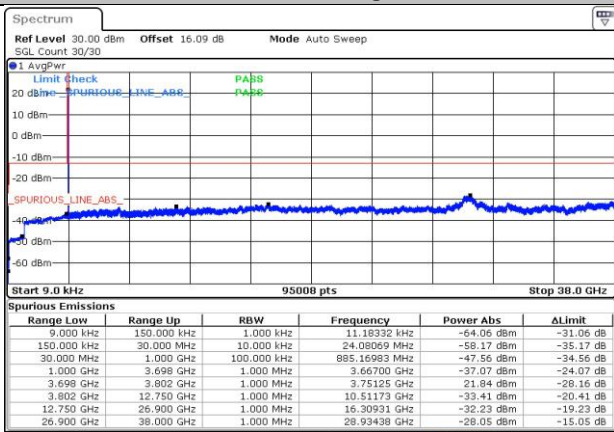
Date: 26 JUN 2024 16:24:01

MCH / DFT-QPSK / Edge\_1RB\_Left



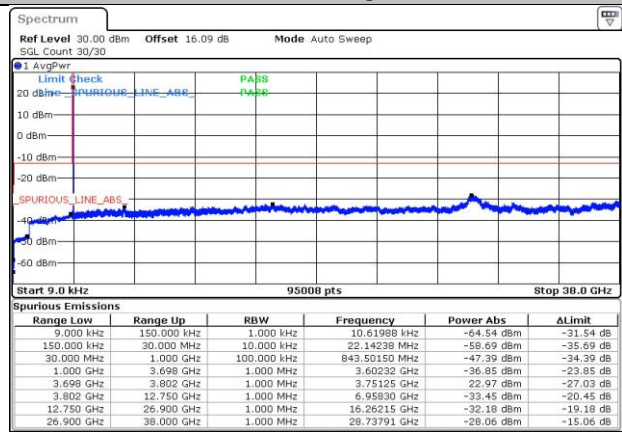
Date: 26 JUN 2024 16:24:27

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:24:55

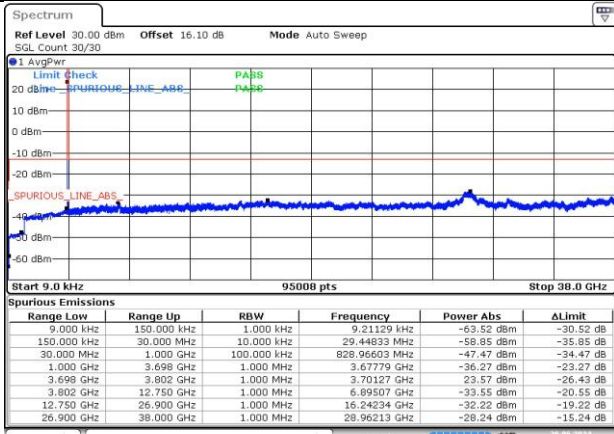
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:25:21

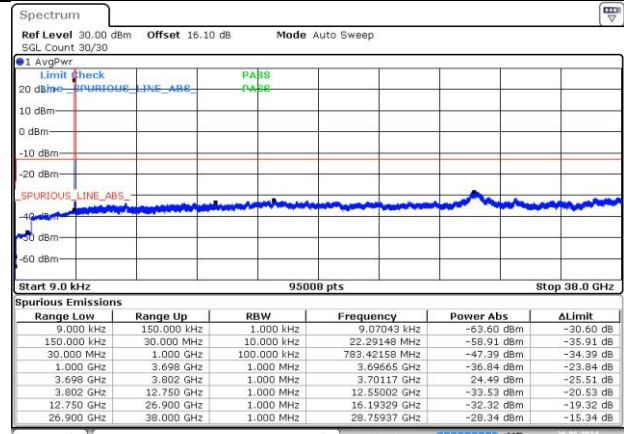
DC\_4A\_n78A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



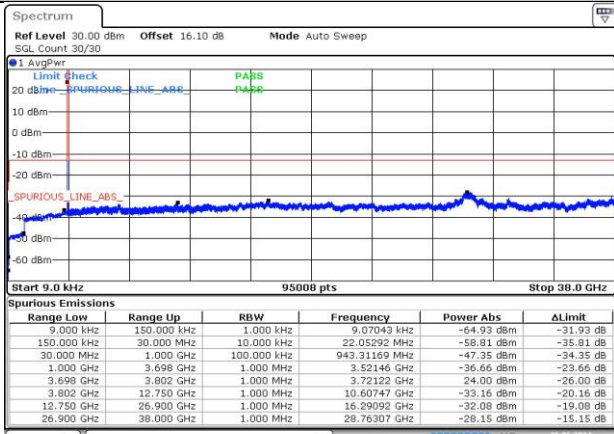
Date: 26 JUN 2024 16:26:03

LCH / DFT-QPSK / Edge\_1RB\_Left



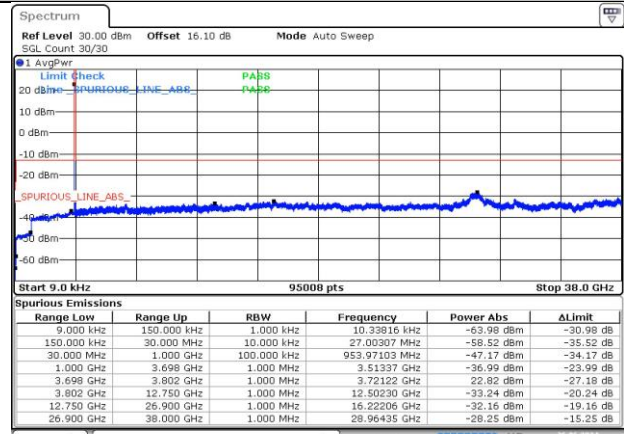
Date: 26 JUN 2024 16:26:30

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



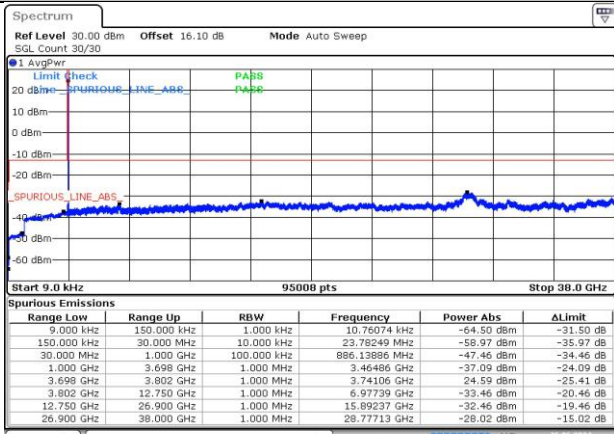
Date: 26 JUN 2024 16:26:57

MCH / DFT-QPSK / Edge\_1RB\_Left



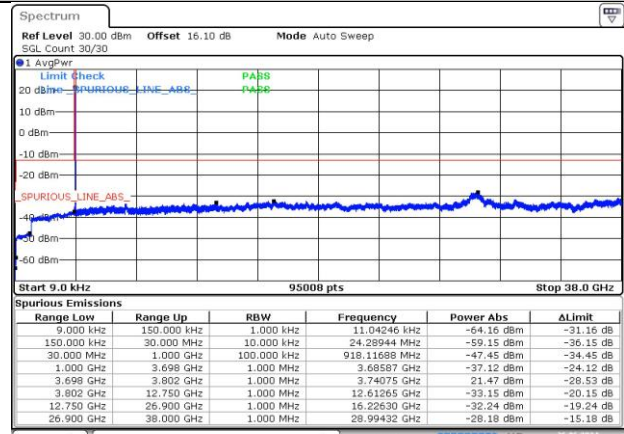
Date: 26 JUN 2024 16:27:24

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left

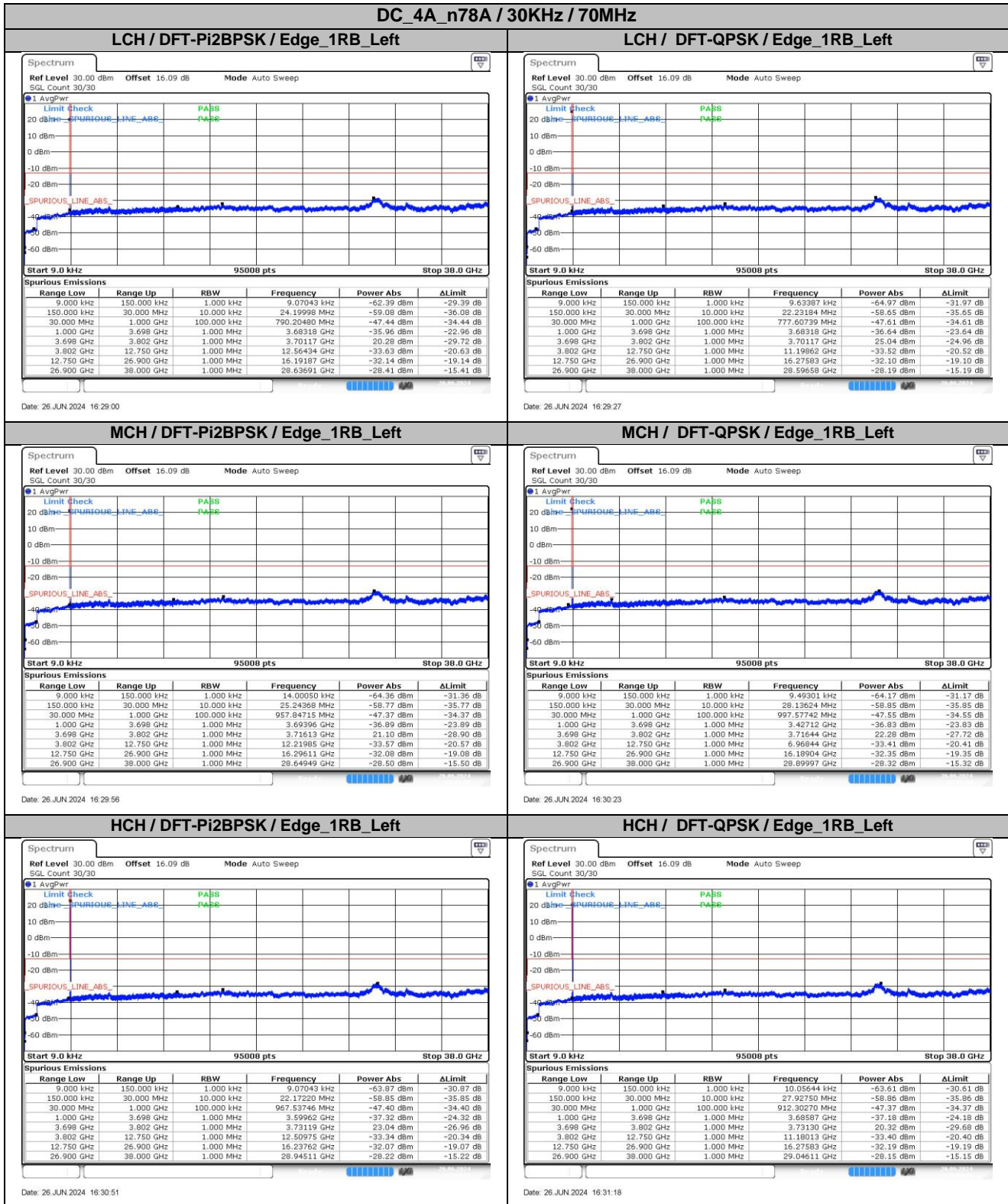


Date: 26 JUN 2024 16:27:52

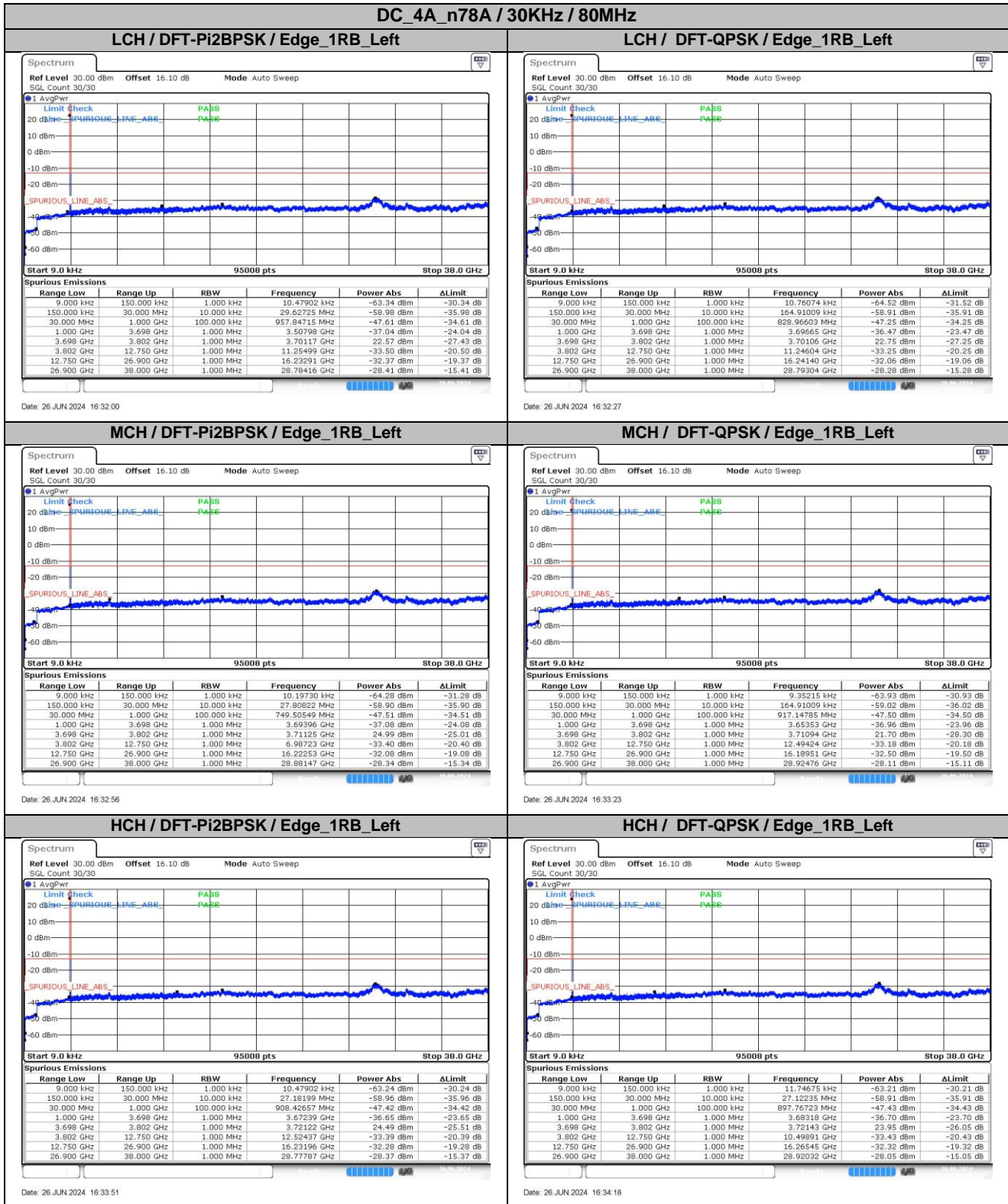
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:28:18

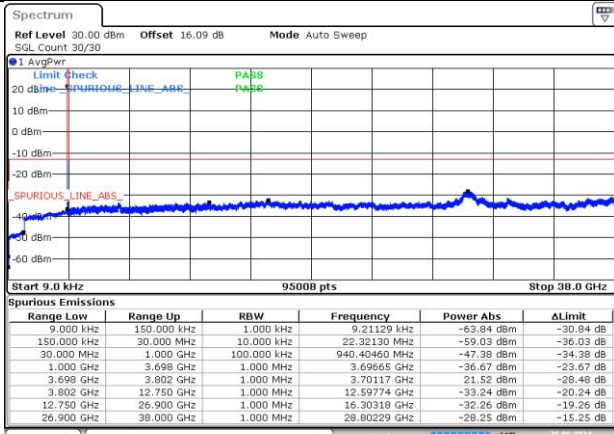






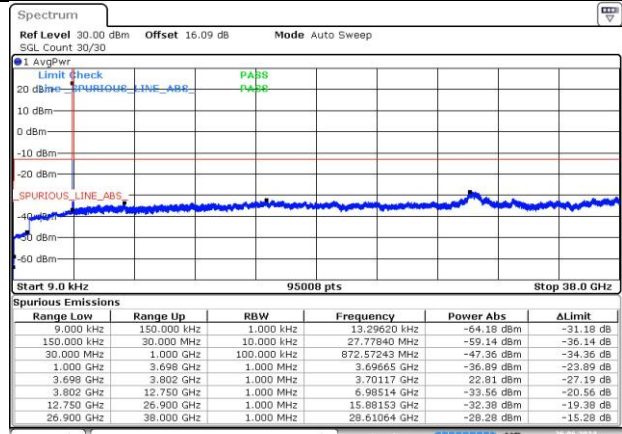
DC\_4A\_n78A / 30KHz / 90MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



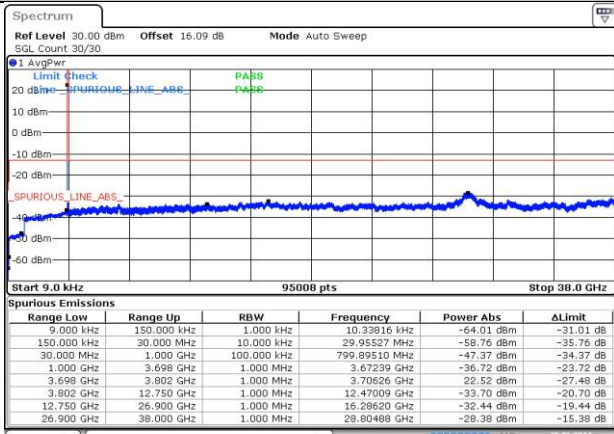
Date: 26 JUN 2024 16:35:00

LCH / DFT-QPSK / Edge\_1RB\_Left



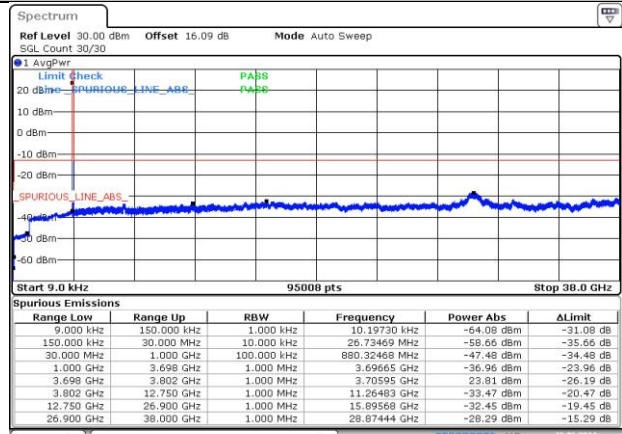
Date: 26 JUN 2024 16:35:27

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



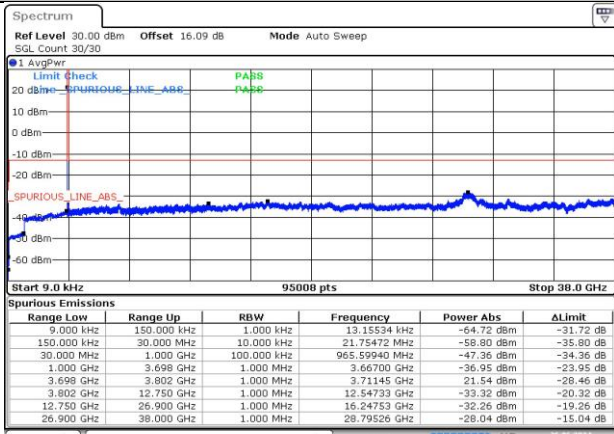
Date: 26 JUN 2024 16:35:55

MCH / DFT-QPSK / Edge\_1RB\_Left



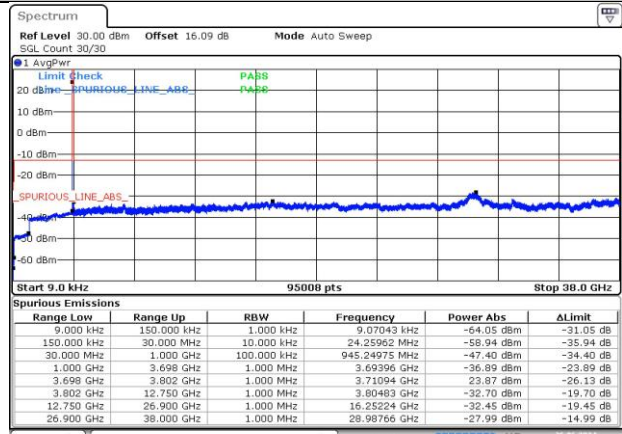
Date: 26 JUN 2024 16:36:22

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left

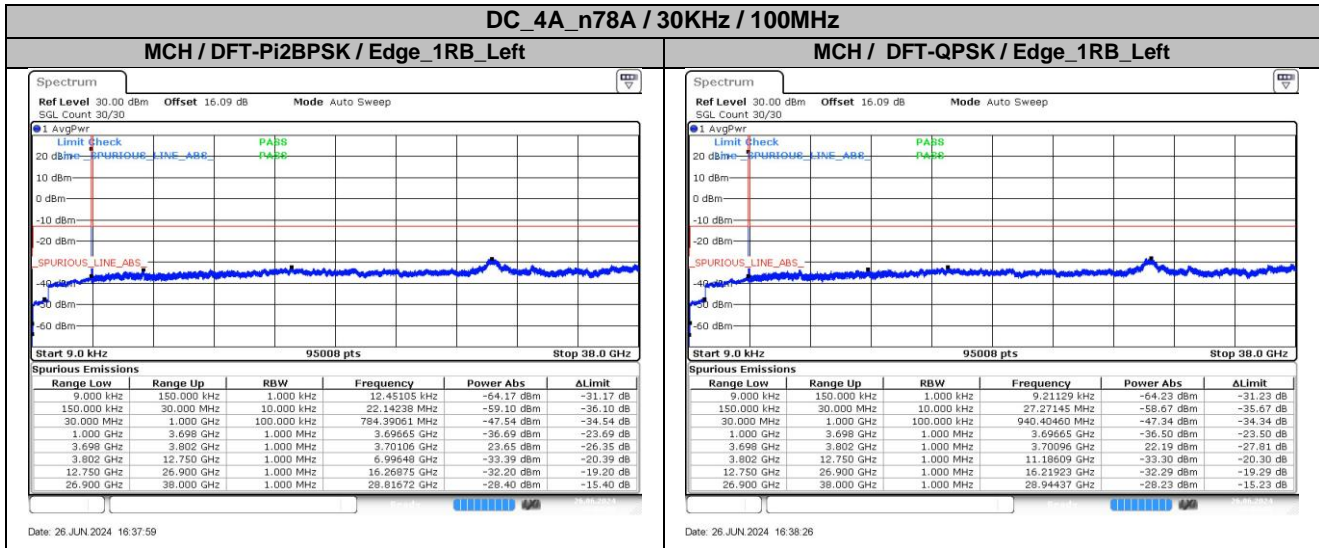


Date: 26 JUN 2024 16:36:50

HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 26 JUN 2024 16:37:17



## 7. Frequency Stability

### 7.1. Test Results

#### 7.1.1. Frequency Error Vs Voltage

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	LV	-10.40	-0.002773	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	NV	-14.50	-0.003867	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	HV	-7.80	-0.002080	Pass

#### 7.1.2. Frequency Error Vs Temperature

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-30°C	NV	-11.10	-0.002960	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-20°C	NV	-13.40	-0.003573	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-10°C	NV	-8.20	-0.002187	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	0°C	NV	-11.30	-0.003013	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	10°C	NV	-16.30	-0.004347	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	20°C	NV	-15.80	-0.004213	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	30°C	NV	-14.20	-0.003787	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	40°C	NV	-13.30	-0.003547	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	50°C	NV	-15.70	-0.004187	Pass

The End