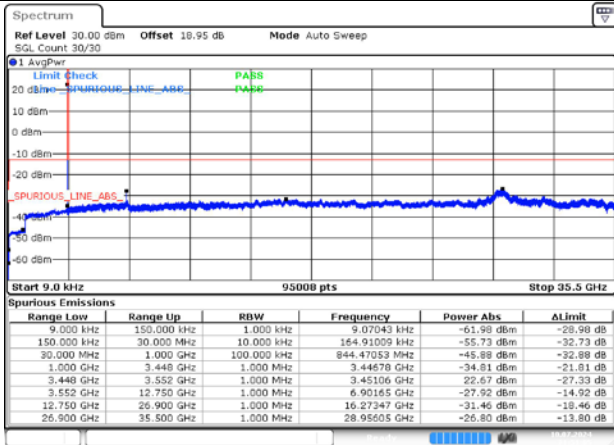


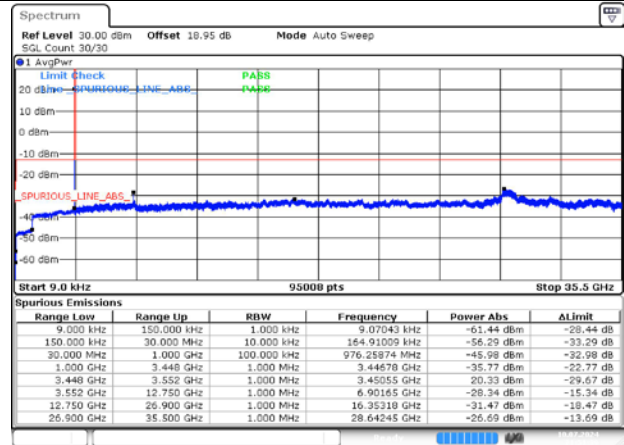
n78A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



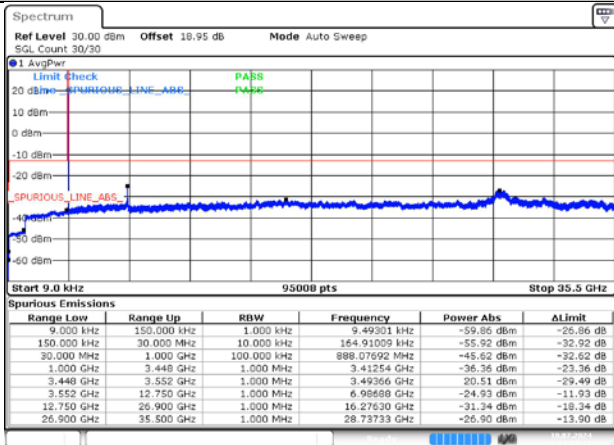
Date: 10.JUL 2024 21:51:05

LCH / DFT-QPSK / Edge\_1RB\_Left



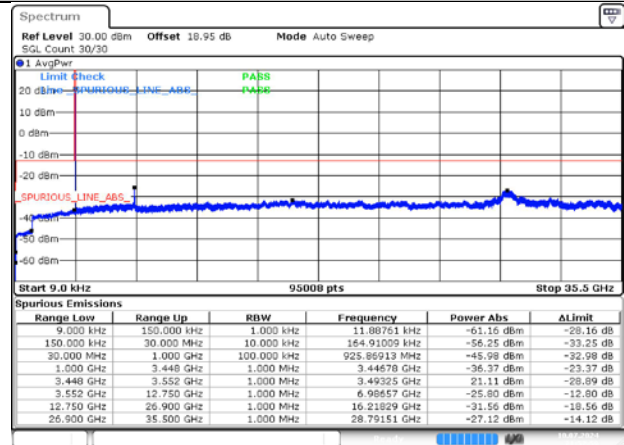
Date: 10.JUL 2024 21:51:38

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



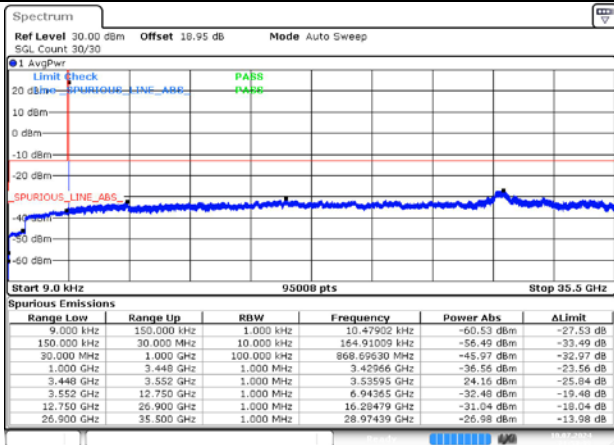
Date: 10.JUL 2024 21:52:11

MCH / DFT-QPSK / Edge\_1RB\_Left



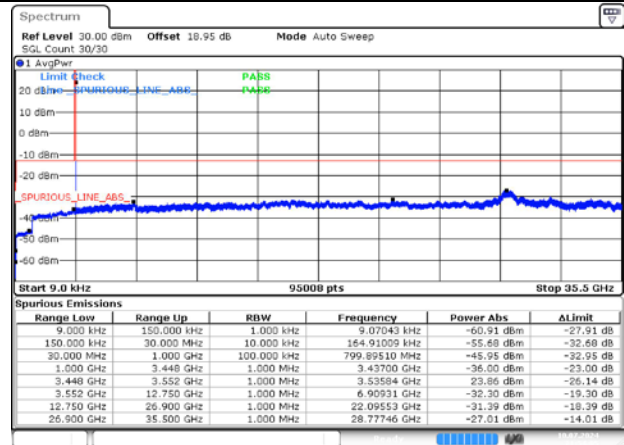
Date: 10.JUL 2024 21:52:59

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL 2024 21:53:32

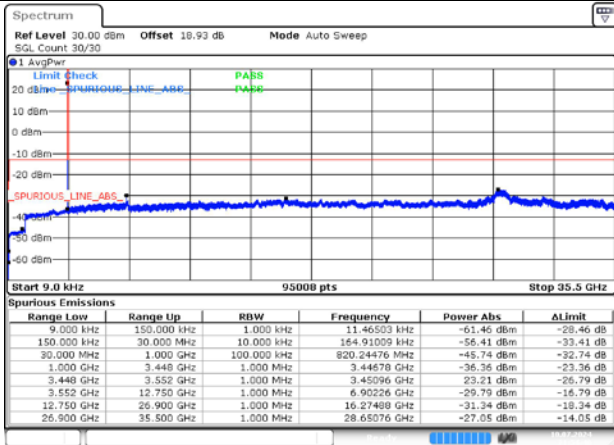
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL 2024 21:54:05

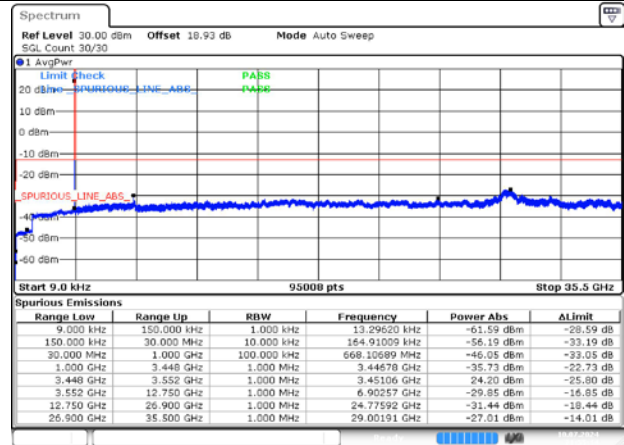
n78A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



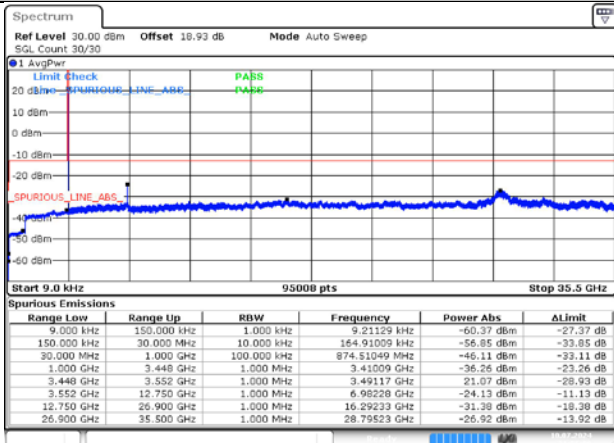
Date: 10.JUL 2024 21:54:40

LCH / DFT-QPSK / Edge\_1RB\_Left



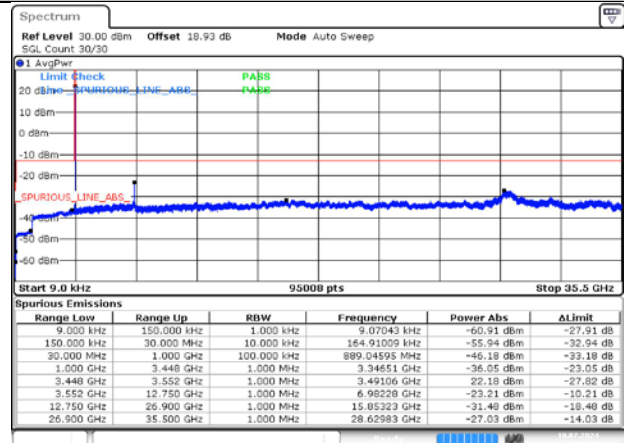
Date: 10.JUL 2024 21:55:13

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



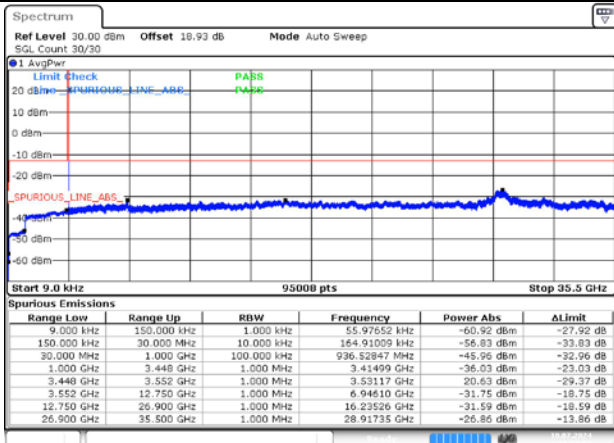
Date: 10.JUL 2024 21:56:01

MCH / DFT-QPSK / Edge\_1RB\_Left



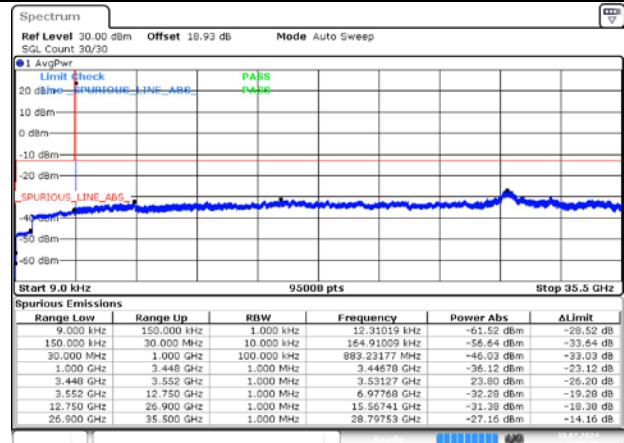
Date: 10.JUL 2024 21:56:34

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL 2024 21:57:07

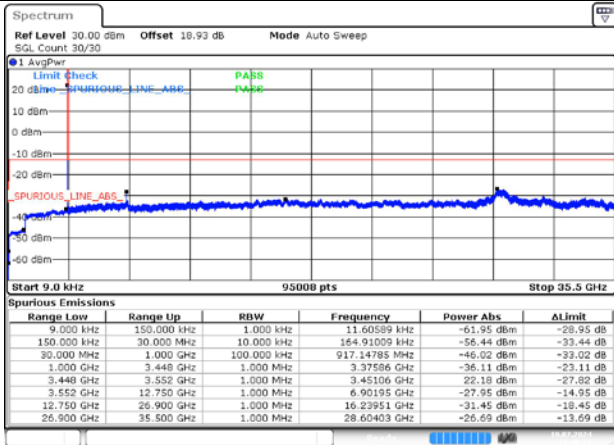
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL 2024 21:57:40

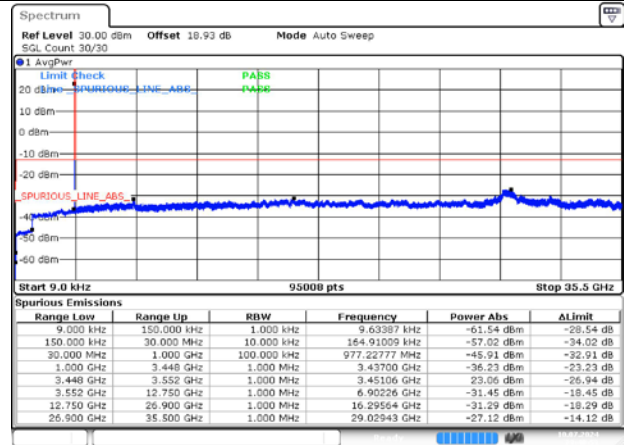
n78A / 30kHz / 25MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



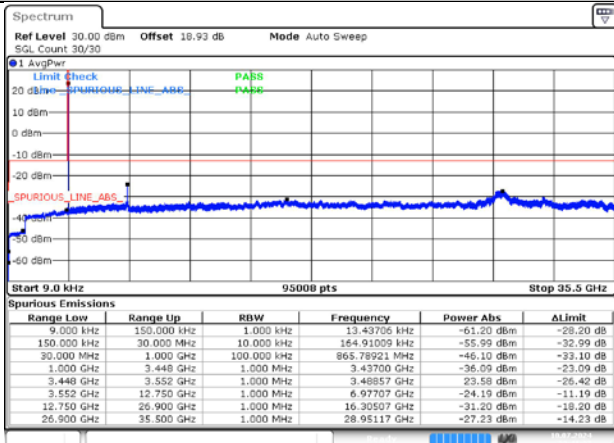
Date: 10.JUL 2024 23:40:20

LCH / DFT-QPSK / Edge\_1RB\_Left



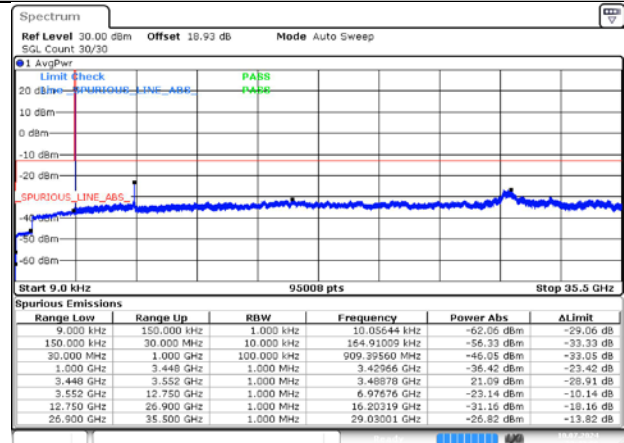
Date: 10.JUL 2024 23:40:53

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



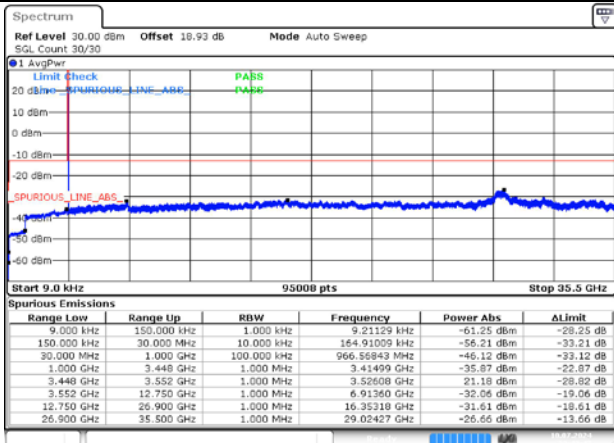
Date: 10.JUL 2024 23:41:26

MCH / DFT-QPSK / Edge\_1RB\_Left



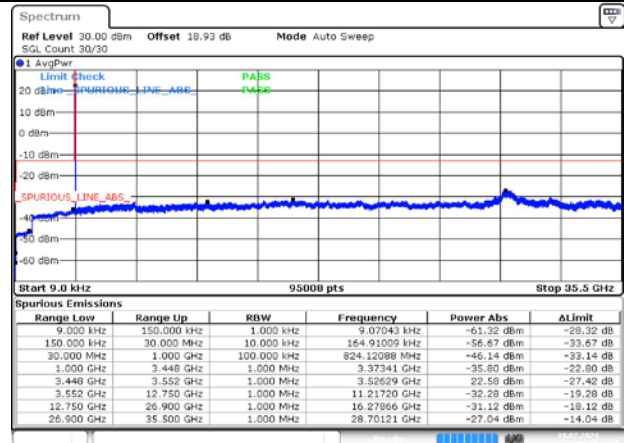
Date: 10.JUL 2024 23:42:00

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL 2024 23:42:47

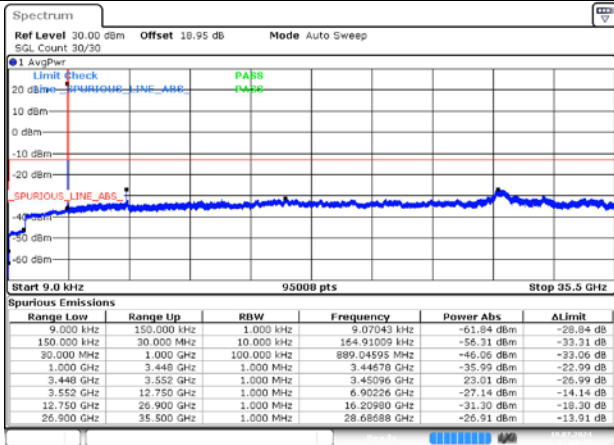
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL 2024 23:43:20

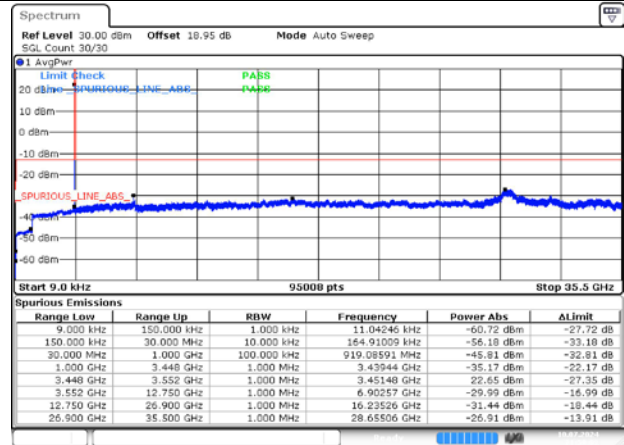
n78A / 30kHz / 30MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



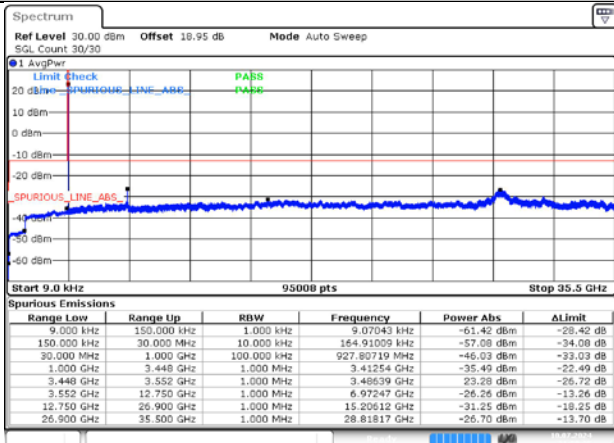
Date: 10.JUL.2024 21:58:15

LCH / DFT-QPSK / Edge\_1RB\_Left



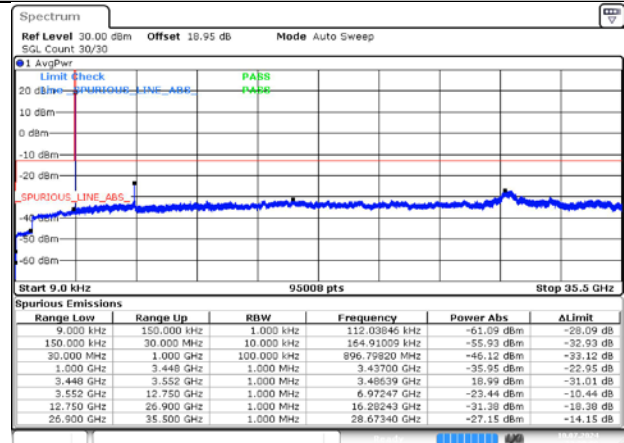
Date: 10.JUL.2024 21:59:02

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



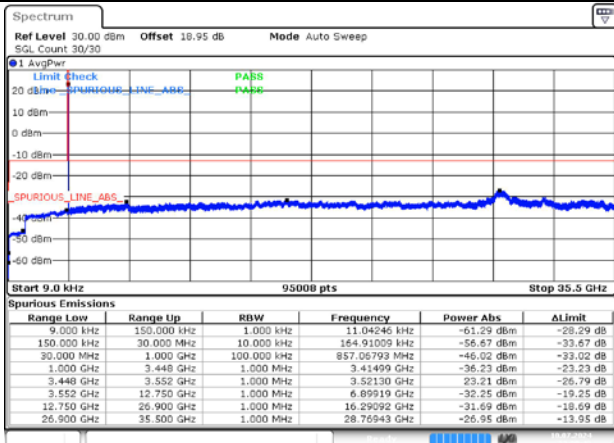
Date: 10.JUL.2024 21:59:35

MCH / DFT-QPSK / Edge\_1RB\_Left



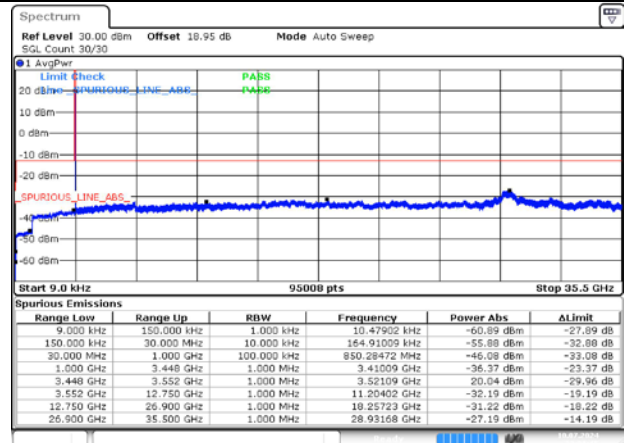
Date: 10.JUL.2024 22:00:08

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:00:41

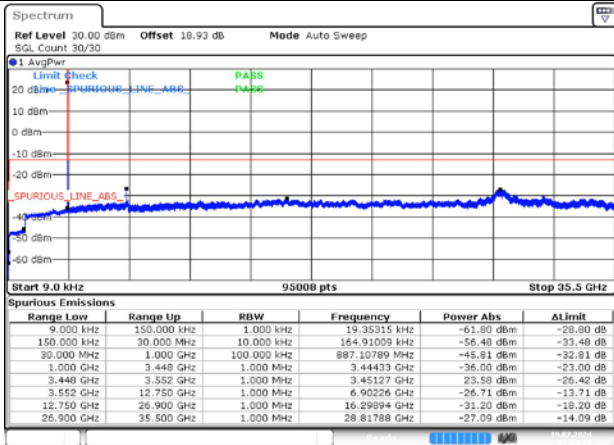
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:01:15

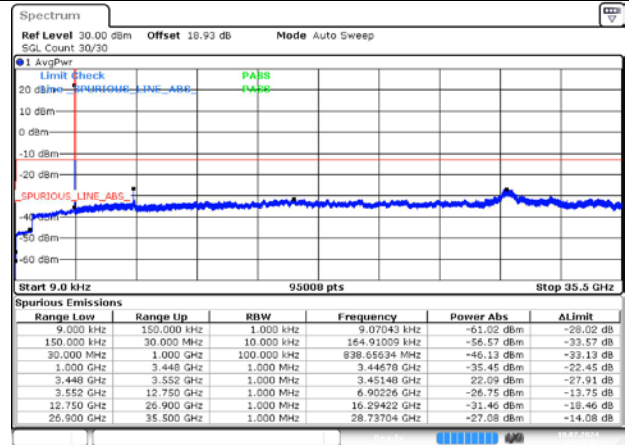
n78A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



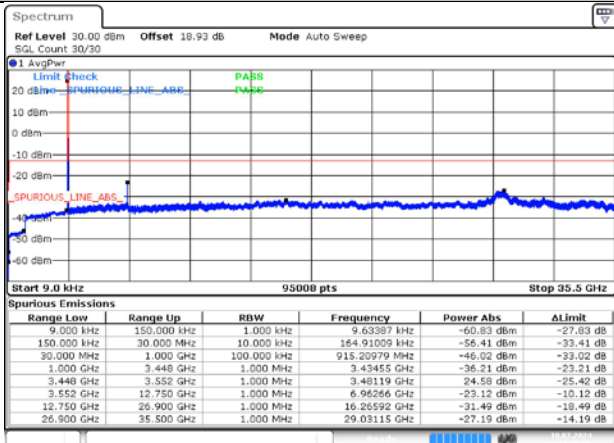
Date: 10.JUL.2024 22:04:29

LCH / DFT-QPSK / Edge\_1RB\_Left



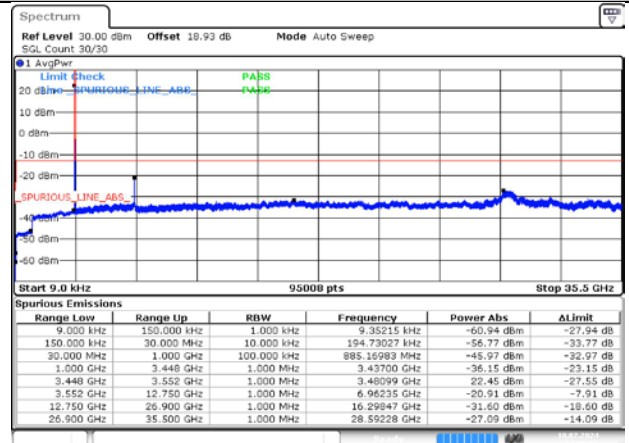
Date: 10.JUL.2024 22:05:01

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



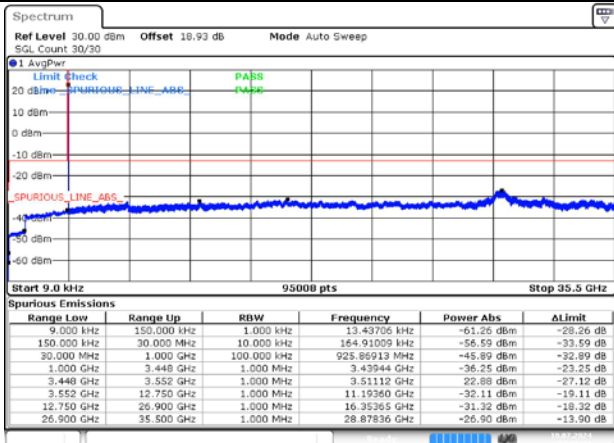
Date: 10.JUL.2024 22:05:35

MCH / DFT-QPSK / Edge\_1RB\_Left



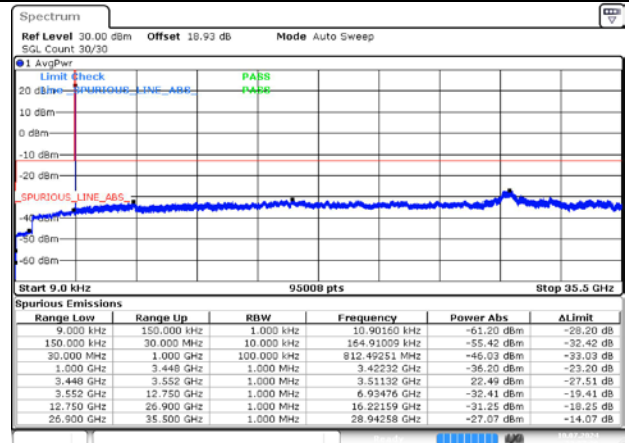
Date: 10.JUL.2024 22:06:08

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:06:41

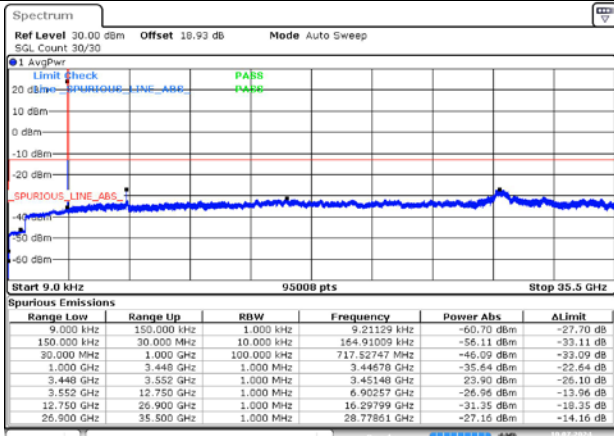
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:07:28

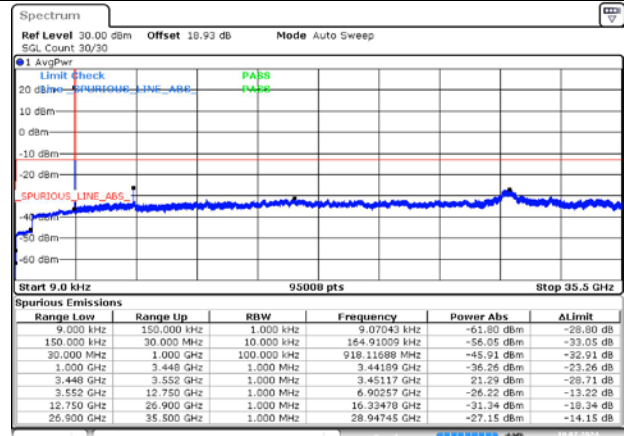
n78A / 30kHz / 50MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



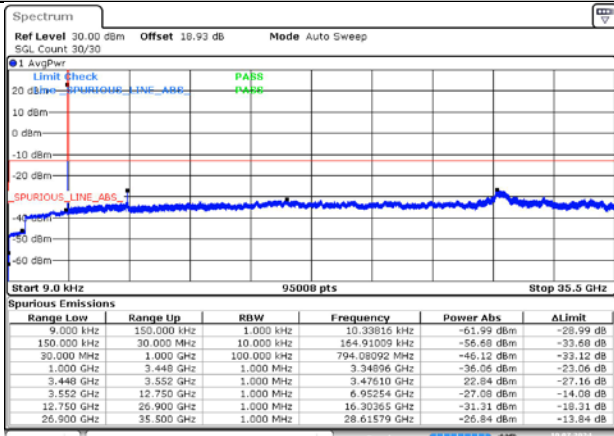
Date: 10.JUL.2024 22:08:03

LCH / DFT-QPSK / Edge\_1RB\_Left



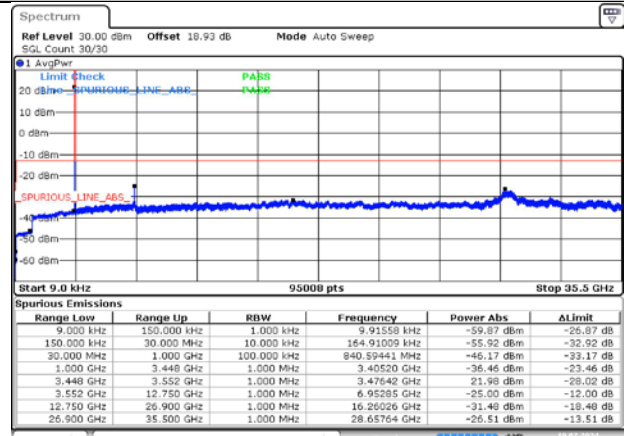
Date: 10.JUL.2024 22:08:35

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



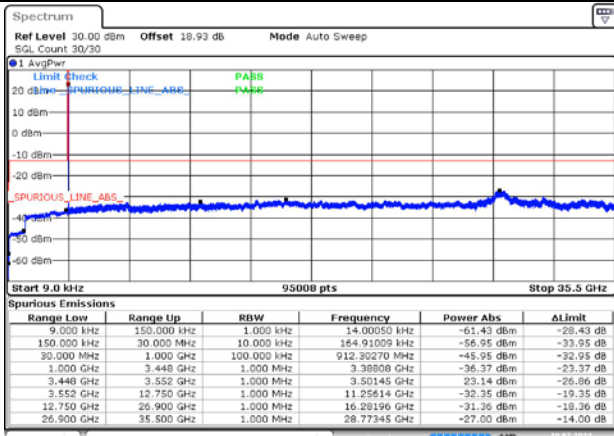
Date: 10.JUL.2024 22:09:09

MCH / DFT-QPSK / Edge\_1RB\_Left



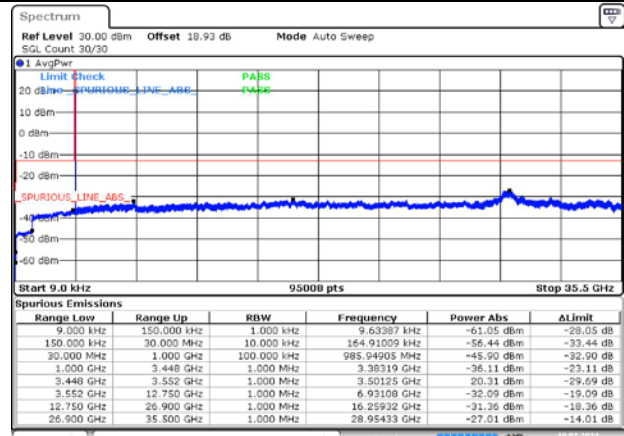
Date: 10.JUL.2024 22:09:42

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:10:29

HCH / DFT-QPSK / Edge\_1RB\_Left

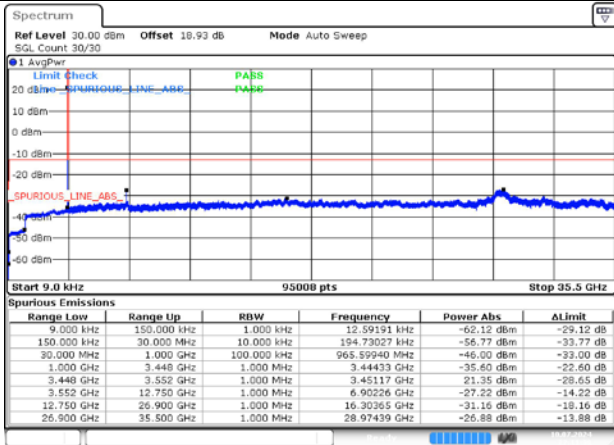


Date: 10.JUL.2024 22:11:03



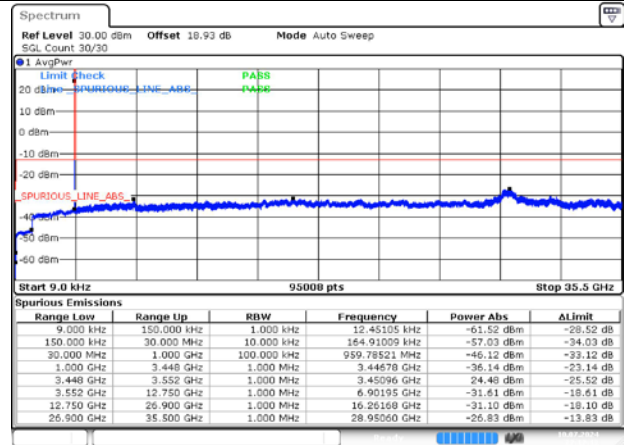
n78A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



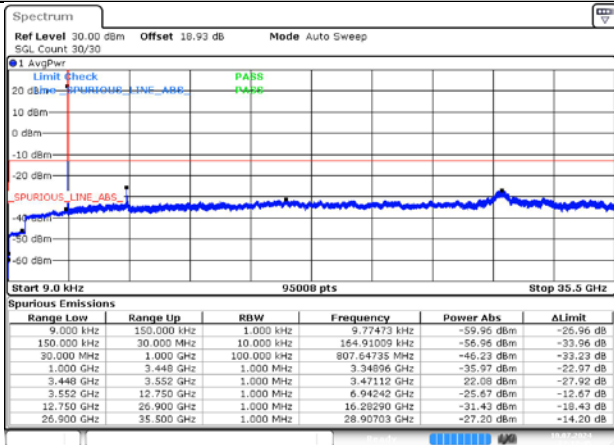
Date: 10.JUL.2024 22:11:38

LCH / DFT-QPSK / Edge\_1RB\_Left



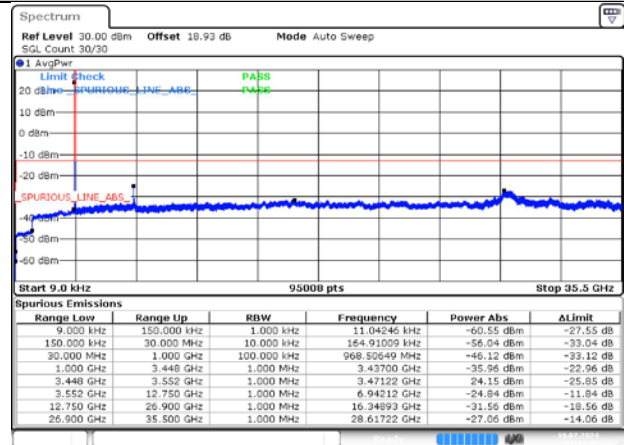
Date: 10.JUL.2024 22:11:11

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



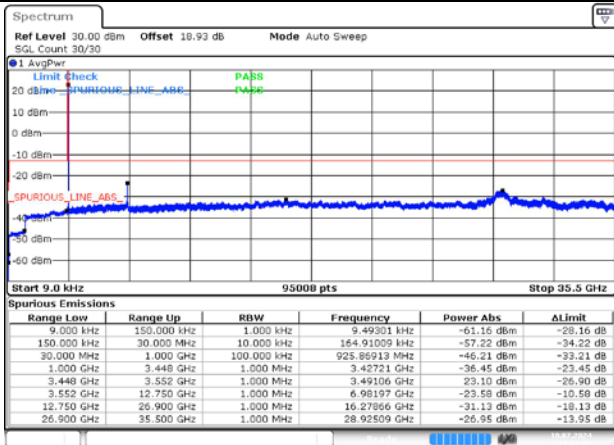
Date: 10.JUL.2024 22:12:45

MCH / DFT-QPSK / Edge\_1RB\_Left



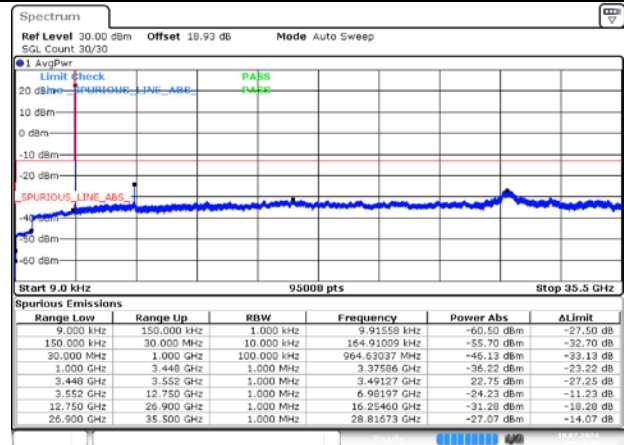
Date: 10.JUL.2024 22:13:35

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:14:09

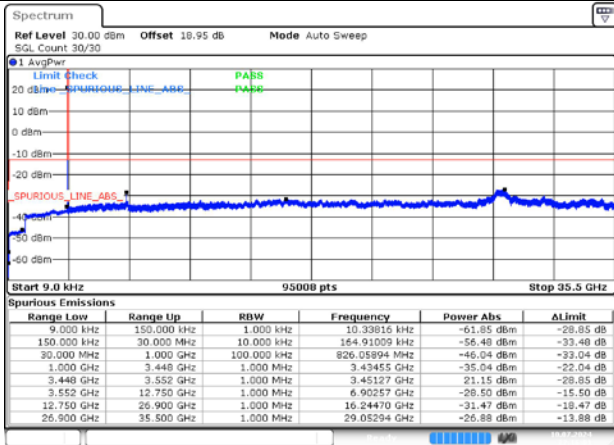
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:14:42

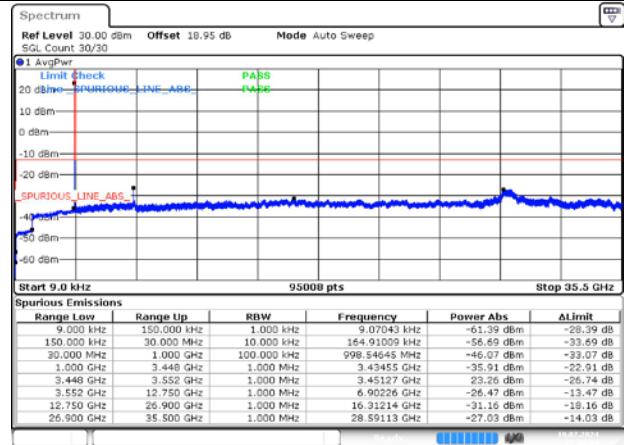
n78A / 30KHz / 70MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



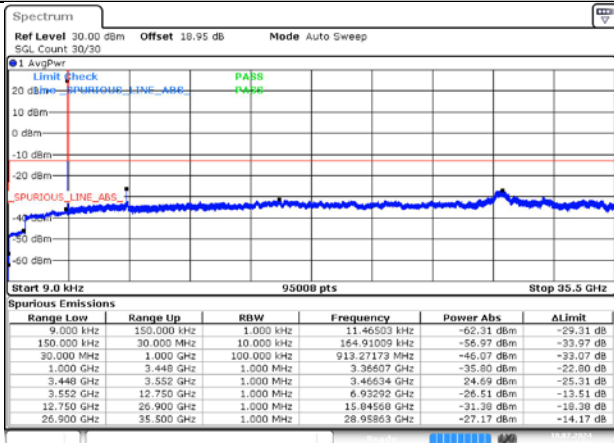
Date: 10.JUL.2024 22:16:00

LCH / DFT-QPSK / Edge\_1RB\_Left



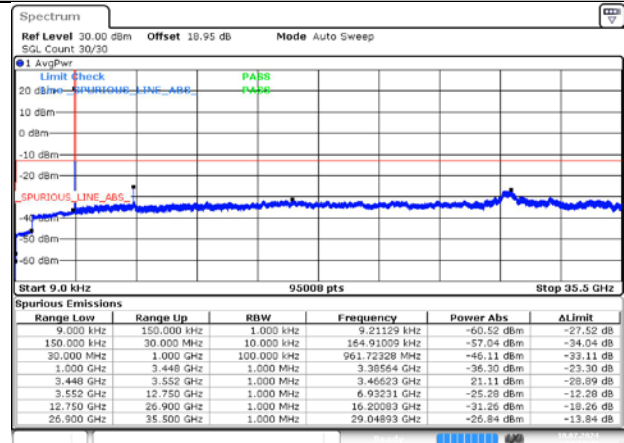
Date: 10.JUL.2024 22:16:34

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



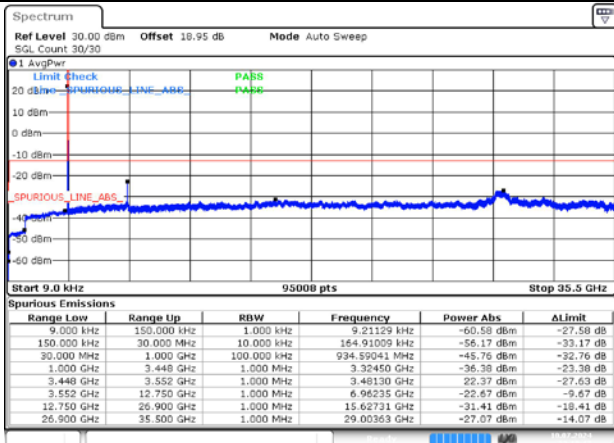
Date: 10.JUL.2024 22:17:09

MCH / DFT-QPSK / Edge\_1RB\_Left



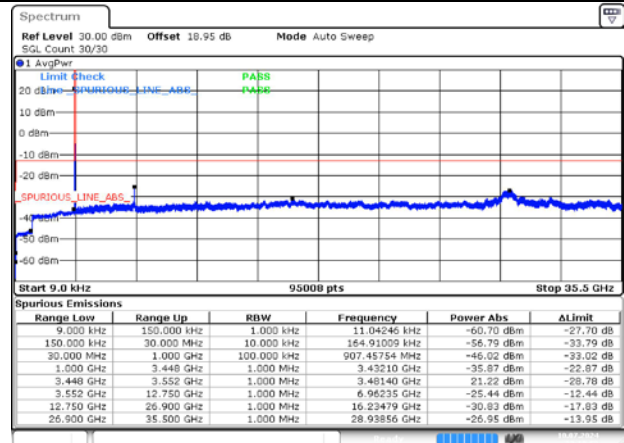
Date: 10.JUL.2024 22:17:42

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:18:17

HCH / DFT-QPSK / Edge\_1RB\_Left

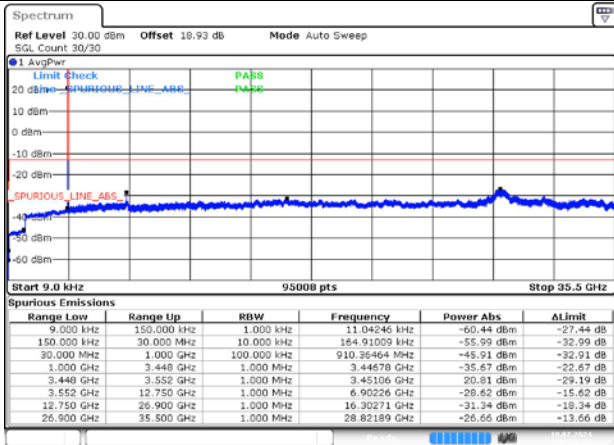


Date: 10.JUL.2024 22:19:52



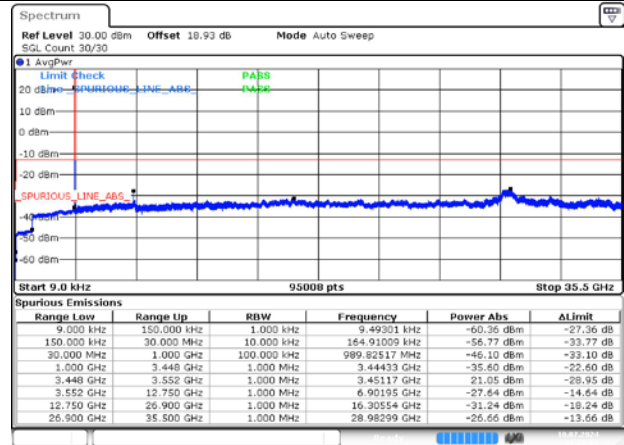
n78A / 30kHz / 80MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



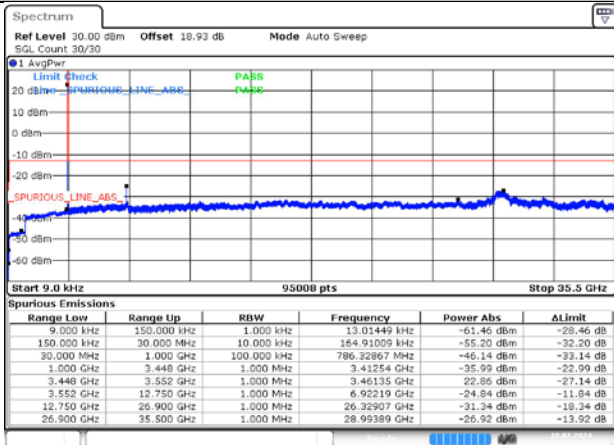
Date: 10.JUL.2024 22:20:28

LCH / DFT-QPSK / Edge\_1RB\_Left



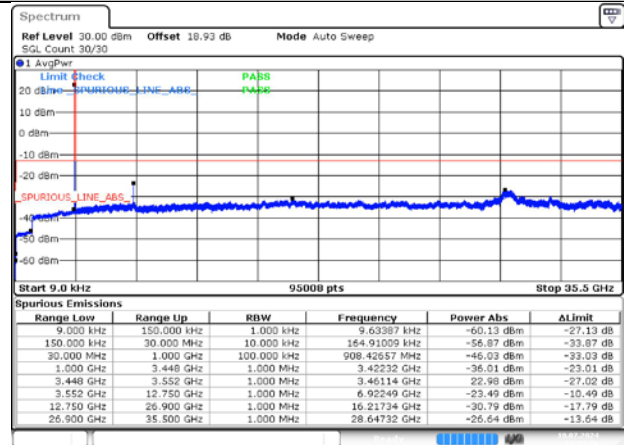
Date: 10.JUL.2024 22:21:02

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



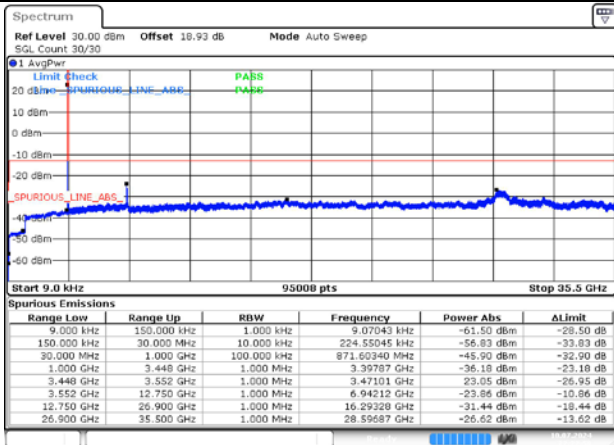
Date: 10.JUL.2024 22:21:36

MCH / DFT-QPSK / Edge\_1RB\_Left



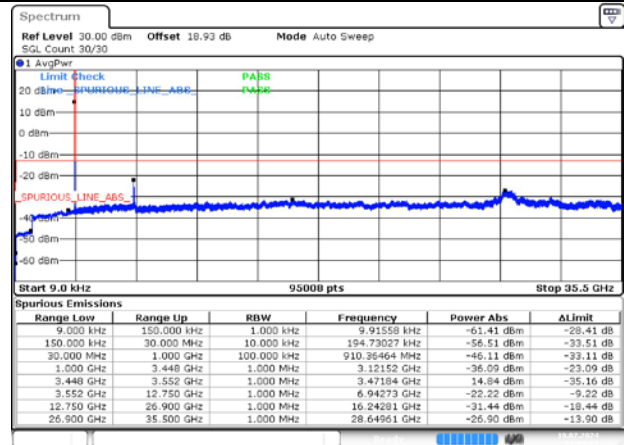
Date: 10.JUL.2024 22:22:10

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:22:58

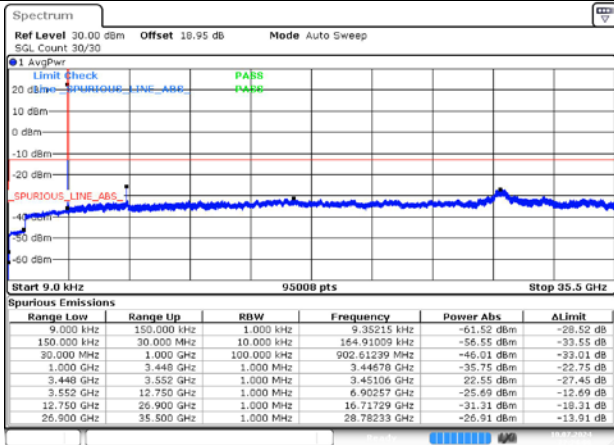
HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:23:31

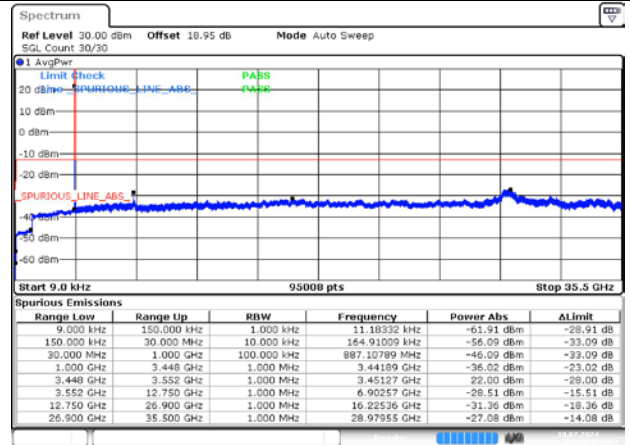
n78A / 30kHz / 90MHz

LCH / DFT-Pi2BPSK / Edge\_1RB\_Left



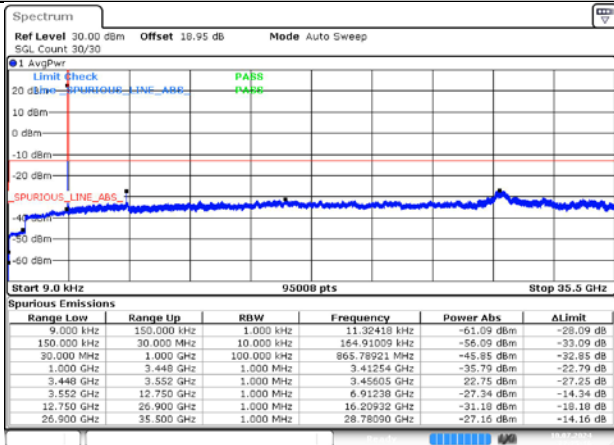
Date: 10.JUL.2024 22:24:07

LCH / DFT-QPSK / Edge\_1RB\_Left



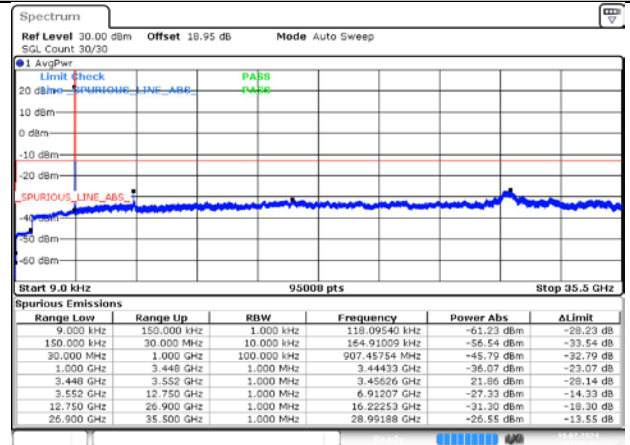
Date: 10.JUL.2024 22:24:41

MCH / DFT-Pi2BPSK / Edge\_1RB\_Left



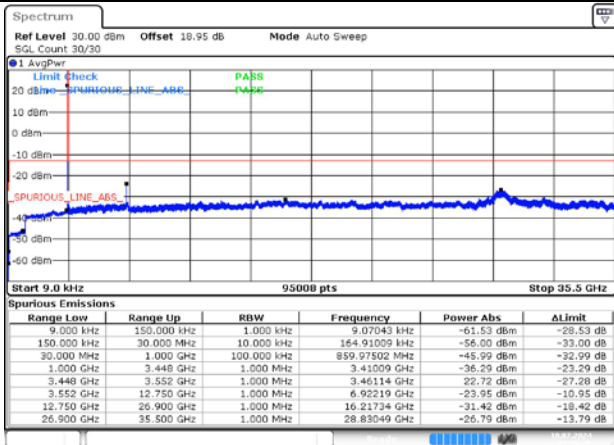
Date: 10.JUL.2024 22:25:15

MCH / DFT-QPSK / Edge\_1RB\_Left



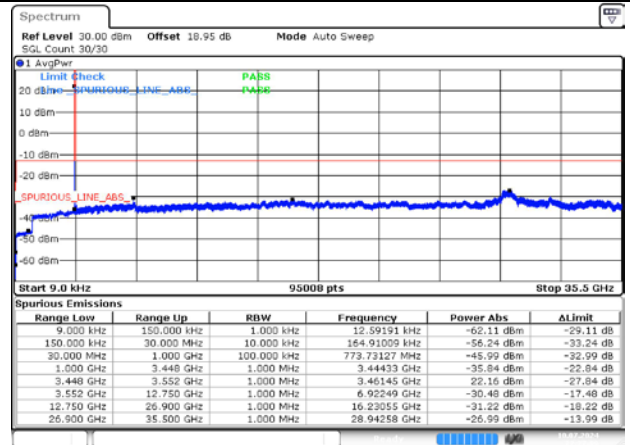
Date: 10.JUL.2024 22:26:03

HCH / DFT-Pi2BPSK / Edge\_1RB\_Left

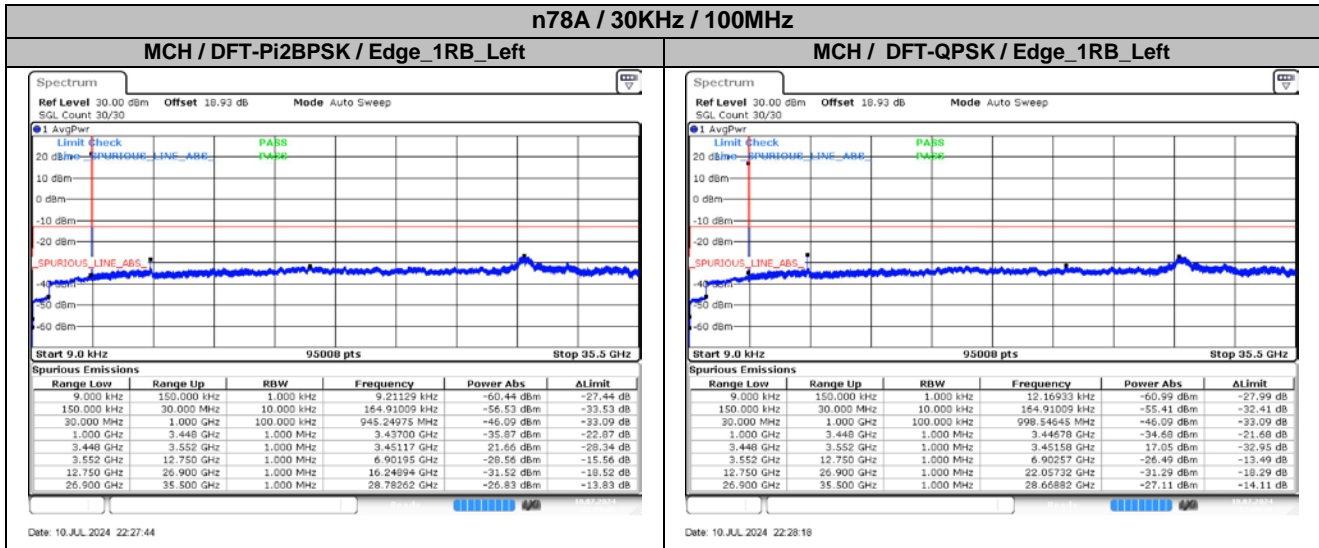


Date: 10.JUL.2024 22:26:34

HCH / DFT-QPSK / Edge\_1RB\_Left



Date: 10.JUL.2024 22:27:08



## 7. Frequency Stability

### 7.1. Test Results

#### 7.1.1. Frequency Error Vs Voltage

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	LV	-9.10	-0.002600	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	NV	-12.80	-0.003657	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	HV	-5.50	-0.001571	Pass

#### 7.1.2. Frequency Error Vs Temperature

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-30°C	NV	-13.50	-0.003857	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-20°C	NV	-8.80	-0.002514	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-10°C	NV	-6.70	-0.001914	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	0°C	NV	-7.40	-0.002114	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	10°C	NV	-9.70	-0.002771	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	20°C	NV	-14.00	-0.004000	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	30°C	NV	-9.50	-0.002714	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	40°C	NV	-11.30	-0.003229	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	50°C	NV	-7.60	-0.002171	Pass

The End