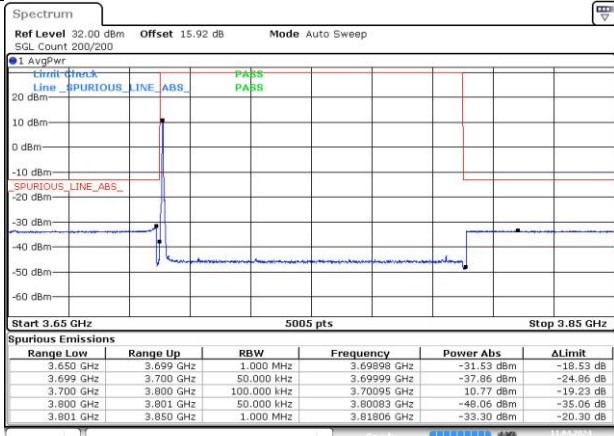


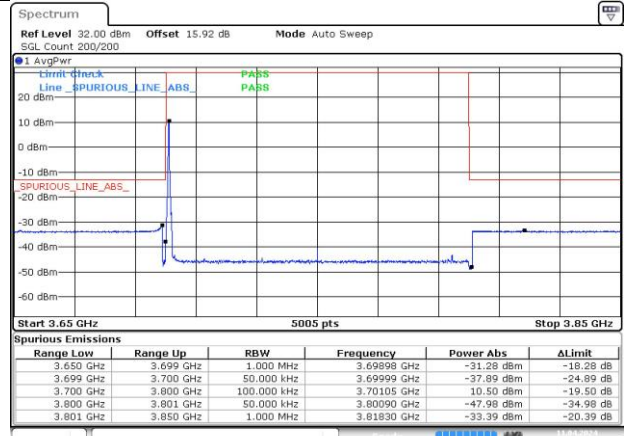
DC_2A_n78A / 30KHz / 100MHz

MCH / DFT-Pi2BPSK / Edge_1RB_Left



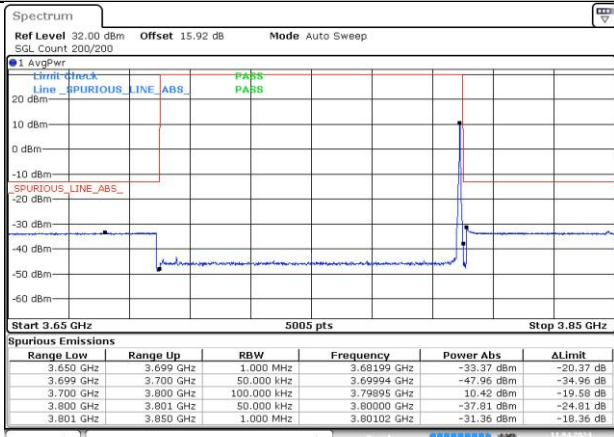
Date: 11 APR 2024 01:33:52

MCH / DFT-QPSK / Edge_1RB_Left



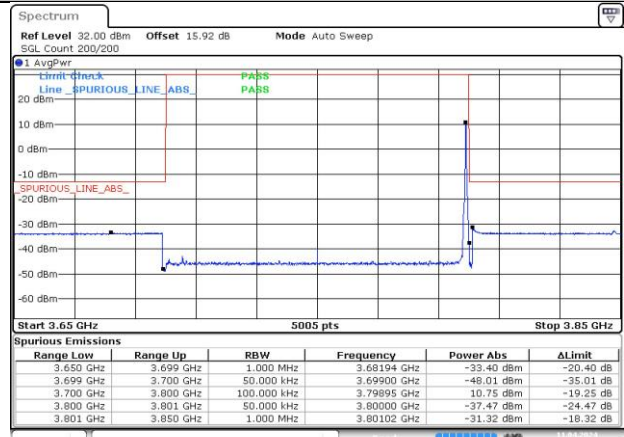
Date: 11 APR 2024 01:35:56

MCH / DFT-Pi2BPSK / Edge_1RB_Right



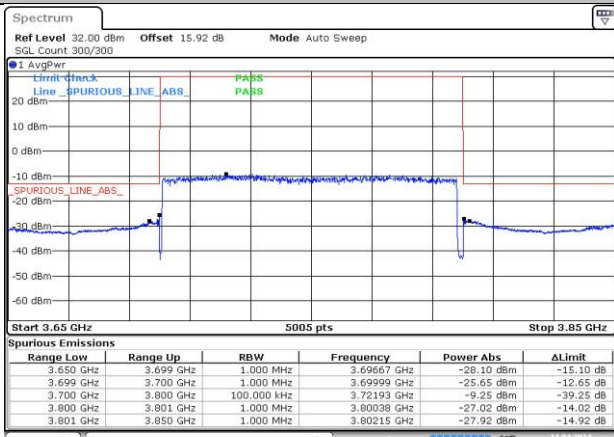
Date: 11 APR 2024 01:34:43

MCH / DFT-QPSK / Edge_1RB_Right



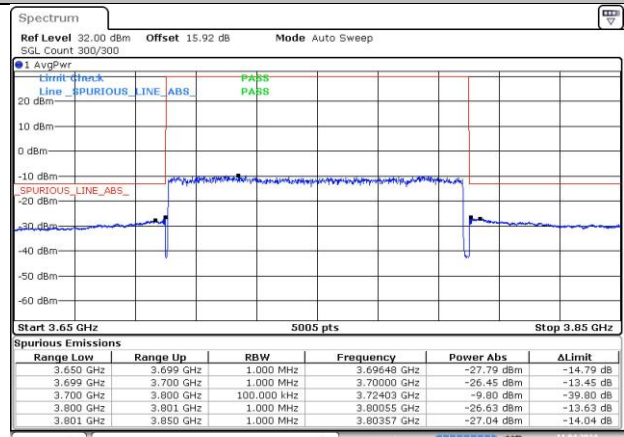
Date: 11 APR 2024 01:36:46

MCH / DFT-Pi2BPSK / Outer_Full



Date: 11 APR 2024 01:35:05

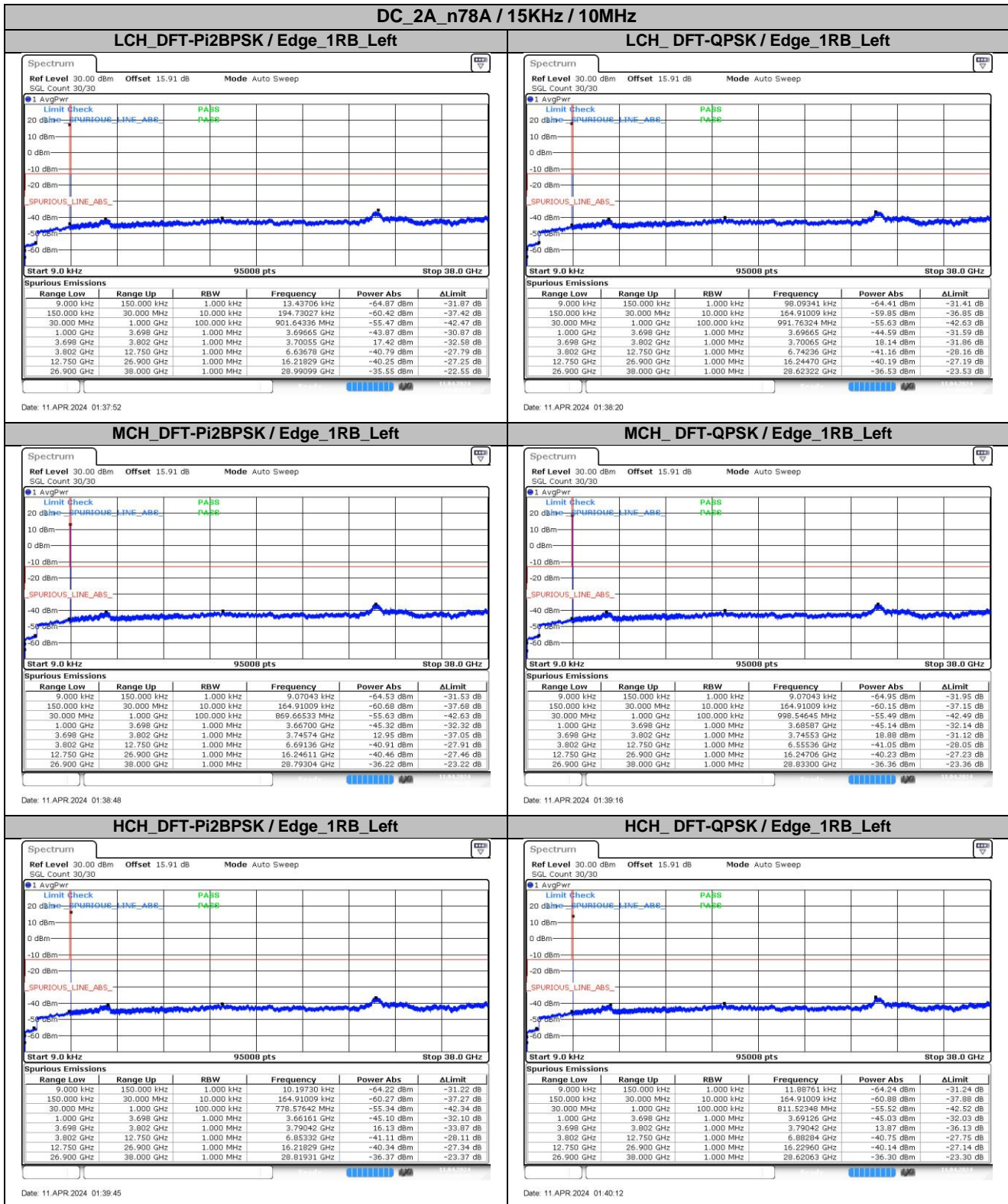
MCH / DFT-QPSK / Outer_Full



Date: 11 APR 2024 01:37:07

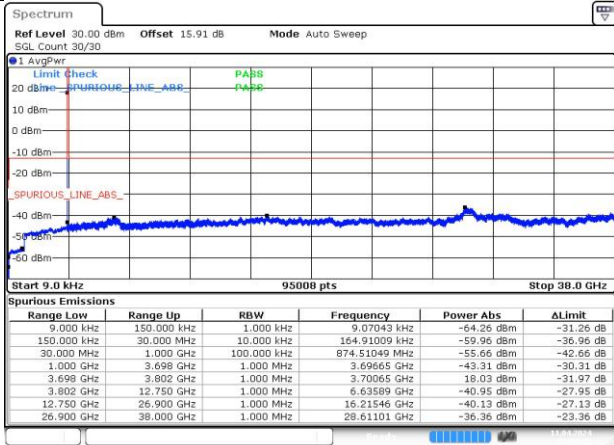
6. Conducted Spurious Emission

6.1. Test Plots



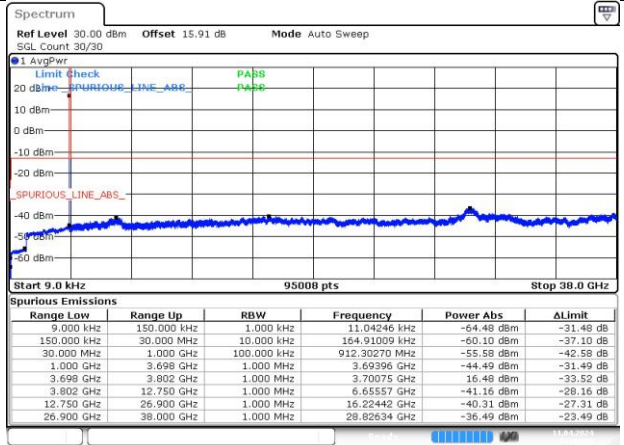
DC_2A_n78A / 15KHz / 15MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



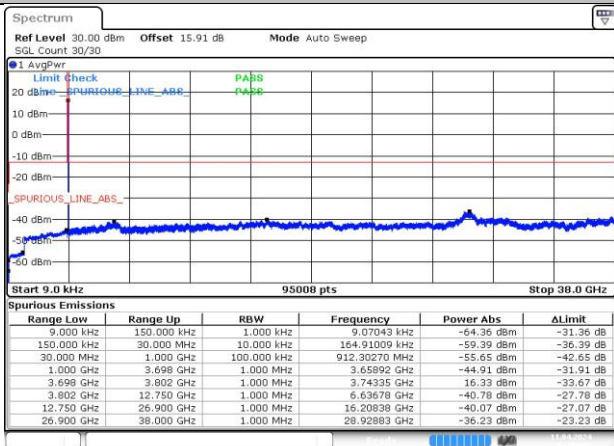
Date: 11 APR 2024 01:40:54

LCH_DFT-QPSK / Edge_1RB_Left



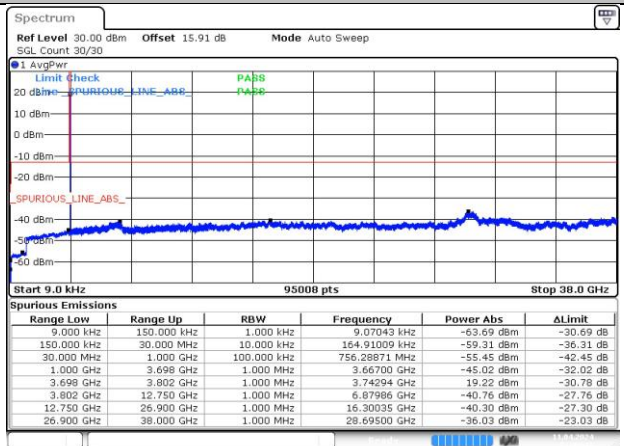
Date: 11 APR 2024 01:41:22

MCH_DFT-Pi2BPSK / Edge_1RB_Left



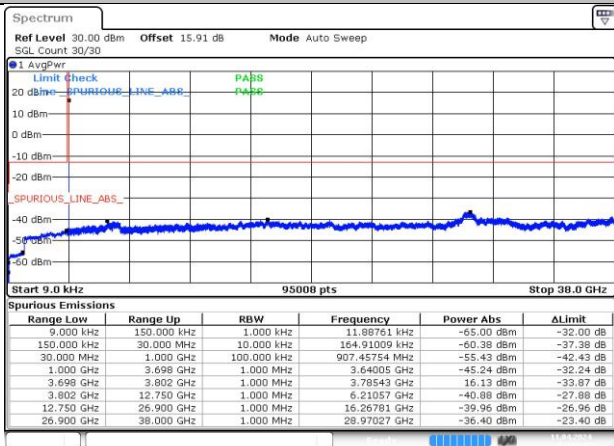
Date: 11 APR 2024 01:41:51

MCH_DFT-QPSK / Edge_1RB_Left



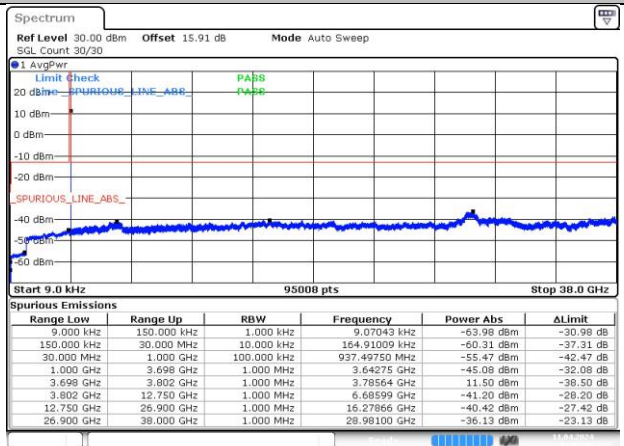
Date: 11 APR 2024 01:42:18

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 01:42:47

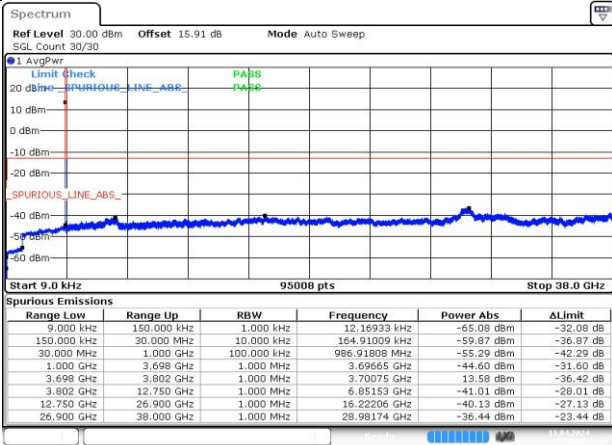
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 01:43:14

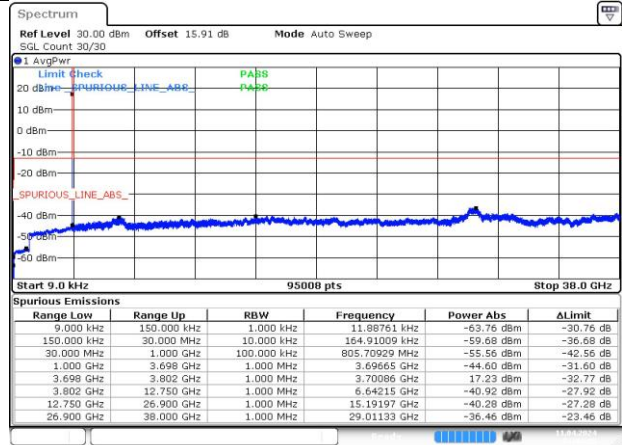
DC_2A_n78A / 15KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



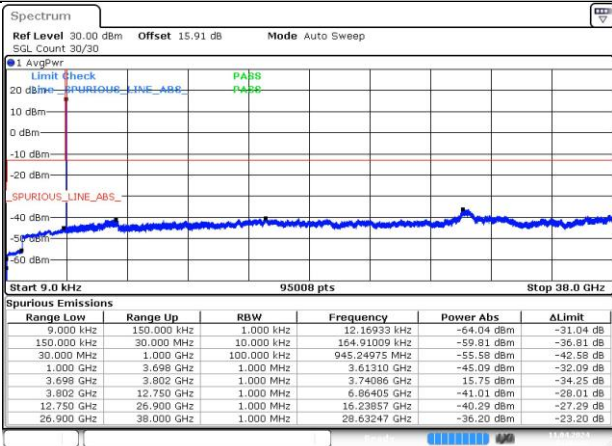
Date: 11 APR 2024 01:43:57

LCH_DFT-QPSK / Edge_1RB_Left



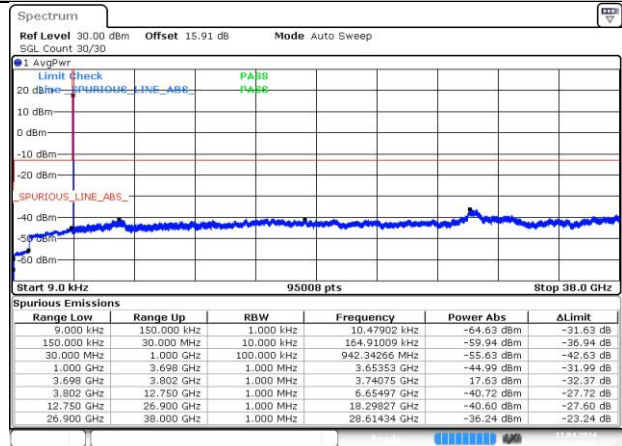
Date: 11 APR 2024 01:44:24

MCH_DFT-Pi2BPSK / Edge_1RB_Left



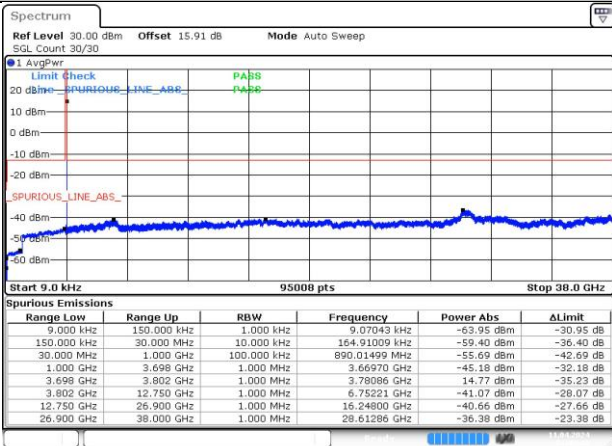
Date: 11 APR 2024 01:44:53

MCH_DFT-QPSK / Edge_1RB_Left



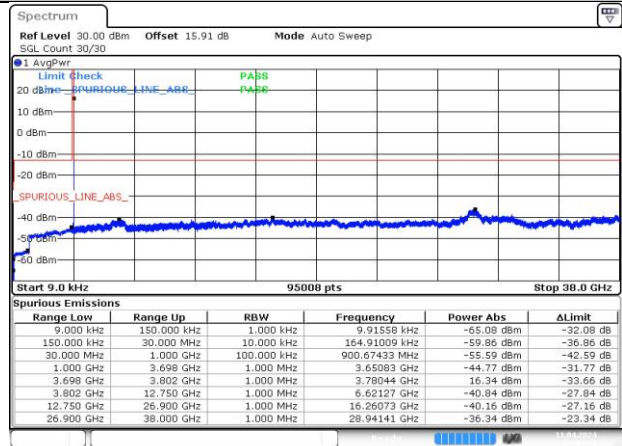
Date: 11 APR 2024 01:45:20

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 01:45:49

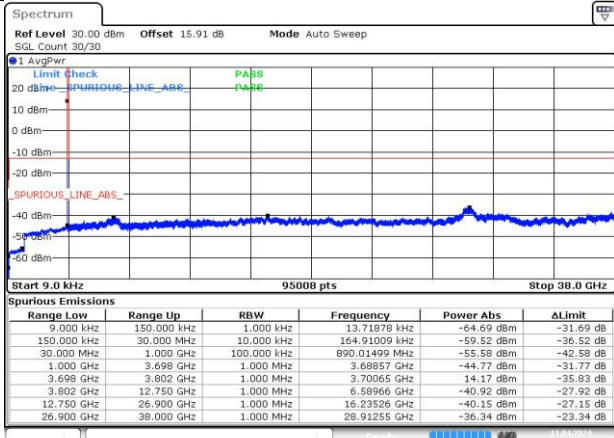
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 01:46:17

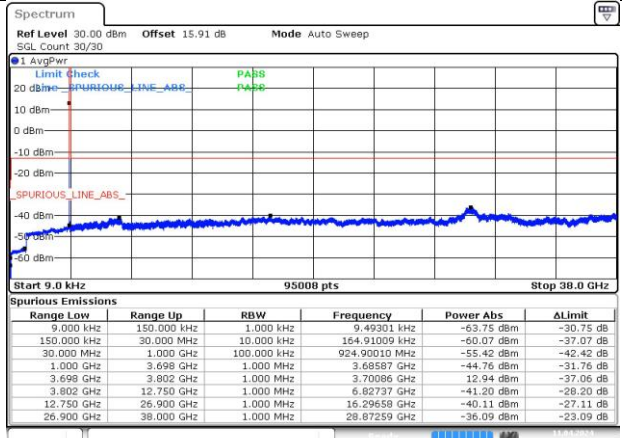
DC_2A_n78A / 15KHz / 25MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



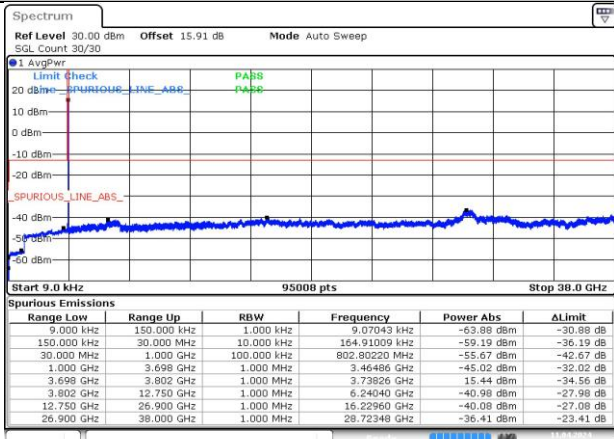
Date: 11 APR 2024 01:46:59

LCH_DFT-QPSK / Edge_1RB_Left



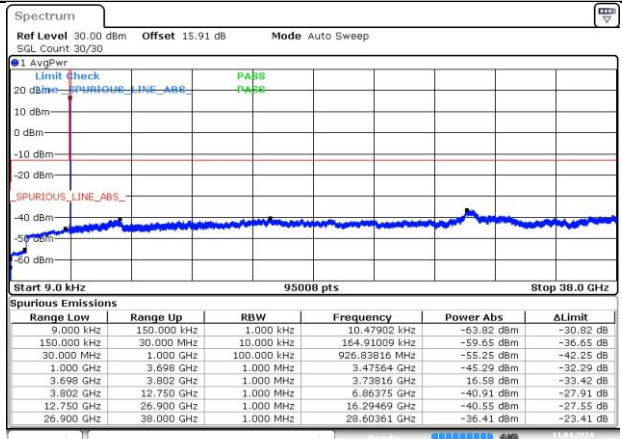
Date: 11 APR 2024 01:47:27

MCH_DFT-Pi2BPSK / Edge_1RB_Left



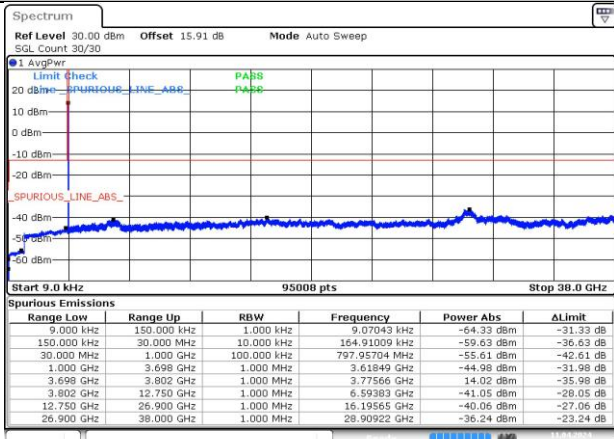
Date: 11 APR 2024 01:47:55

MCH_DFT-QPSK / Edge_1RB_Left



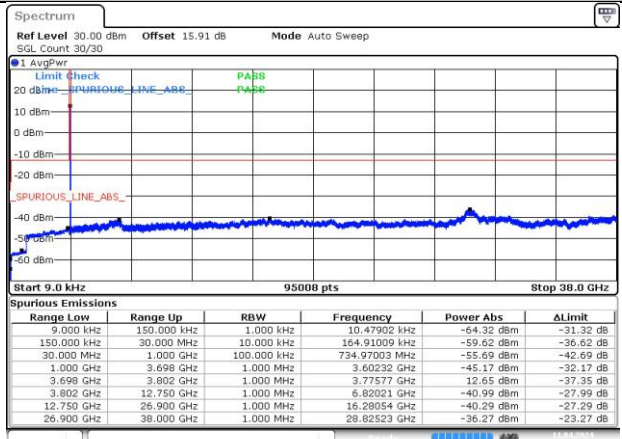
Date: 11 APR 2024 01:48:23

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 01:48:52

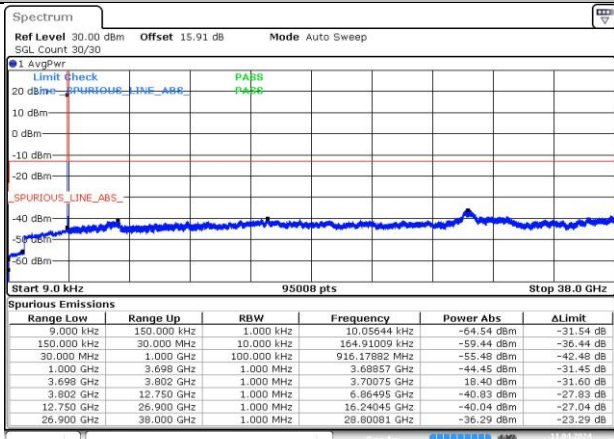
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 01:49:20

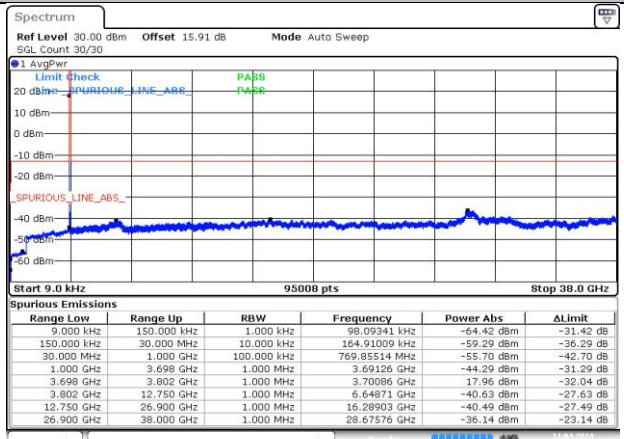
DC_2A_n78A / 15KHz / 30MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



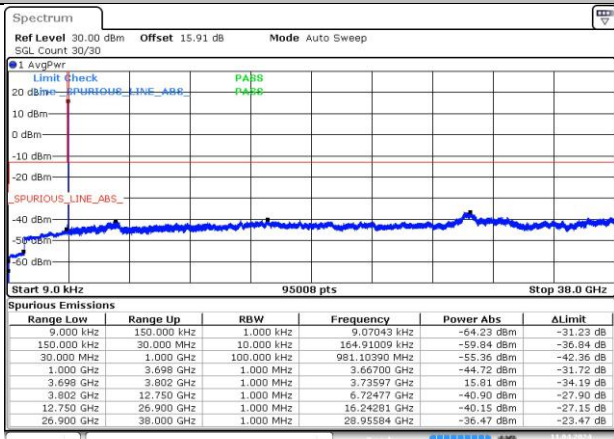
Date: 11 APR 2024 01:50:02

LCH_DFT-QPSK / Edge_1RB_Left



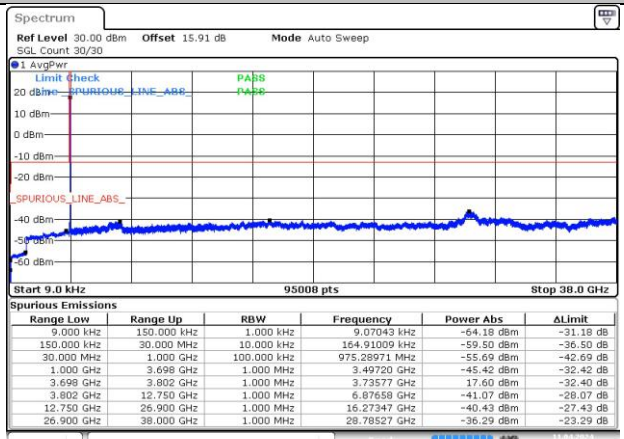
Date: 11 APR 2024 01:50:29

MCH_DFT-Pi2BPSK / Edge_1RB_Left



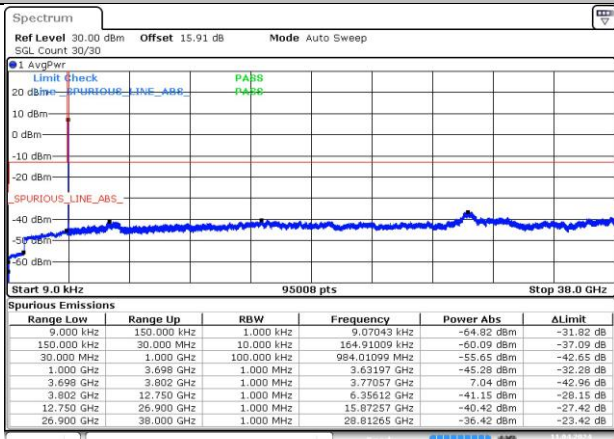
Date: 11 APR 2024 01:50:58

MCH_DFT-QPSK / Edge_1RB_Left



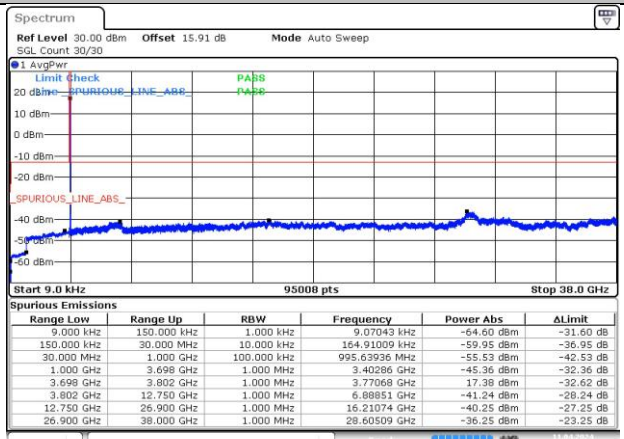
Date: 11 APR 2024 01:51:26

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 01:51:55

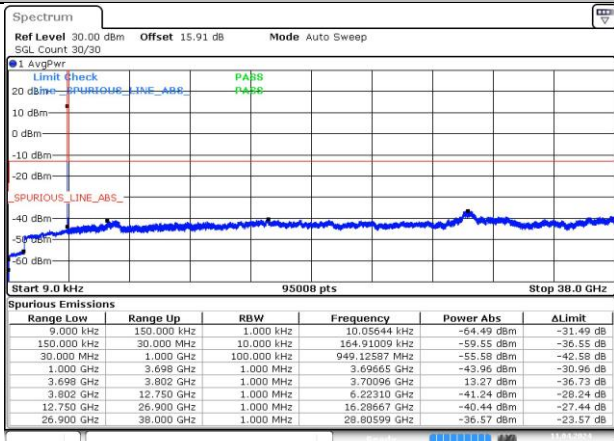
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 01:52:22

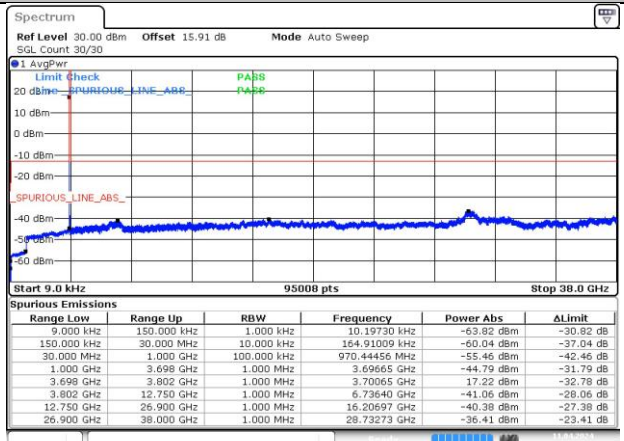
DC_2A_n78A / 15KHz / 40MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



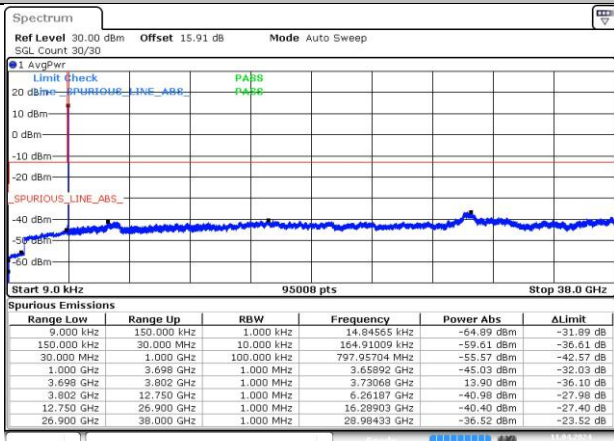
Date: 11 APR 2024 01:53:05

LCH_DFT-QPSK / Edge_1RB_Left



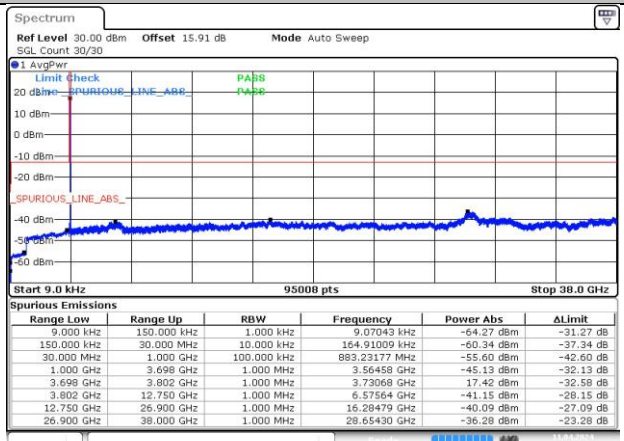
Date: 11 APR 2024 01:53:33

MCH_DFT-Pi2BPSK / Edge_1RB_Left



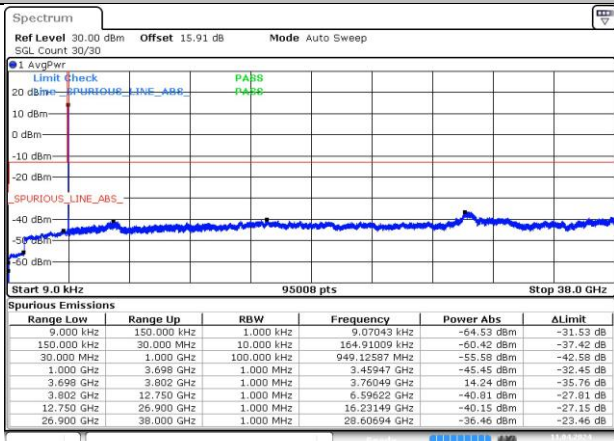
Date: 11 APR 2024 01:54:01

MCH_DFT-QPSK / Edge_1RB_Left



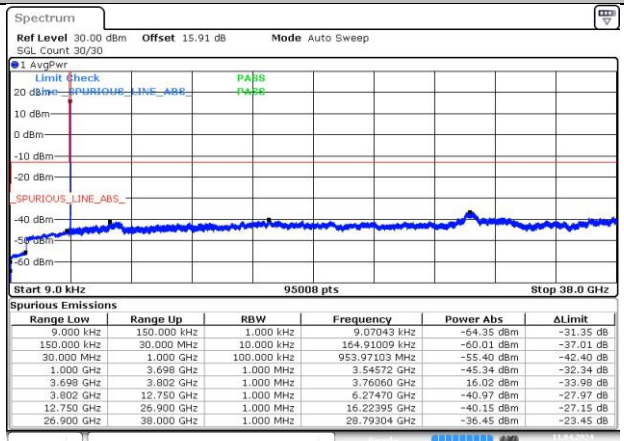
Date: 11 APR 2024 01:54:29

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 01:54:58

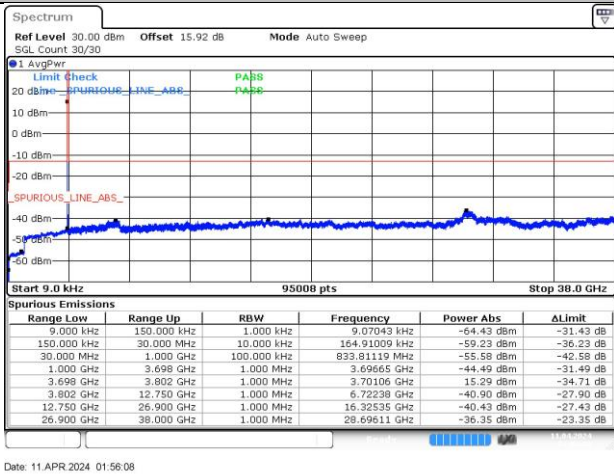
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 01:55:26

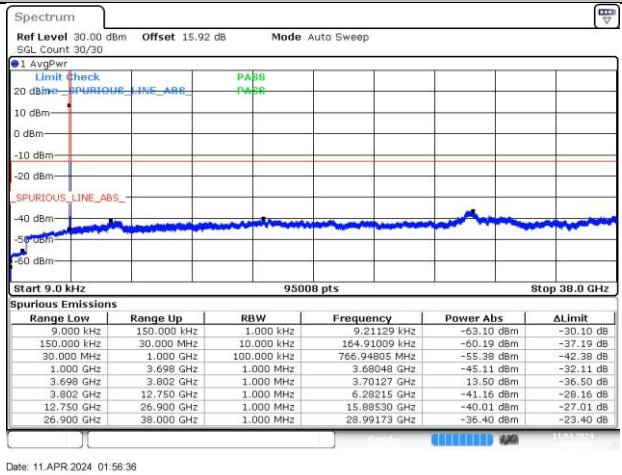
DC_2A_n78A / 15KHz / 50MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



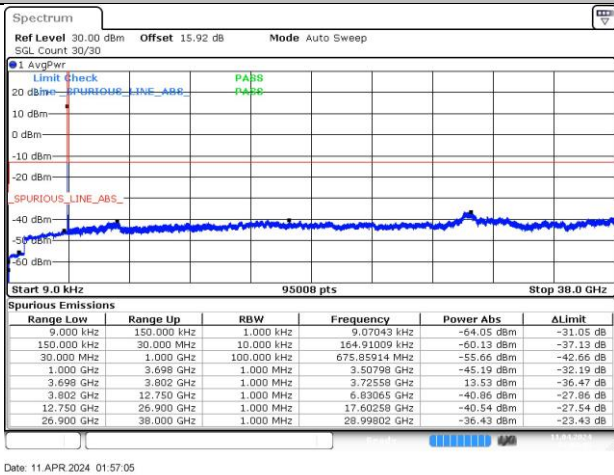
Date: 11 APR 2024 01:56:08

LCH_DFT-QPSK / Edge_1RB_Left



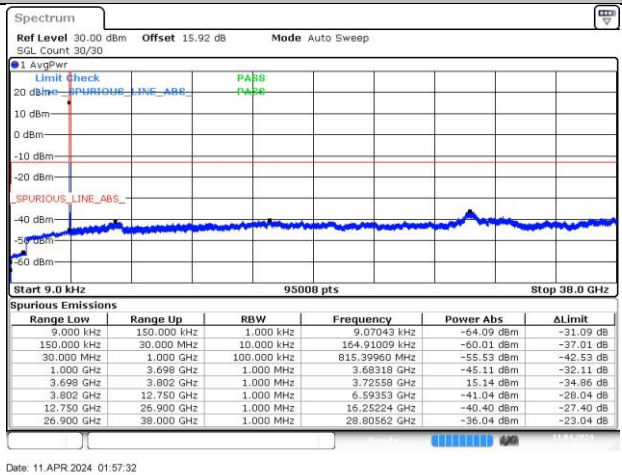
Date: 11 APR 2024 01:56:36

MCH_DFT-Pi2BPSK / Edge_1RB_Left



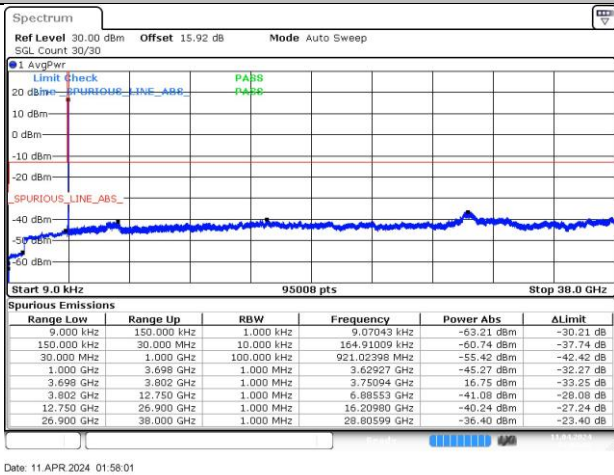
Date: 11 APR 2024 01:57:05

MCH_DFT-QPSK / Edge_1RB_Left



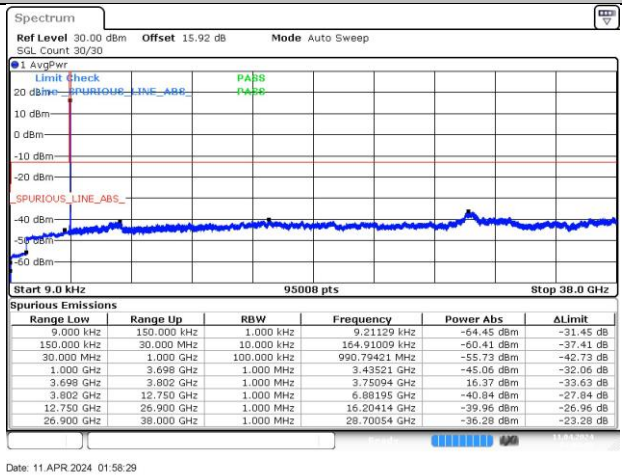
Date: 11 APR 2024 01:57:32

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 01:58:01

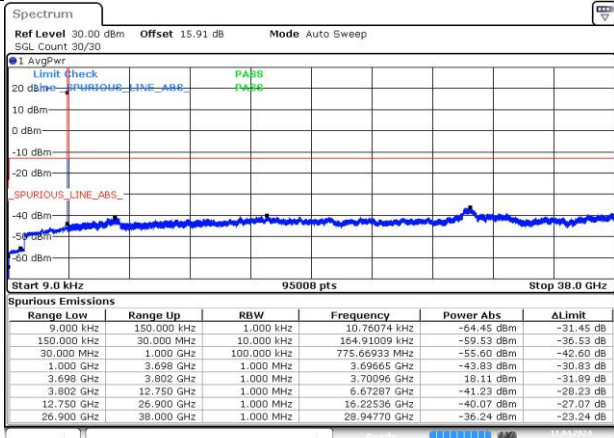
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 01:58:29

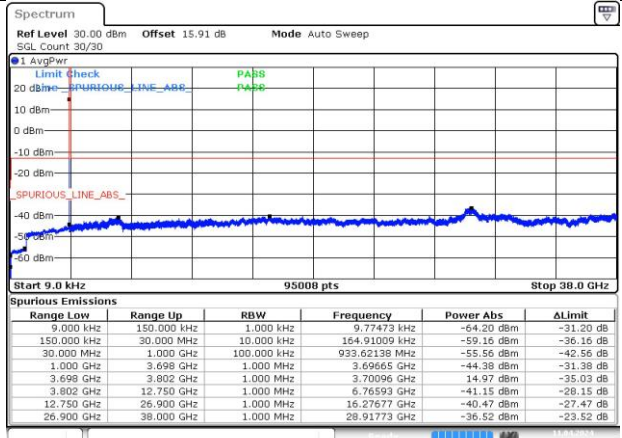
DC_2A_n78A / 30KHz / 10MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



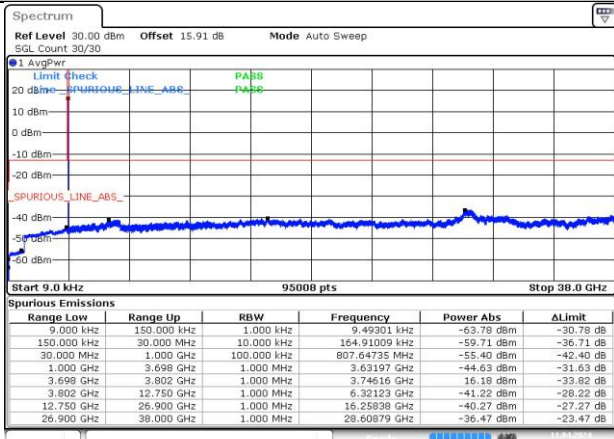
Date: 11 APR 2024 01:59:08

LCH_DFT-QPSK / Edge_1RB_Left



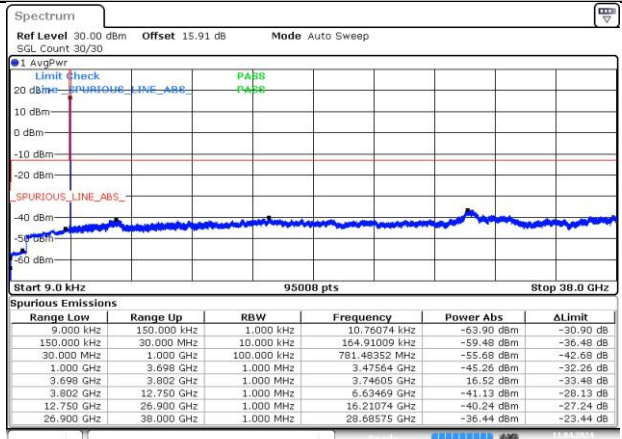
Date: 11 APR 2024 01:59:35

MCH_DFT-Pi2BPSK / Edge_1RB_Left



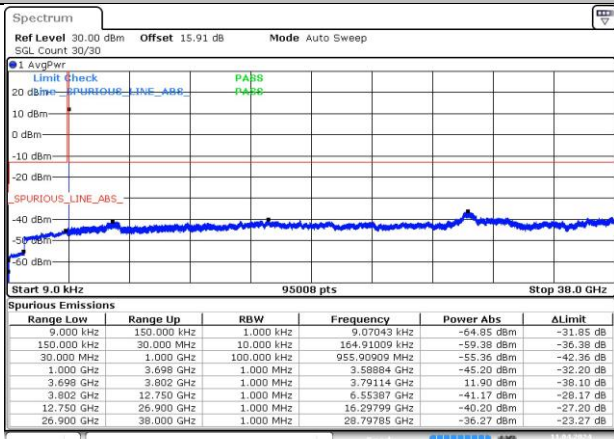
Date: 11 APR 2024 02:00:33

MCH_DFT-QPSK / Edge_1RB_Left



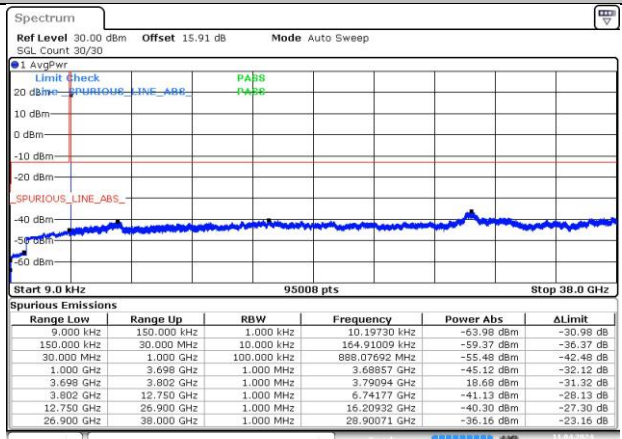
Date: 11 APR 2024 02:00:30

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 02:00:58

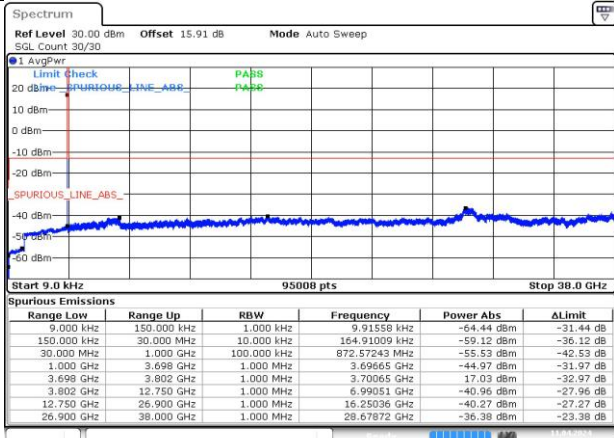
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 02:01:25

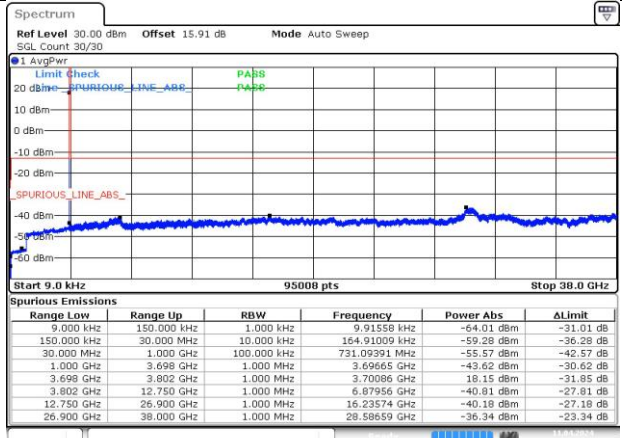
DC_2A_n78A / 30KHz / 15MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



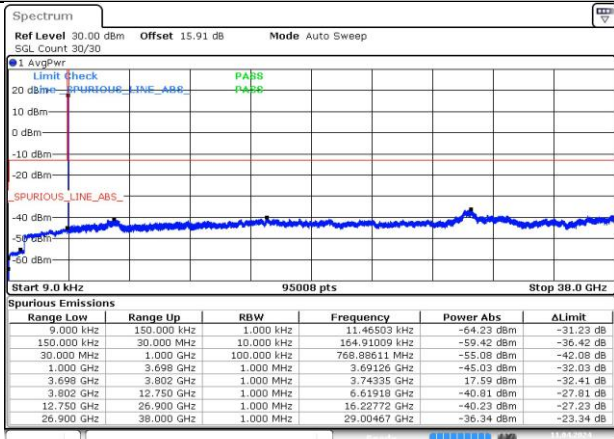
Date: 11 APR 2024 02:02:07

LCH_DFT-QPSK / Edge_1RB_Left



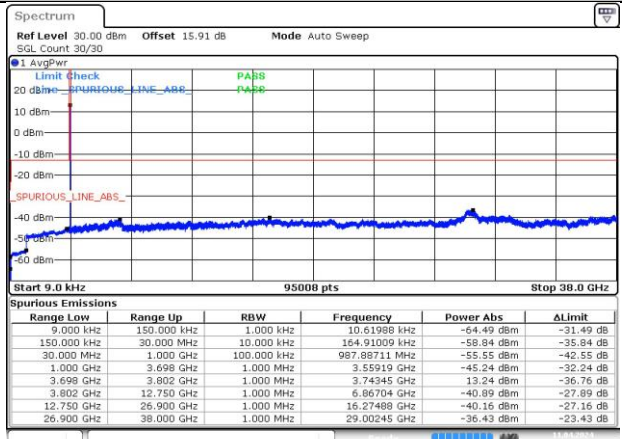
Date: 11 APR 2024 02:02:34

MCH_DFT-Pi2BPSK / Edge_1RB_Left



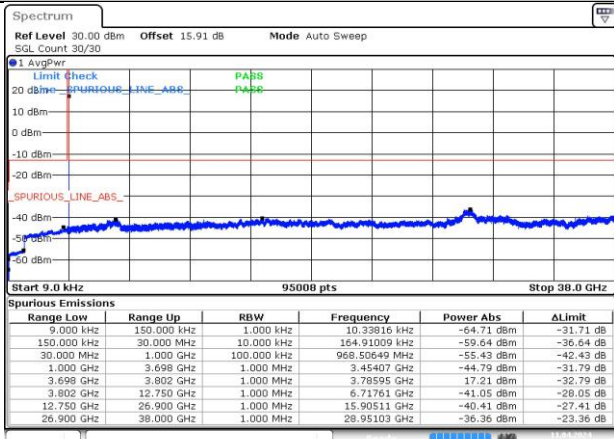
Date: 11 APR 2024 02:03:02

MCH_DFT-QPSK / Edge_1RB_Left



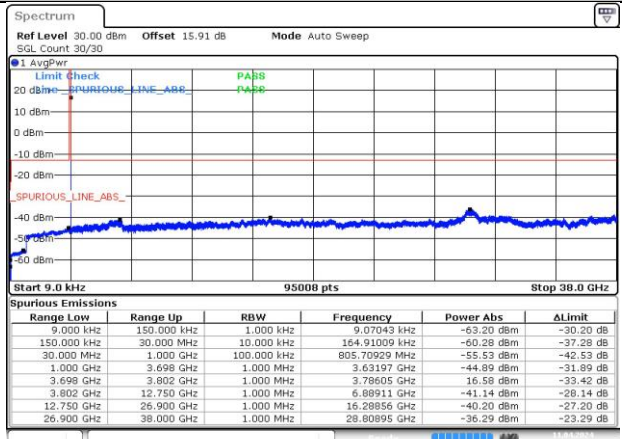
Date: 11 APR 2024 02:03:29

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 02:03:57

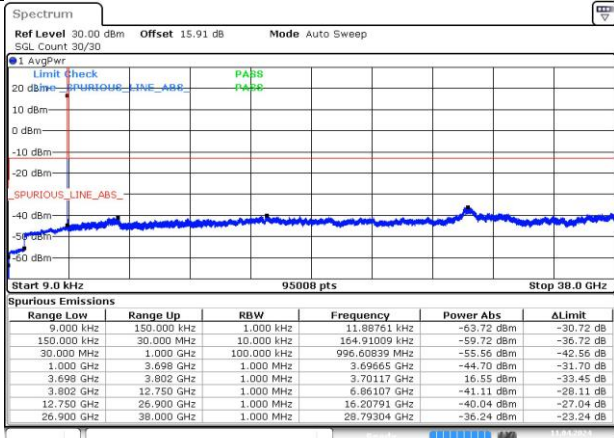
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 02:04:24

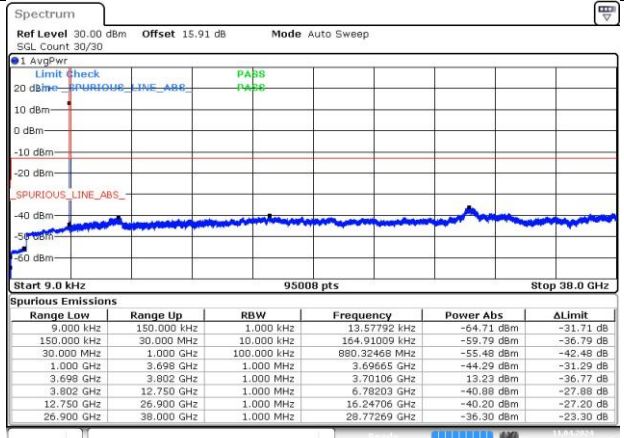
DC_2A_n78A / 30KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



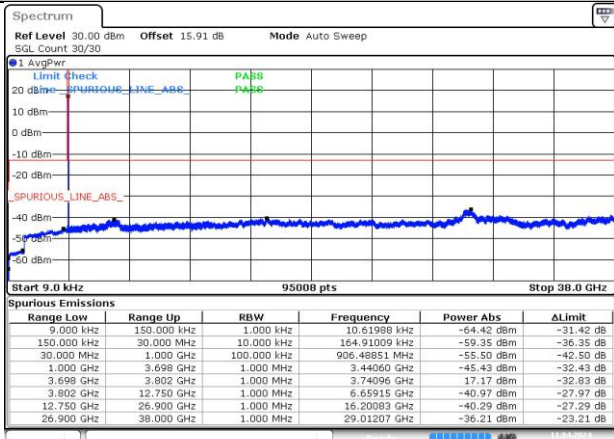
Date: 11 APR 2024 02:05:07

LCH_DFT-QPSK / Edge_1RB_Left



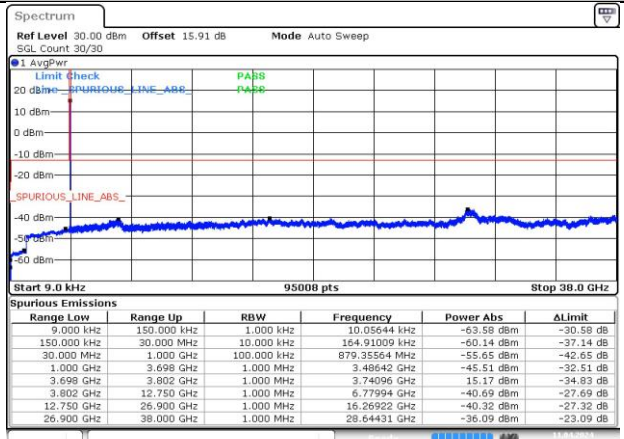
Date: 11 APR 2024 02:05:33

MCH_DFT-Pi2BPSK / Edge_1RB_Left



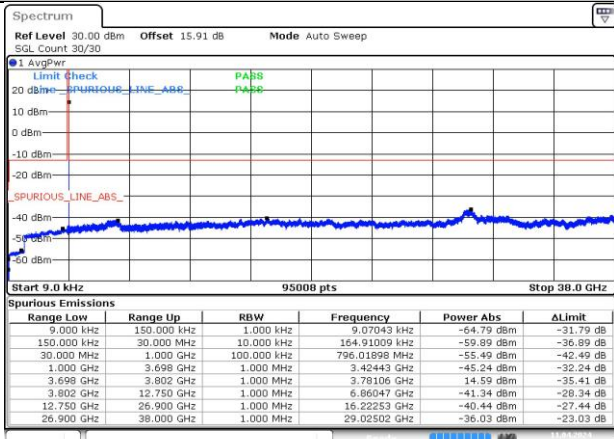
Date: 11 APR 2024 02:06:02

MCH_DFT-QPSK / Edge_1RB_Left



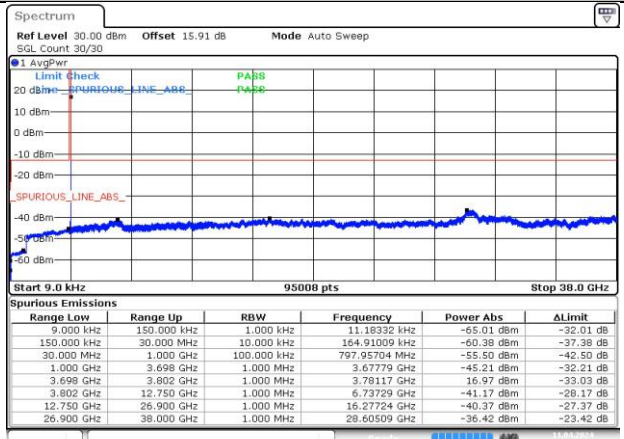
Date: 11 APR 2024 02:06:29

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 02:06:57

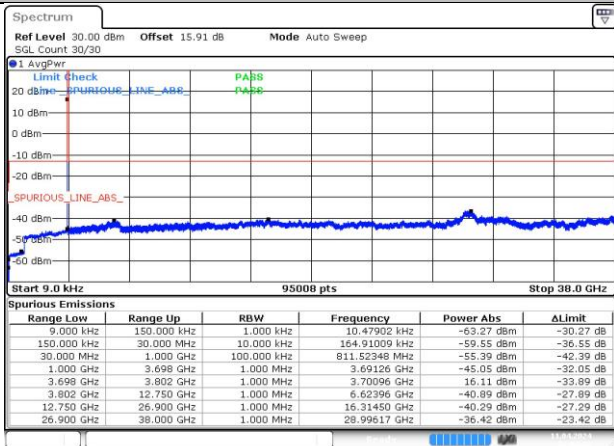
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 02:07:24

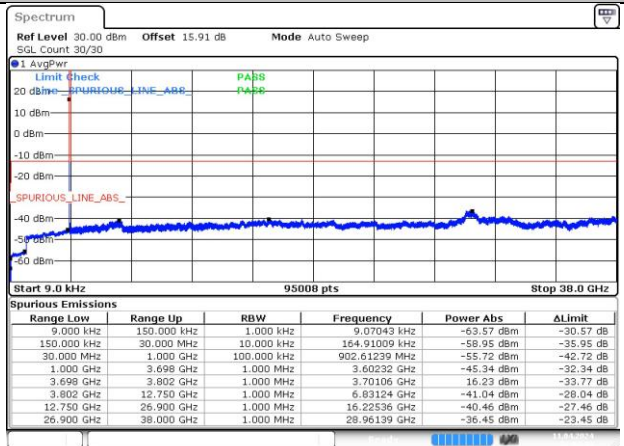
DC_2A_n78A / 30KHz / 25MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



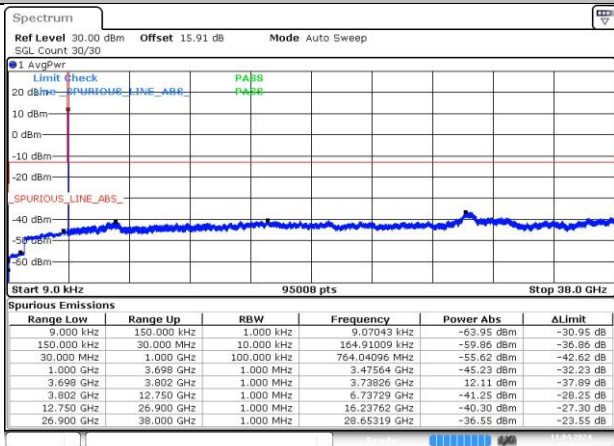
Date: 11 APR 2024 02:08:06

LCH_DFT-QPSK / Edge_1RB_Left



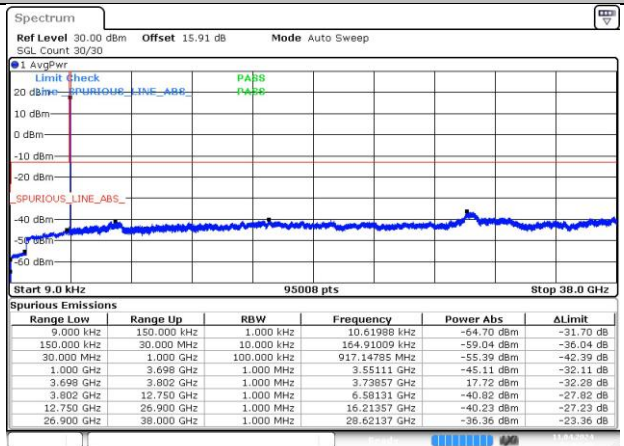
Date: 11 APR 2024 02:08:33

MCH_DFT-Pi2BPSK / Edge_1RB_Left



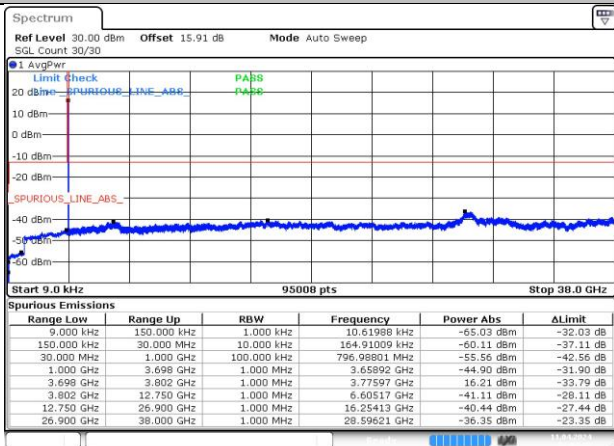
Date: 11 APR 2024 02:09:01

MCH_DFT-QPSK / Edge_1RB_Left



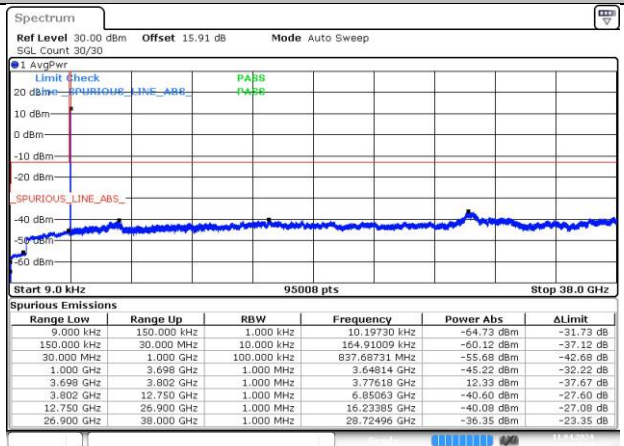
Date: 11 APR 2024 02:09:28

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 02:09:56

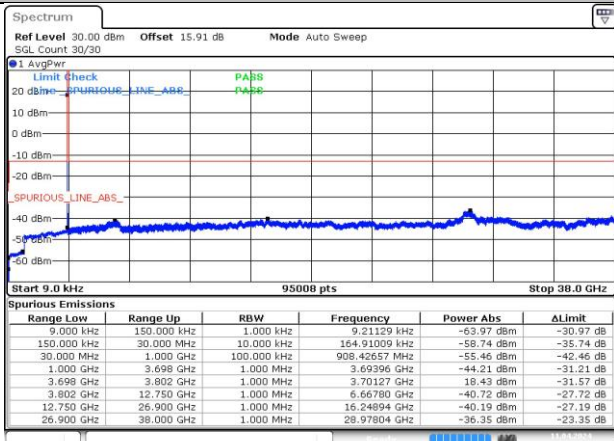
HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 02:10:23

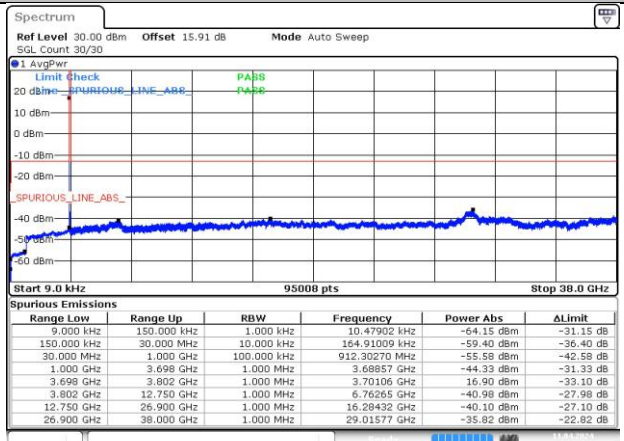
DC_2A_n78A / 30KHz / 30MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



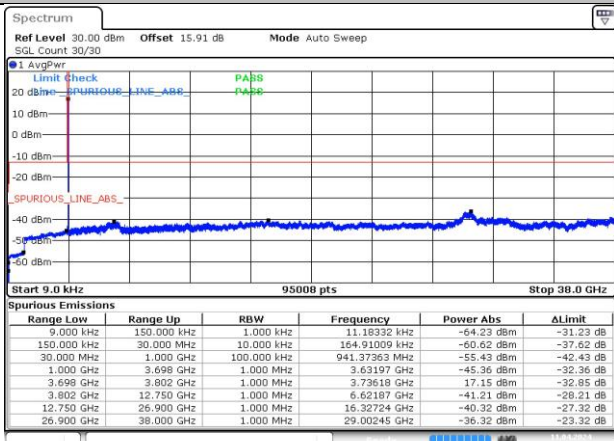
Date: 11 APR 2024 02:11:05

LCH_DFT-QPSK / Edge_1RB_Left



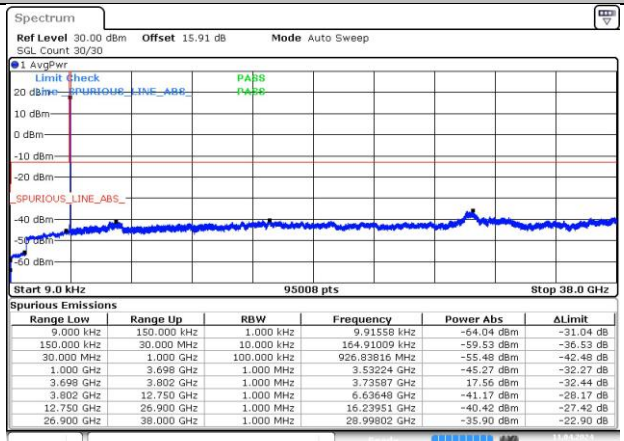
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MCH_DFT-Pi2BPSK / Edge_1RB_Left



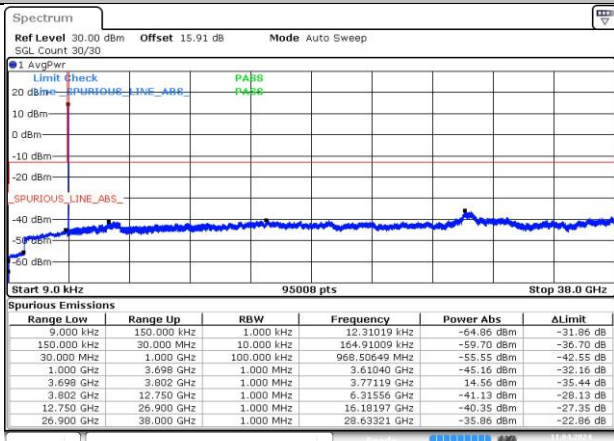
Date: 11 APR 2024 02:12:00

MCH_DFT-QPSK / Edge_1RB_Left



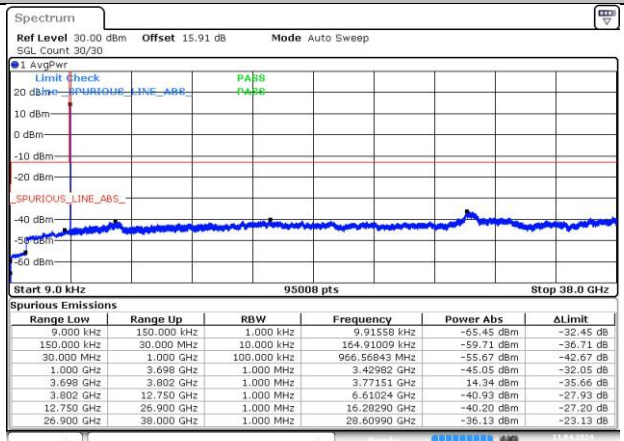
Date: 11 APR 2024 02:12:27

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 02:12:55

HCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 02:13:22