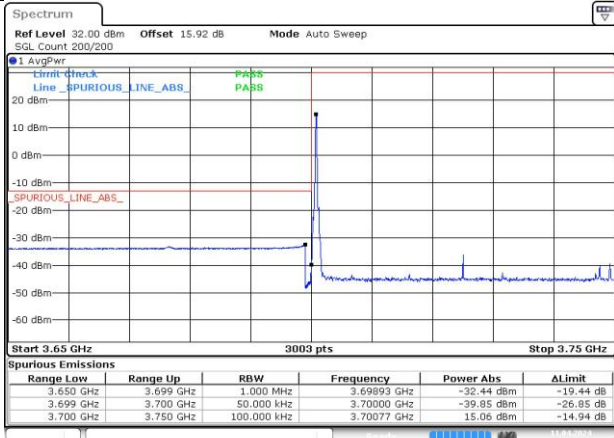


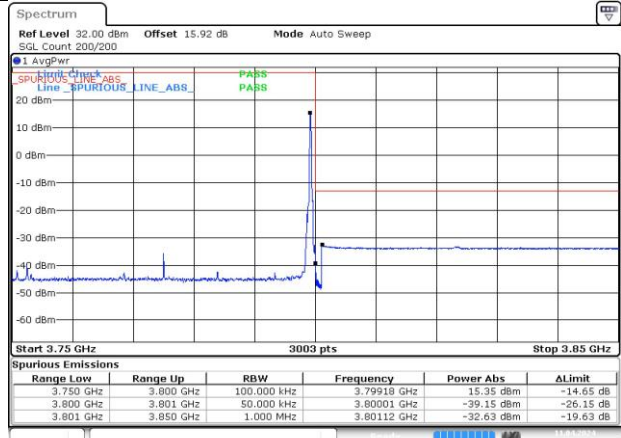
DC_2A_n78A / 15KHz / 50MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



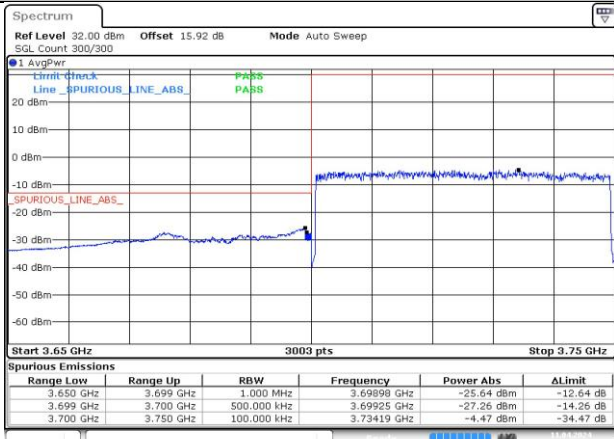
Date: 11 APR 2024 00:48:48

HCH_DFT-Pi2BPSK / Edge_1RB_Right



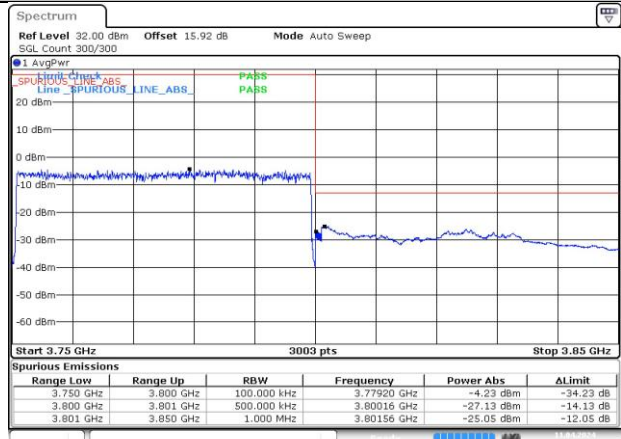
Date: 11 APR 2024 00:50:43

LCH_DFT-Pi2BPSK / Outer_Full



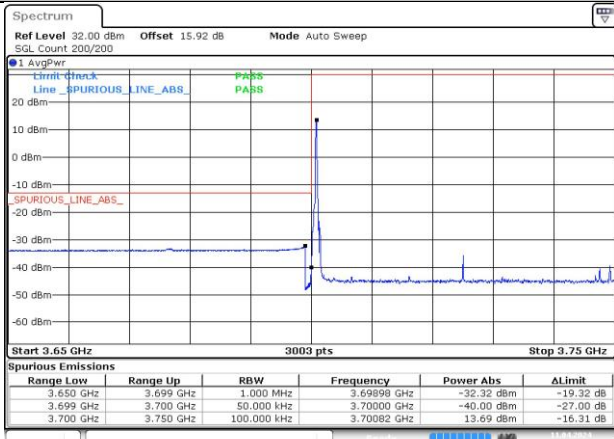
Date: 11 APR 2024 00:49:11

HCH_DFT-Pi2BPSK / Outer_Full



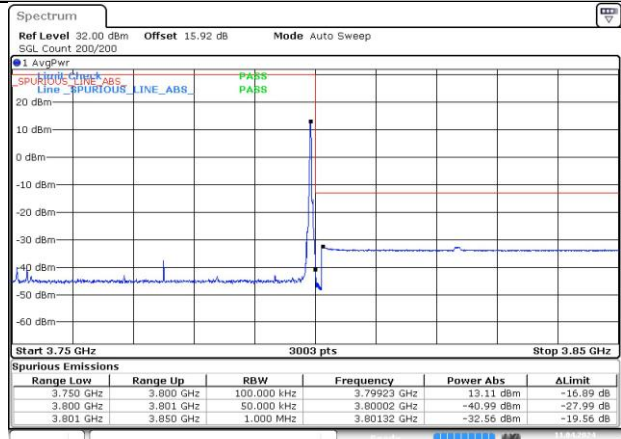
Date: 11 APR 2024 00:51:04

LCH_DFT-QPSK / Edge_1RB_Left

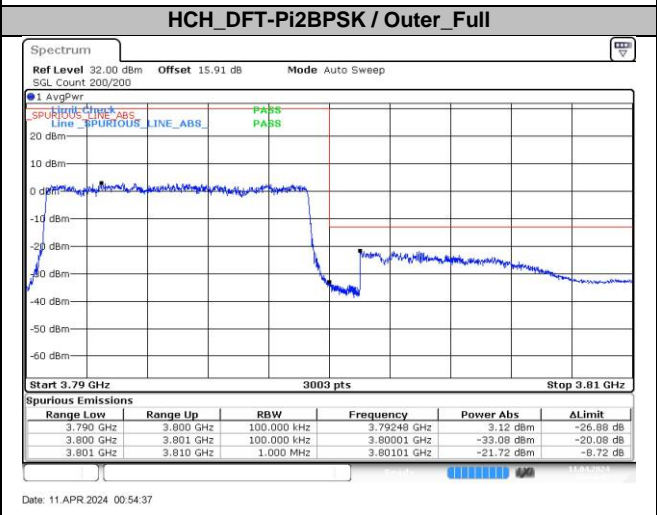
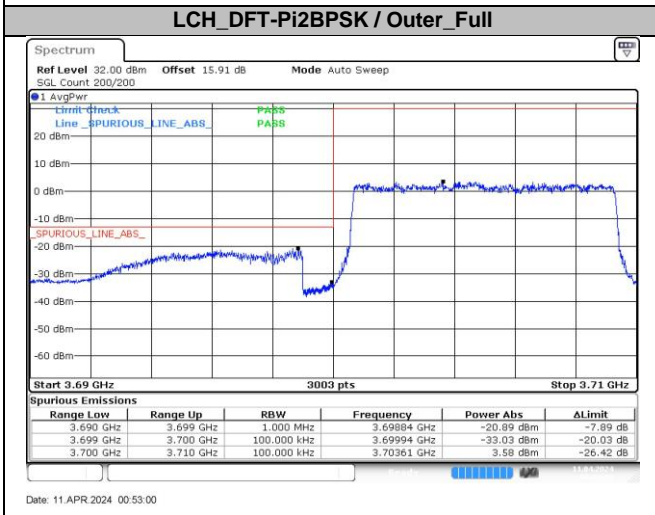
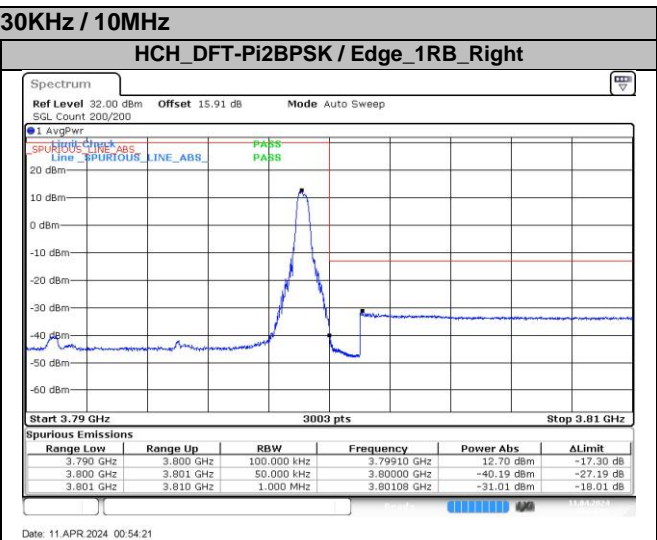
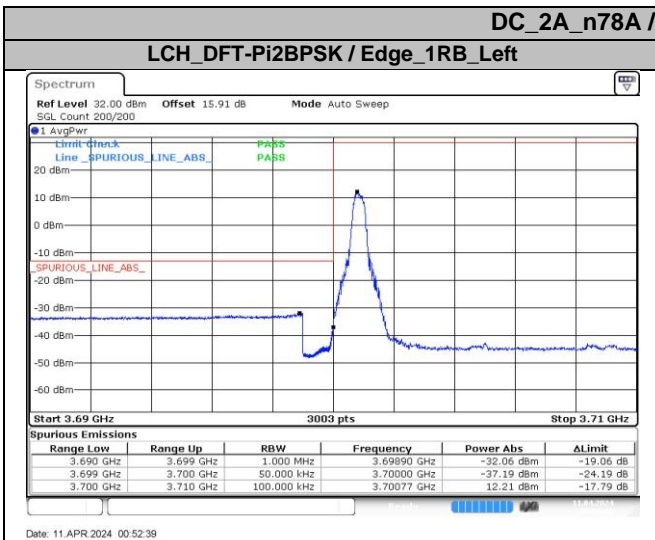
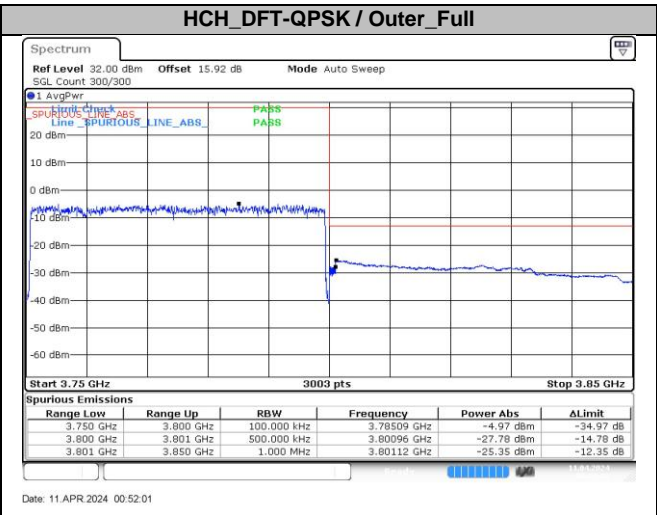
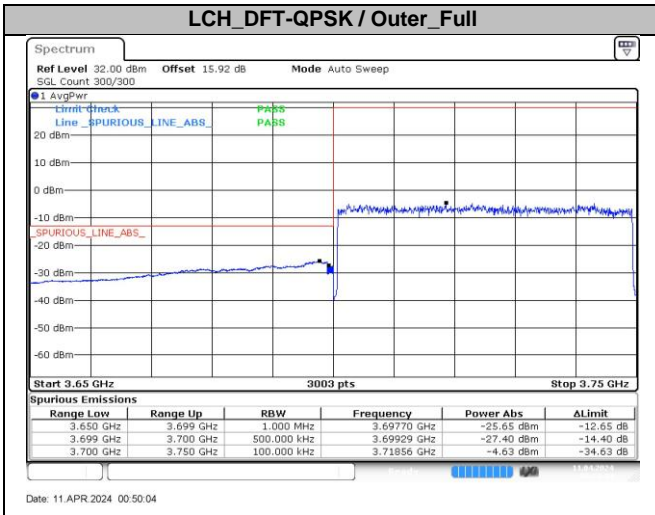


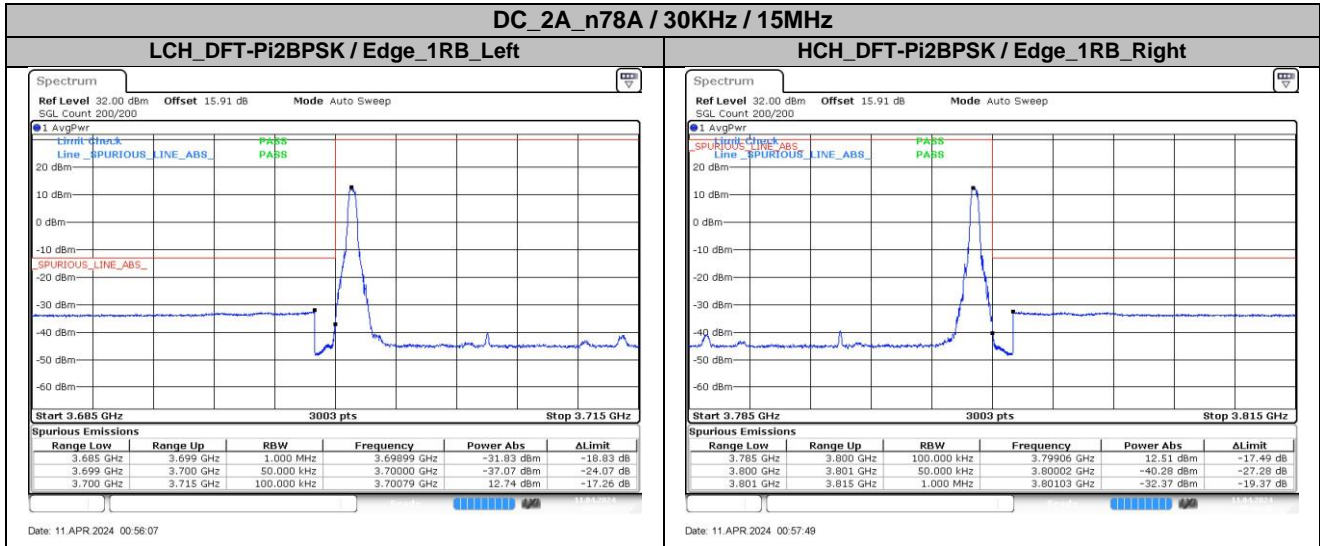
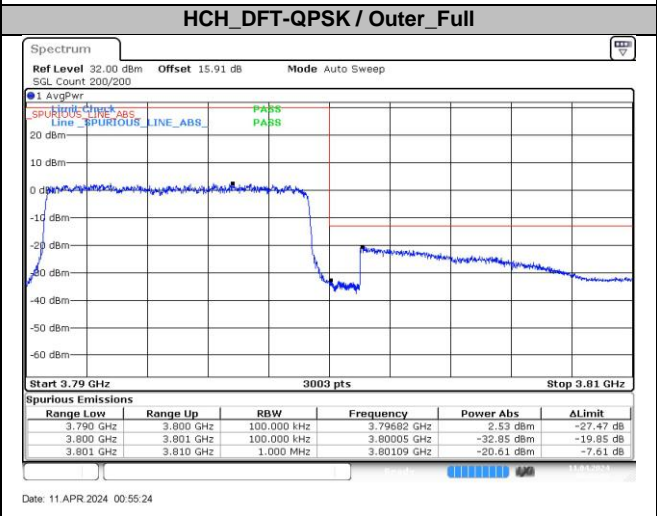
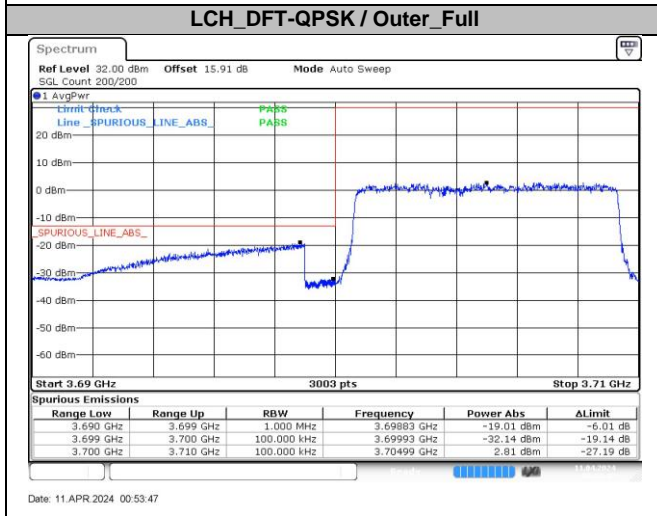
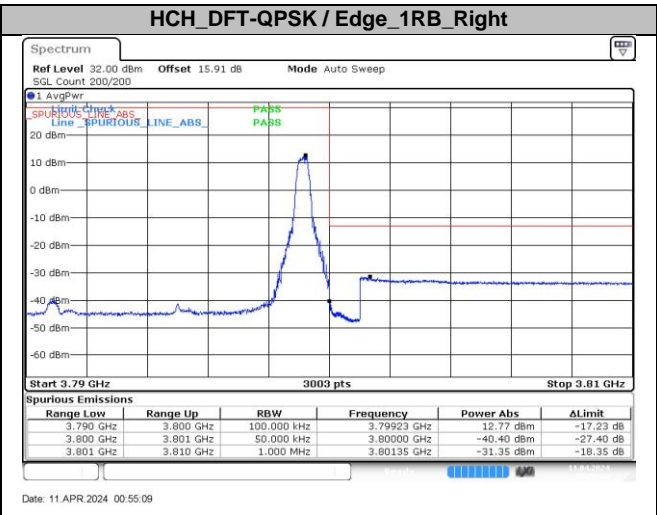
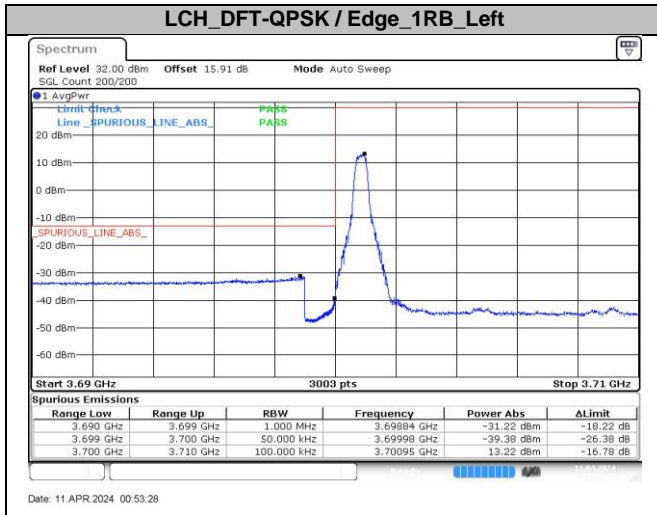
Date: 11 APR 2024 00:49:43

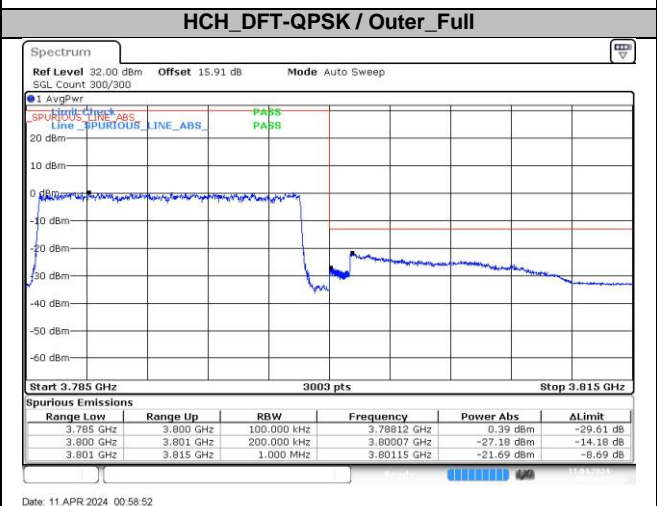
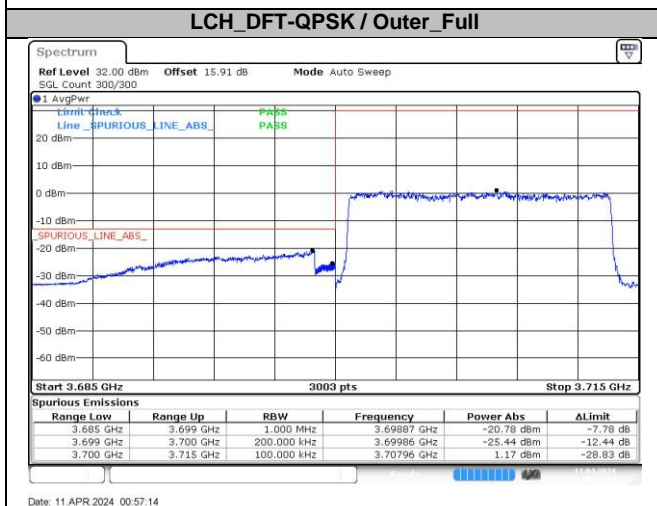
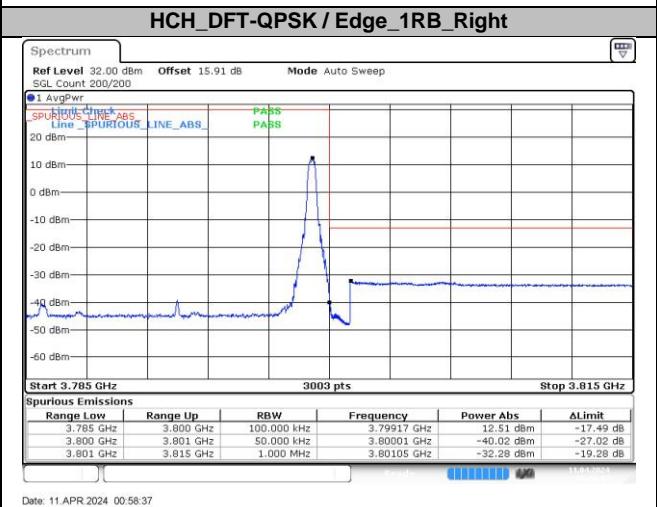
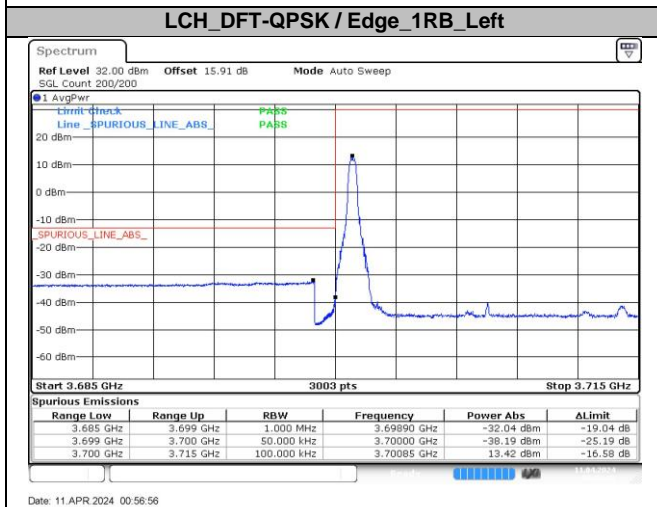
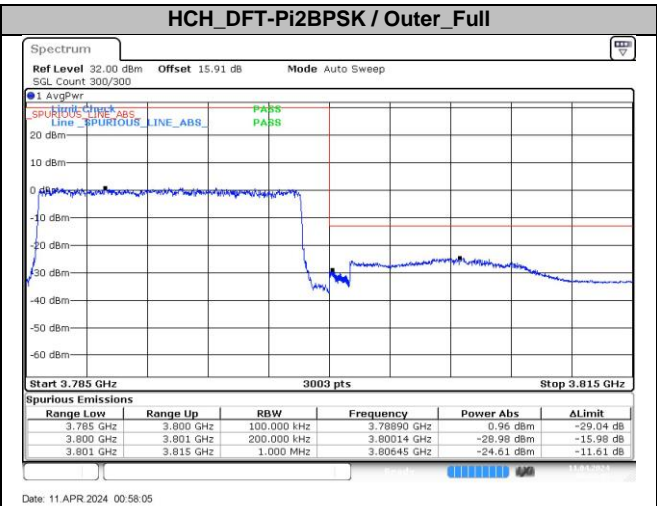
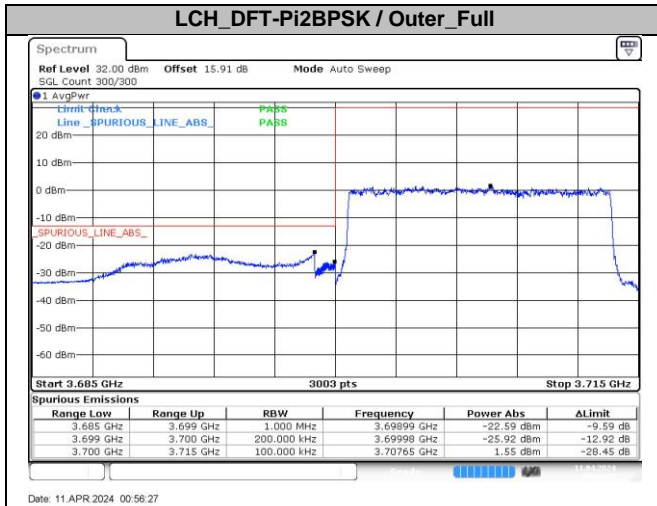
HCH_DFT-QPSK / Edge_1RB_Right



Date: 11 APR 2024 00:51:41

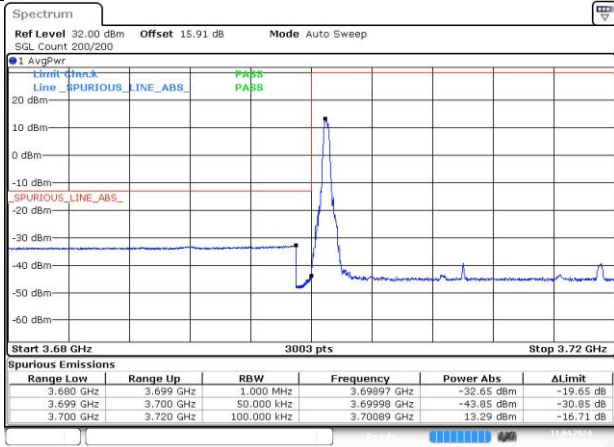






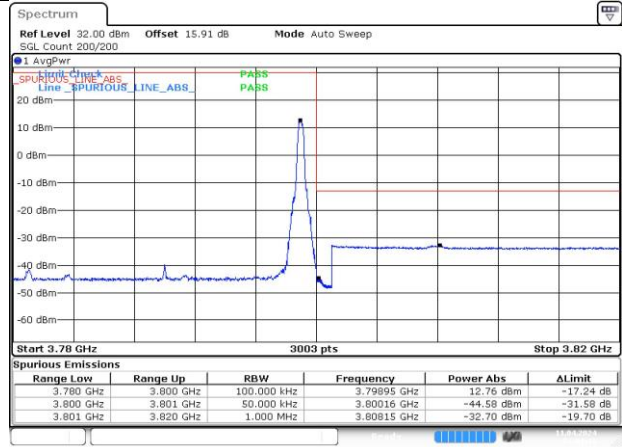
DC_2A_n78A / 30KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



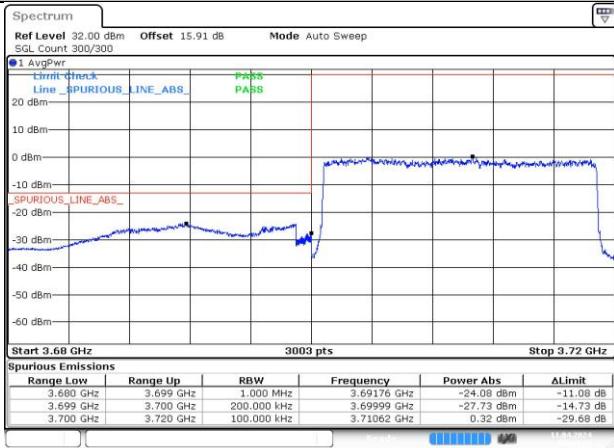
Date: 11 APR 2024 00:59:35

HCH_DFT-Pi2BPSK / Edge_1RB_Right



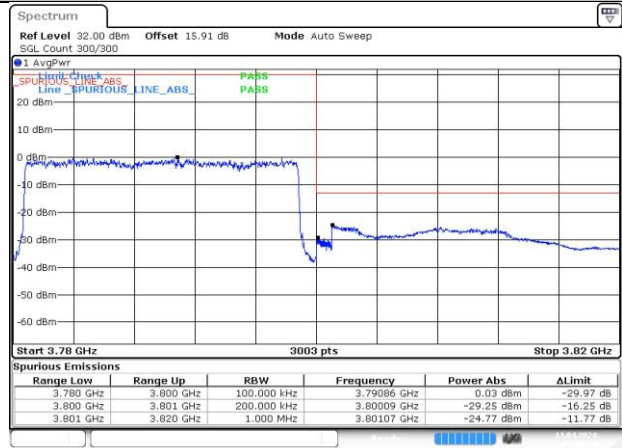
Date: 11 APR 2024 01:01:18

LCH_DFT-Pi2BPSK / Outer_Full



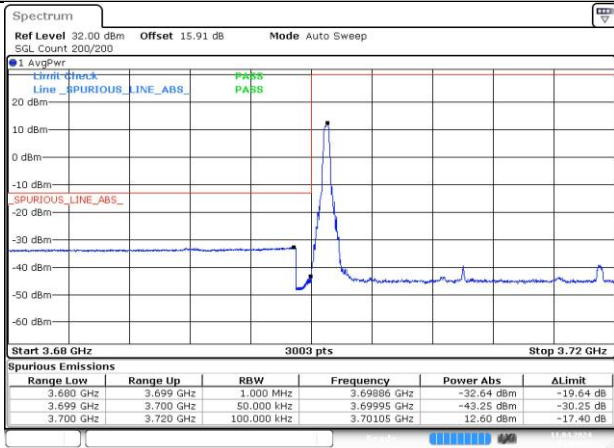
Date: 11 APR 2024 00:59:35

HCH_DFT-Pi2BPSK / Outer_Full



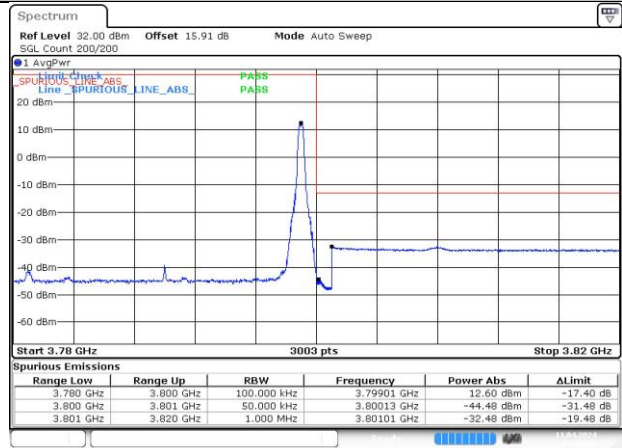
Date: 11 APR 2024 01:01:35

LCH_DFT-QPSK / Edge_1RB_Left

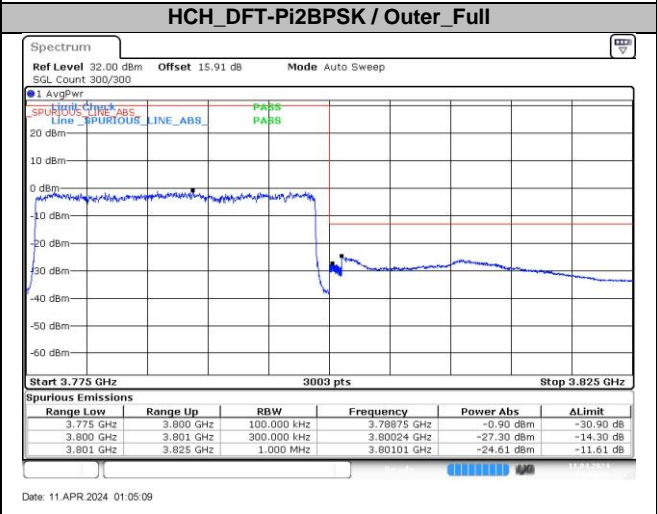
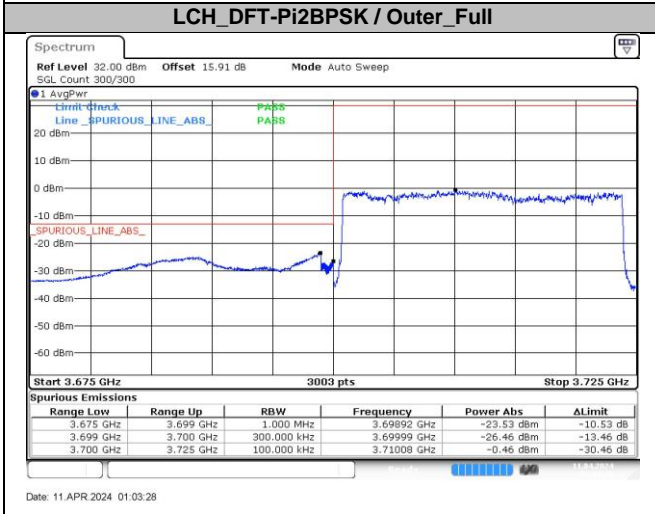
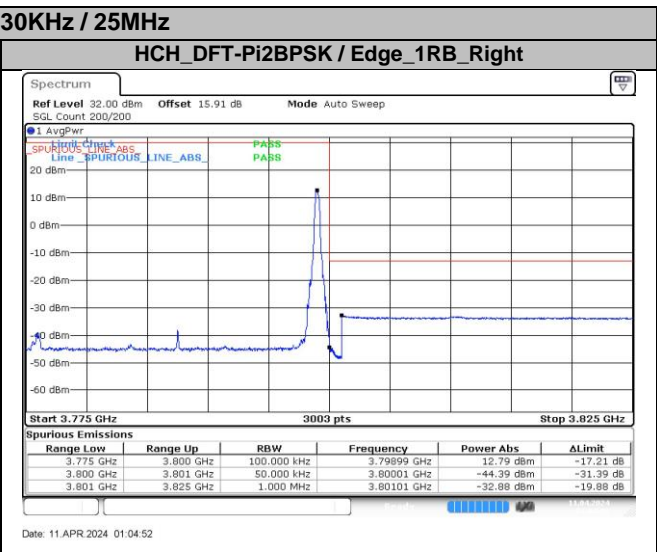
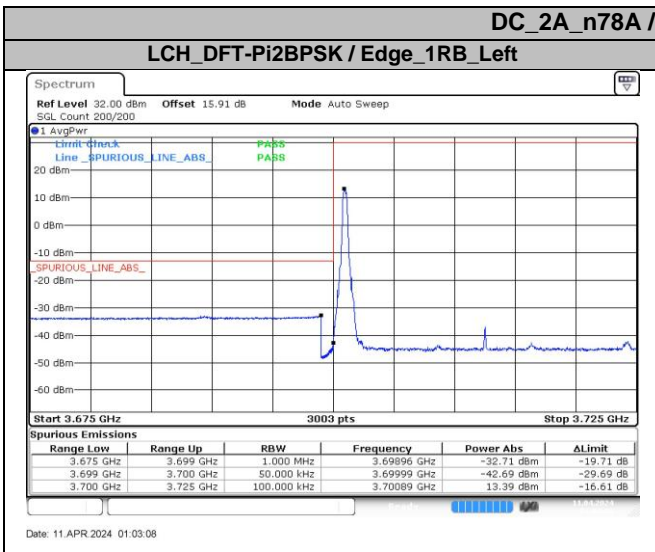
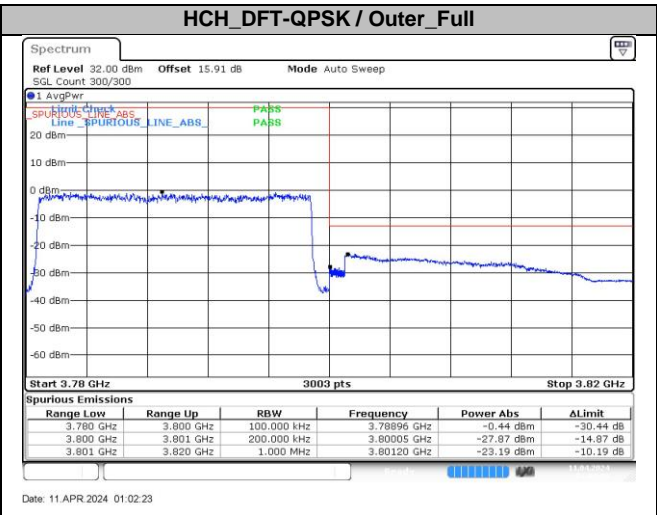
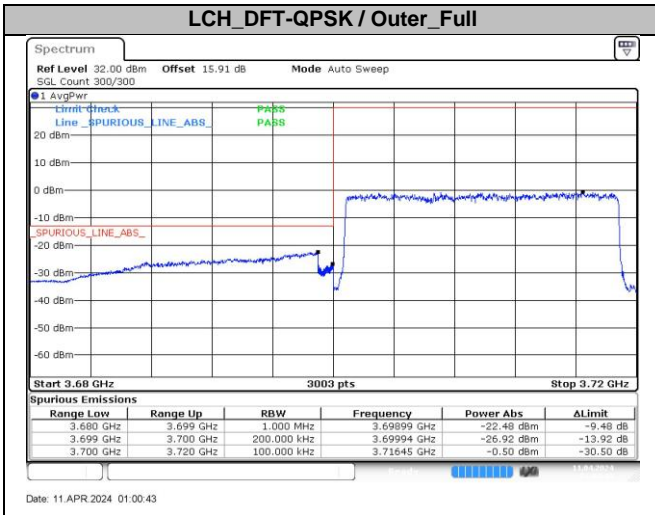


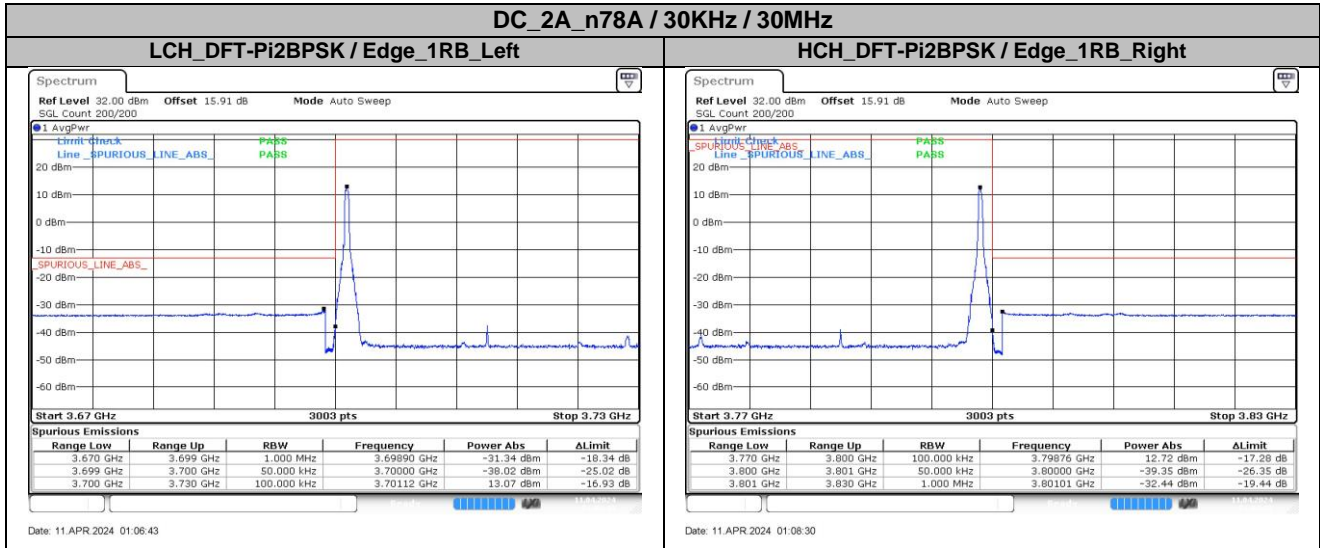
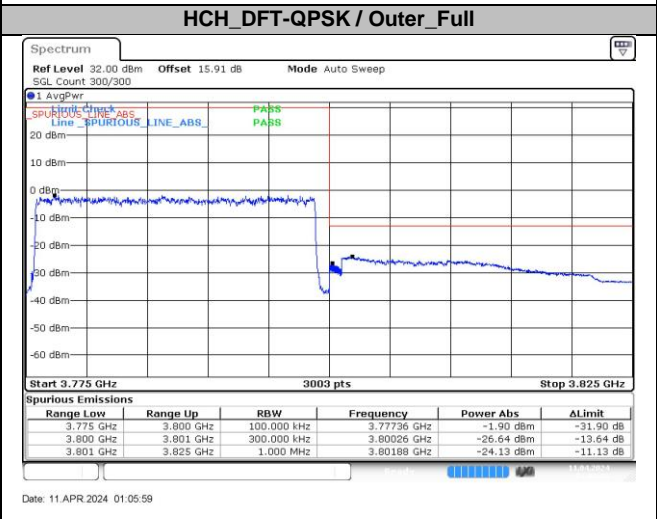
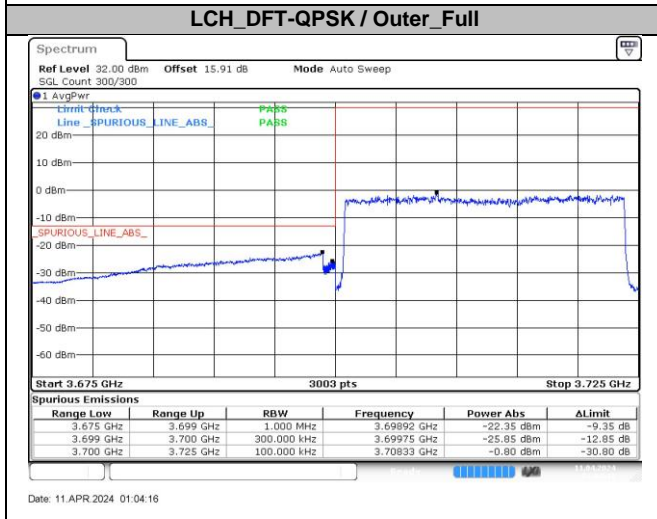
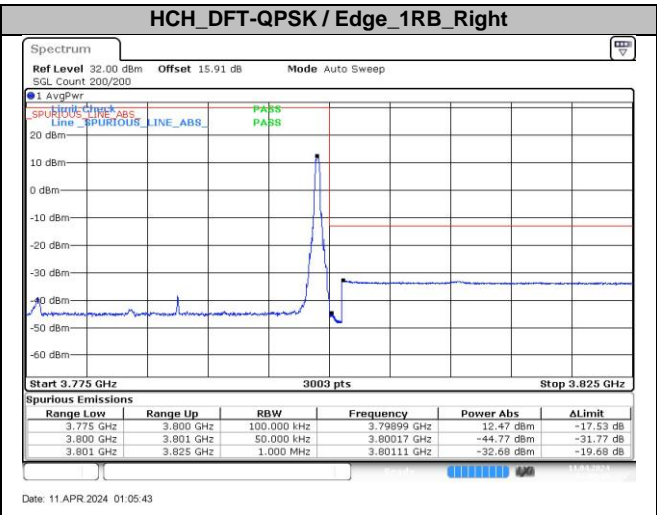
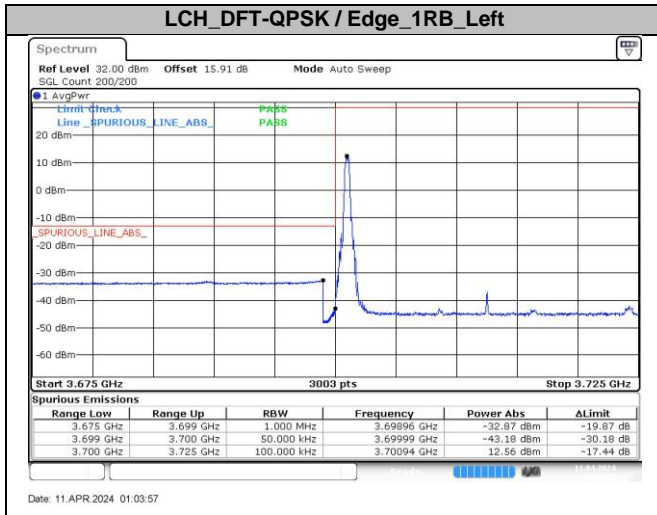
Date: 11 APR 2024 01:00:25

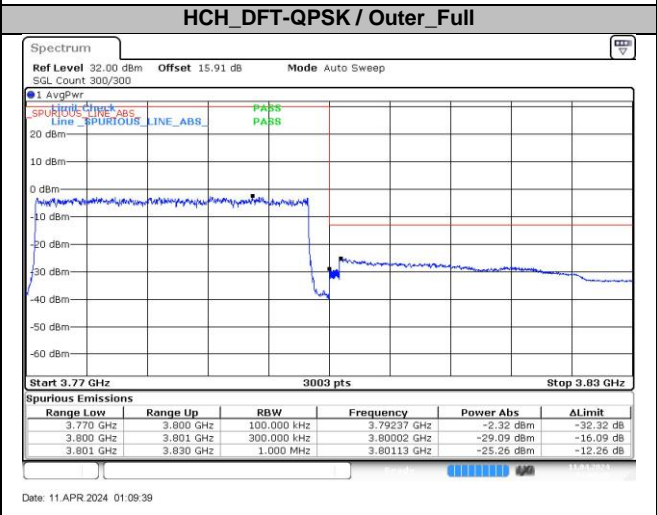
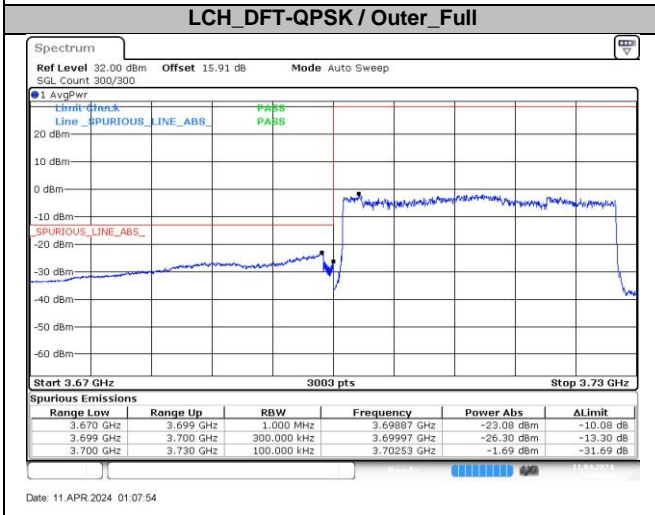
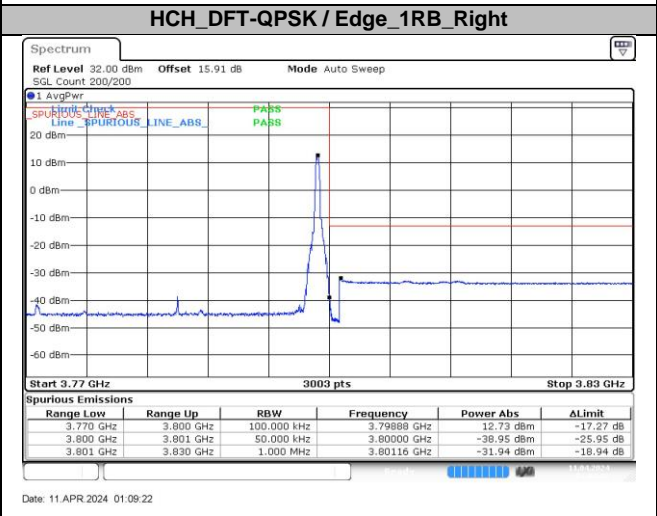
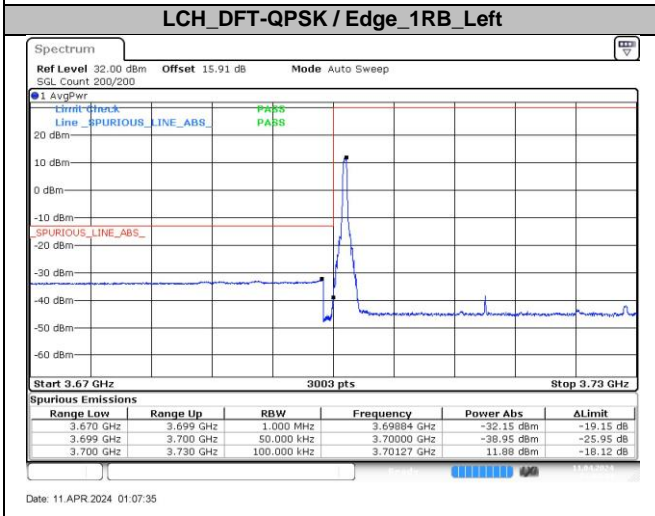
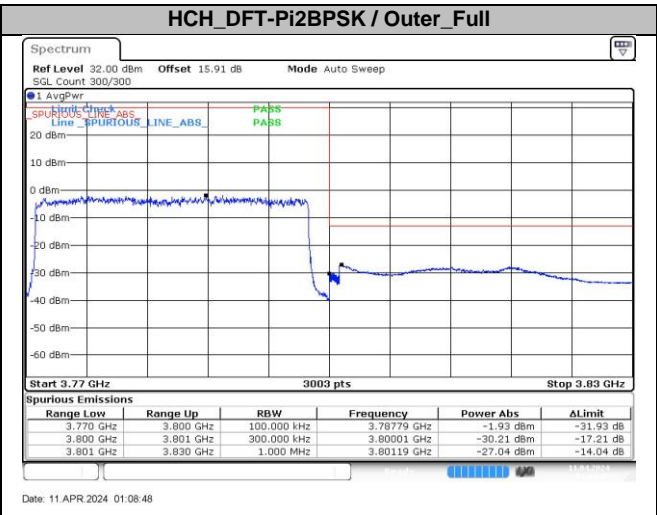
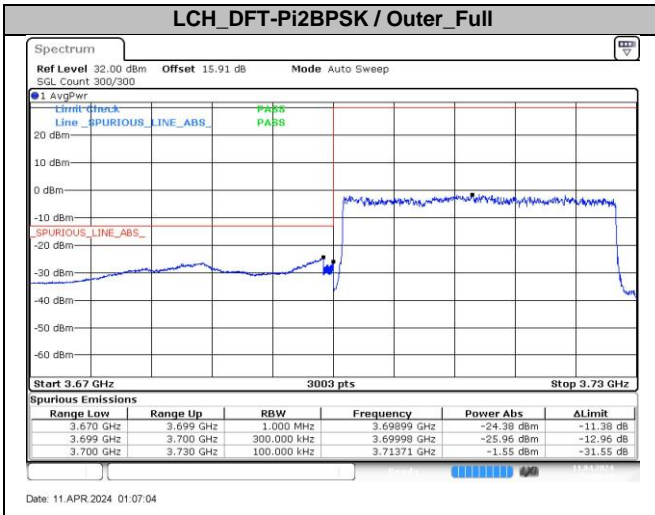
HCH_DFT-QPSK / Edge_1RB_Right



Date: 11 APR 2024 01:02:08

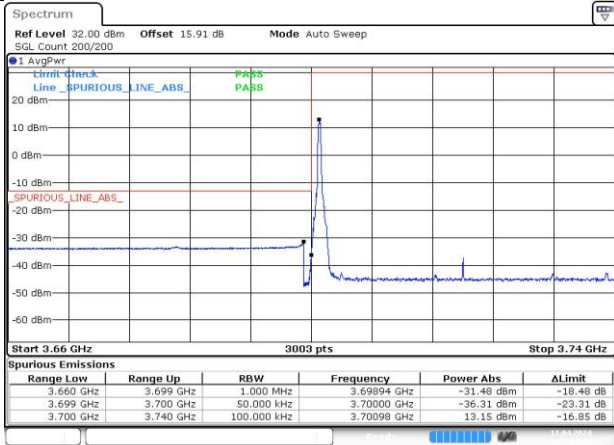






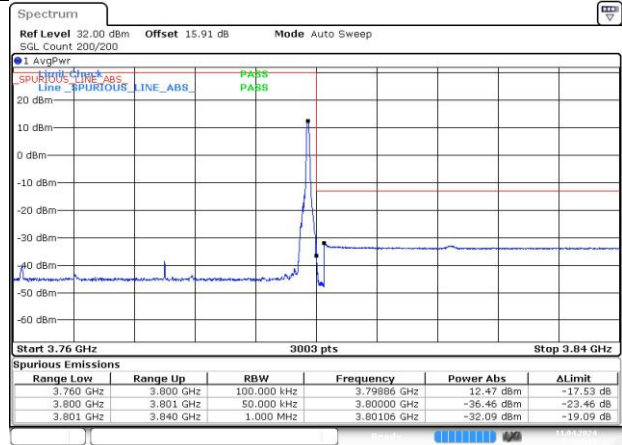
DC_2A_n78A / 30KHz / 40MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



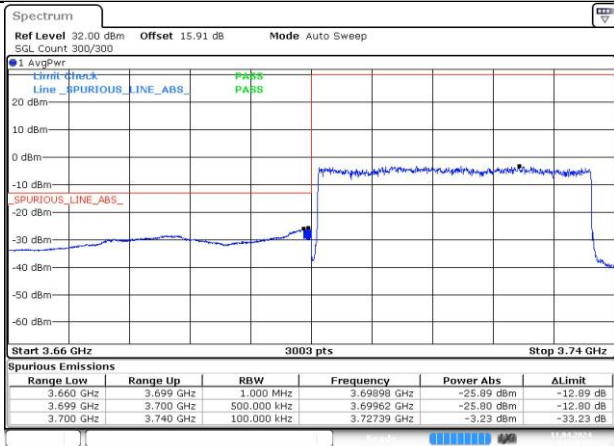
Date: 11.APR.2024 01:10:24

HCH_DFT-Pi2BPSK / Edge_1RB_Right



Date: 11.APR.2024 01:12:13

LCH_DFT-Pi2BPSK / Outer_Full



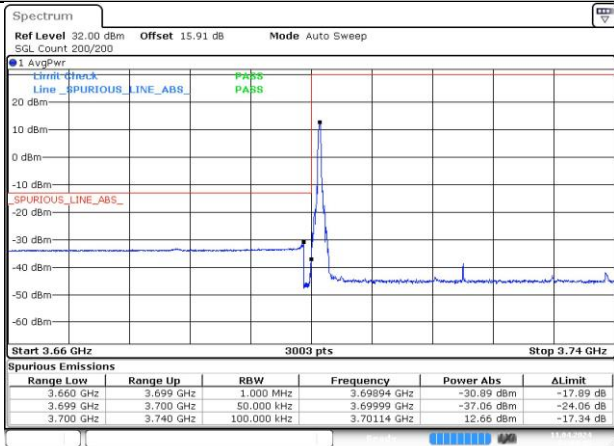
Date: 11.APR.2024 01:10:45

HCH_DFT-Pi2BPSK / Outer_Full



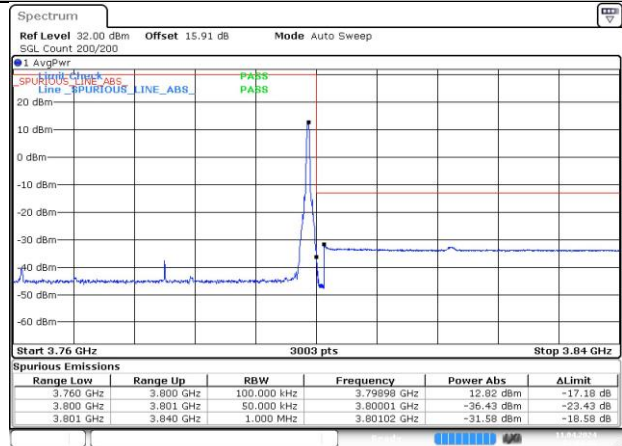
Date: 11.APR.2024 01:12:32

LCH_DFT-QPSK / Edge_1RB_Left

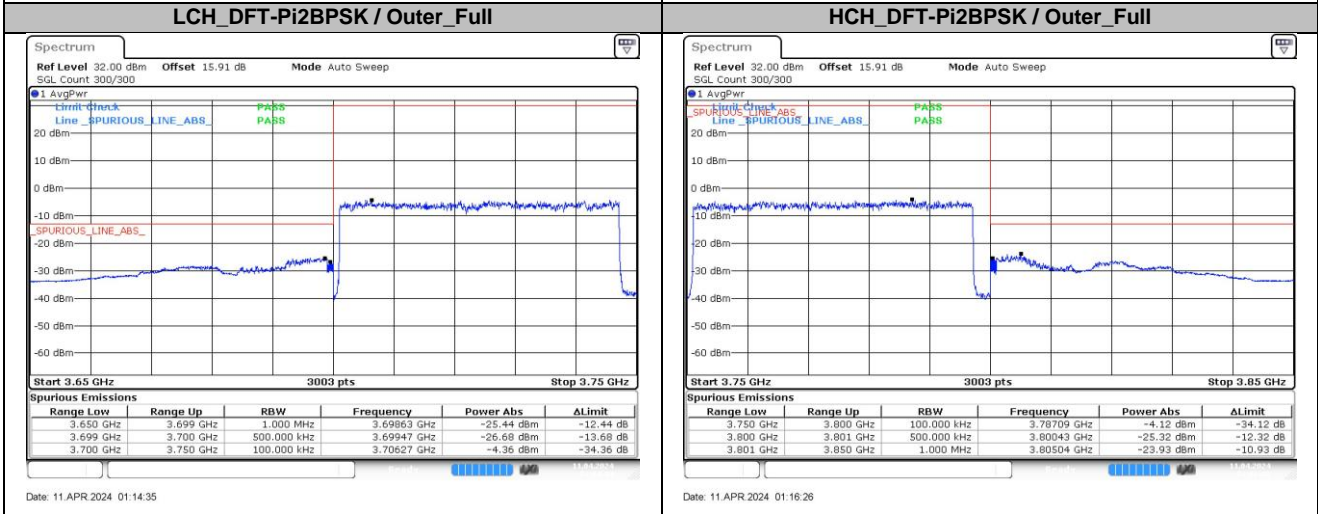
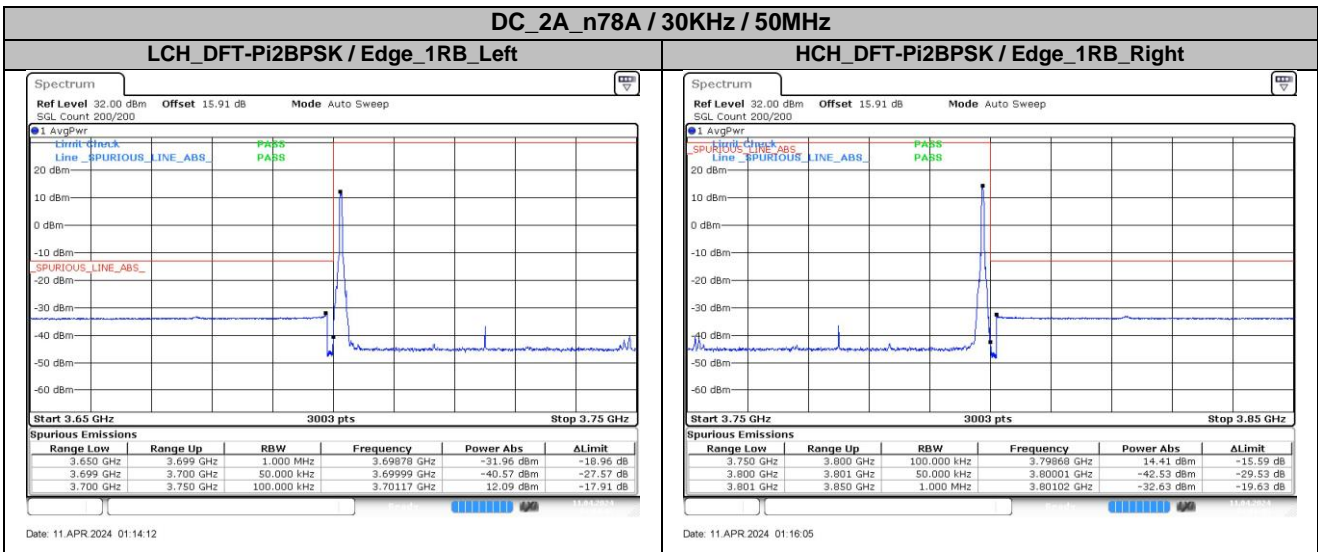
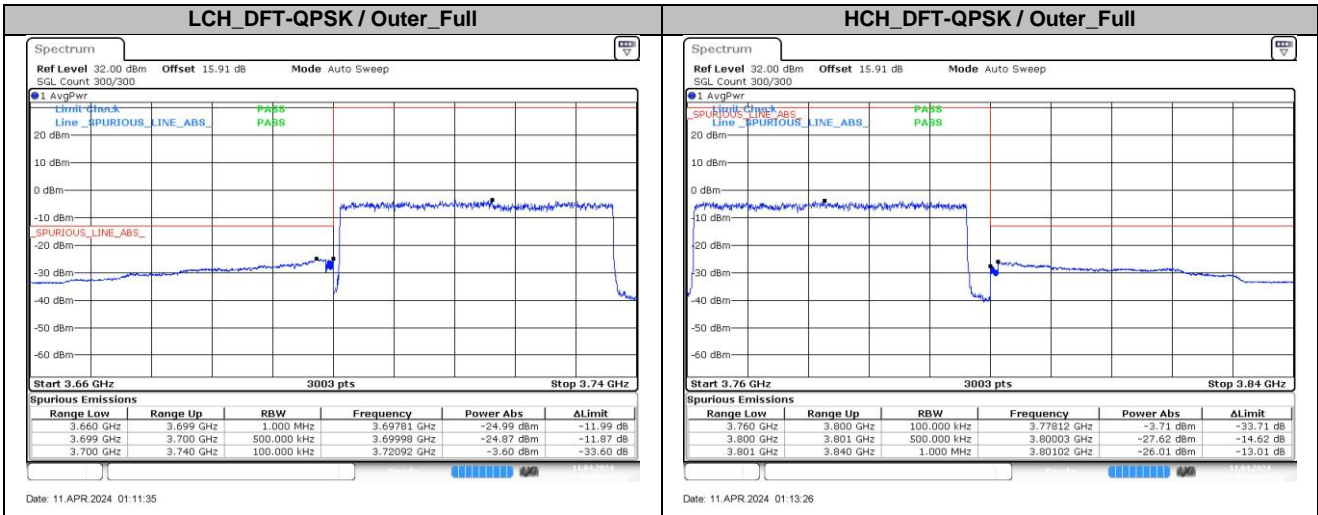


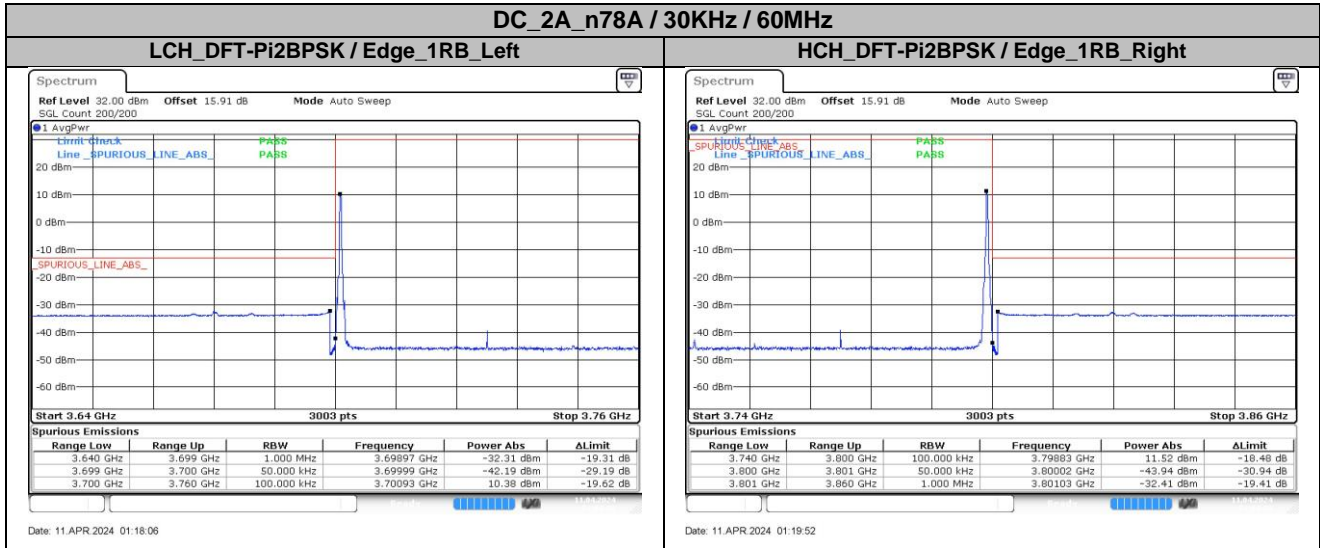
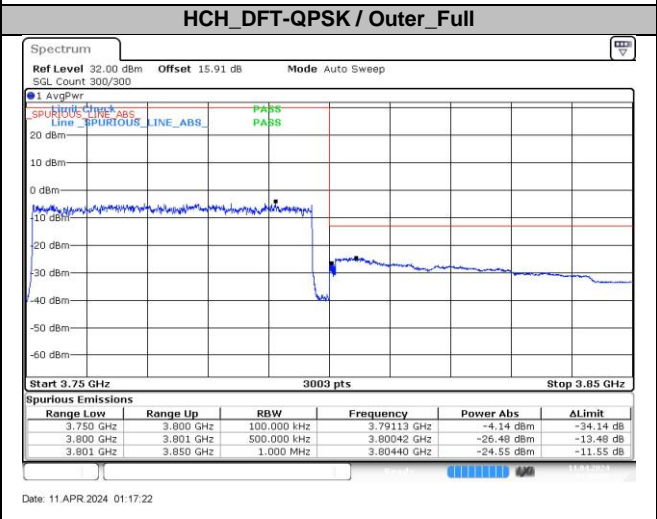
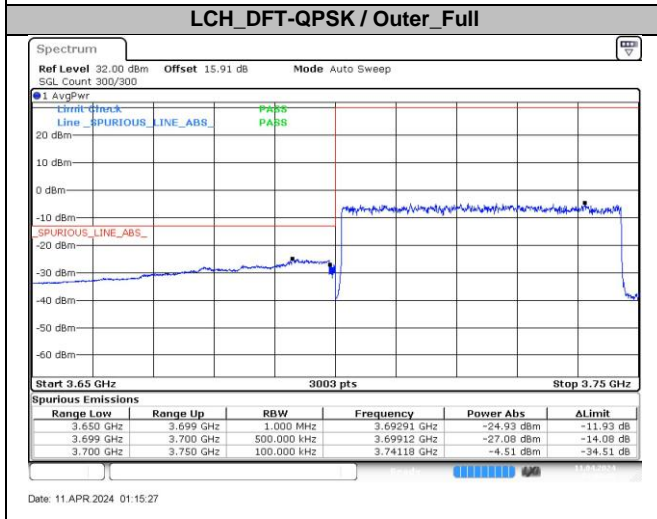
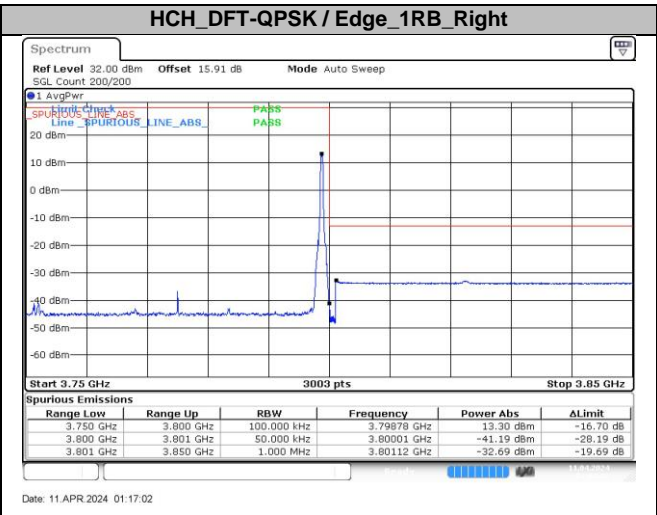
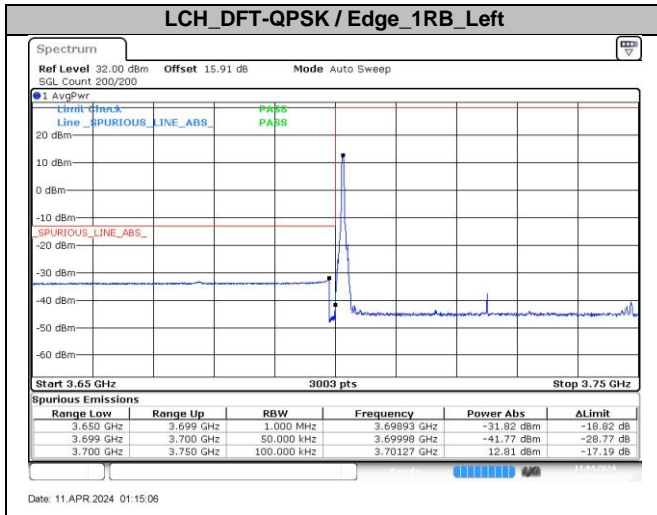
Date: 11.APR.2024 01:11:16

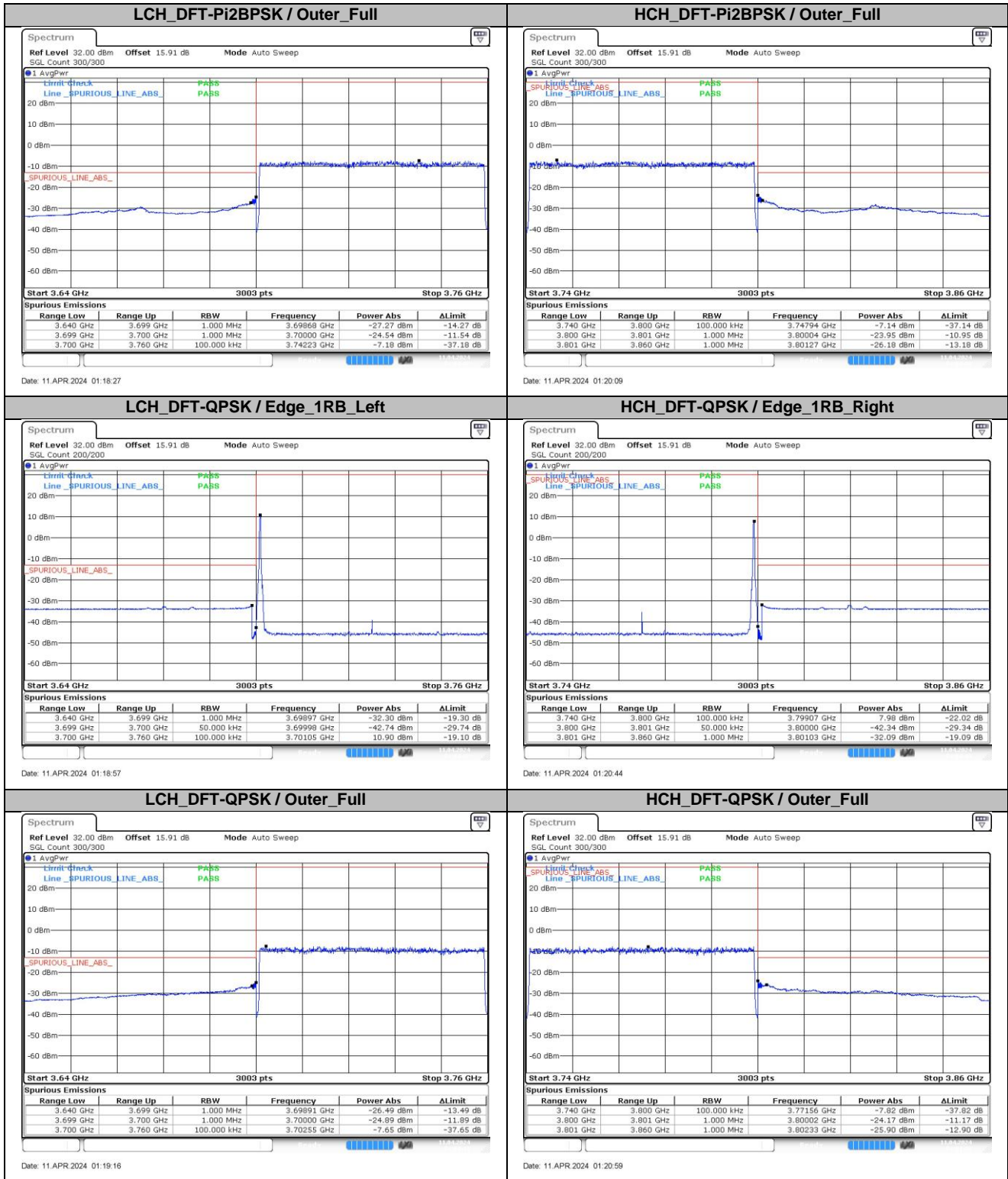
HCH_DFT-QPSK / Edge_1RB_Right



Date: 11.APR.2024 01:13:08

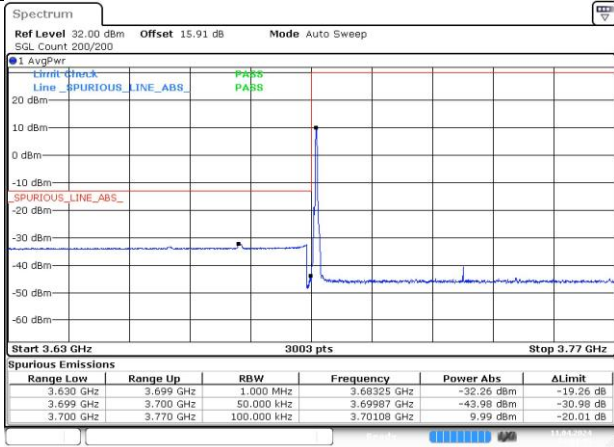






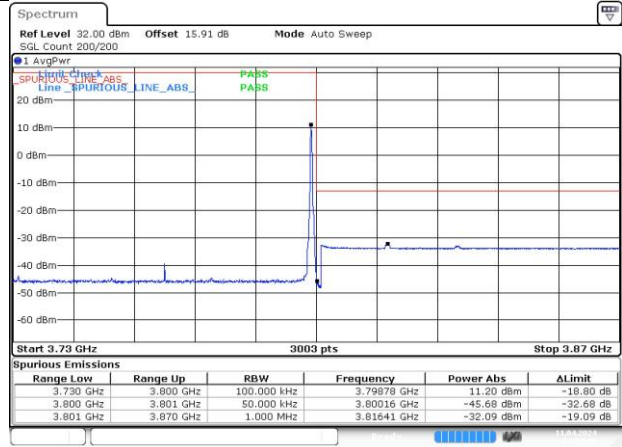
DC_2A_n78A / 30KHz / 70MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



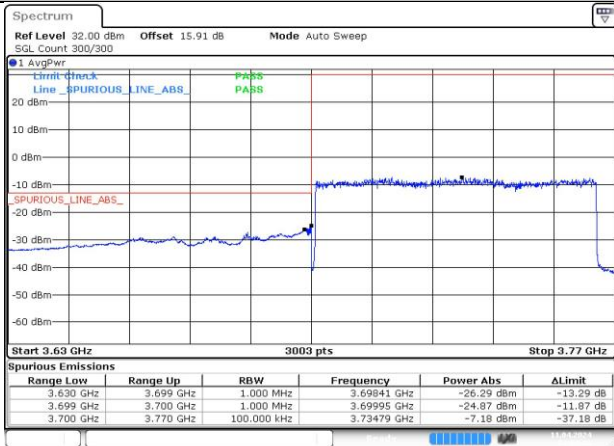
Date: 11 APR 2024 01:21:45

HCH_DFT-Pi2BPSK / Edge_1RB_Right



Date: 11 APR 2024 01:23:39

LCH_DFT-Pi2BPSK / Outer_Full



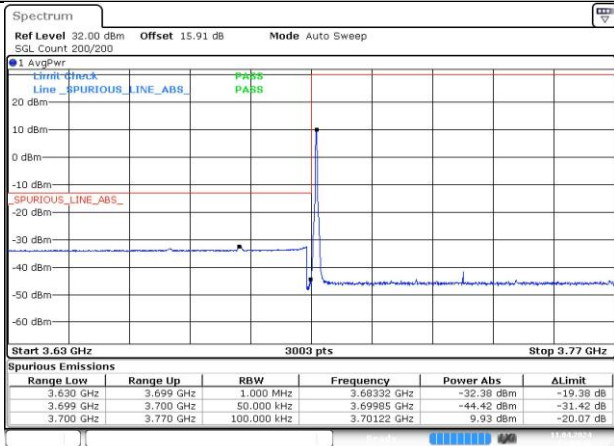
Date: 11 APR 2024 01:22:08

HCH_DFT-Pi2BPSK / Outer_Full



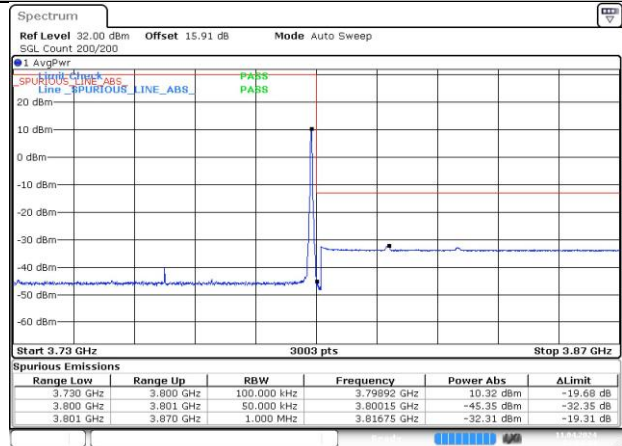
Date: 11 APR 2024 01:23:58

LCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 01:22:40

HCH_DFT-QPSK / Edge_1RB_Right



Date: 11 APR 2024 01:24:33

