

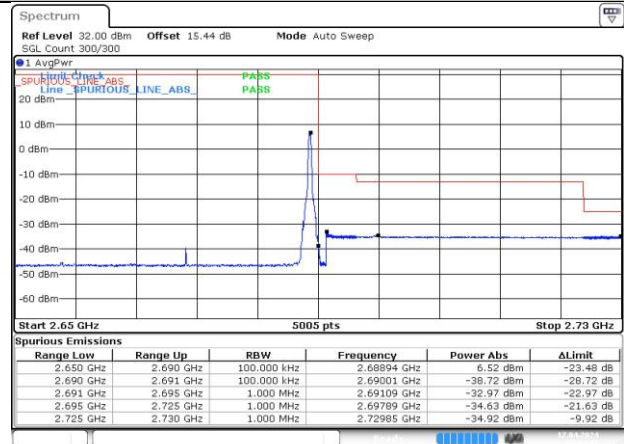
n41A / 30kHz / 35MHz

LCH / CP-QPSK / Edge_1RB_Left



Date: 17 APR 2024 10:54:22

HCH / CP-QPSK / Edge_1RB_Right



Date: 17 APR 2024 10:55:11

LCH / CP-QPSK / Outer_Full



Date: 17 APR 2024 10:54:43

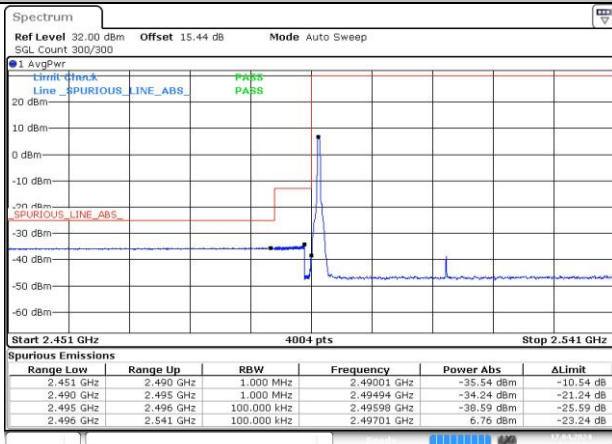
HCH / CP-QPSK / Outer_Full



Date: 17 APR 2024 10:55:30

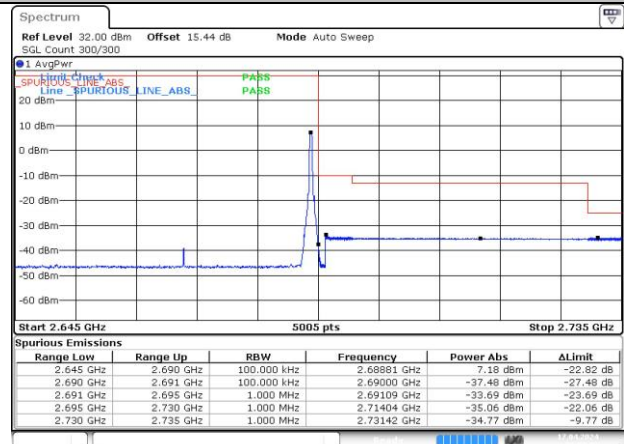
n41A / 30kHz / 40MHz

LCH / CP-QPSK / Edge_1RB_Left

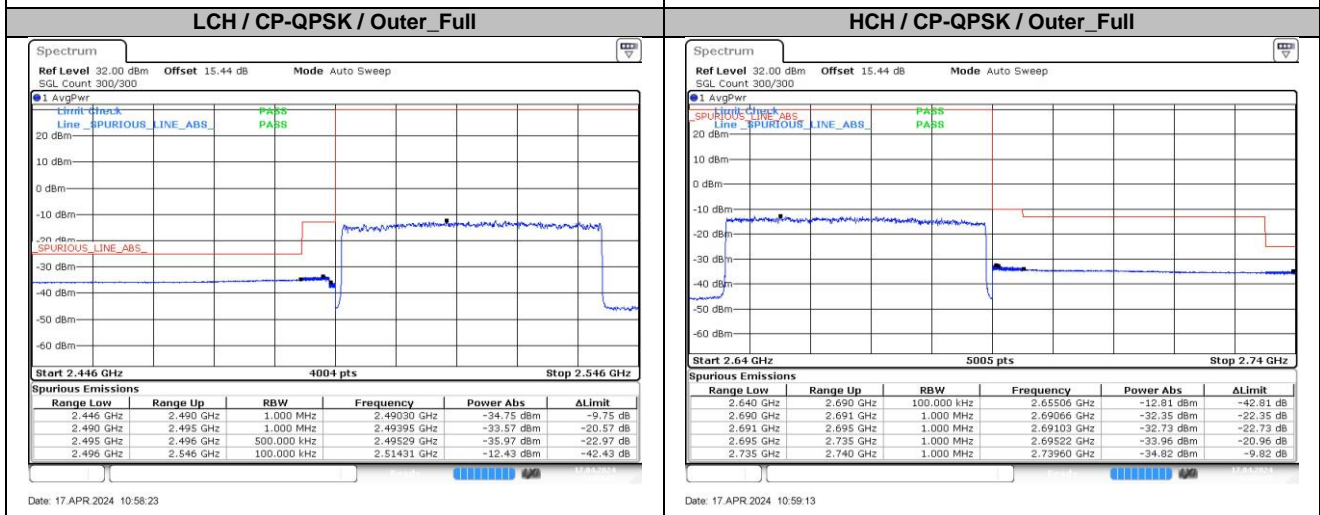
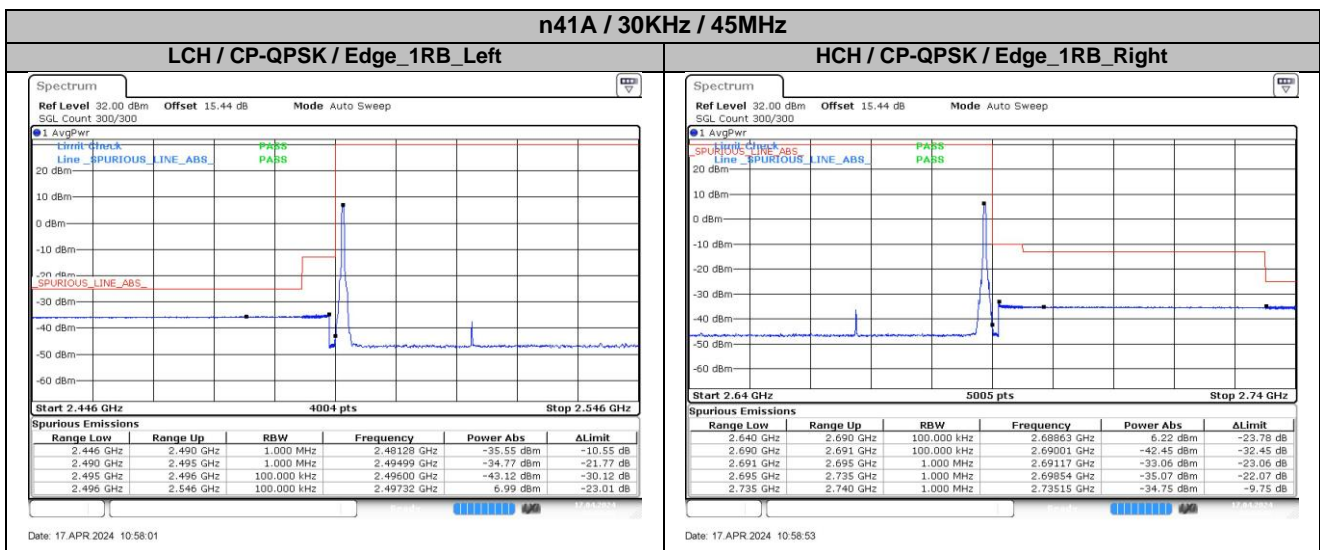
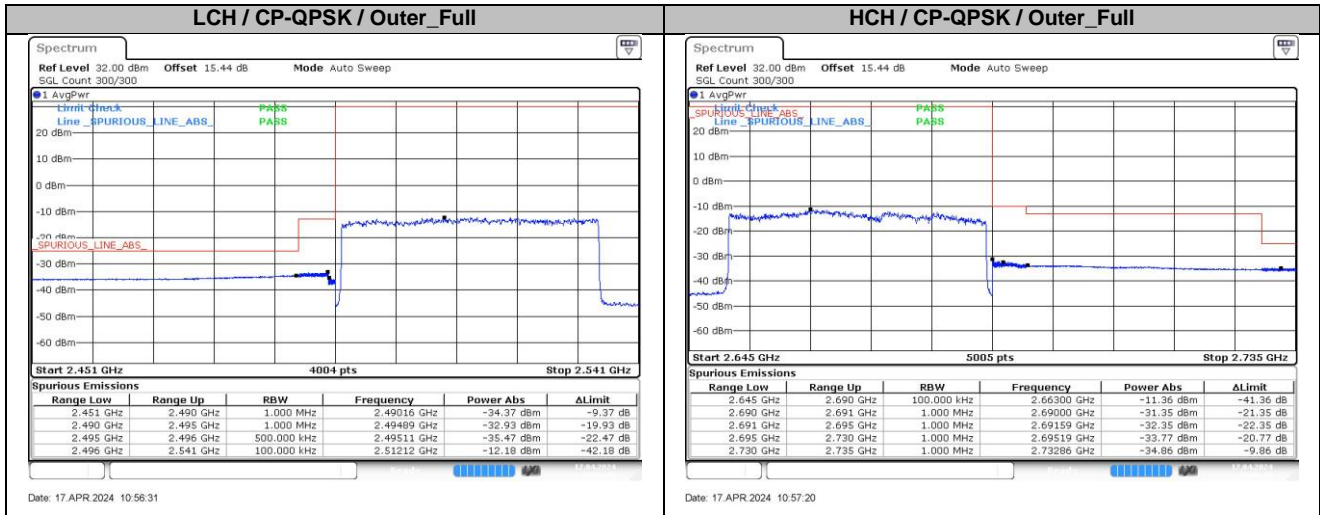


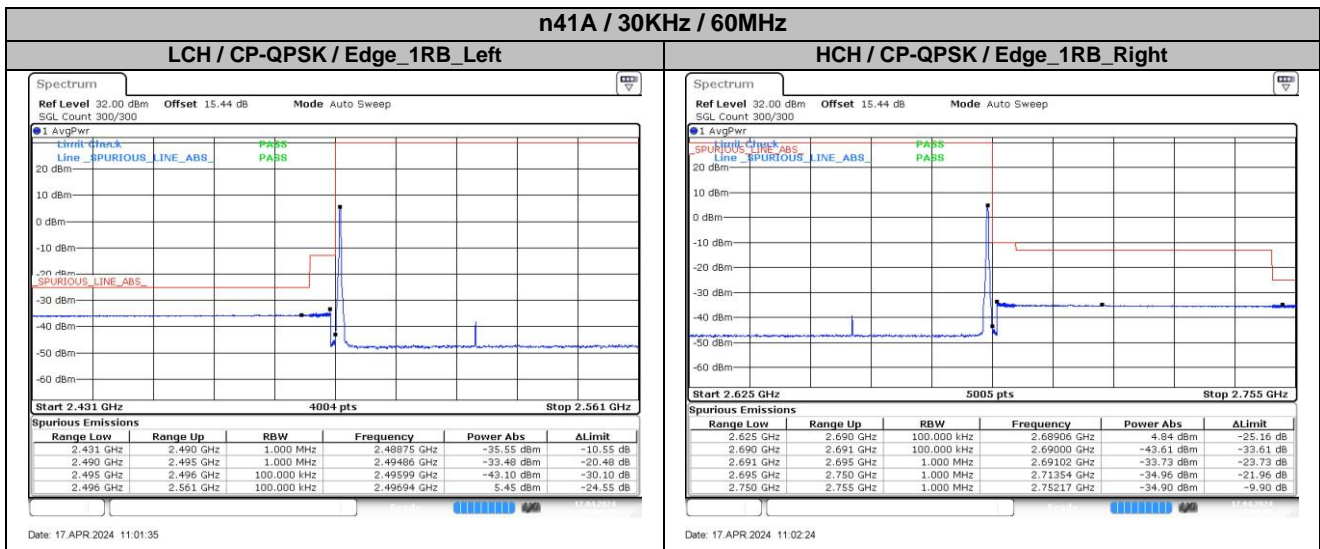
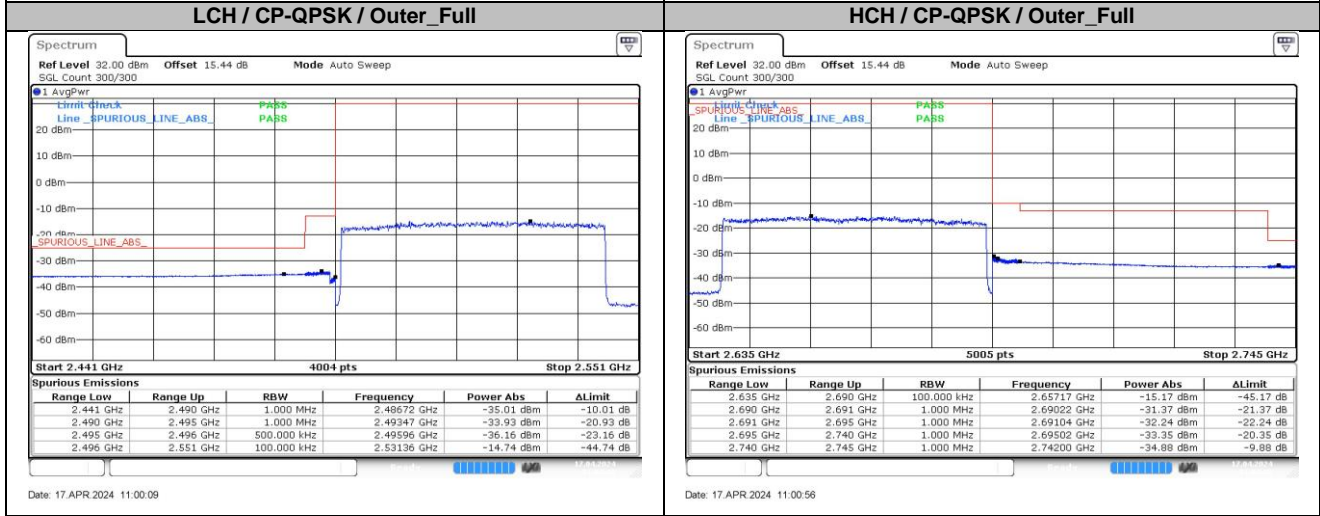
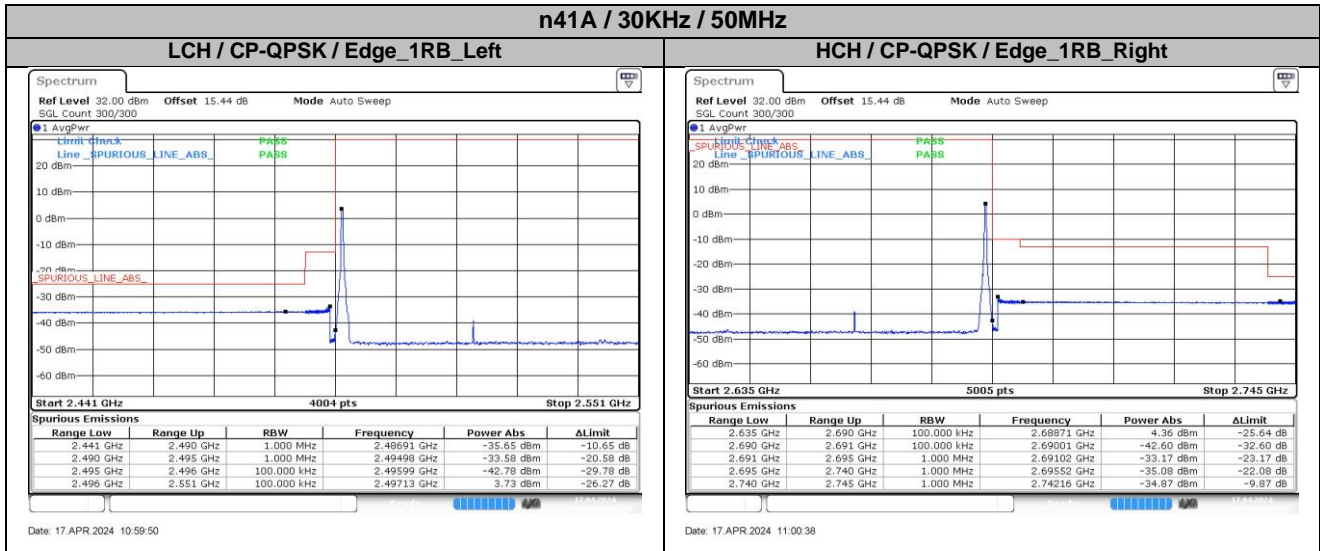
Date: 17 APR 2024 10:56:10

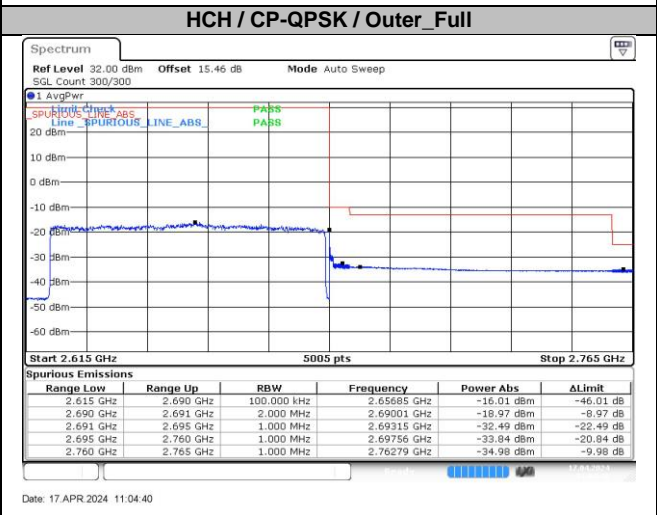
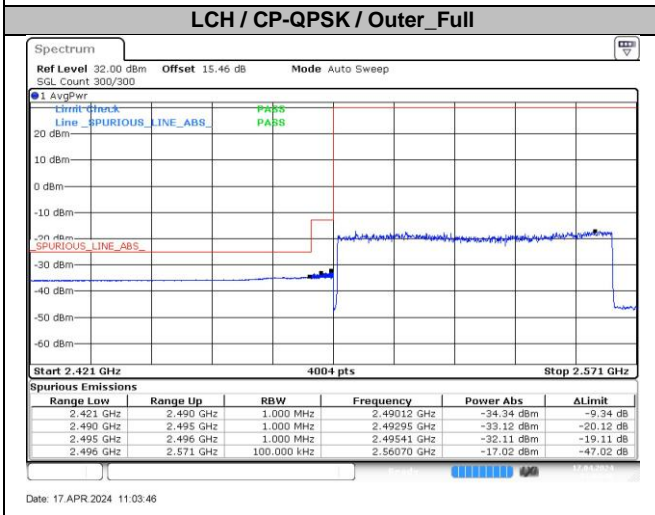
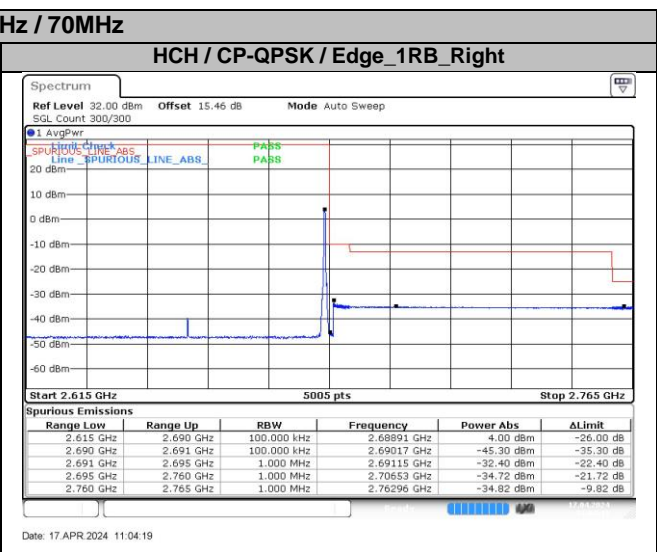
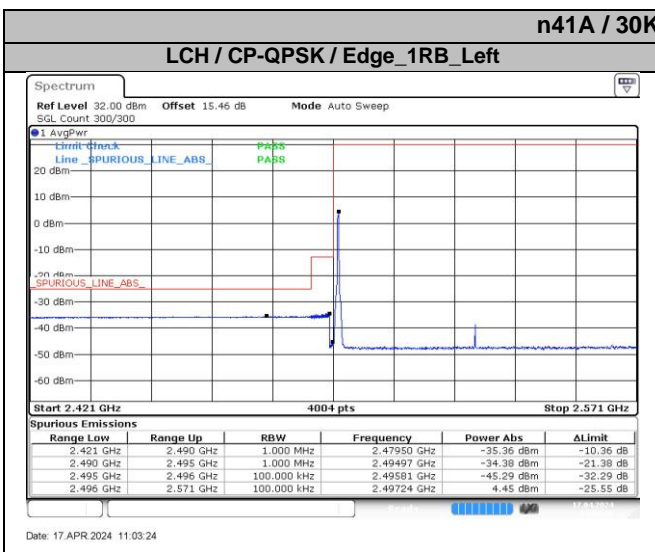
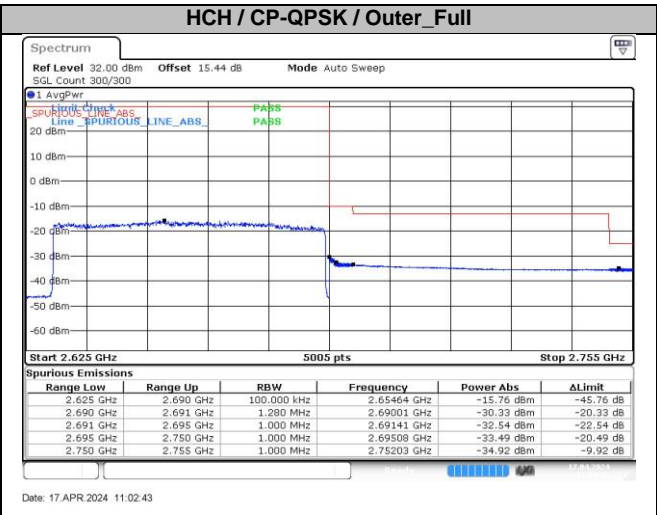
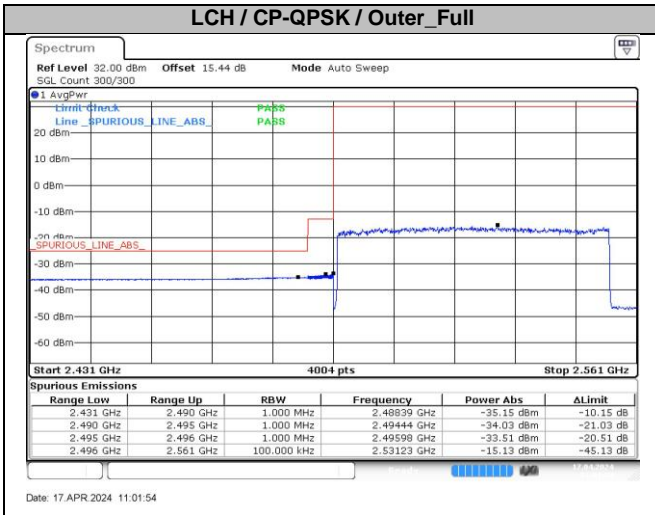
HCH / CP-QPSK / Edge_1RB_Right

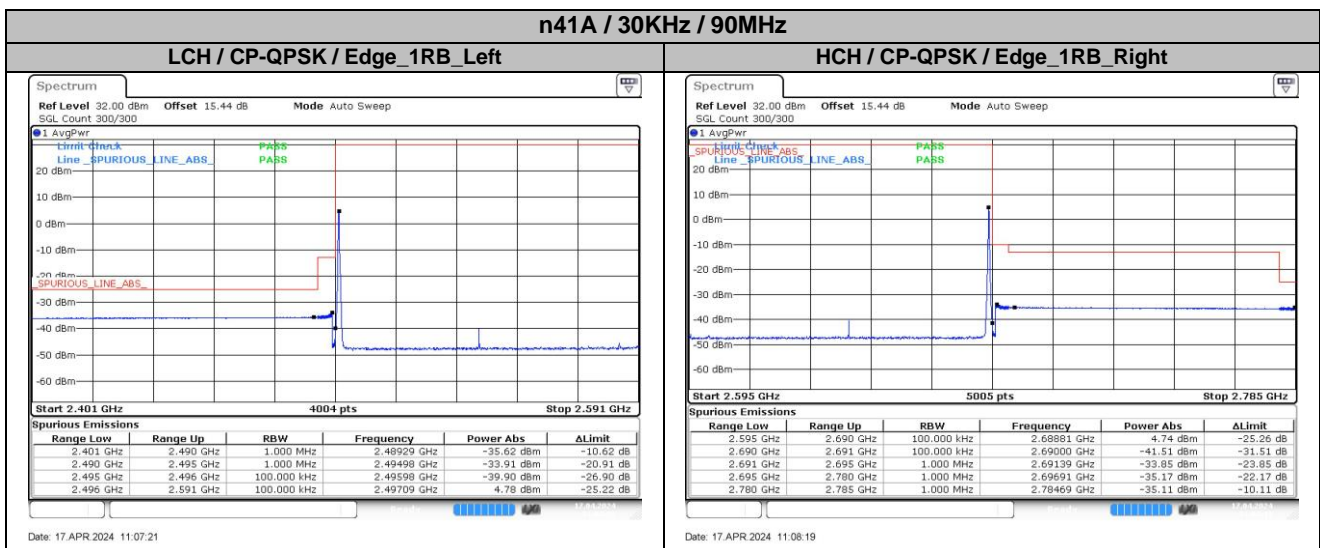
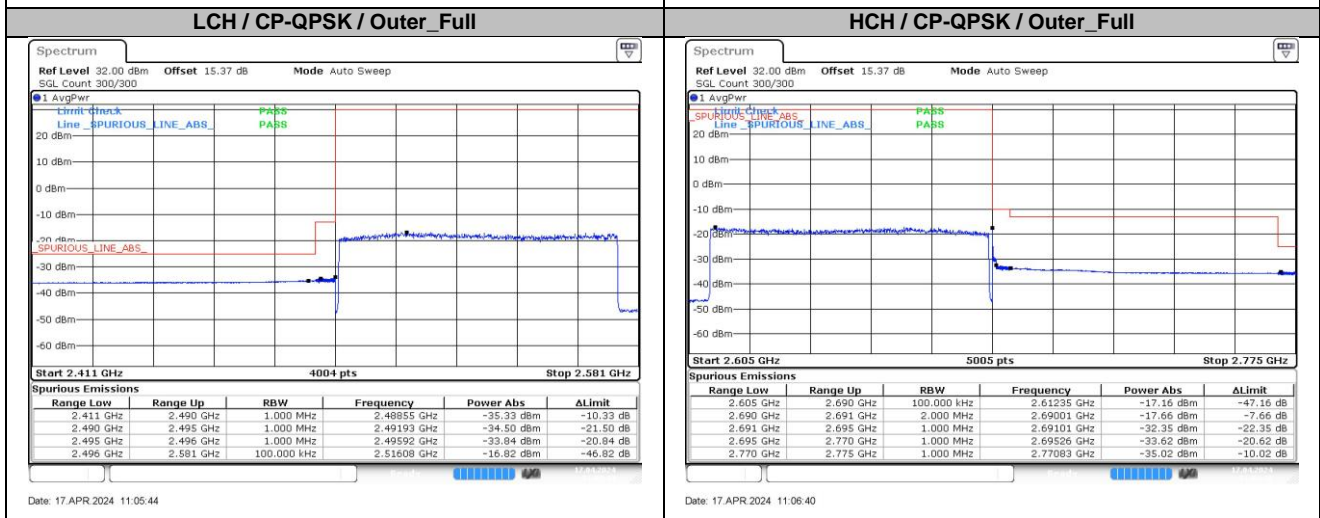
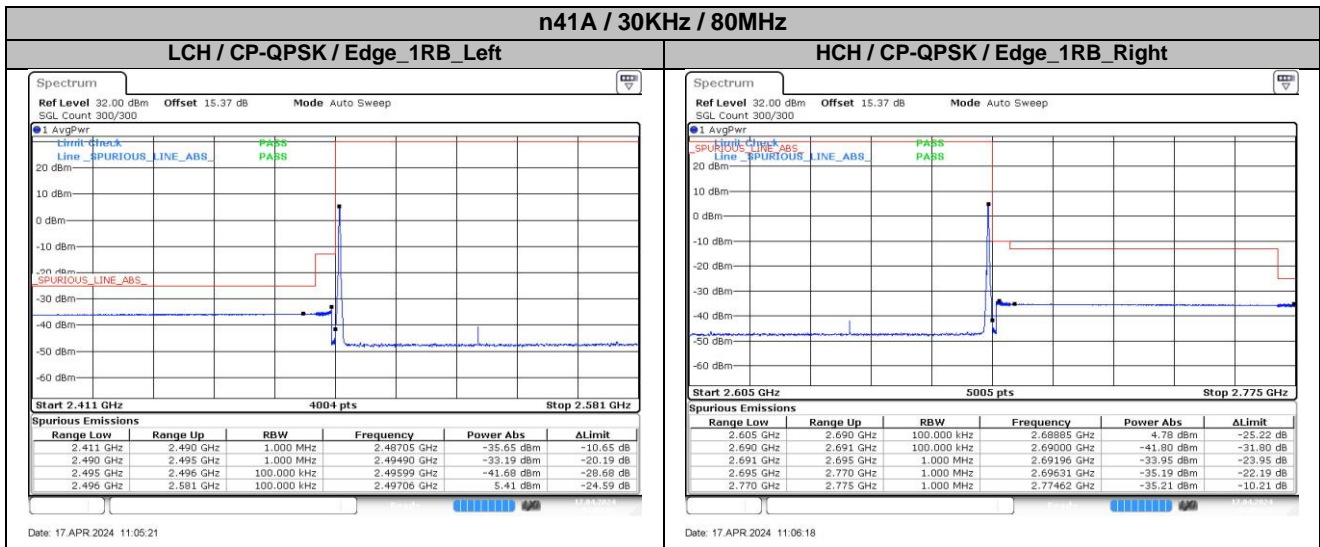


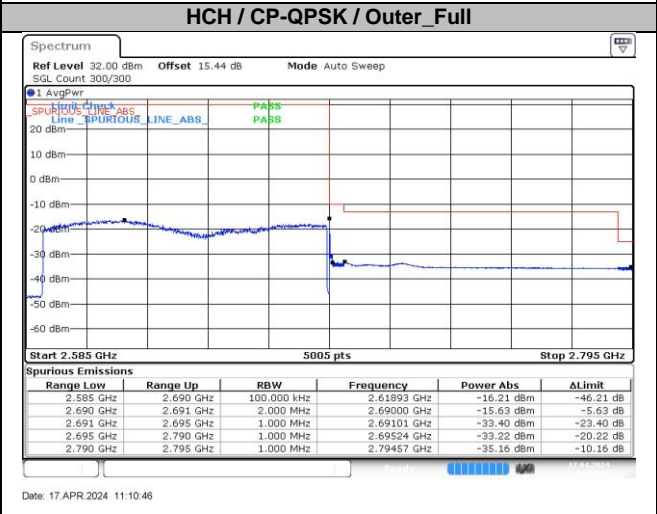
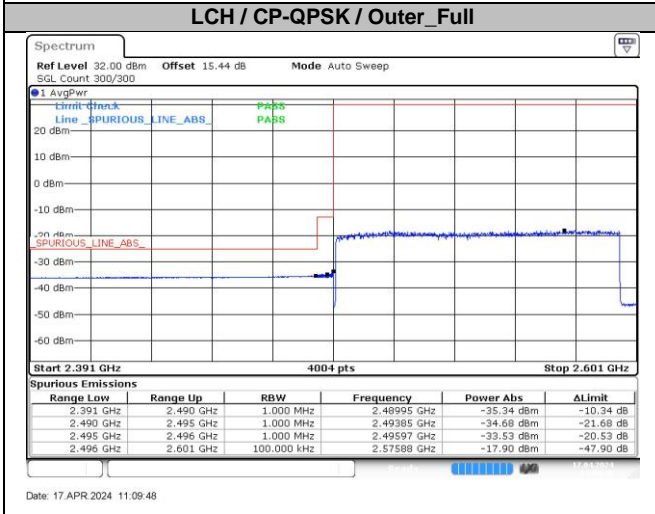
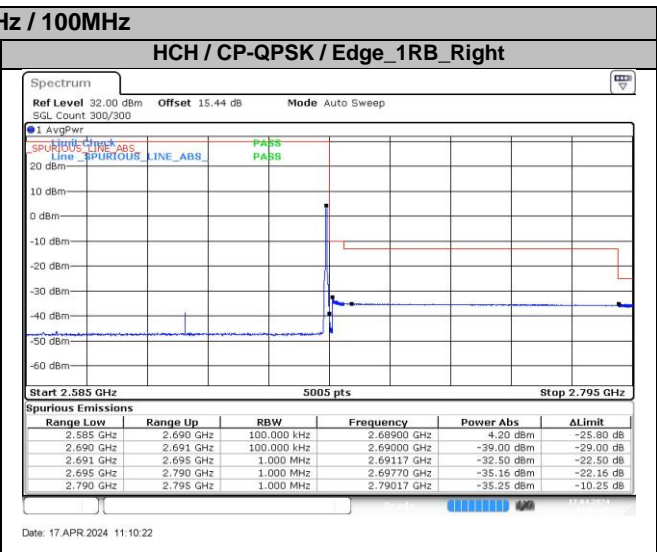
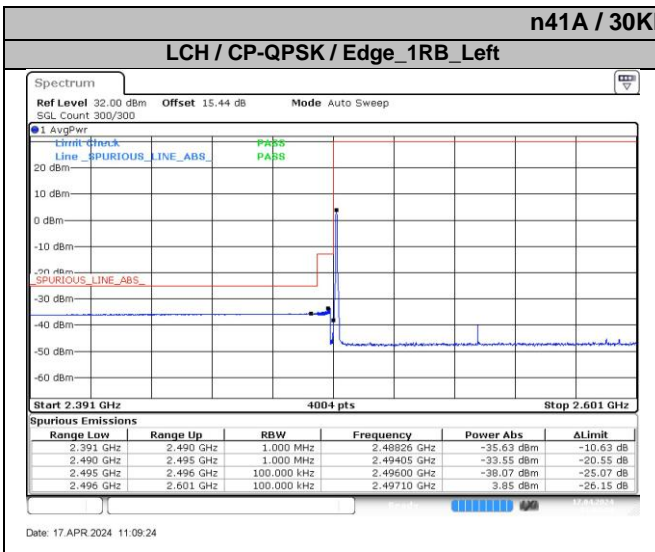
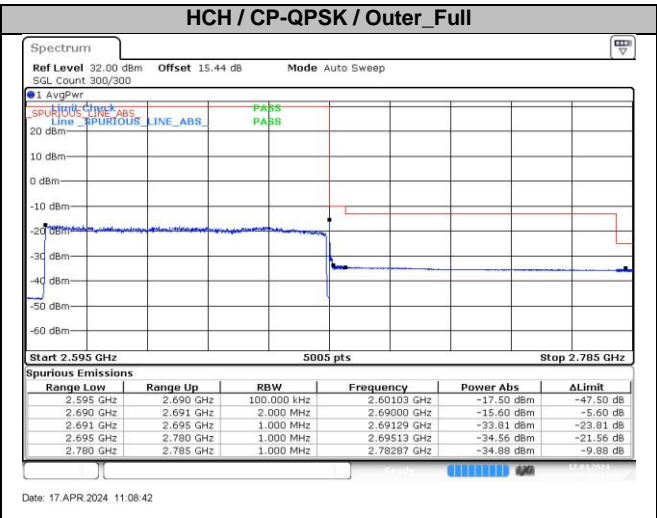
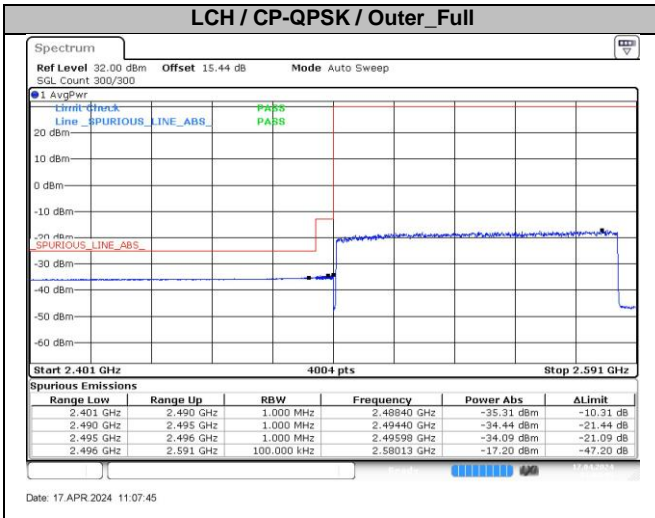
Date: 17 APR 2024 10:57:01











6. Conducted Spurious Emission

6.1. Test Plots

