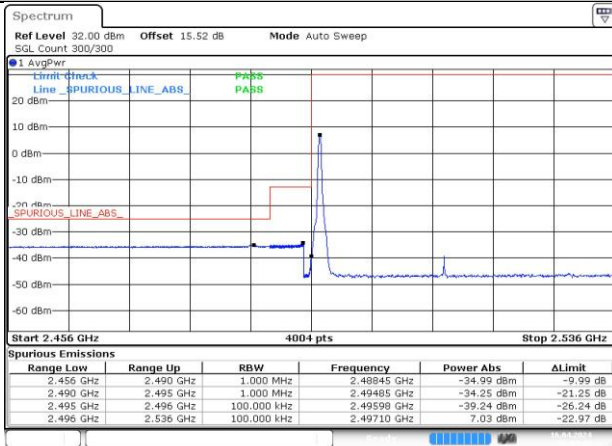


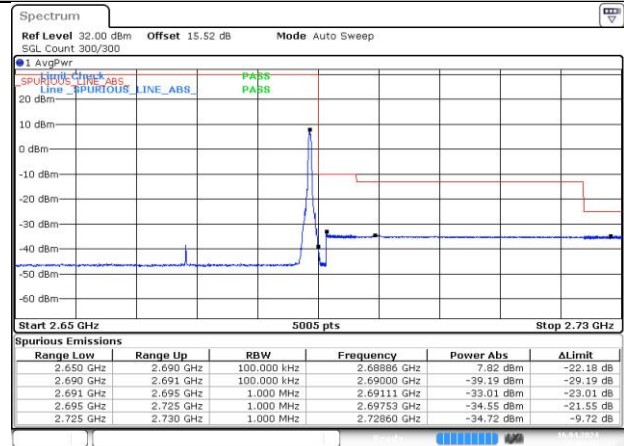
n41A / 30kHz / 35MHz

LCH / CP-QPSK / Edge\_1RB\_Left



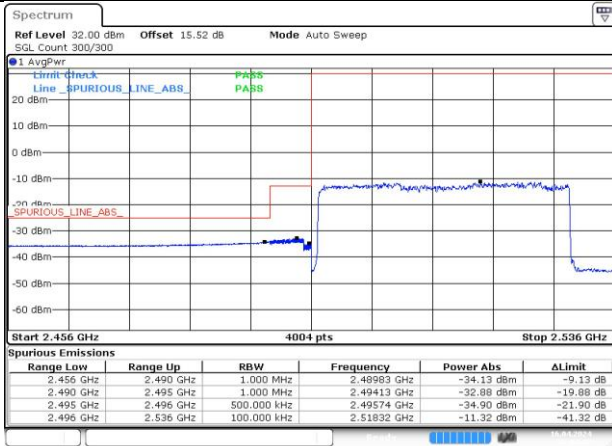
Date: 16 APR 2024 22:09:05

HCH / CP-QPSK / Edge\_1RB\_Right



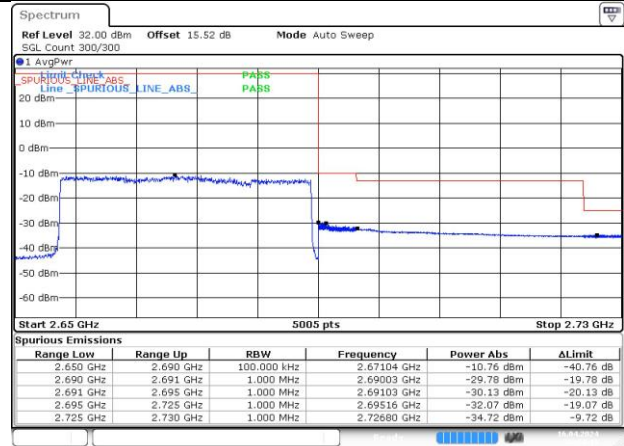
Date: 16 APR 2024 22:09:55

LCH / CP-QPSK / Outer\_Full



Date: 16 APR 2024 22:09:26

HCH / CP-QPSK / Outer\_Full



Date: 16 APR 2024 22:10:13

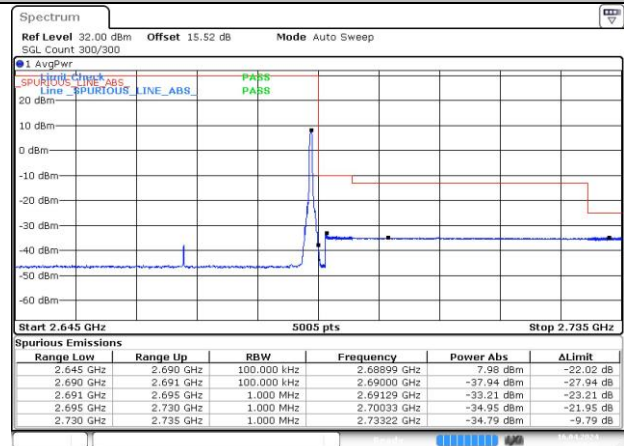
n41A / 30kHz / 40MHz

LCH / CP-QPSK / Edge\_1RB\_Left

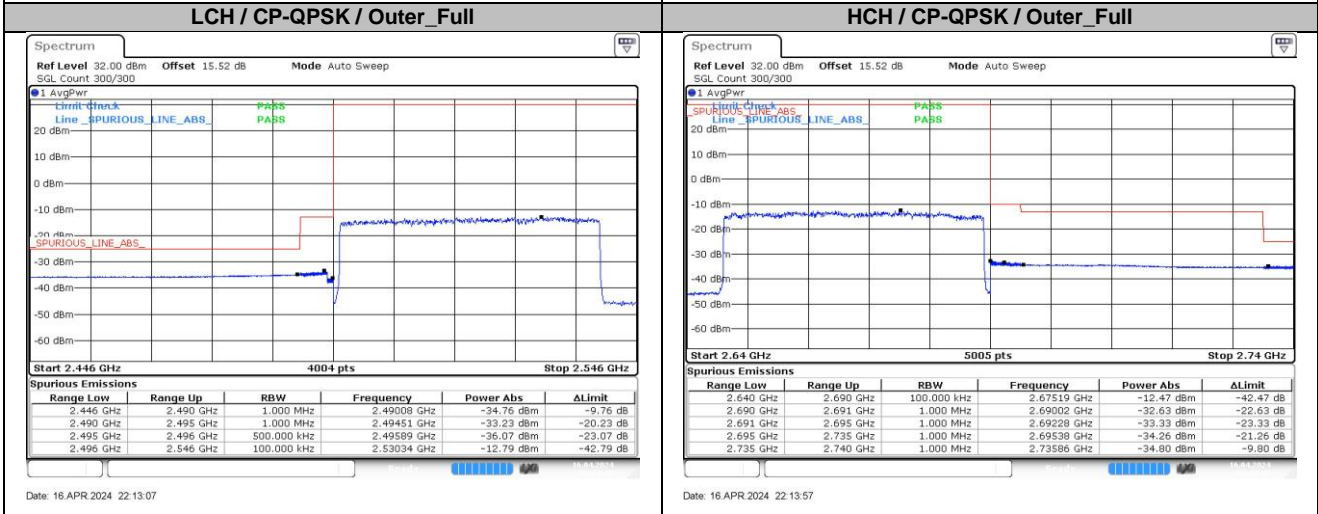
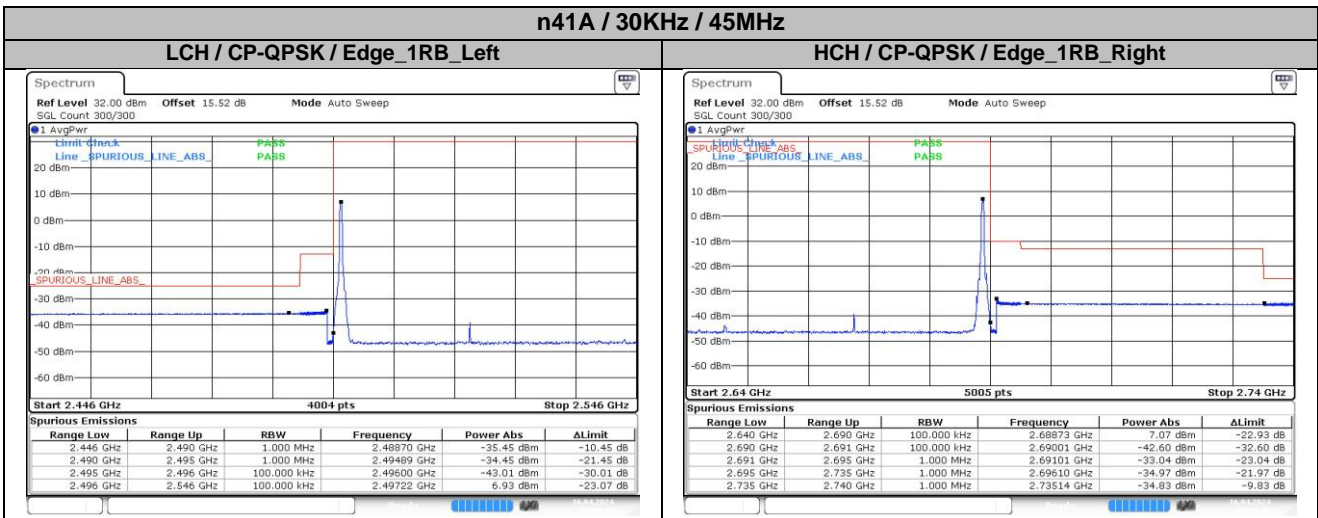
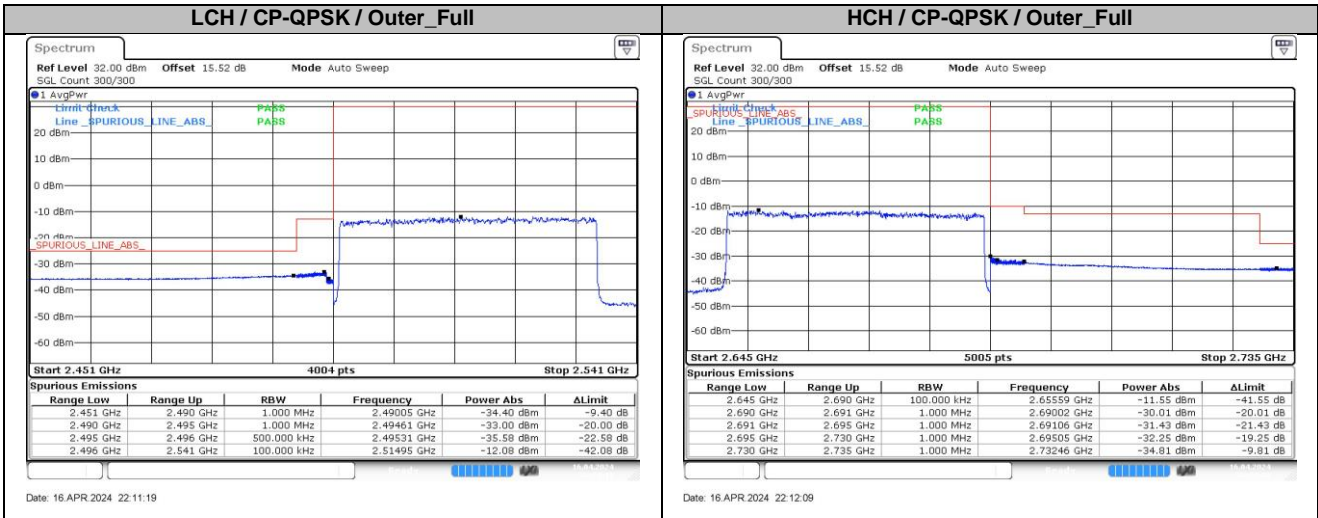


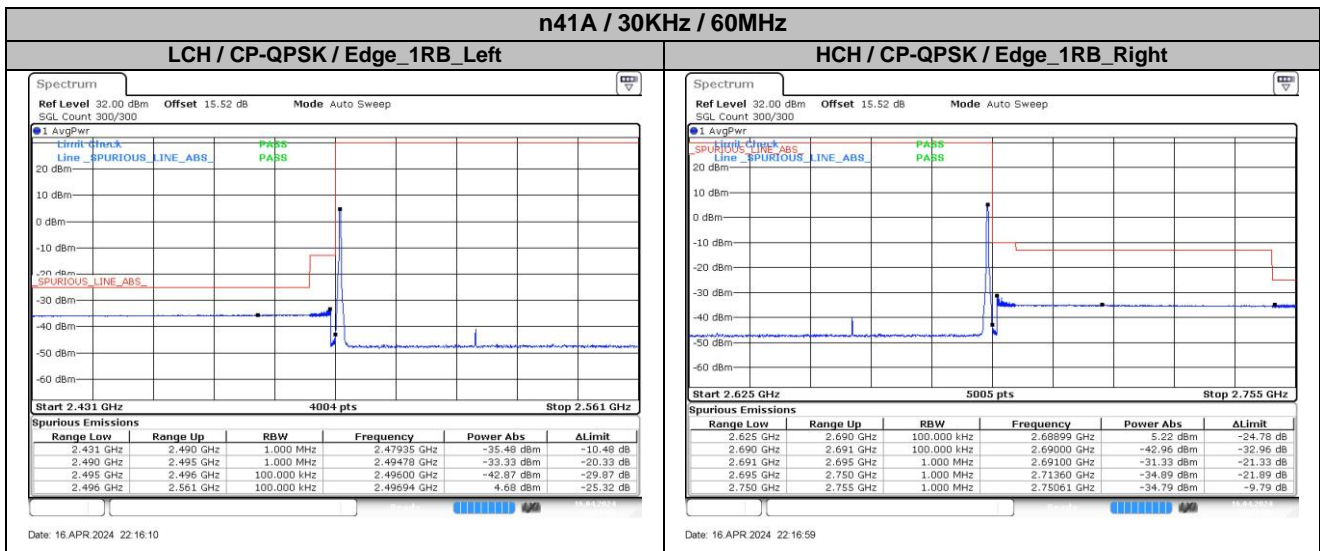
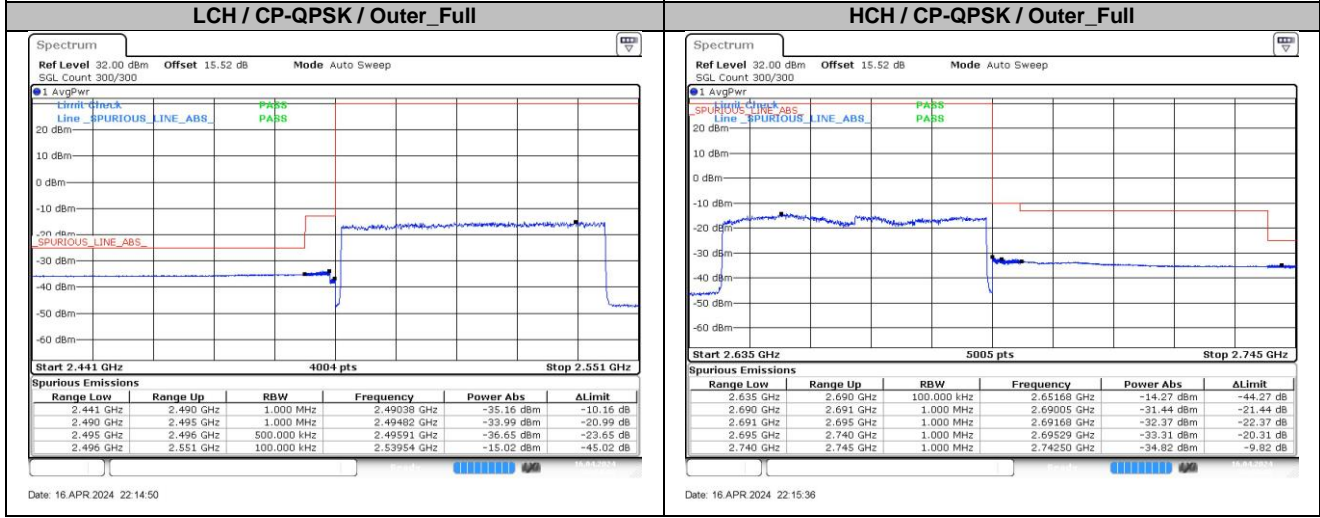
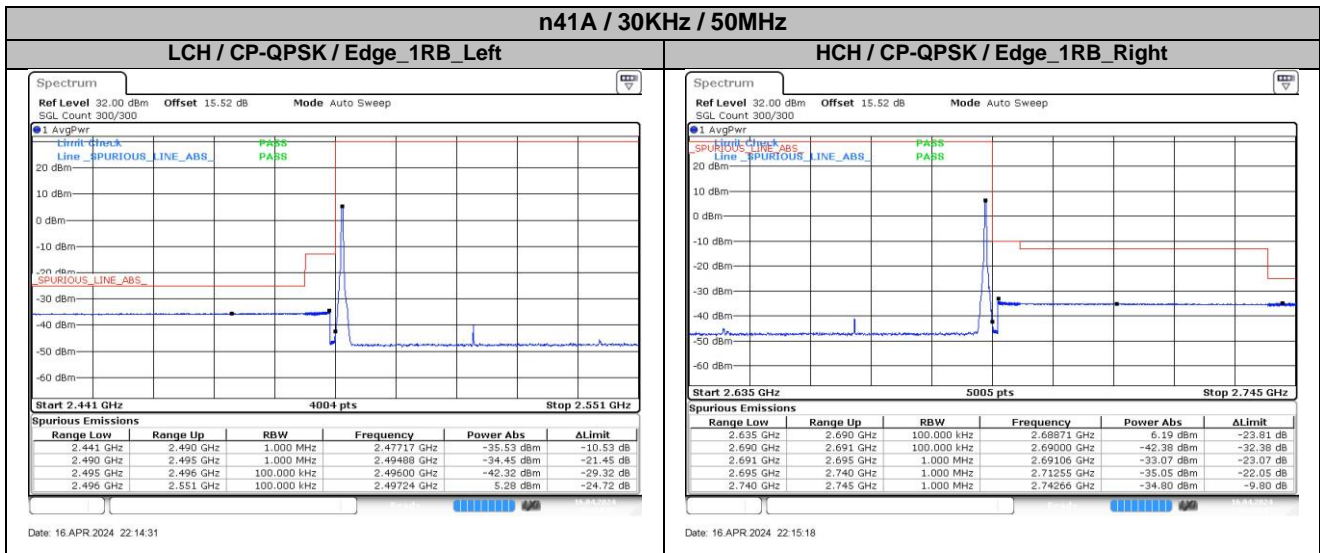
Date: 16 APR 2024 22:10:58

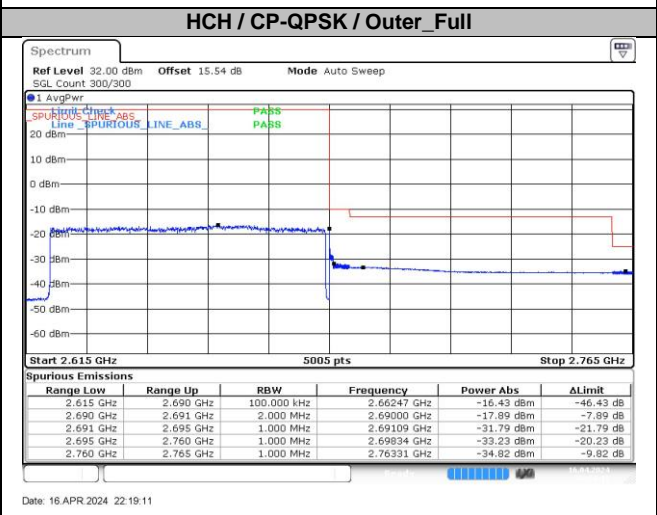
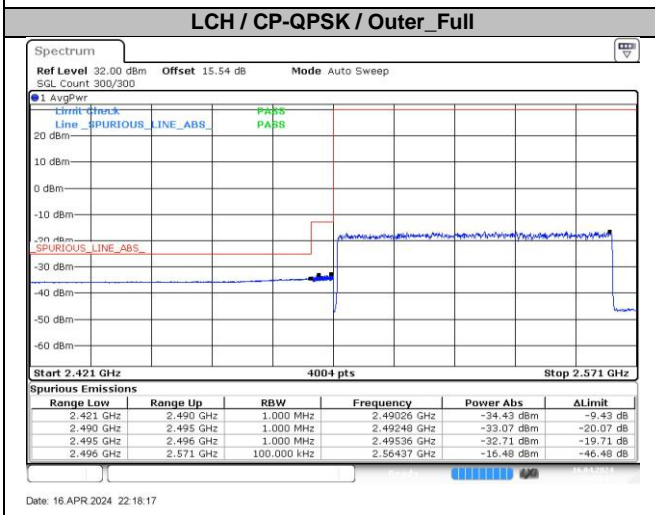
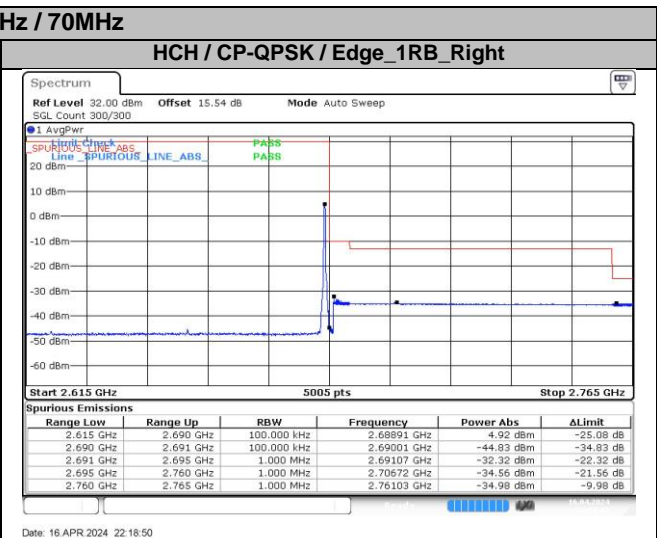
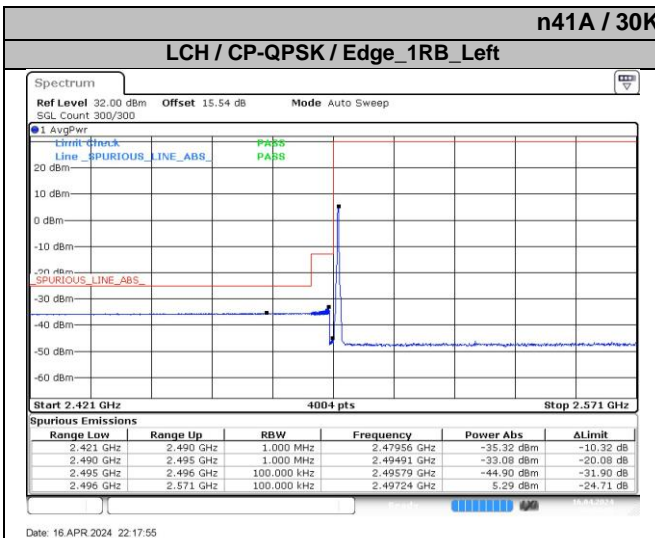
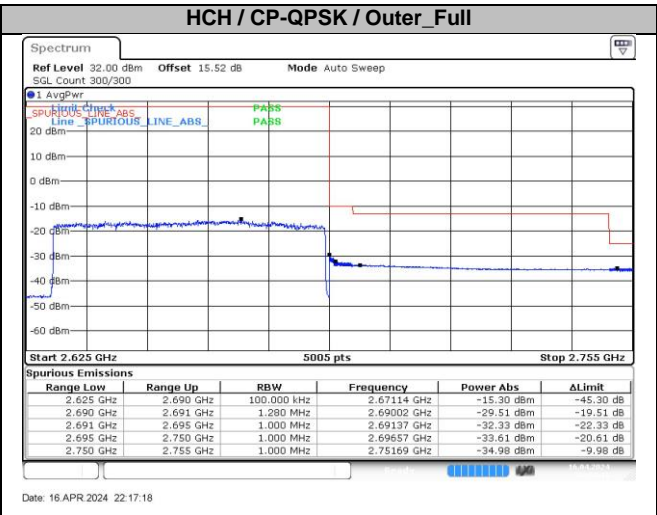
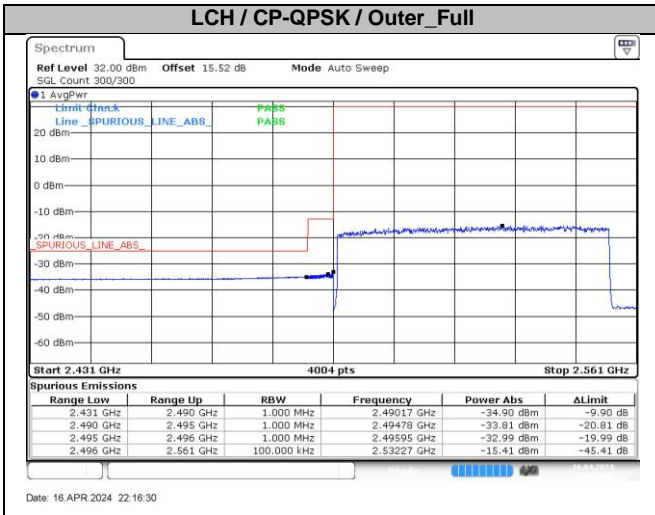
HCH / CP-QPSK / Edge\_1RB\_Right



Date: 16 APR 2024 22:11:49









n41A / 30kHz / 80MHz

LCH / CP-QPSK / Edge\_1RB\_Left



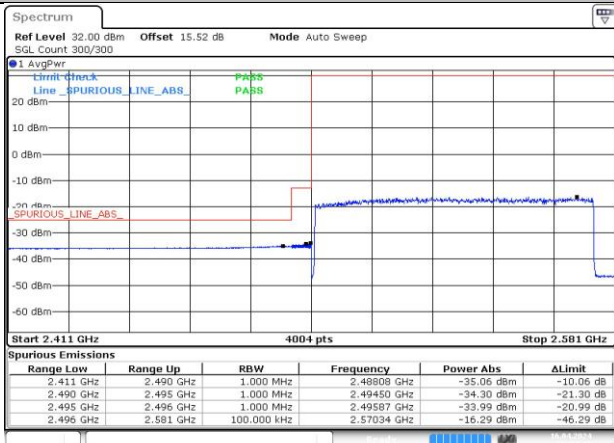
Date: 16 APR 2024 22:19:48

HCH / CP-QPSK / Edge\_1RB\_Right



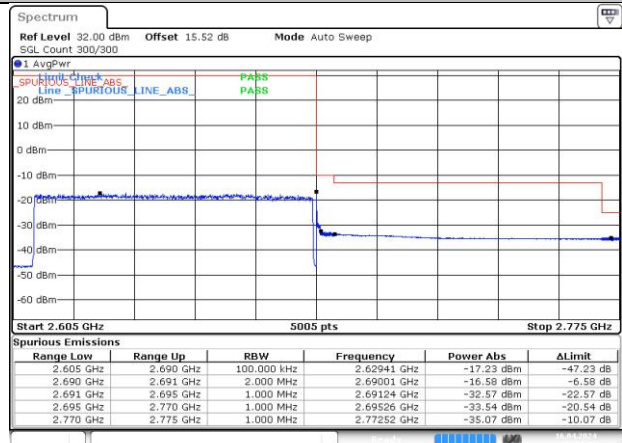
Date: 16 APR 2024 22:20:44

LCH / CP-QPSK / Outer\_Full



Date: 16 APR 2024 22:20:11

HCH / CP-QPSK / Outer\_Full



Date: 16 APR 2024 22:21:06

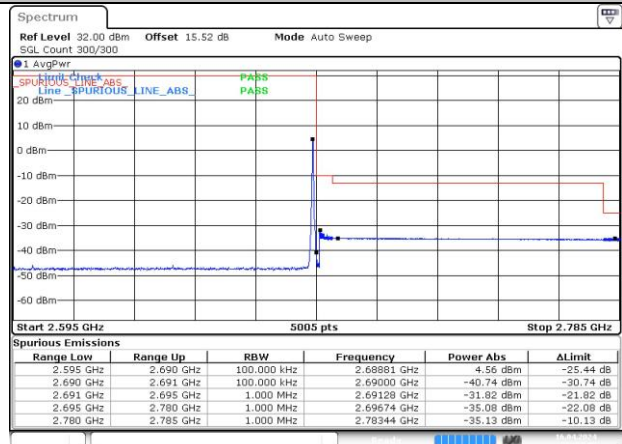
n41A / 30kHz / 90MHz

LCH / CP-QPSK / Edge\_1RB\_Left

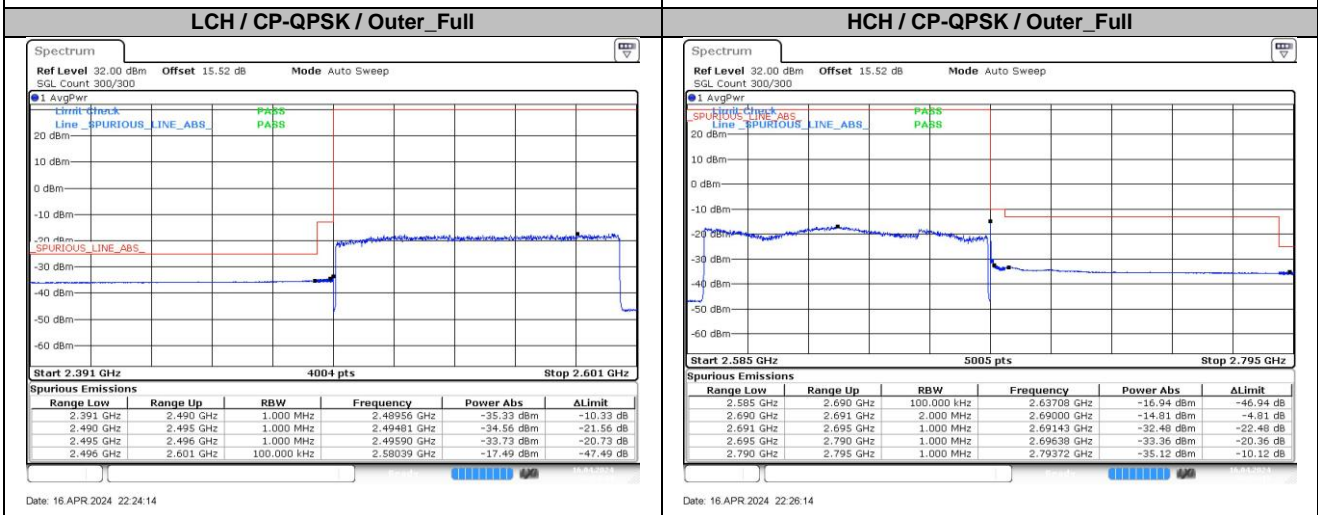
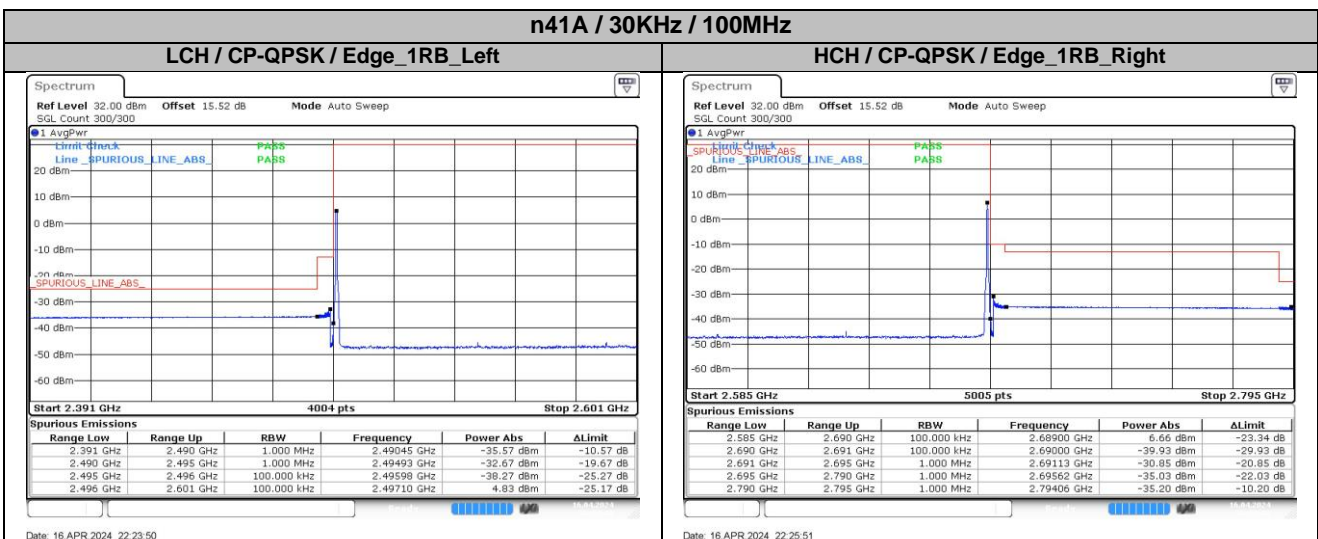
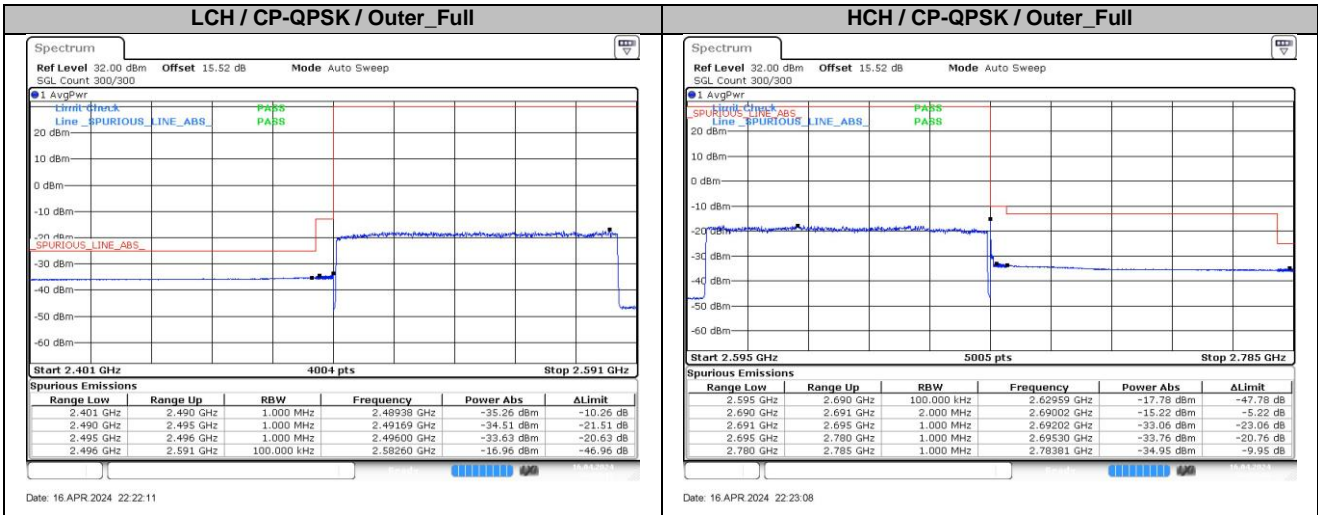


Date: 16 APR 2024 22:21:48

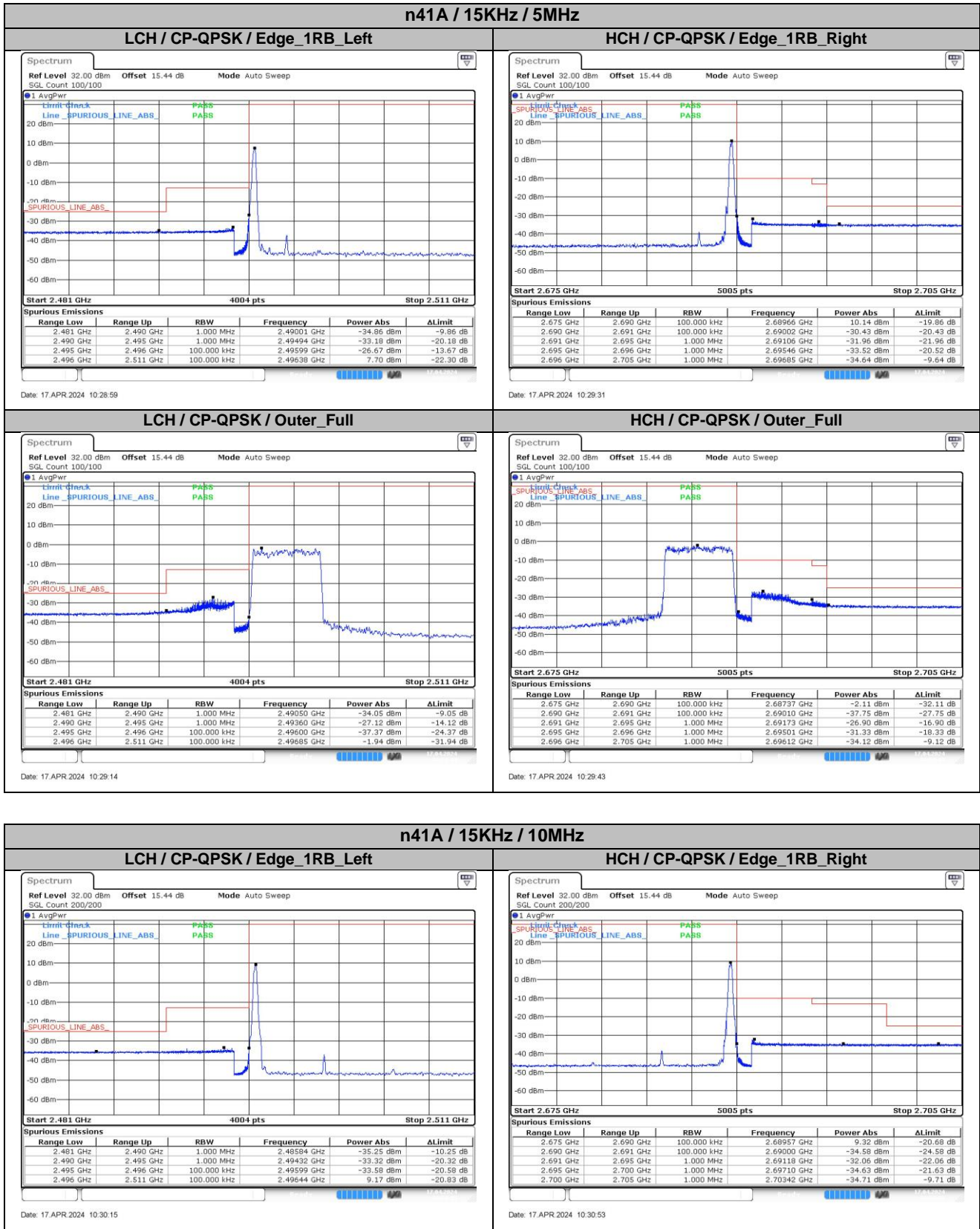
HCH / CP-QPSK / Edge\_1RB\_Right

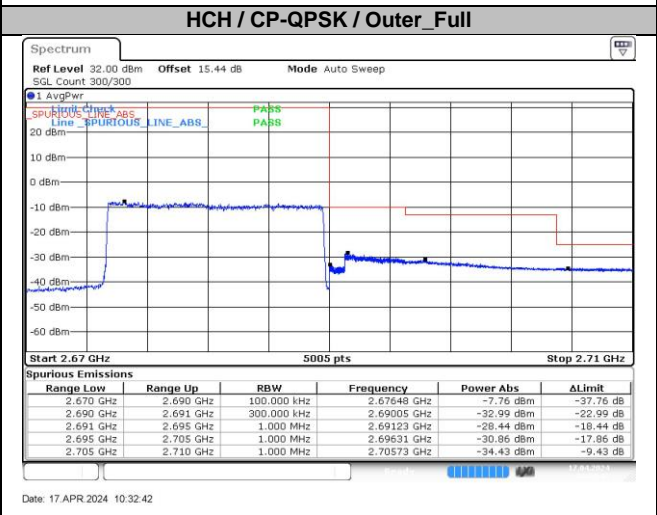
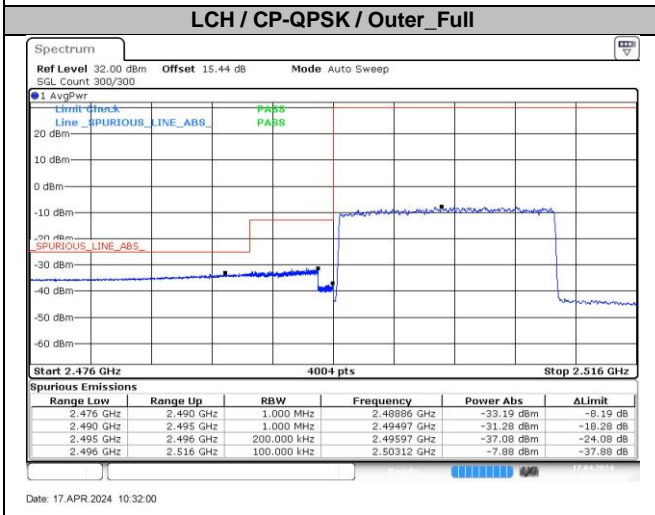
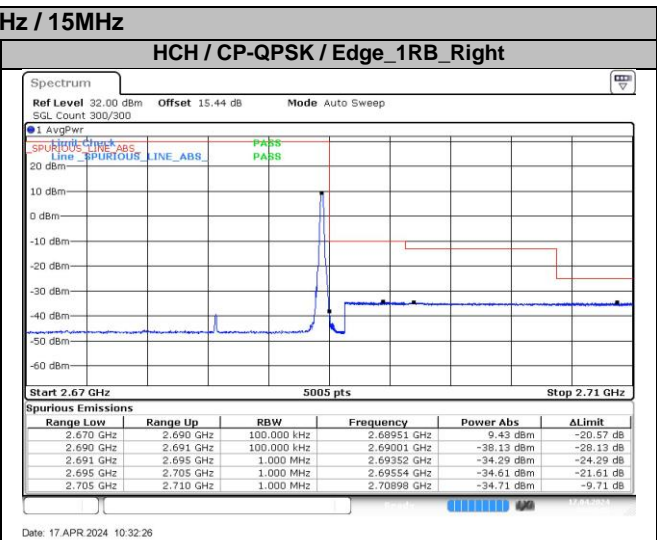
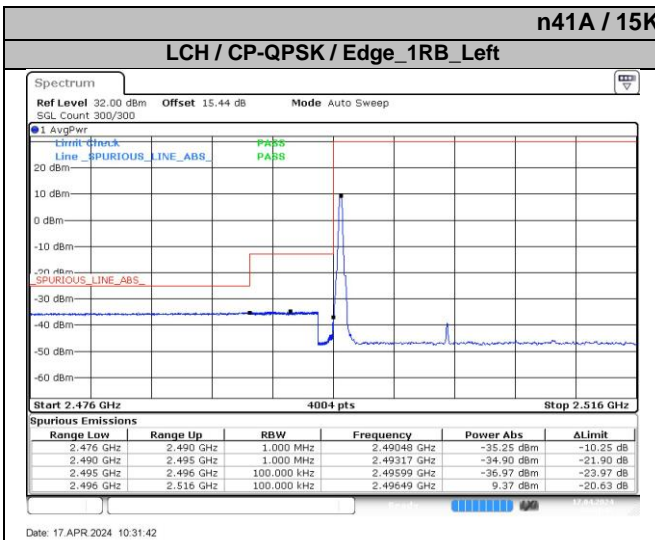
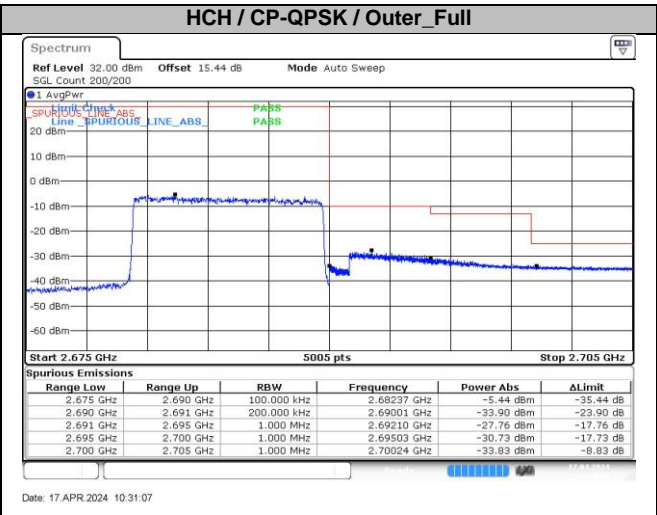
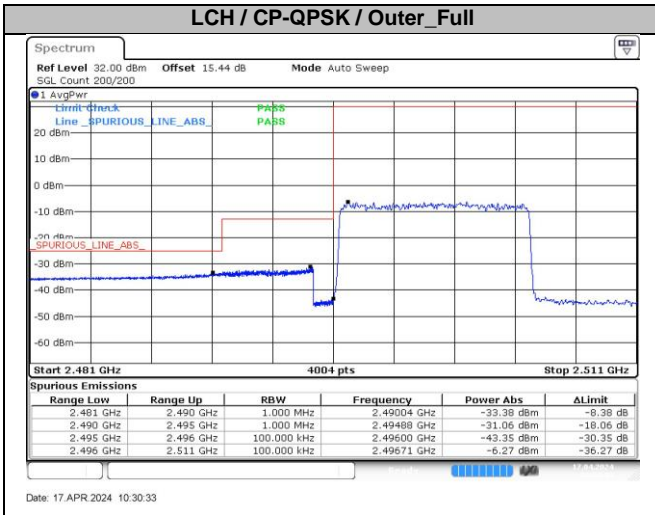


Date: 16 APR 2024 22:22:45

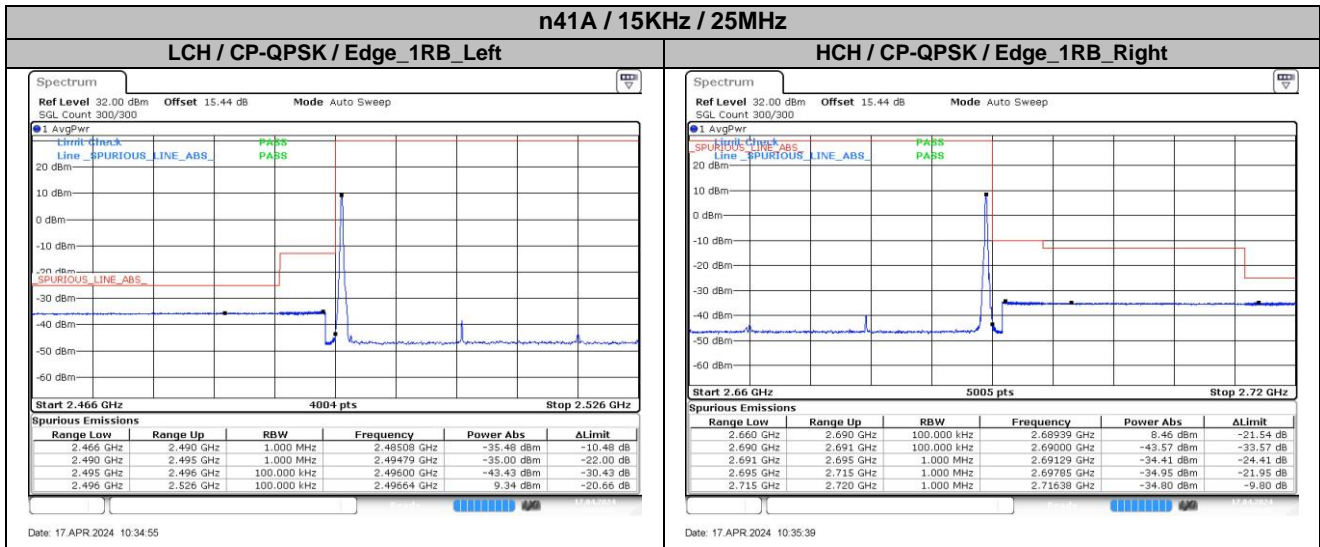
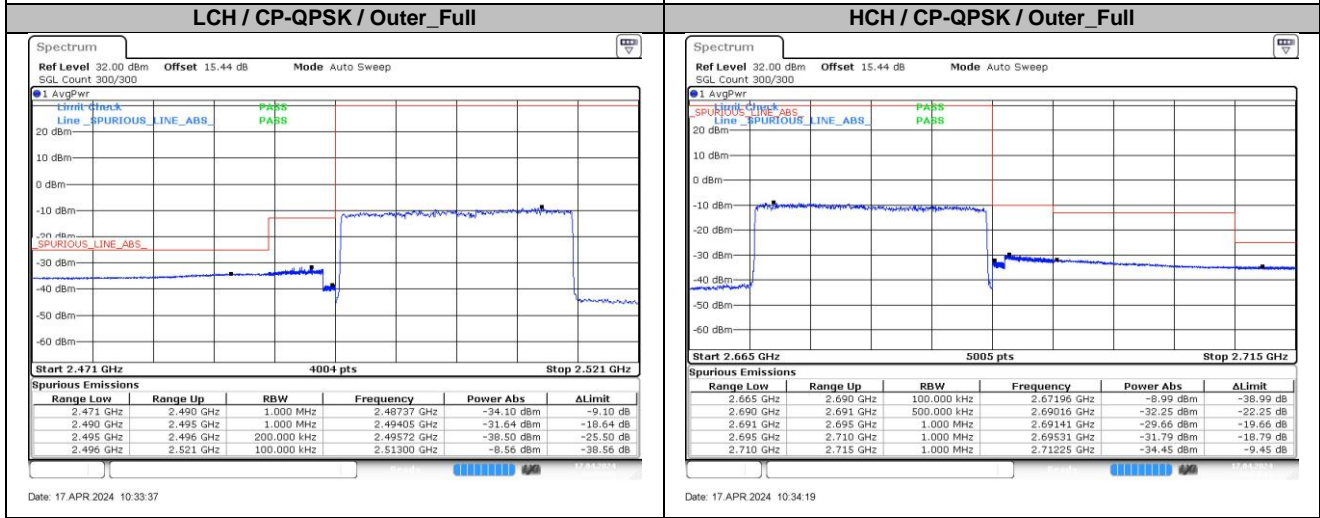
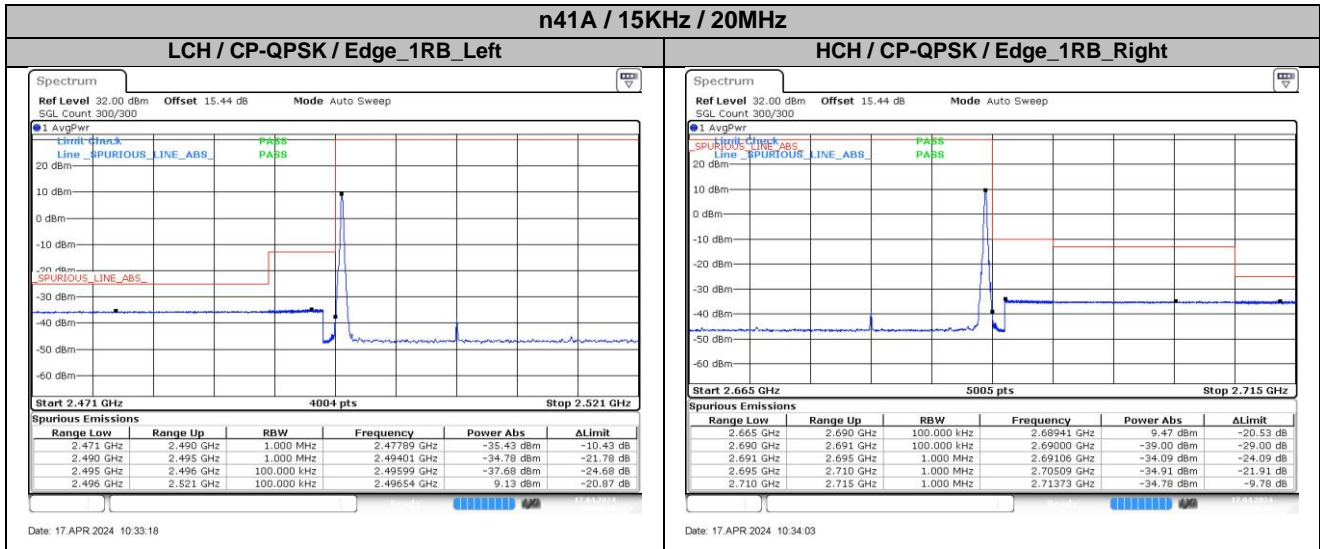


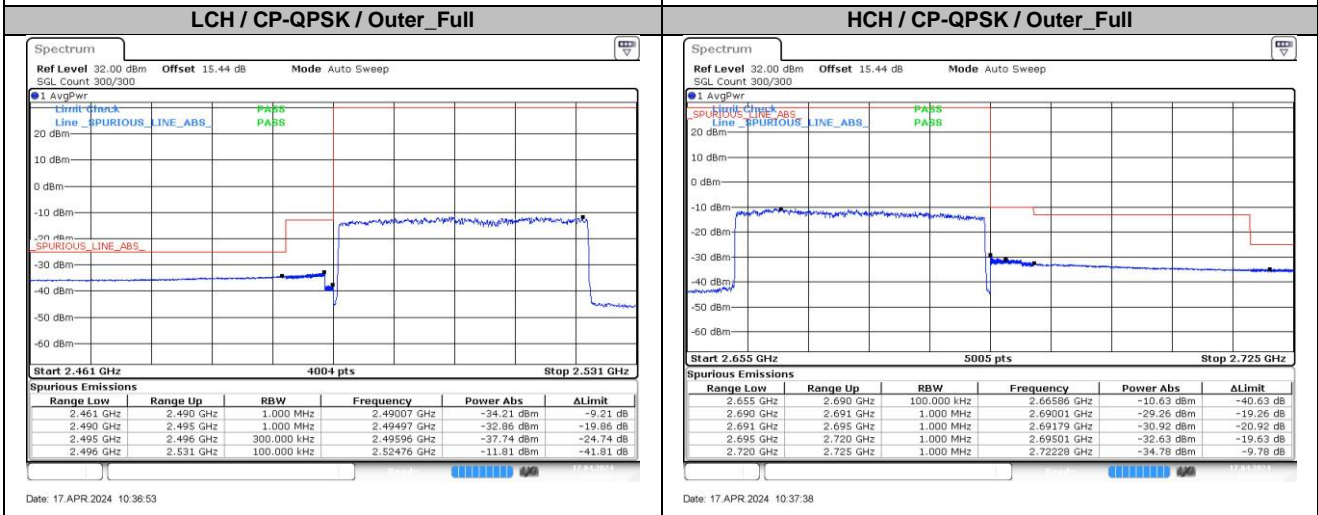
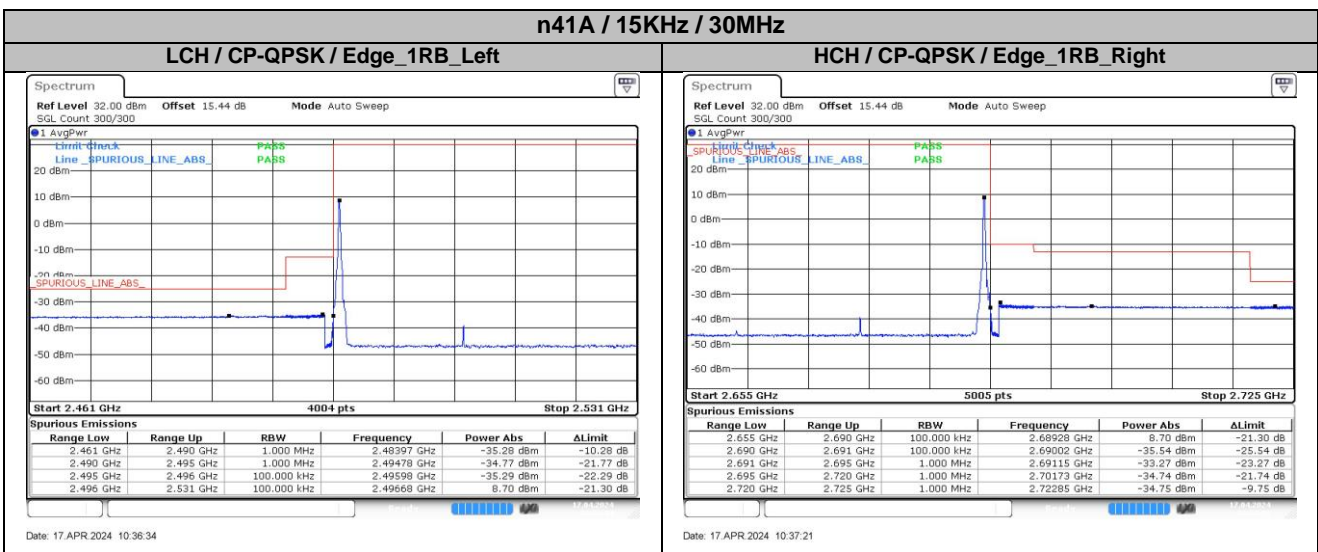
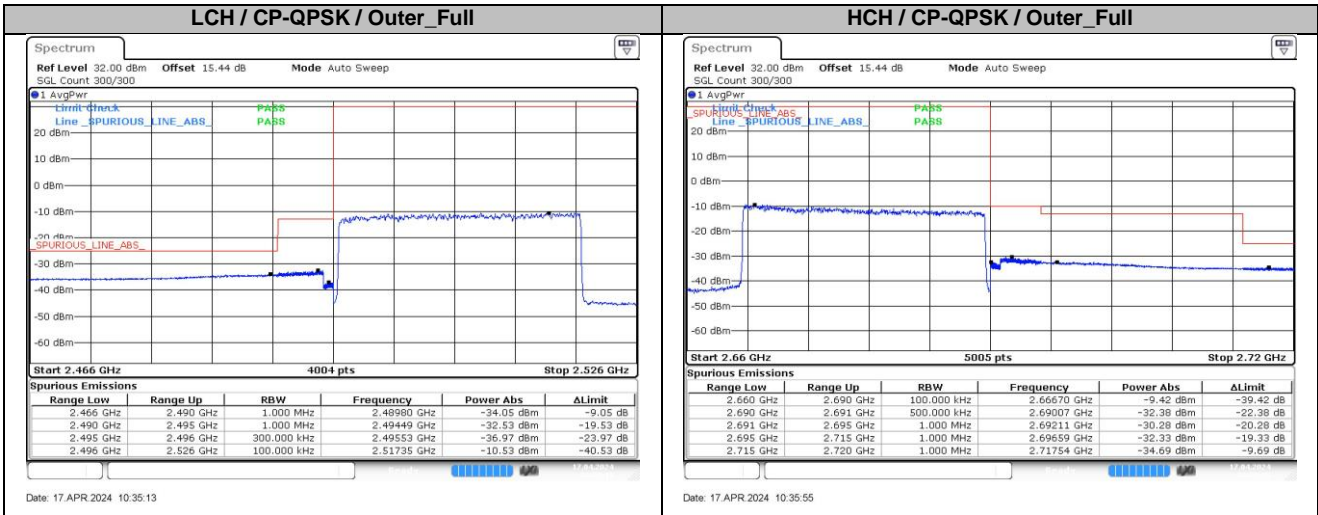
5.2. Test Plots for Ant3





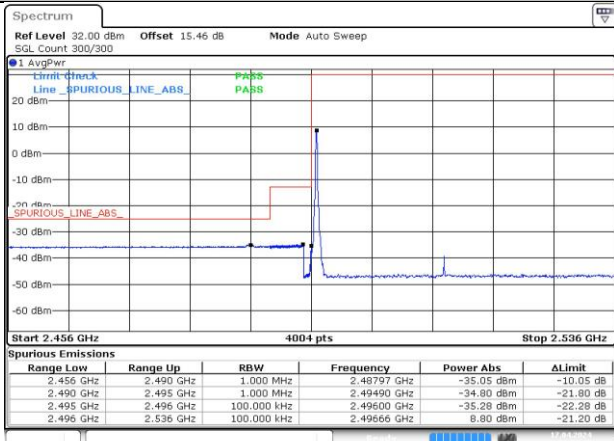






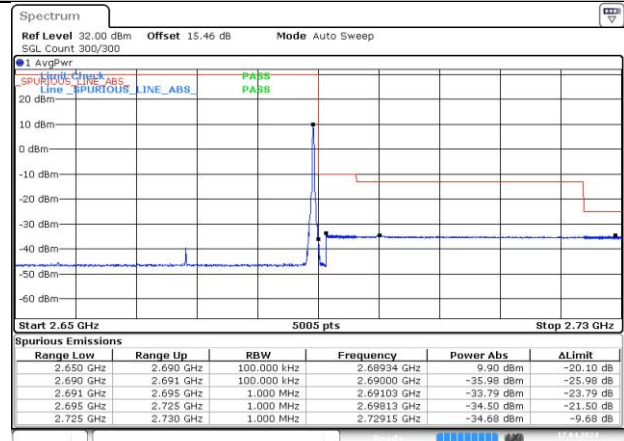
n41A / 15KHz / 35MHz

LCH / CP-QPSK / Edge\_1RB\_Left



Date: 17 APR 2024 10:38:18

HCH / CP-QPSK / Edge\_1RB\_Right



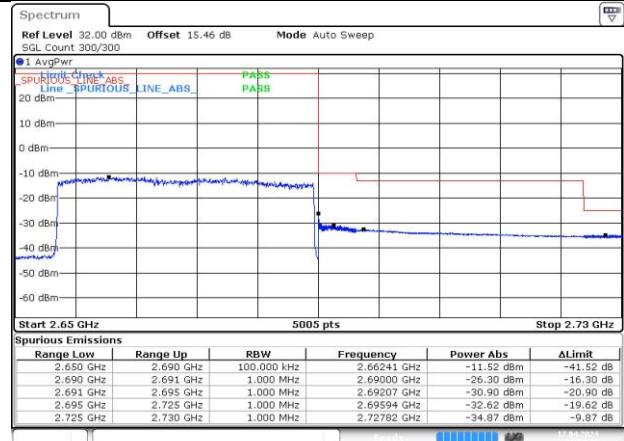
Date: 17 APR 2024 10:39:08

LCH / CP-QPSK / Outer\_Full



Date: 17 APR 2024 10:38:39

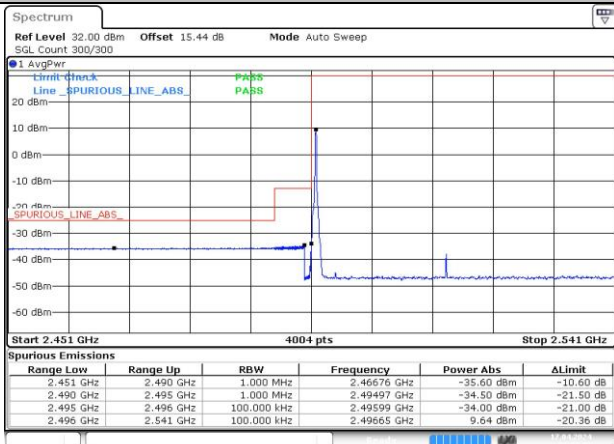
HCH / CP-QPSK / Outer\_Full



Date: 17 APR 2024 10:39:26

n41A / 15KHz / 40MHz

LCH / CP-QPSK / Edge\_1RB\_Left



Date: 17 APR 2024 10:40:06

HCH / CP-QPSK / Edge\_1RB\_Right



Date: 17 APR 2024 10:40:57

