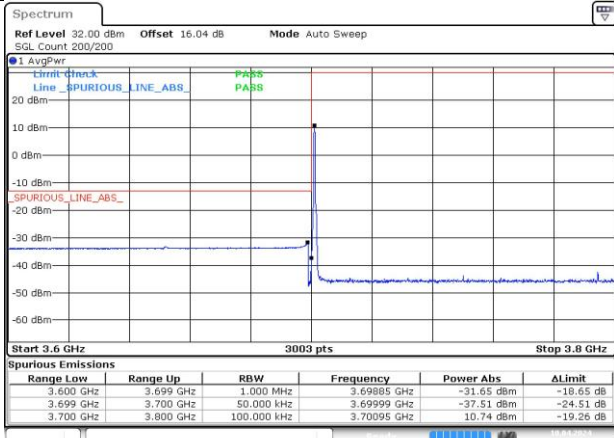


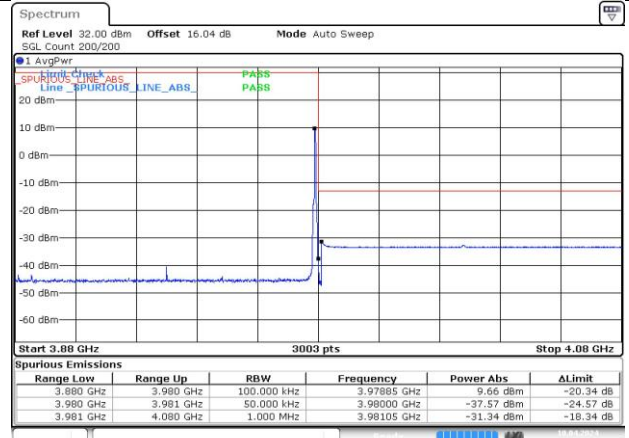
DC_7A_n77A / 30KHz / 100MHz

LCH / DFT-Pi2BPSK / Edge_1RB_Left



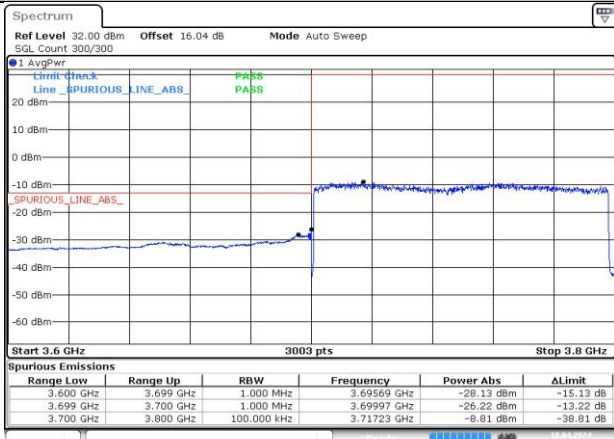
Date: 10 APR 2024 21:44:09

HCH / DFT-Pi2BPSK / Edge_1RB_Right



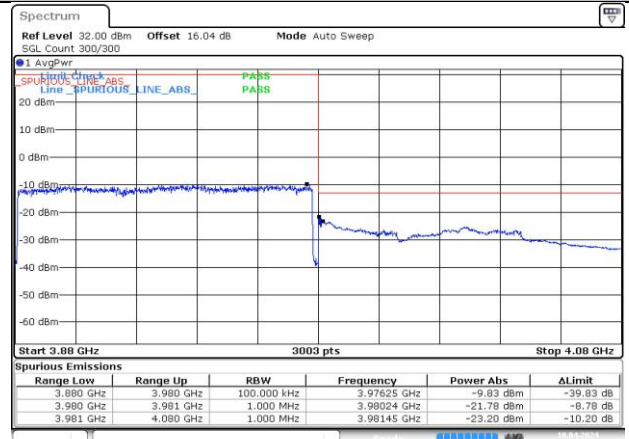
Date: 10 APR 2024 21:46:12

LCH / DFT-Pi2BPSK / Outer_Full



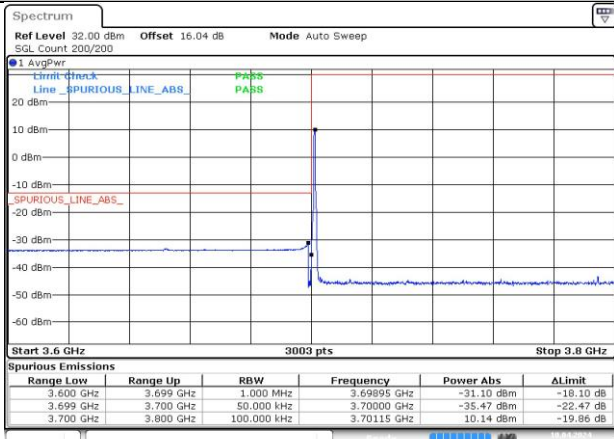
Date: 10 APR 2024 21:44:34

HCH / DFT-Pi2BPSK / Outer_Full



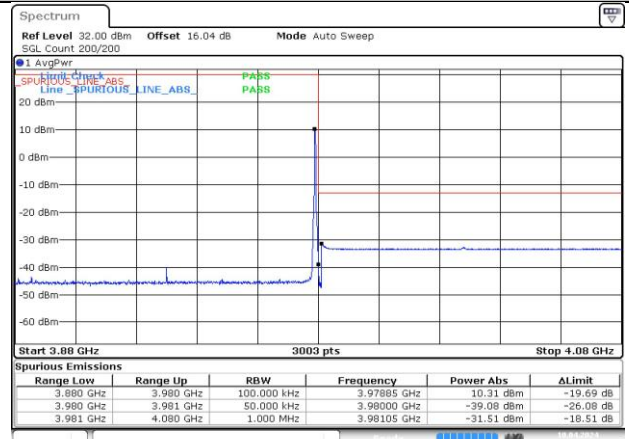
Date: 10 APR 2024 21:46:33

LCH / DFT-QPSK / Edge_1RB_Left

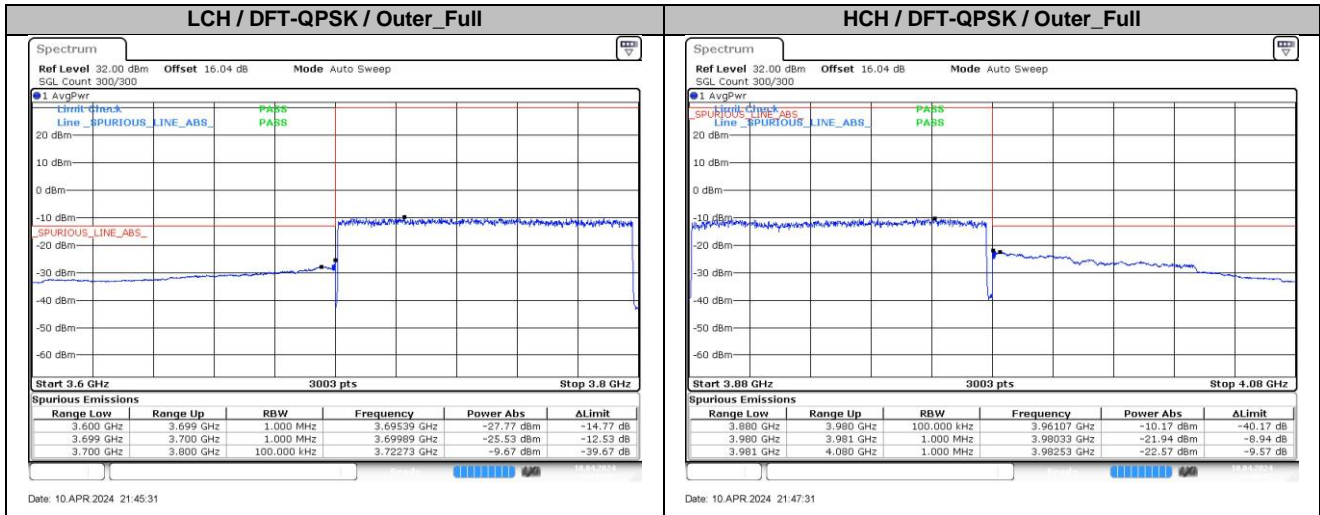


Date: 10 APR 2024 21:45:07

HCH / DFT-QPSK / Edge_1RB_Right

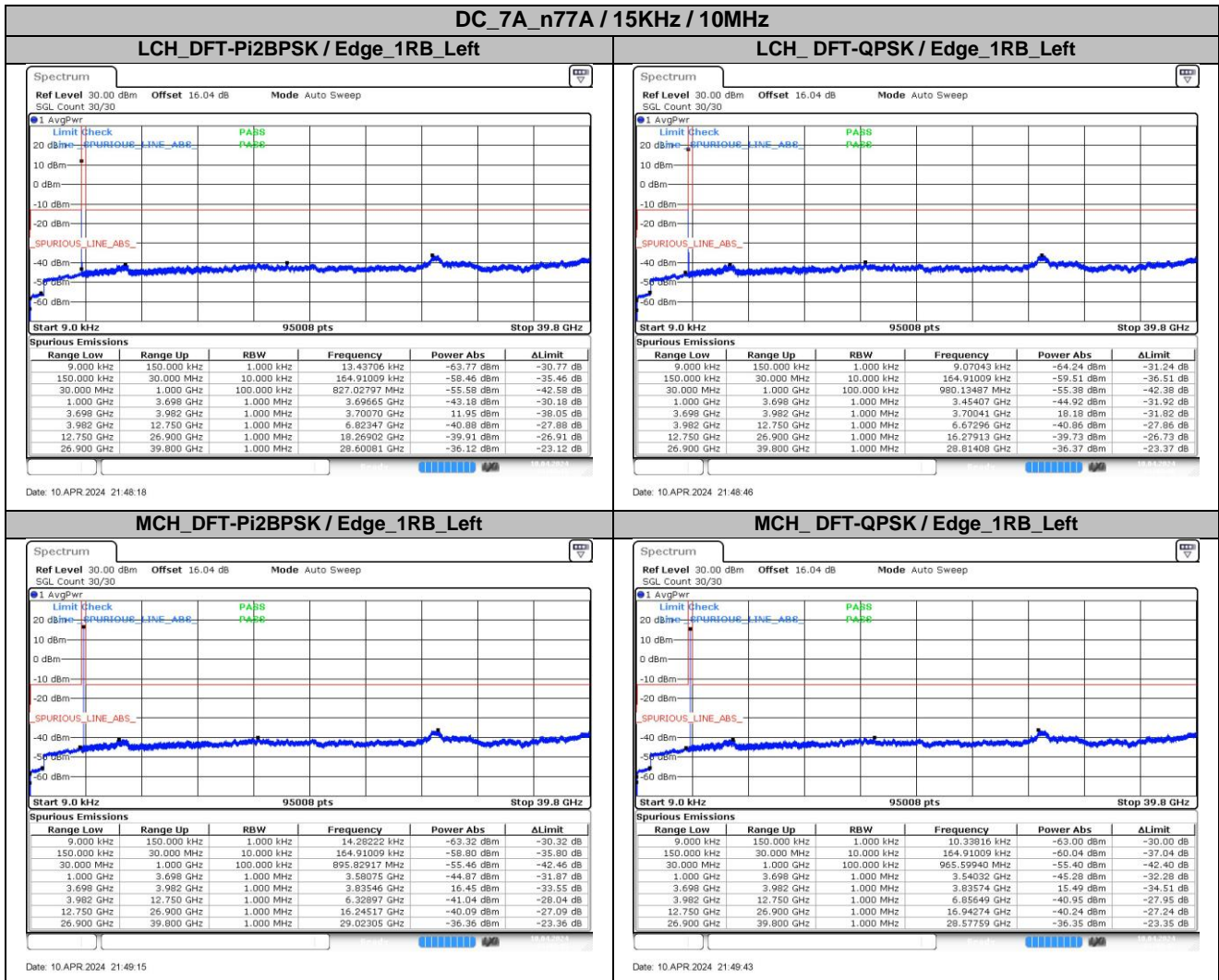


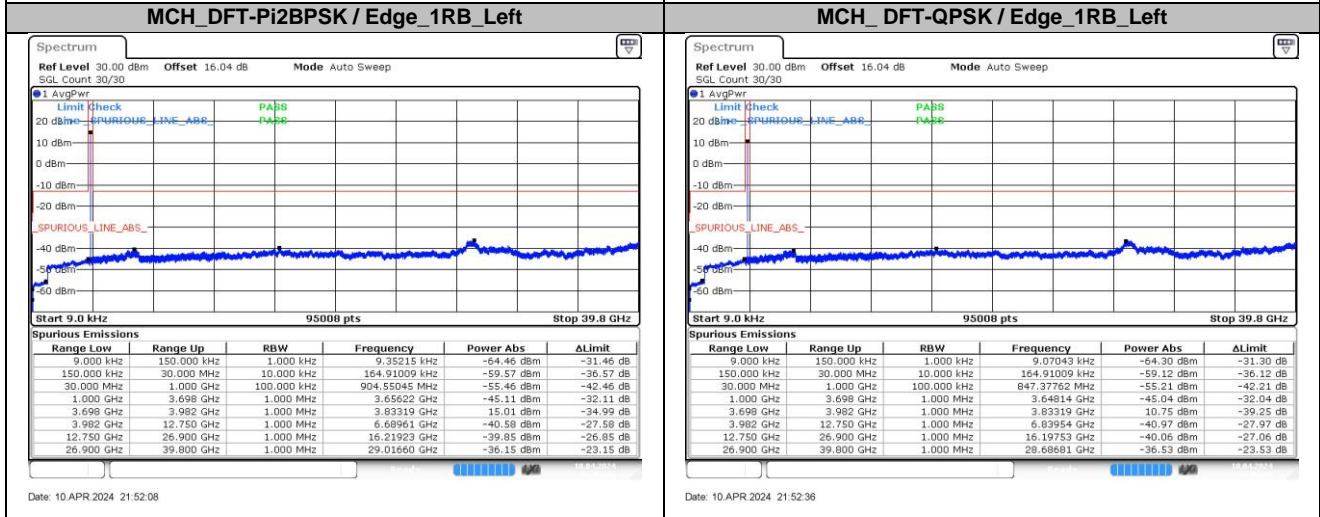
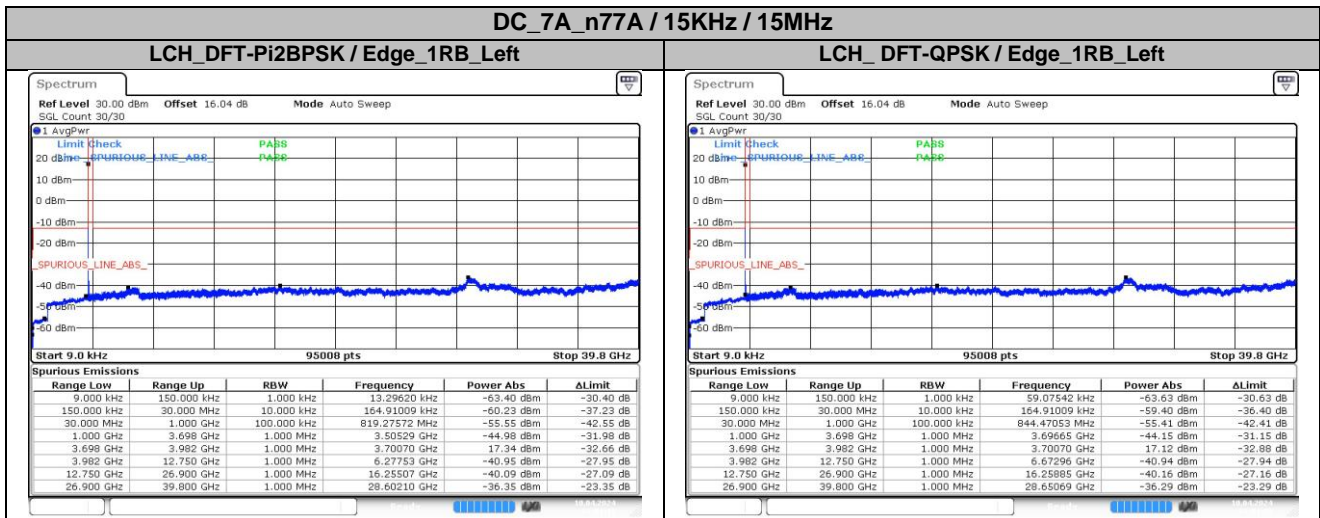
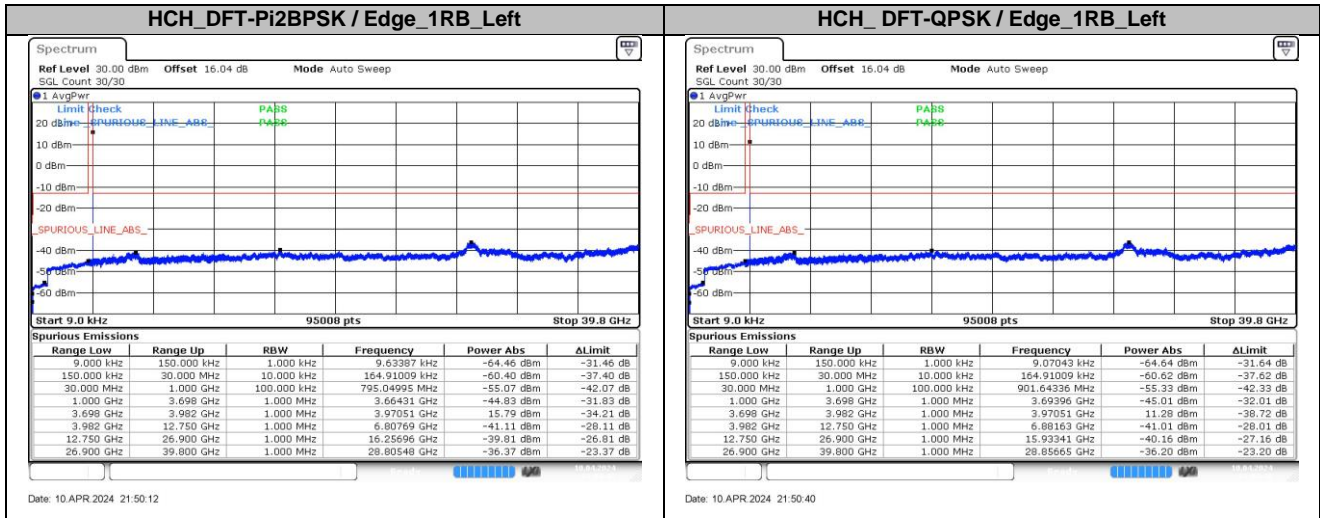
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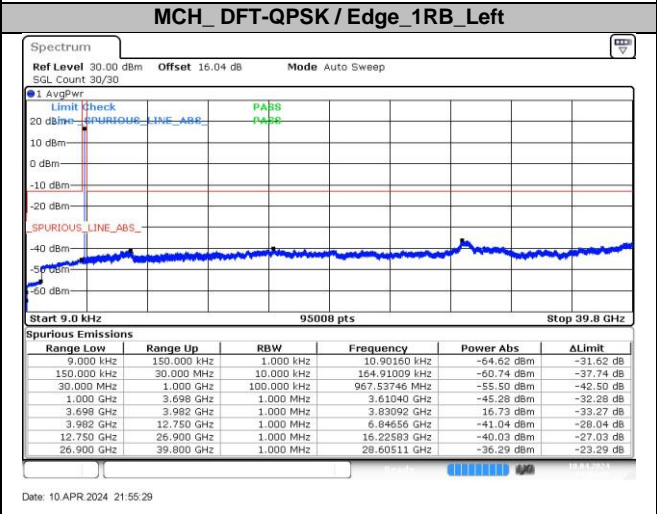
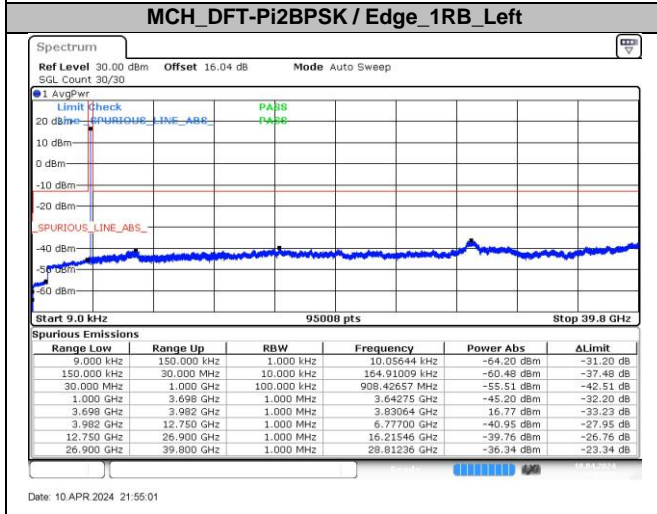
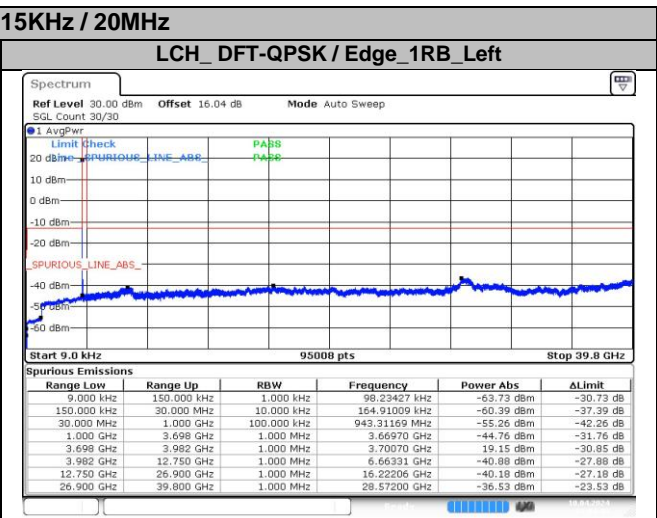
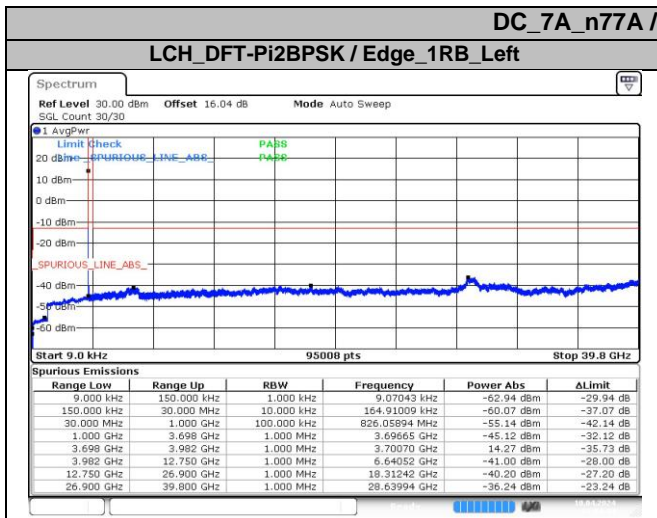
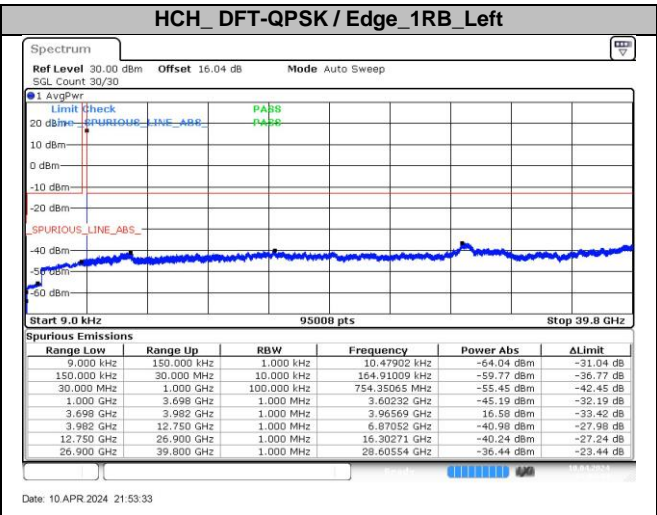
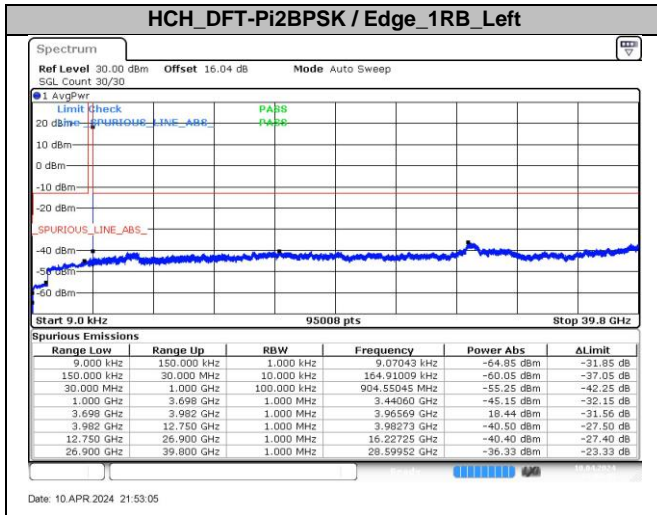


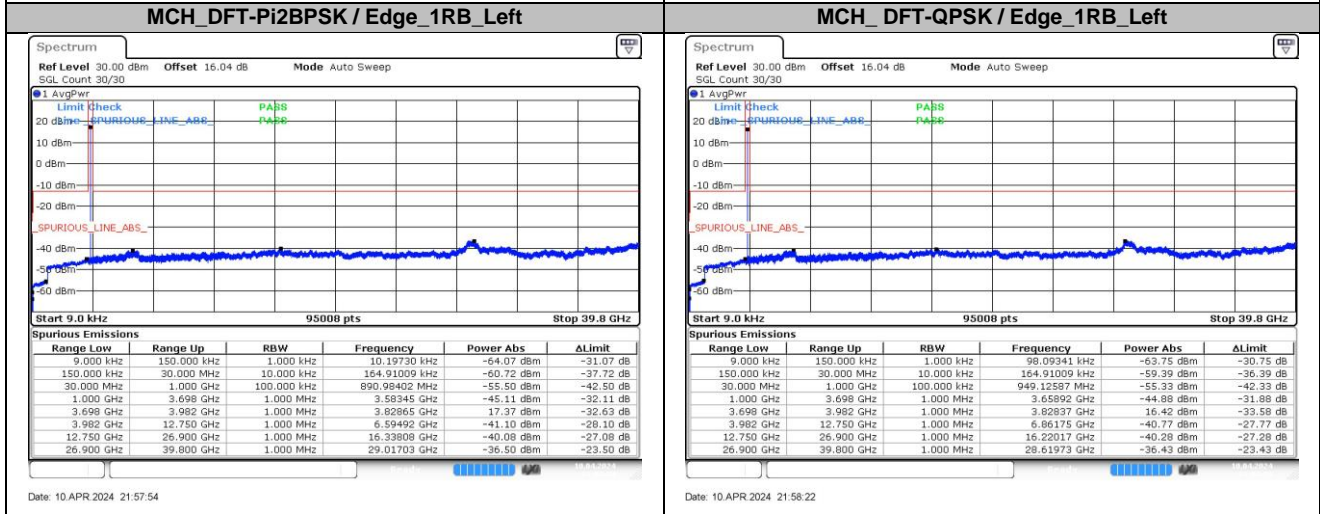
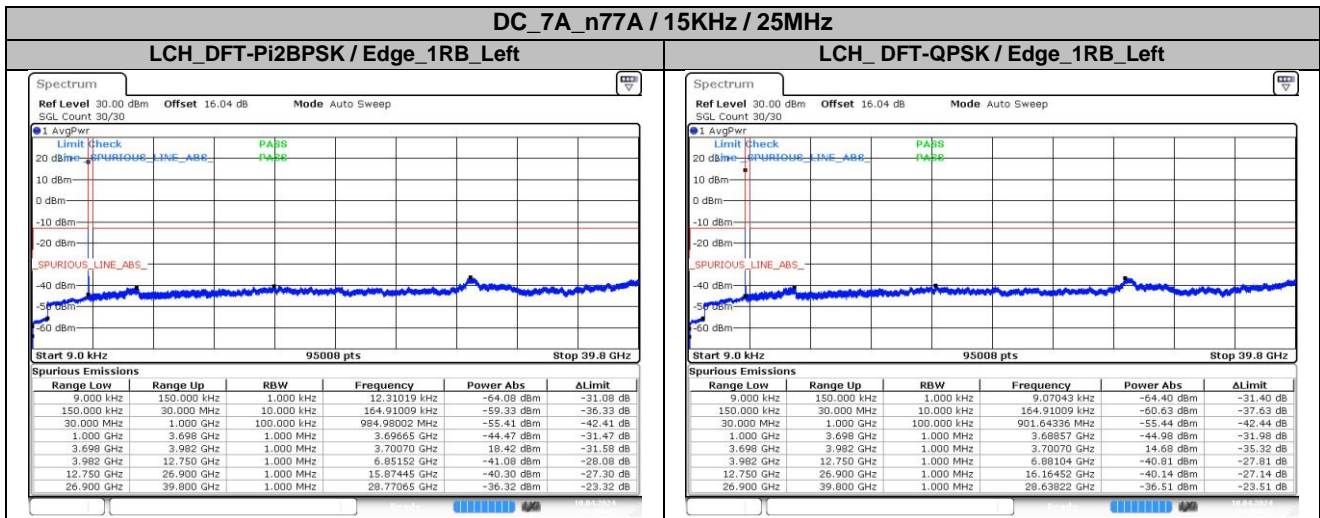
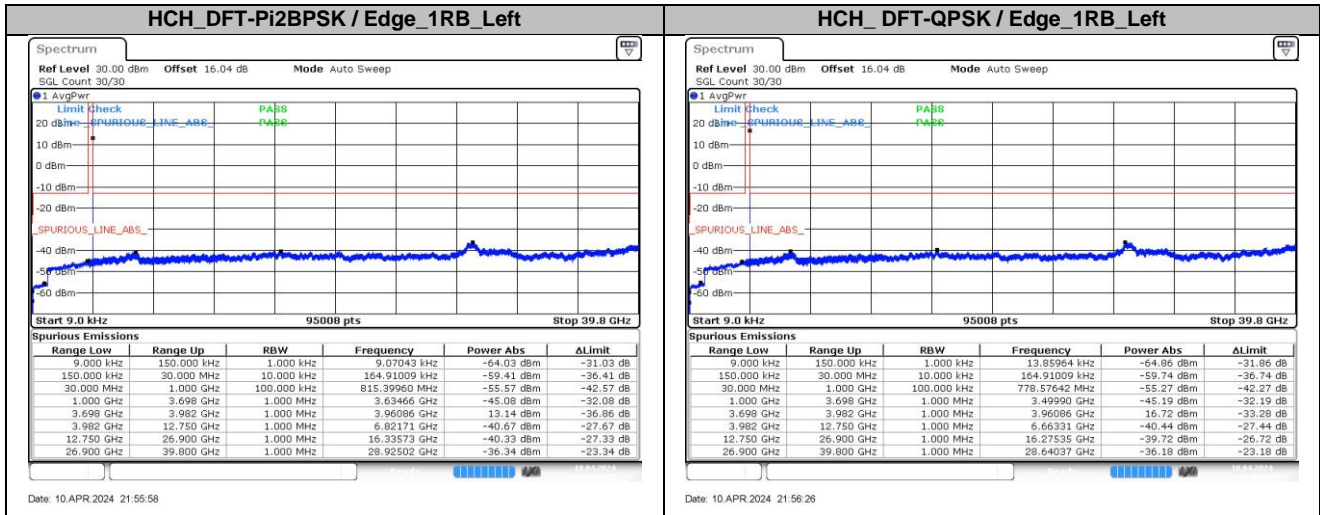
6. Conducted Spurious Emission

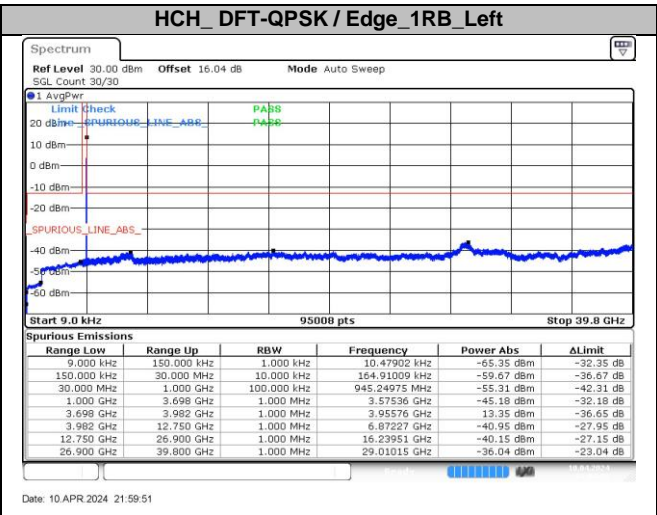
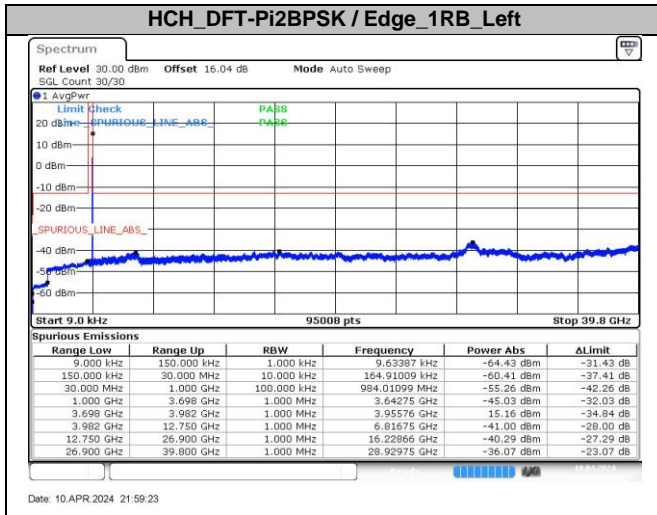
6.1. Test Plots











DC_7A_n77A / 15KHz / 30MHz

