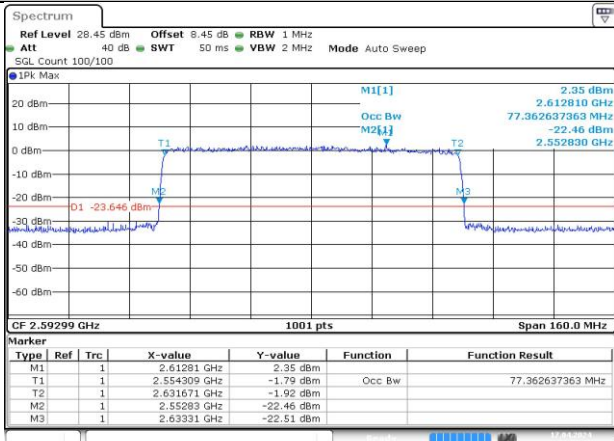
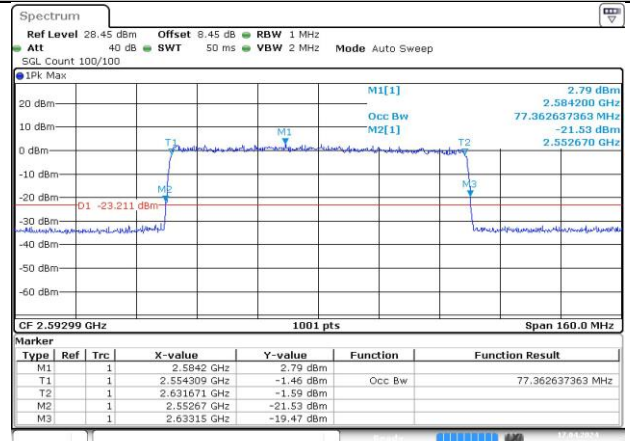


n41A / 30KHz / 80MHz

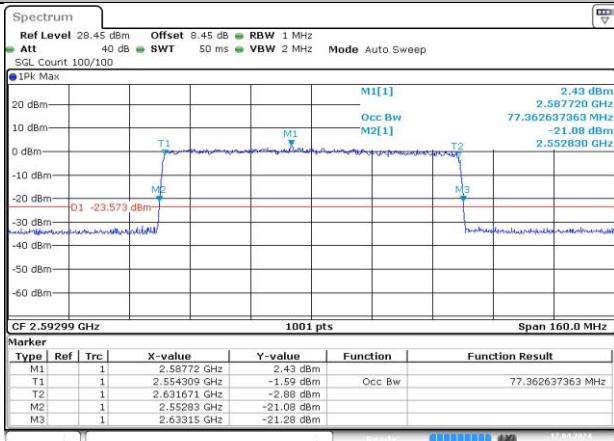
MCH / CP-QPSK / Outer\_Full



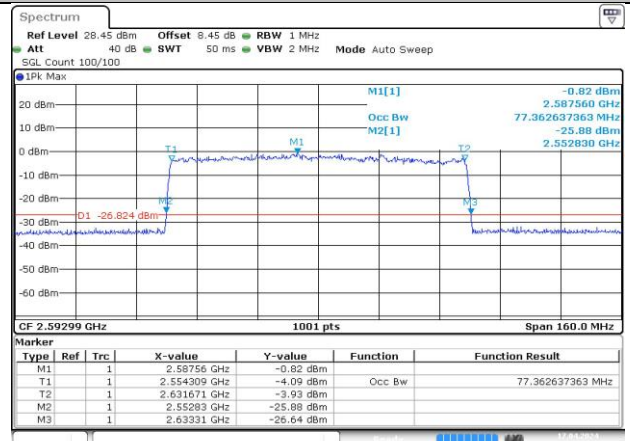
MCH / CP-16QAM / Outer\_Full



MCH / CP-64QAM / Outer\_Full

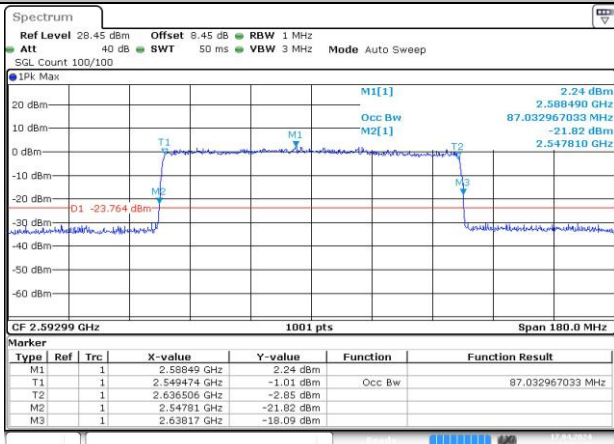


MCH / CP-256QAM / Outer\_Full

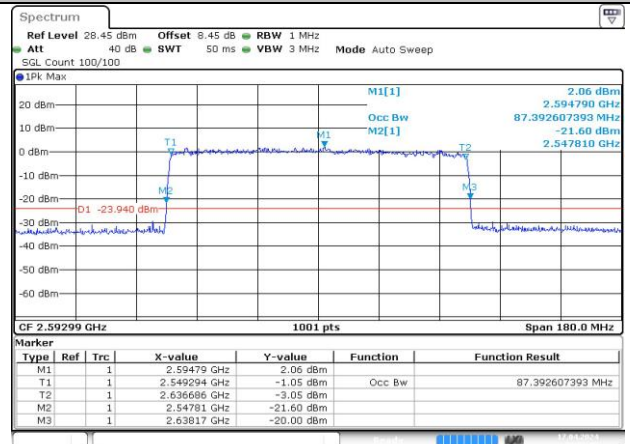


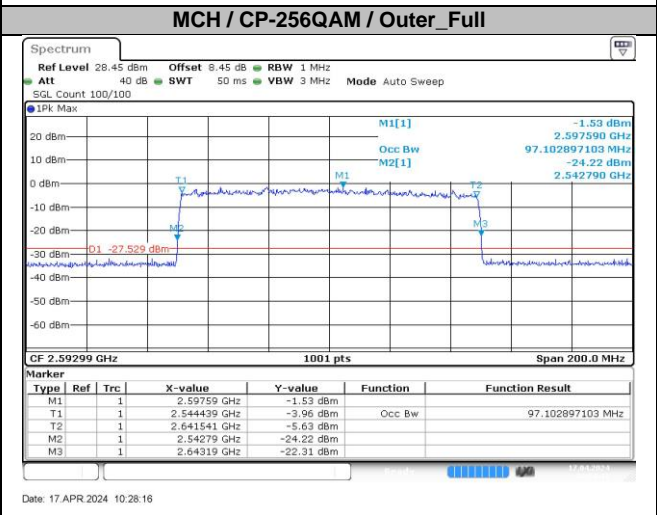
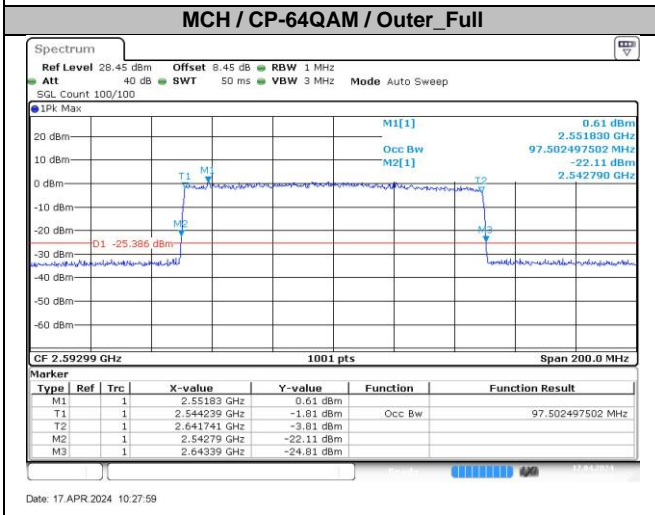
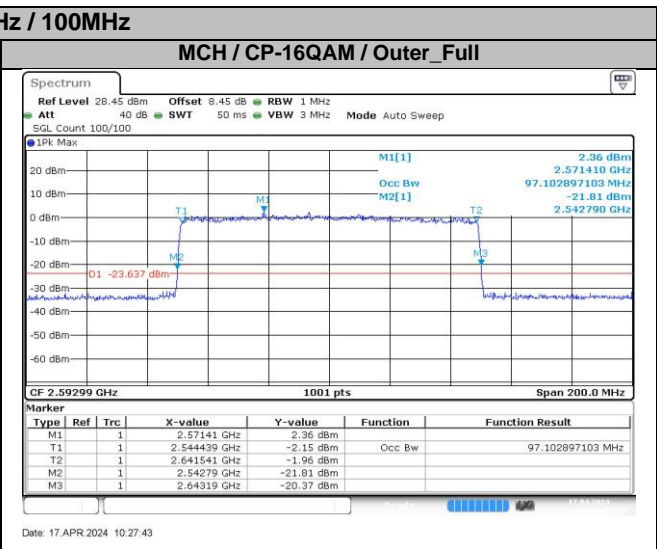
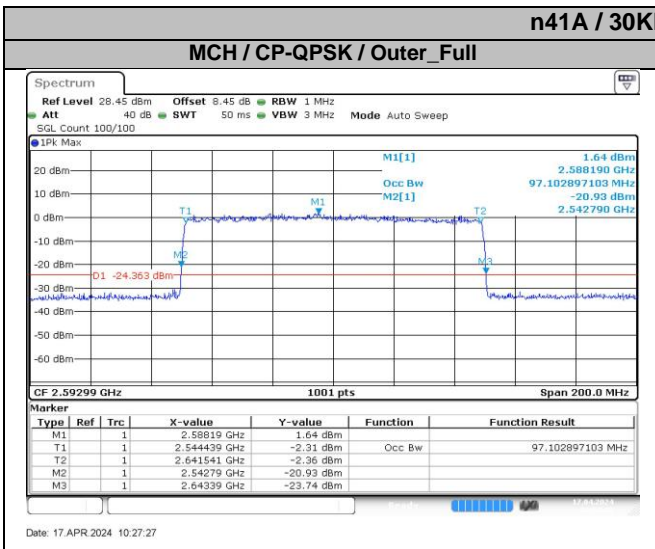
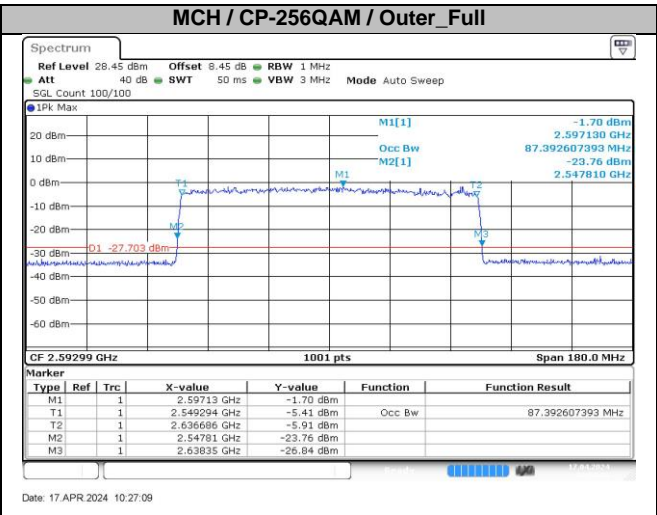
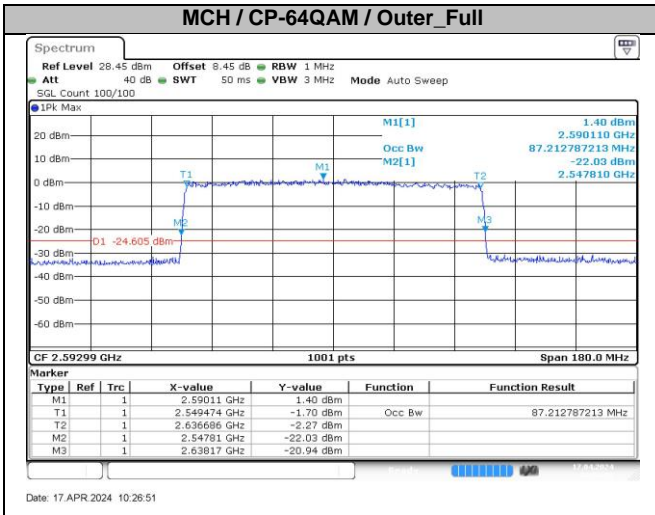
n41A / 30KHz / 90MHz

MCH / CP-QPSK / Outer\_Full



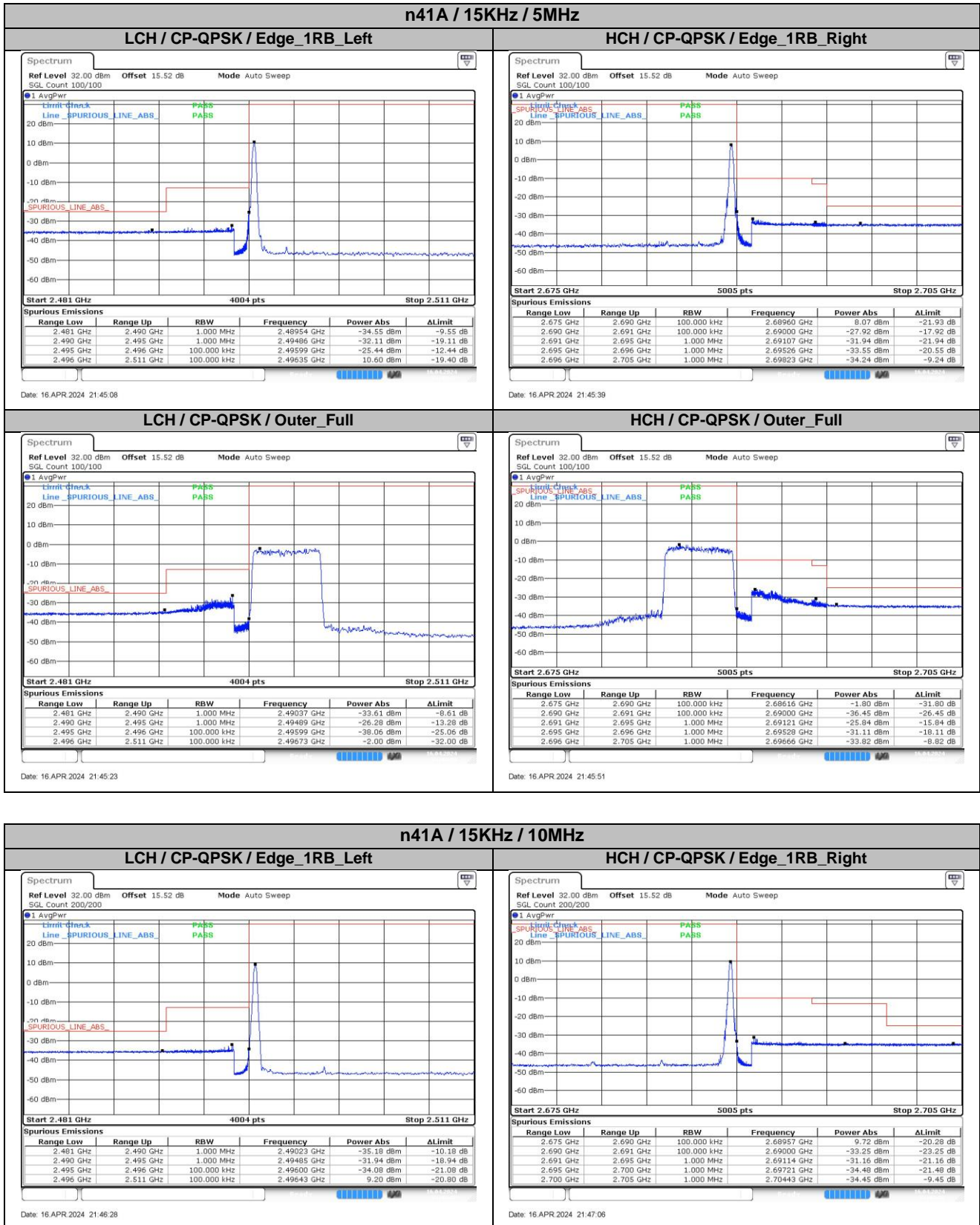
MCH / CP-16QAM / Outer\_Full

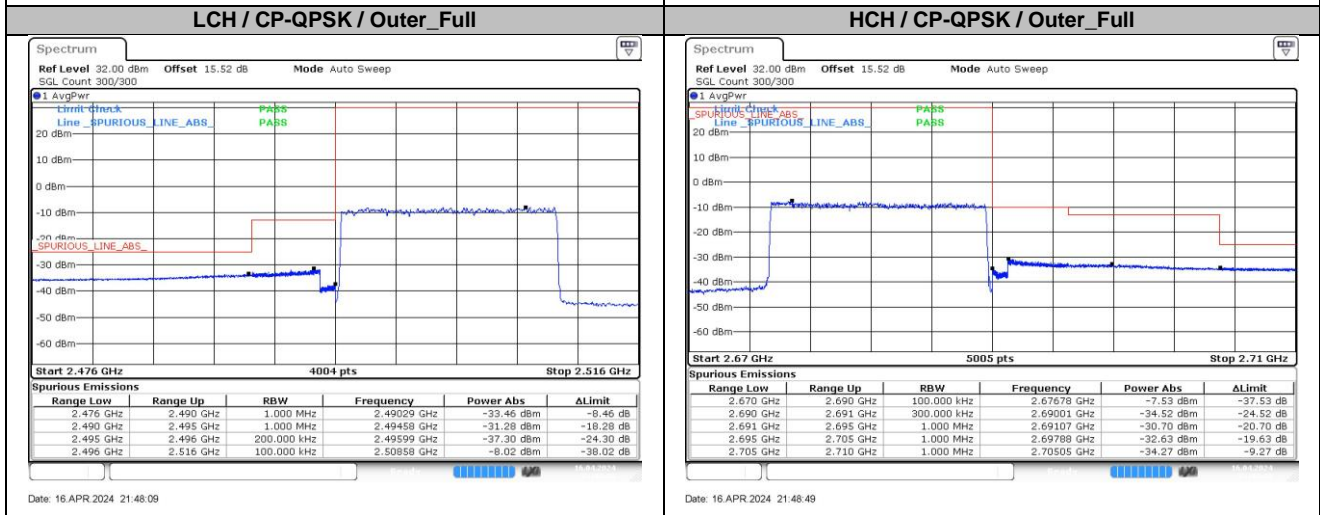
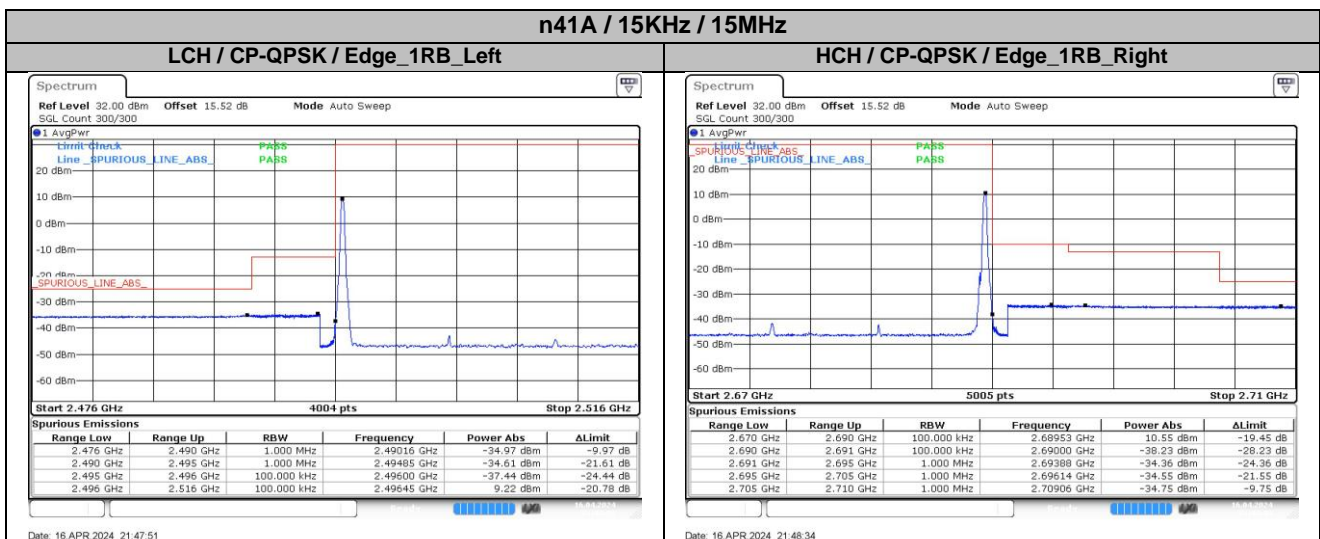
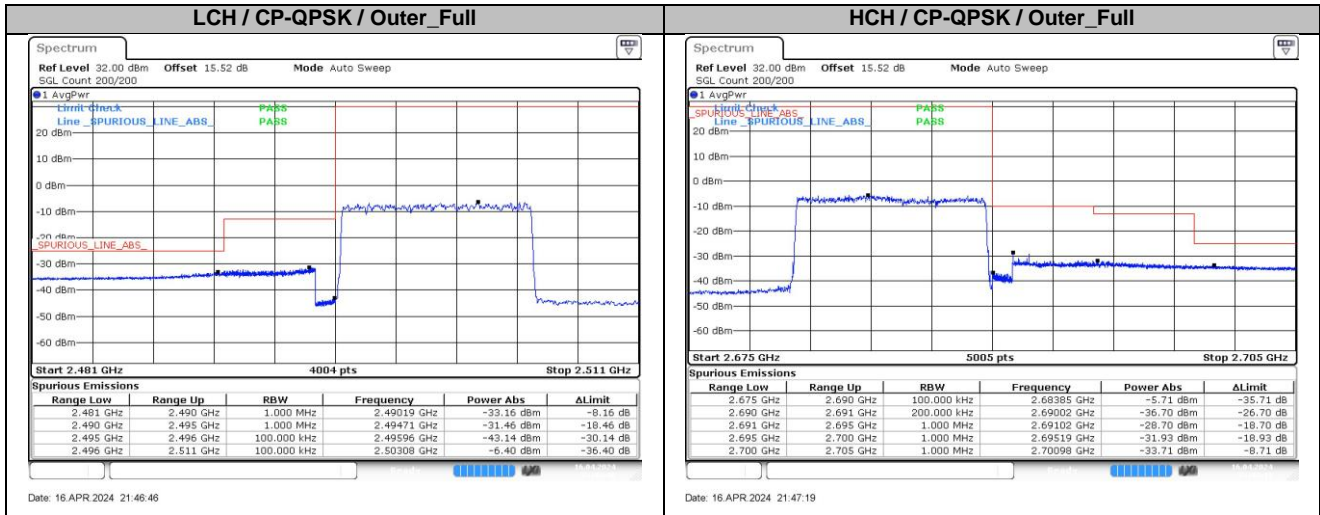


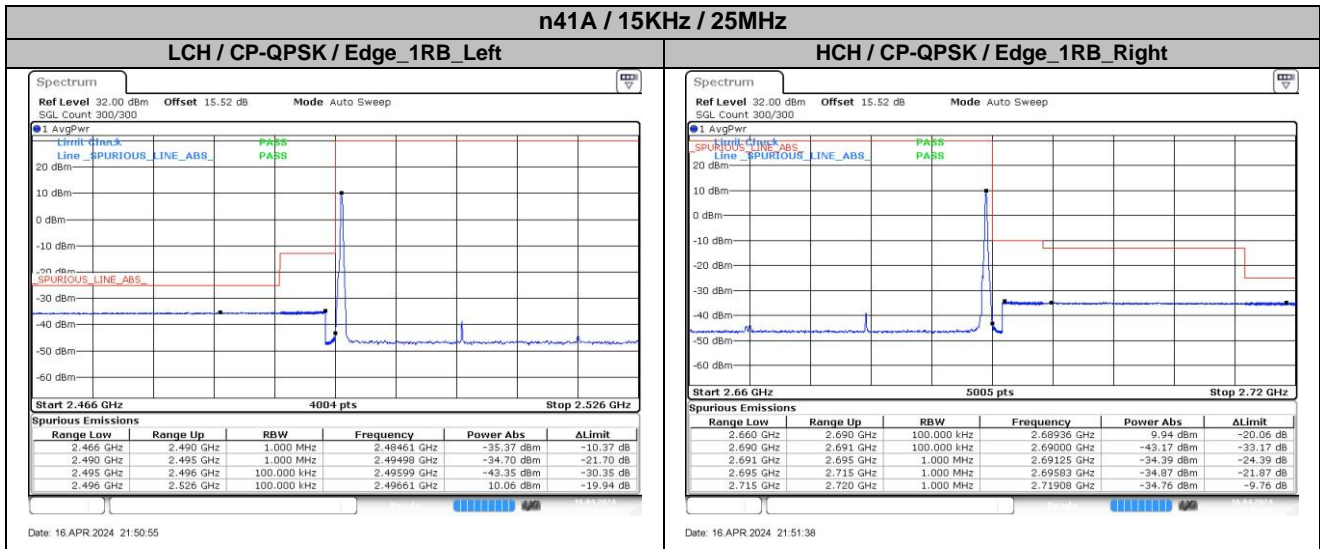
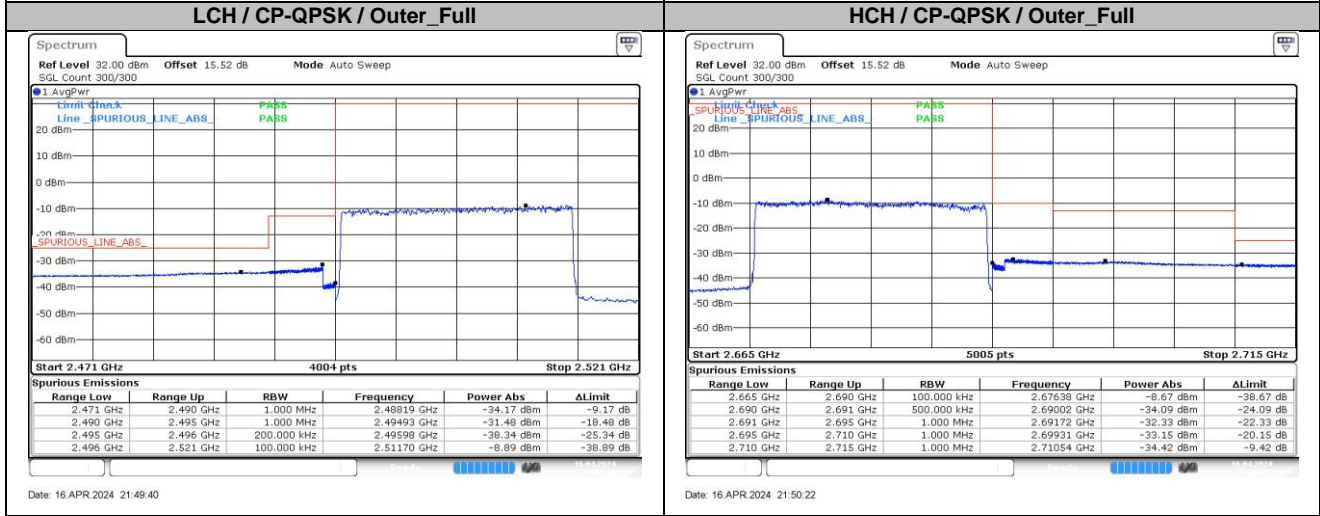
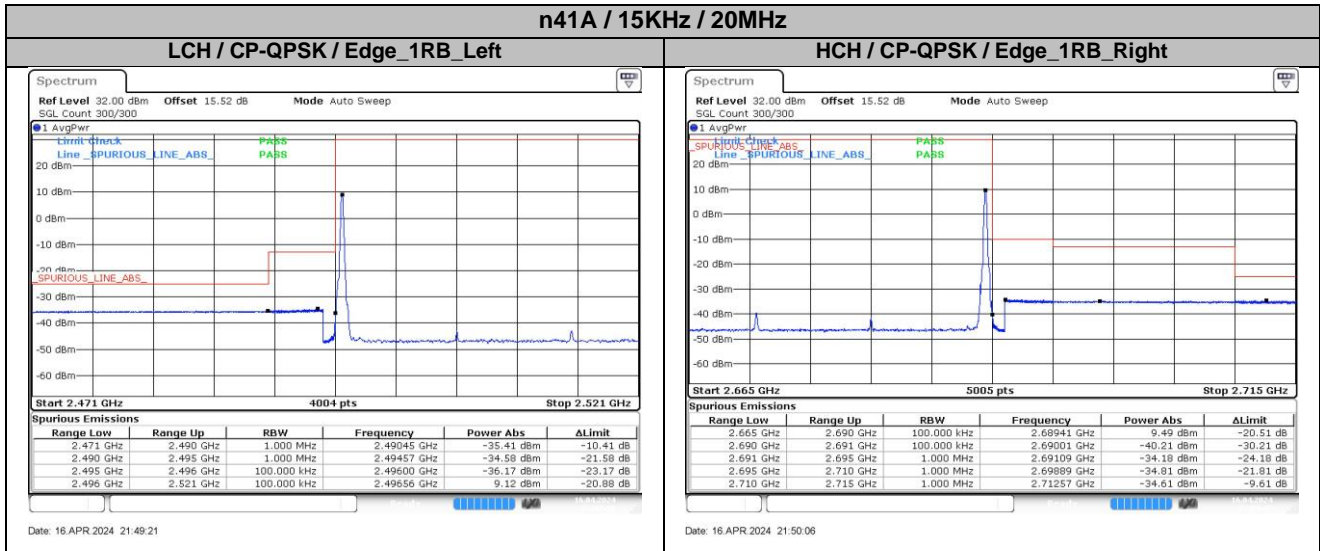


### 5. Conducted Band Edges

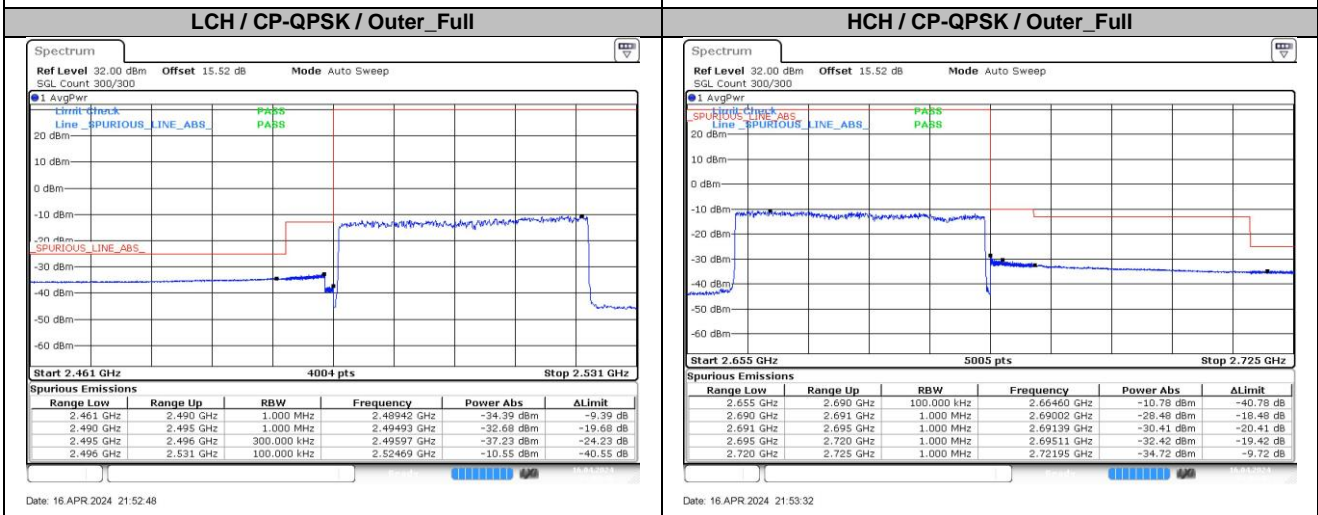
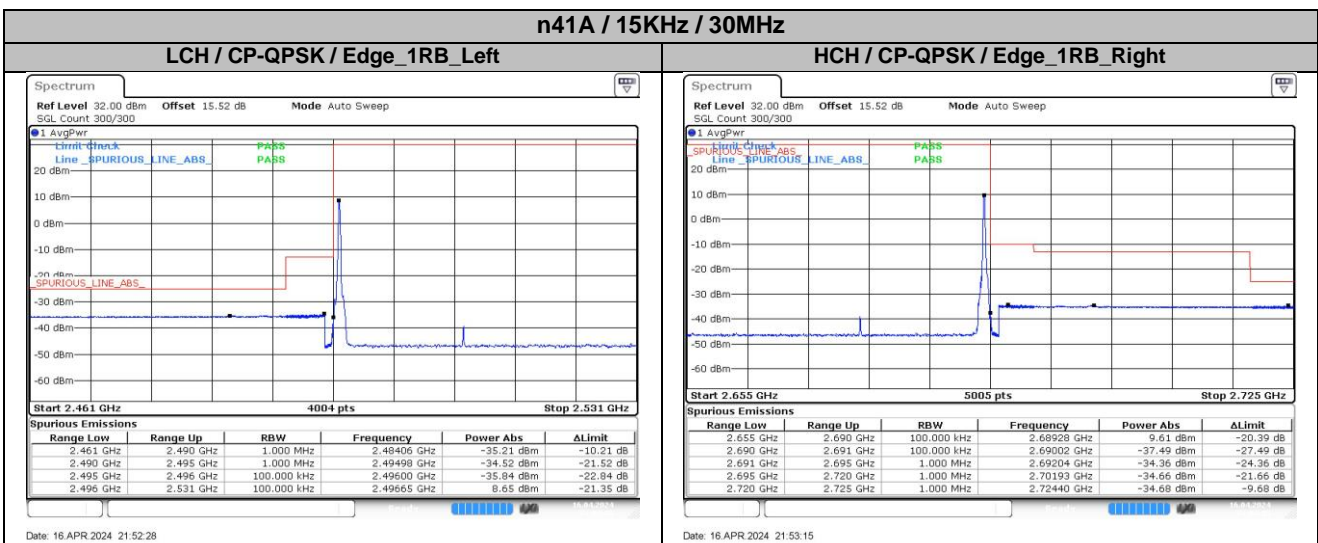
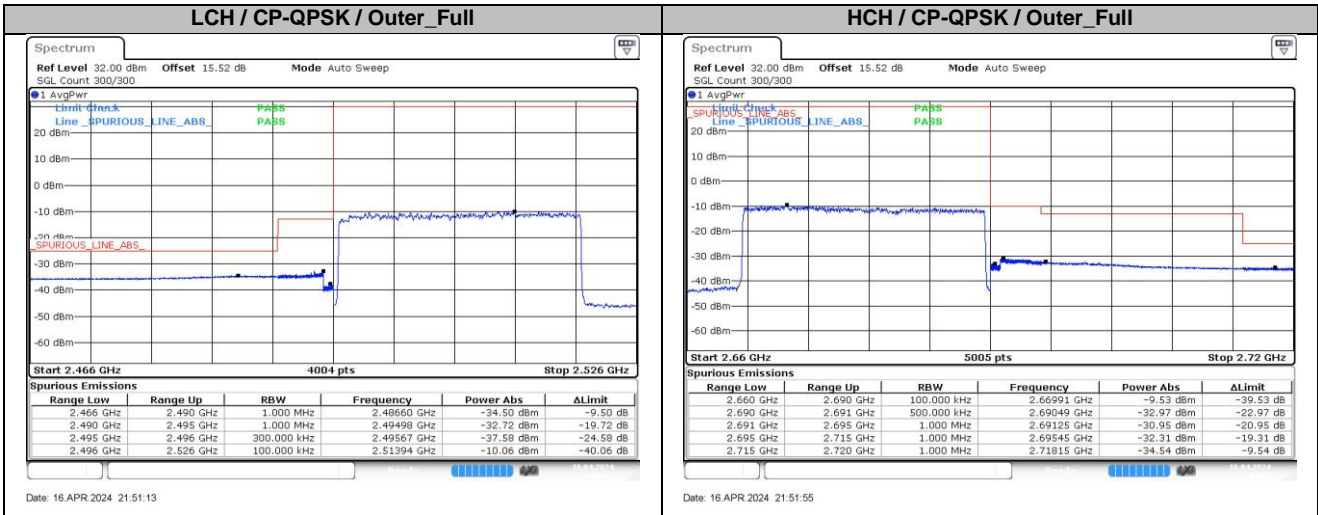
#### 5.1. Test Plots for Ant2











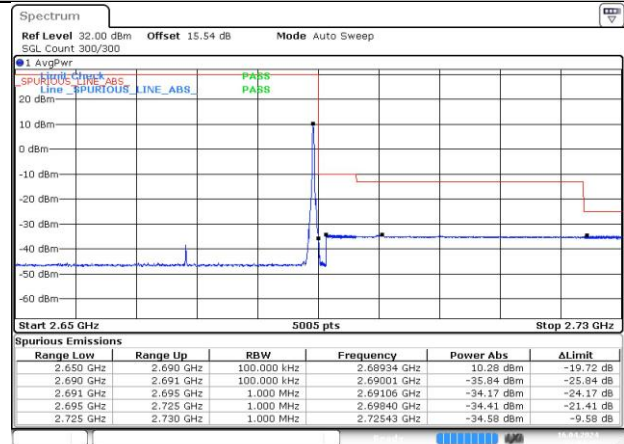
n41A / 15KHz / 35MHz

LCH / CP-QPSK / Edge\_1RB\_Left



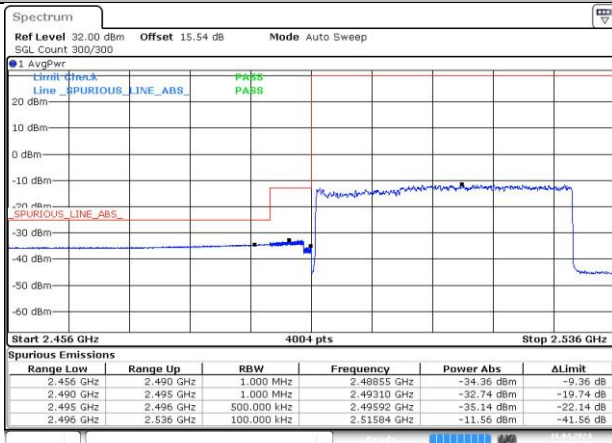
Date: 16 APR 2024 21:54:08

HCH / CP-QPSK / Edge\_1RB\_Right



Date: 16 APR 2024 21:54:57

LCH / CP-QPSK / Outer\_Full



Date: 16 APR 2024 21:54:29

HCH / CP-QPSK / Outer\_Full



Date: 16 APR 2024 21:55:15

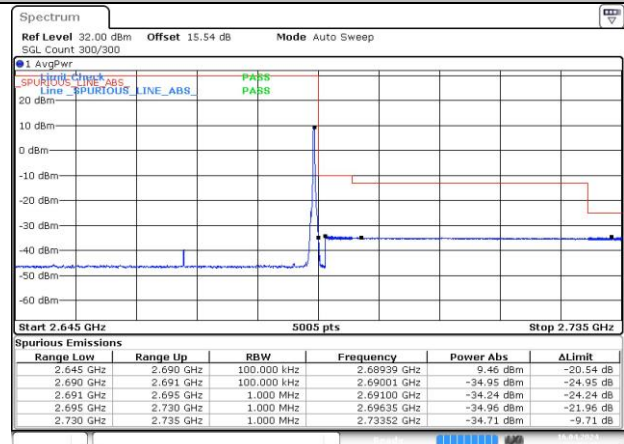
n41A / 15KHz / 40MHz

LCH / CP-QPSK / Edge\_1RB\_Left



Date: 16 APR 2024 21:55:55

HCH / CP-QPSK / Edge\_1RB\_Right



Date: 16 APR 2024 21:56:46

