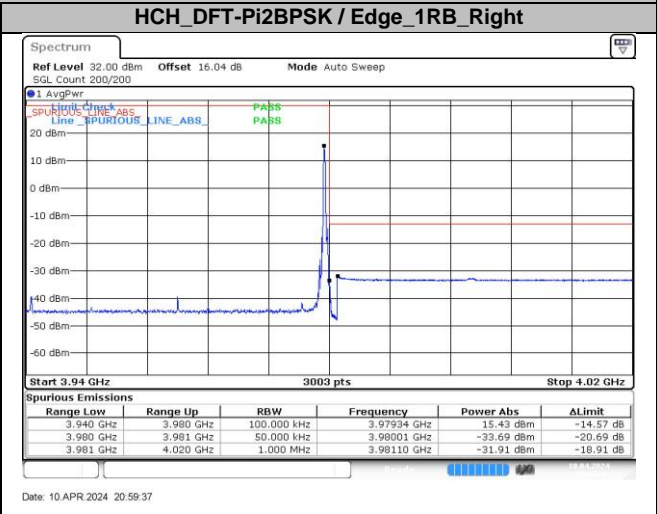
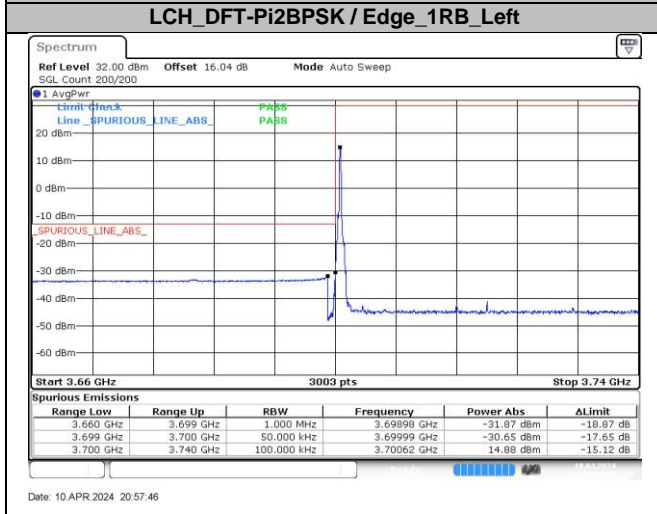
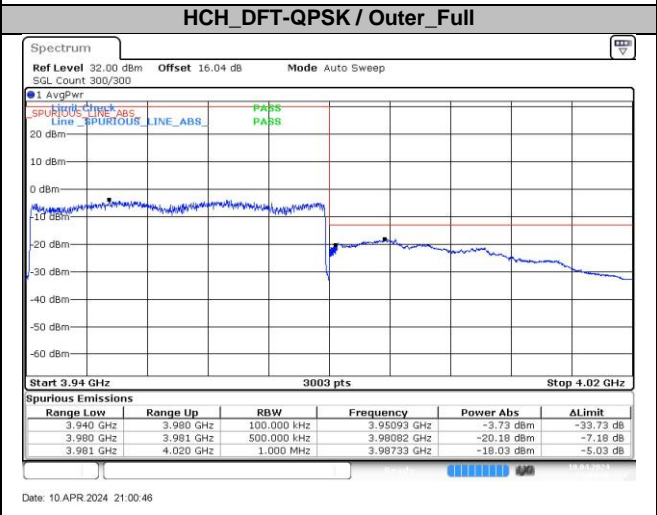
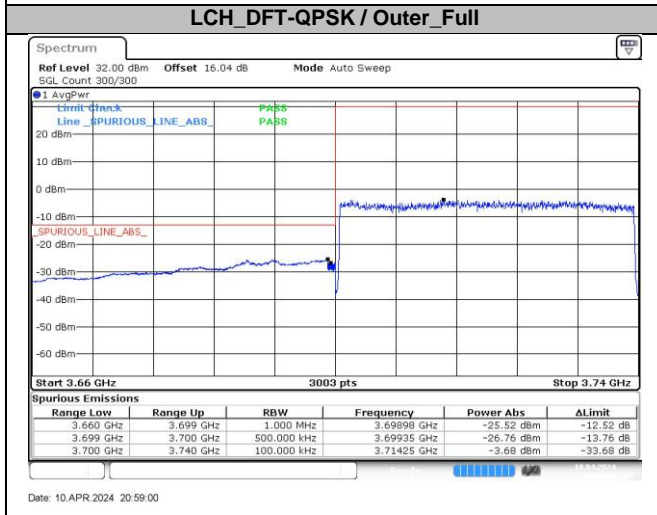
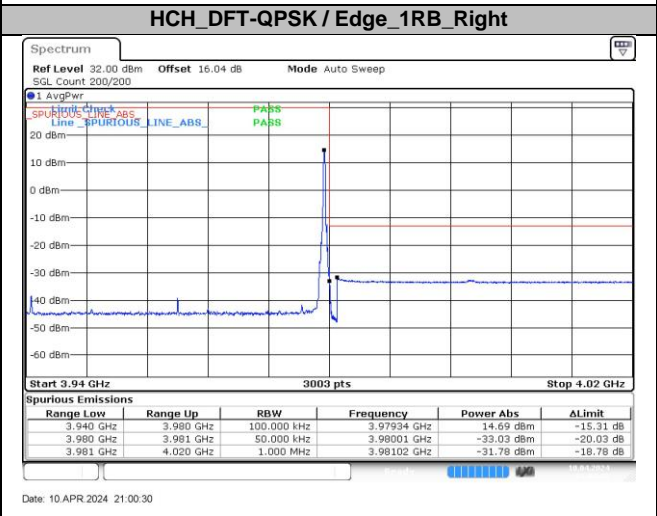
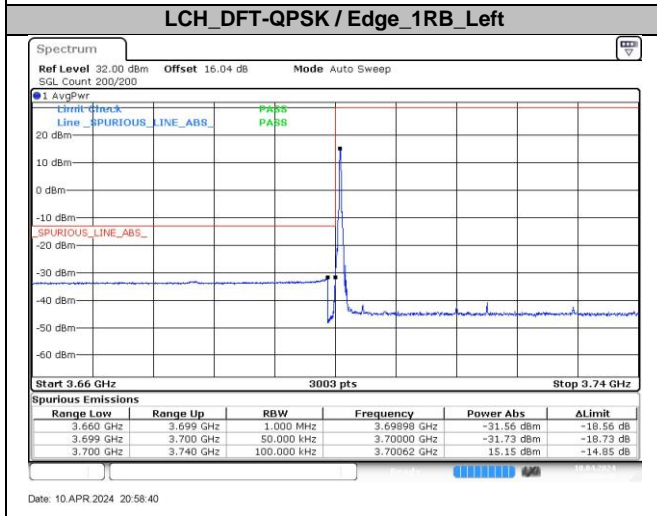
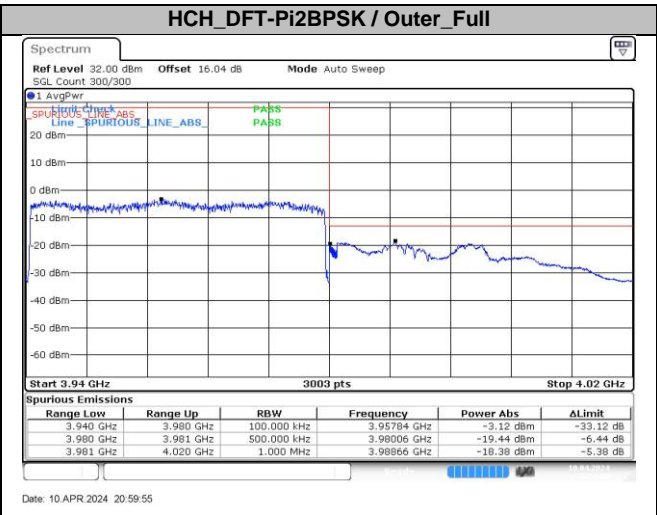
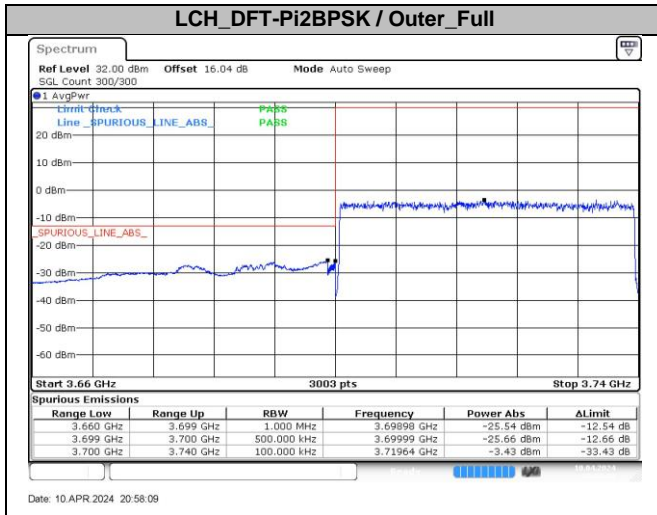


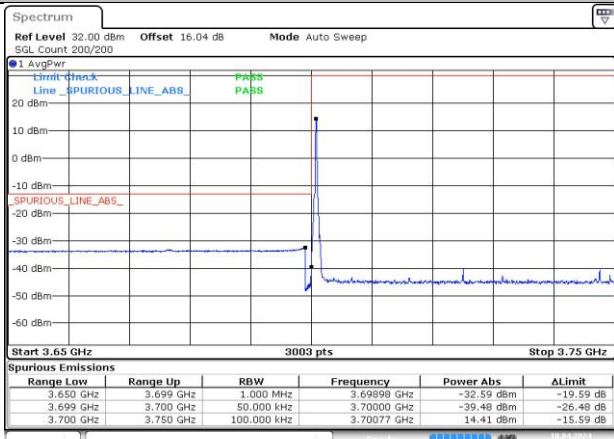
DC_7A_n77A / 15KHz / 40MHz





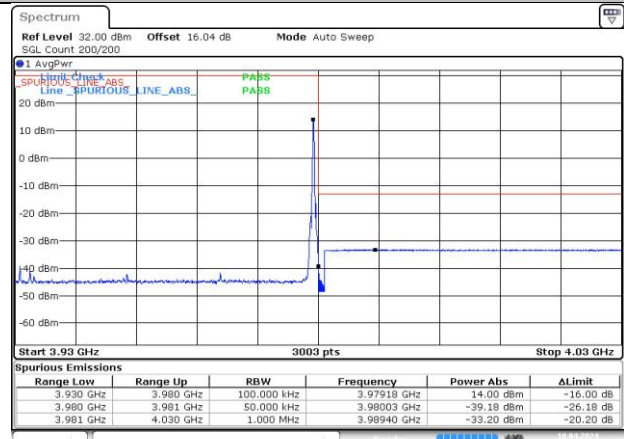
DC_7A_n77A / 15KHz / 50MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



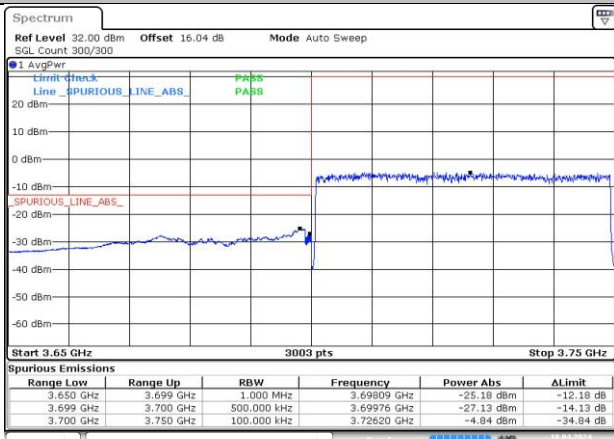
Date: 10 APR 2024 21:01:20

HCH_DFT-Pi2BPSK / Edge_1RB_Right



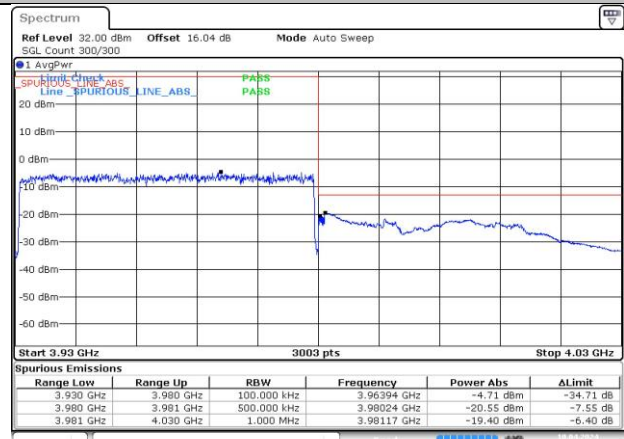
Date: 10 APR 2024 21:03:15

LCH_DFT-Pi2BPSK / Outer_Full



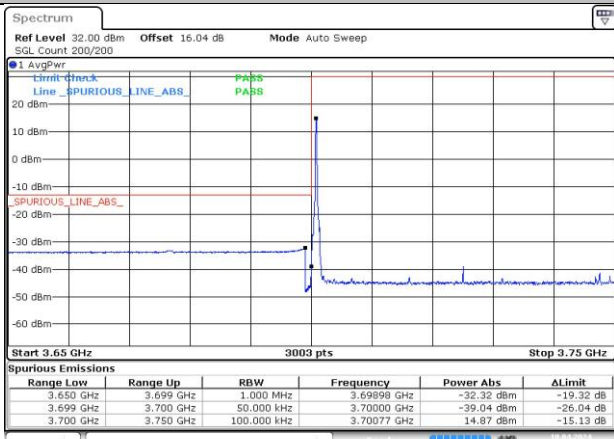
Date: 10 APR 2024 21:01:44

HCH_DFT-Pi2BPSK / Outer_Full



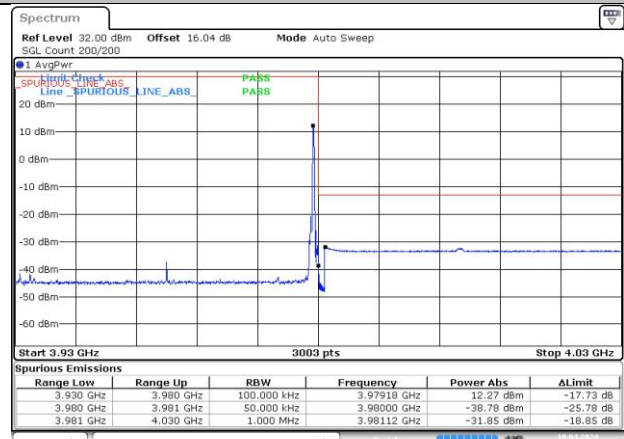
Date: 10 APR 2024 21:03:33

LCH_DFT-QPSK / Edge_1RB_Left

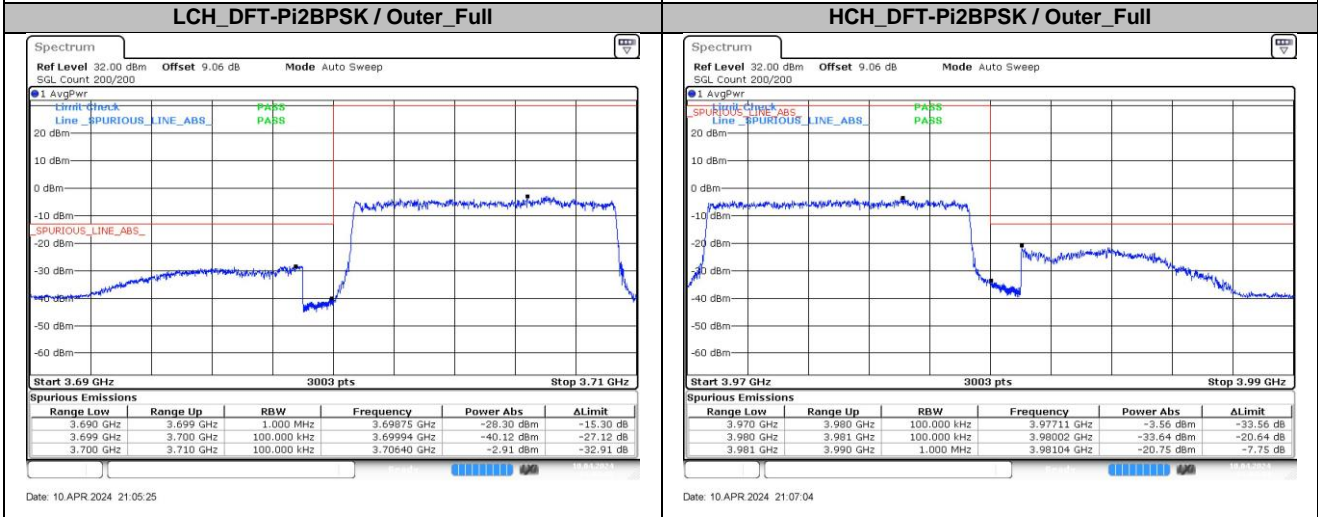
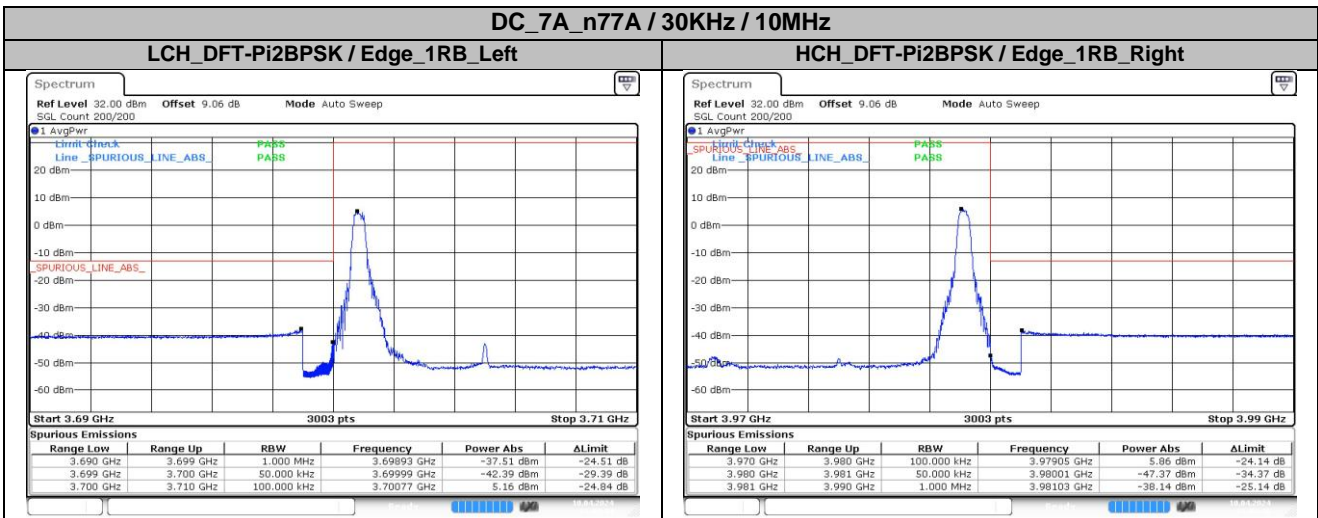
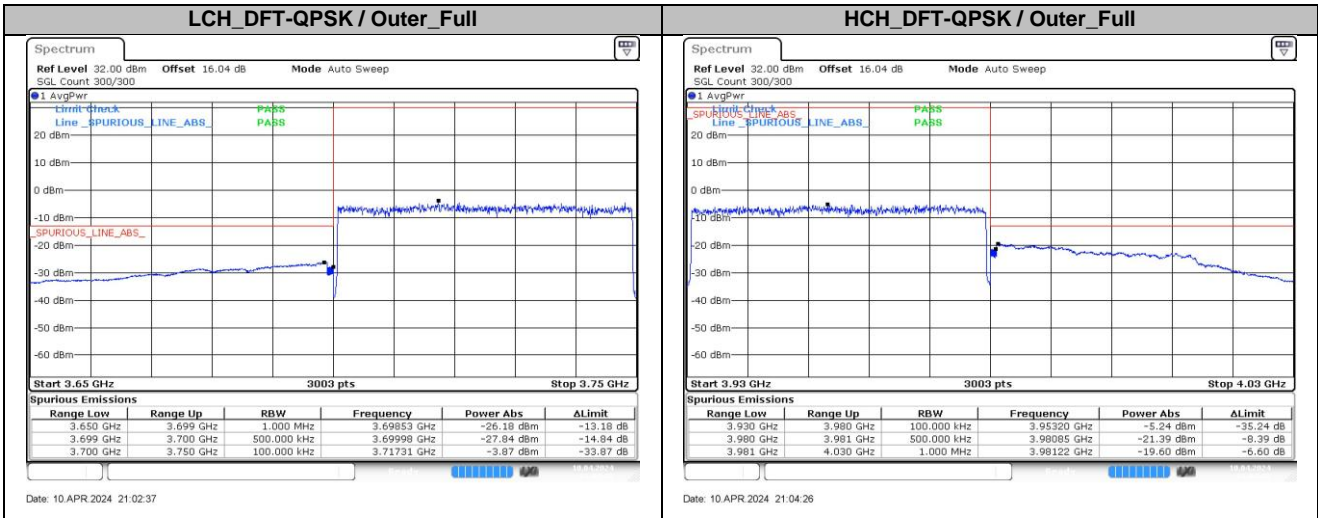


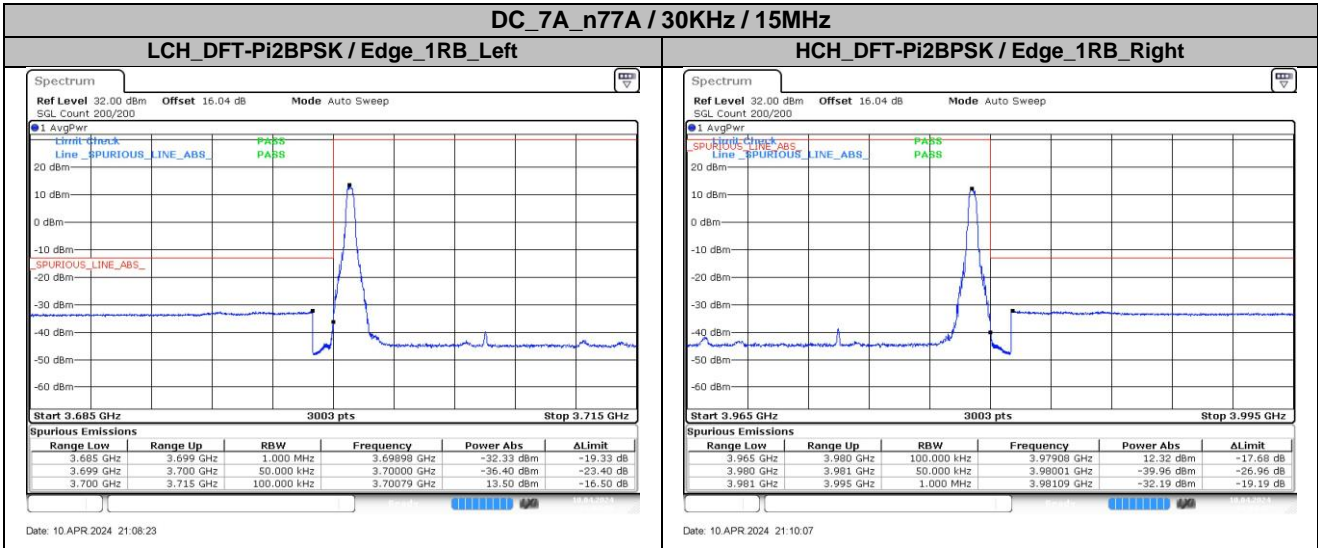
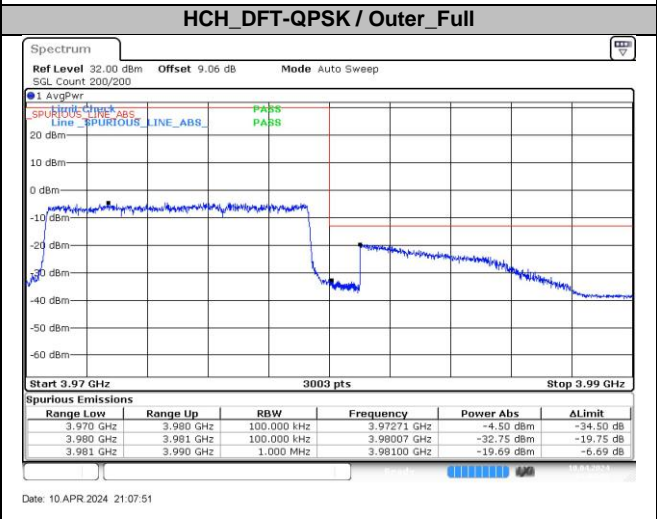
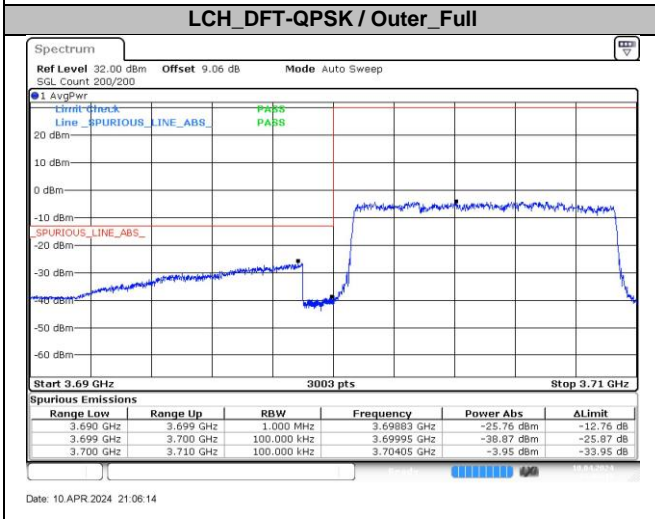
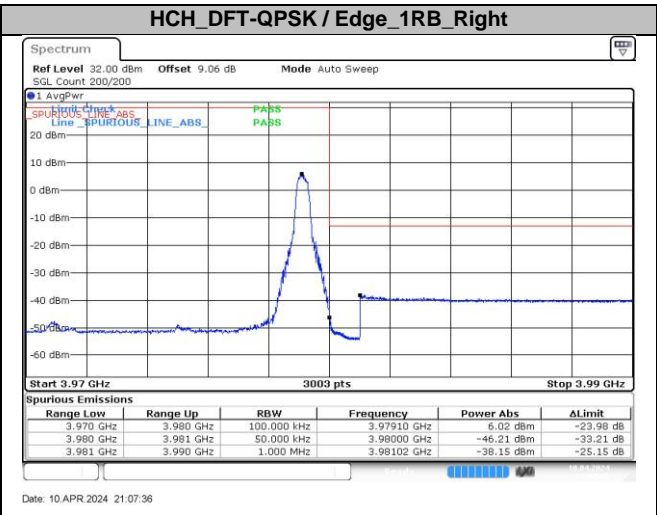
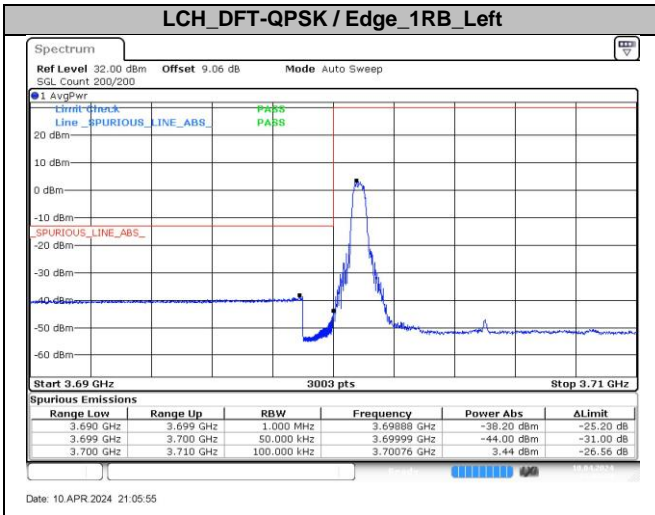
Date: 10 APR 2024 21:02:15

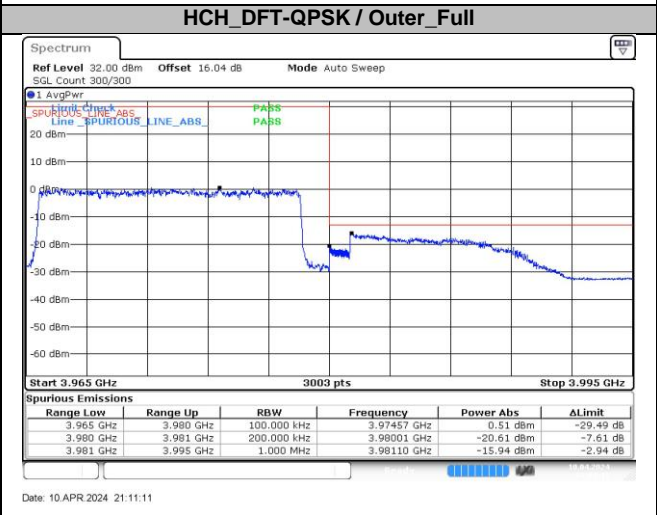
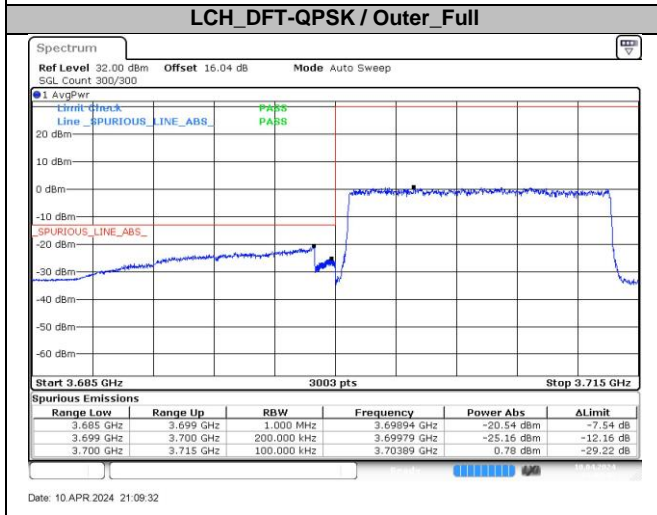
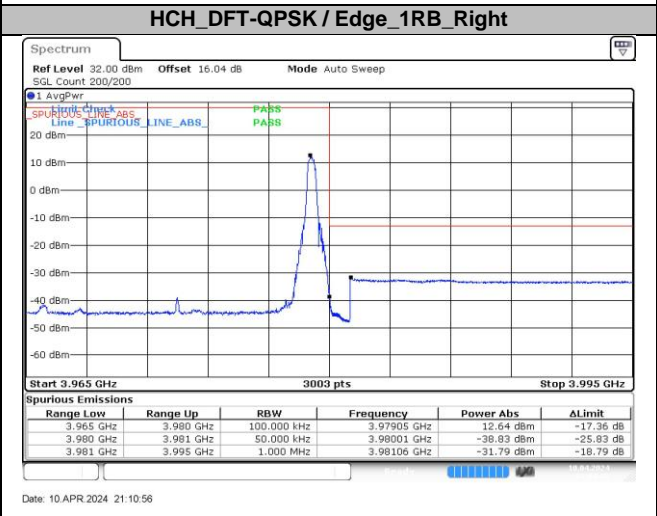
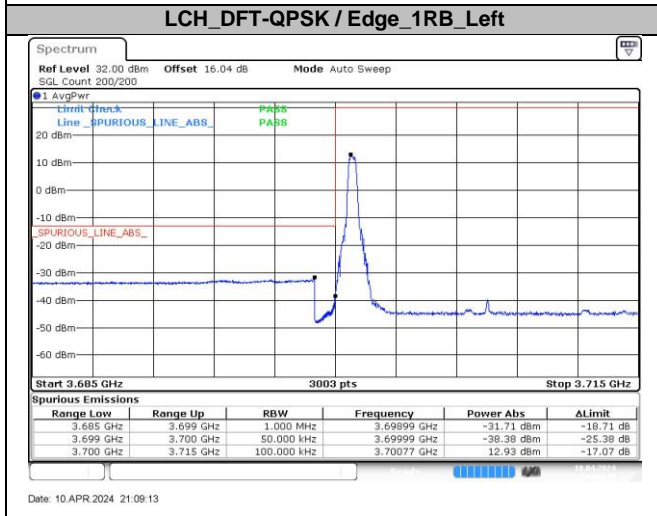
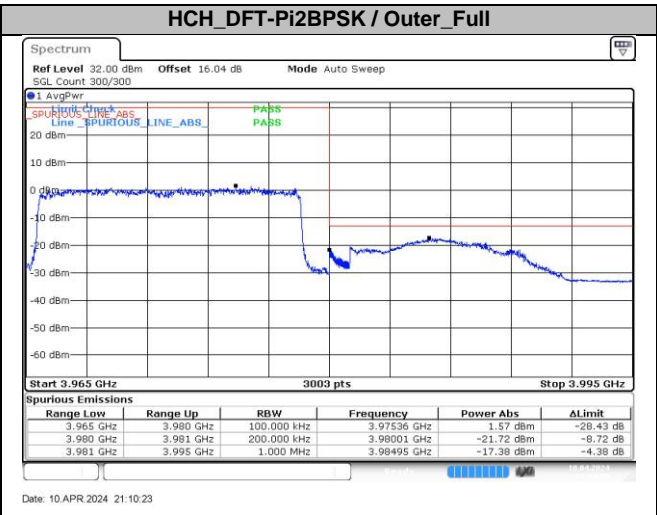
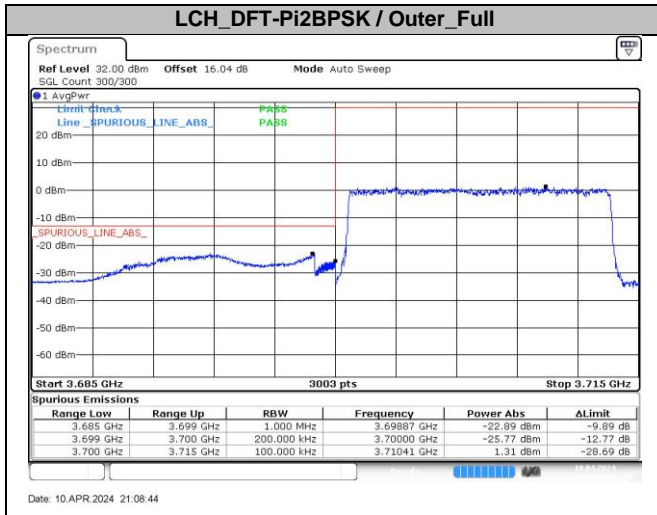
HCH_DFT-QPSK / Edge_1RB_Right



Date: 10 APR 2024 21:04:08

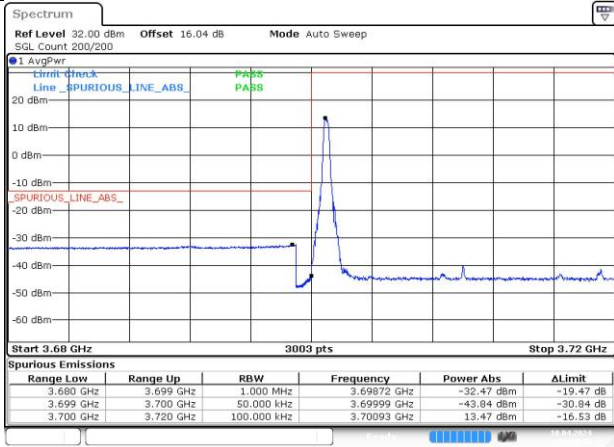






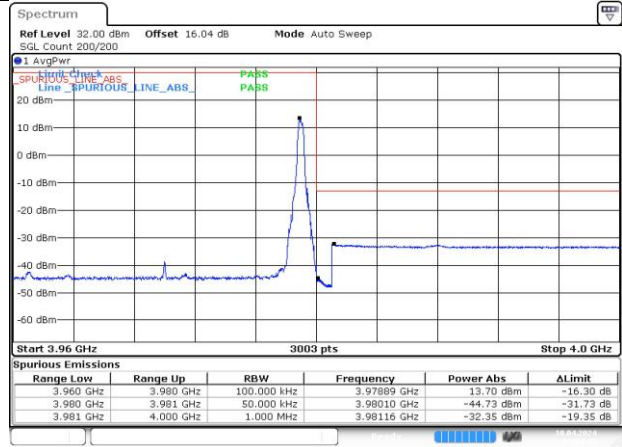
DC_7A_n77A / 30KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



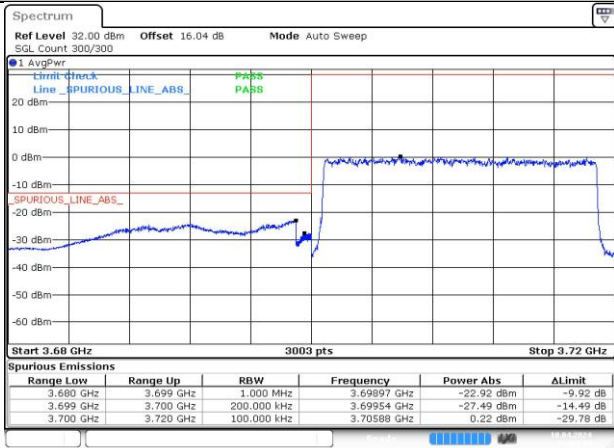
Date: 10 APR 2024 21:11:43

HCH_DFT-Pi2BPSK / Edge_1RB_Right



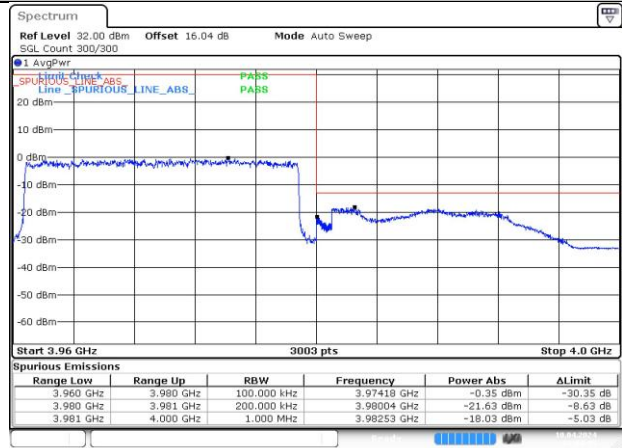
Date: 10 APR 2024 21:13:28

LCH_DFT-Pi2BPSK / Outer_Full



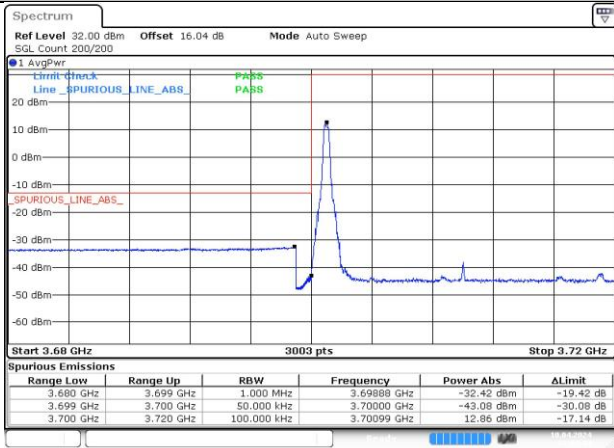
Date: 10 APR 2024 21:12:04

HCH_DFT-Pi2BPSK / Outer_Full



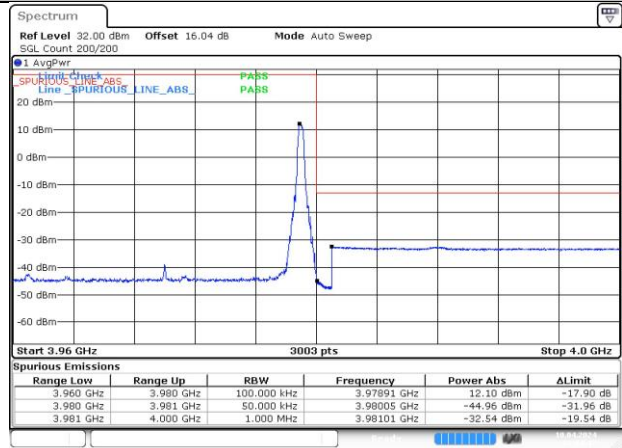
Date: 10 APR 2024 21:13:44

LCH_DFT-QPSK / Edge_1RB_Left

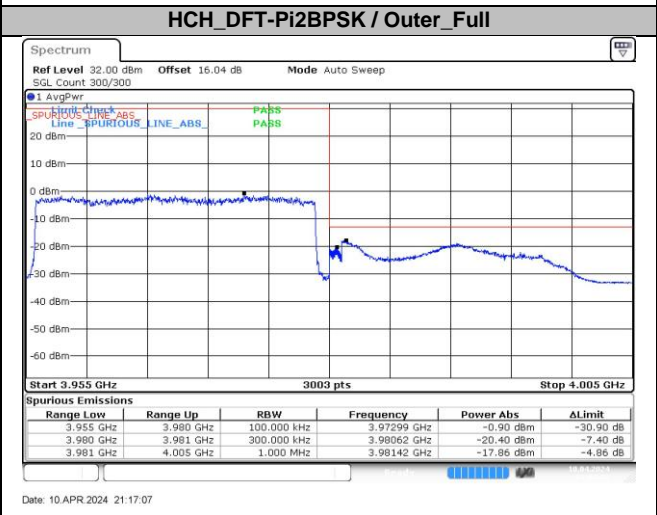
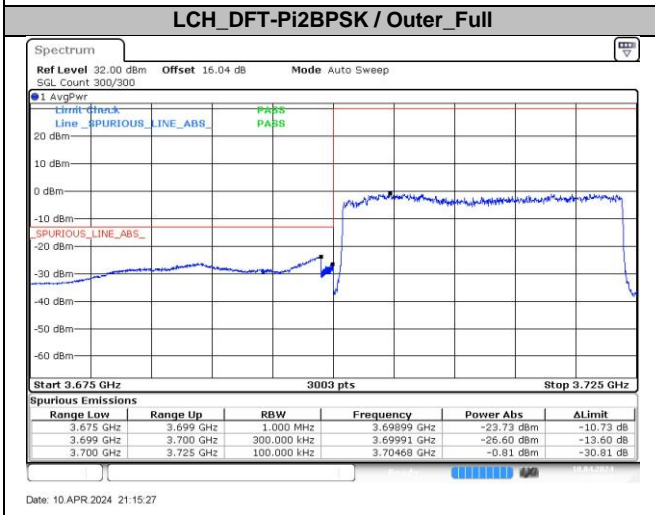
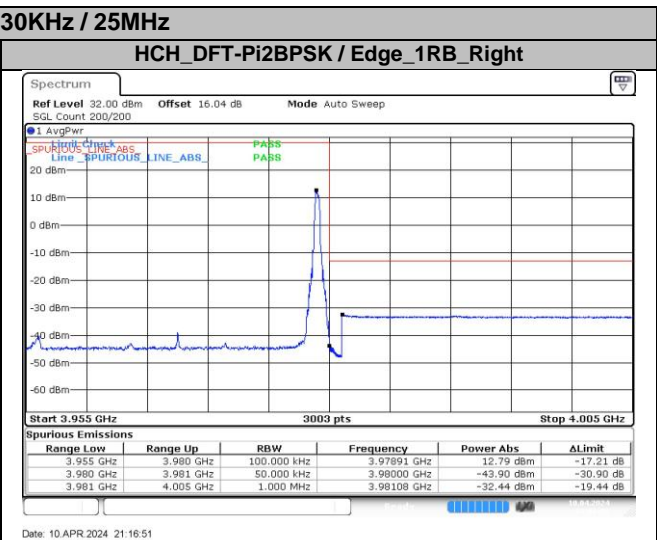
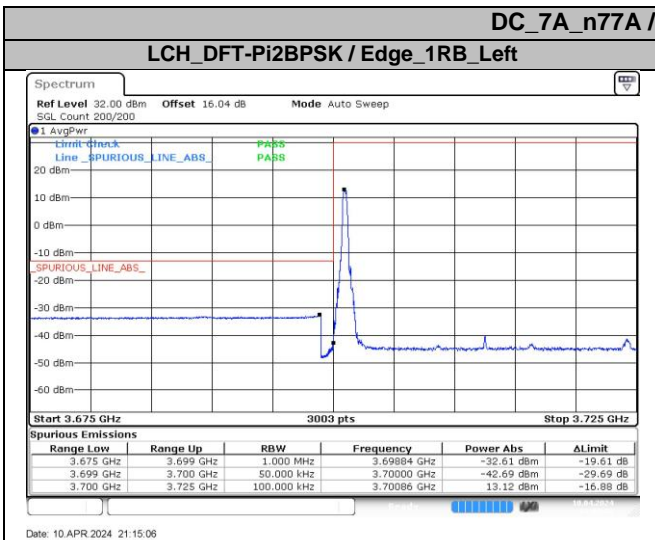
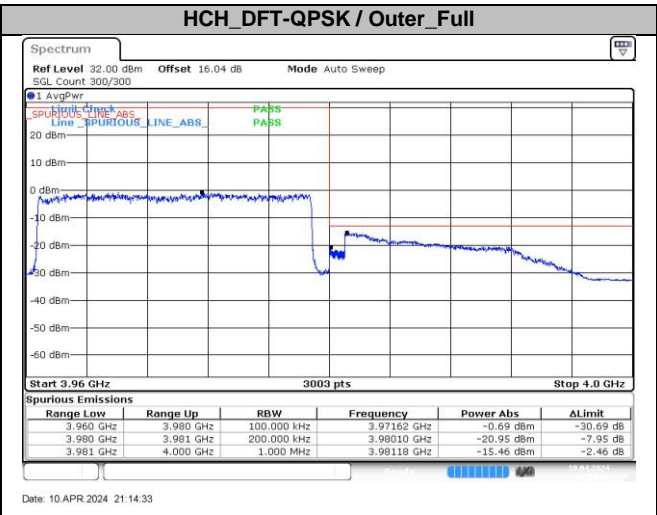
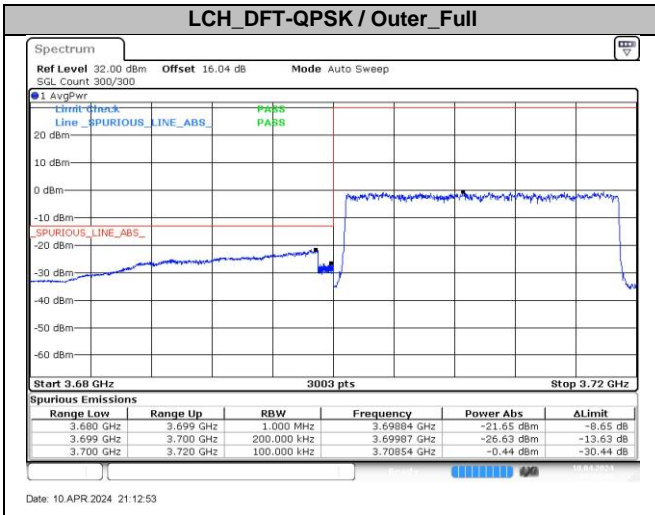


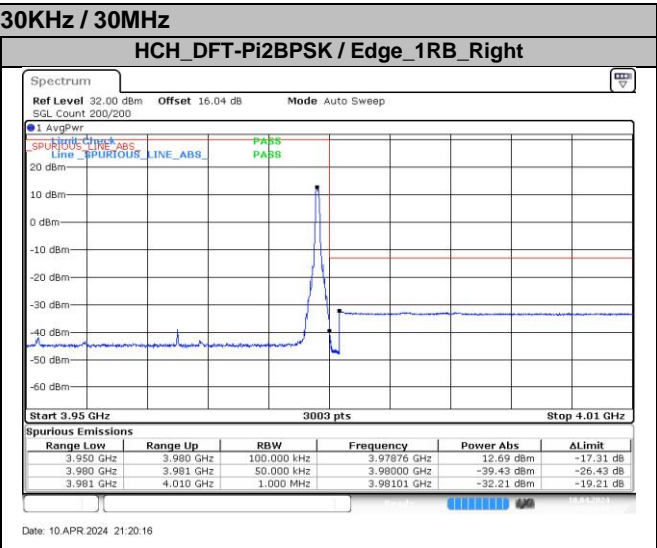
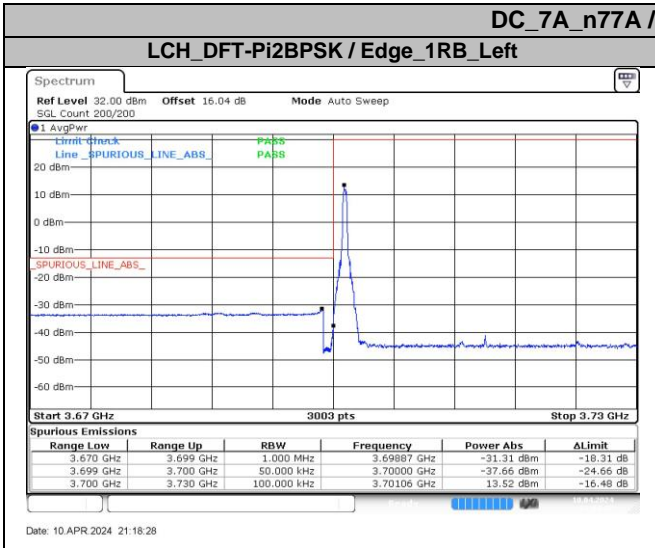
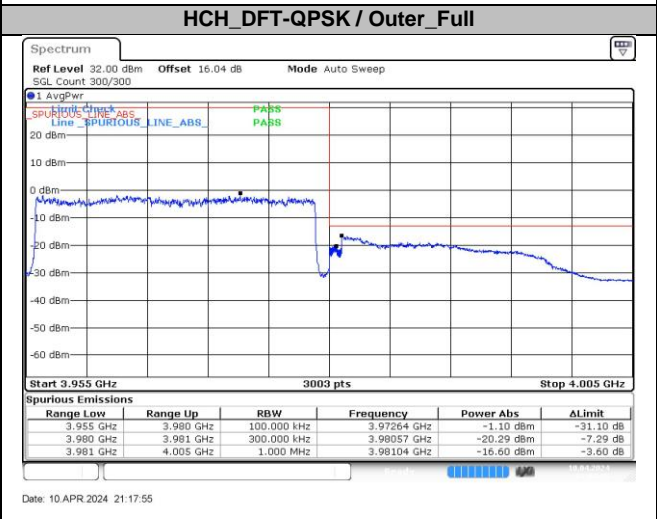
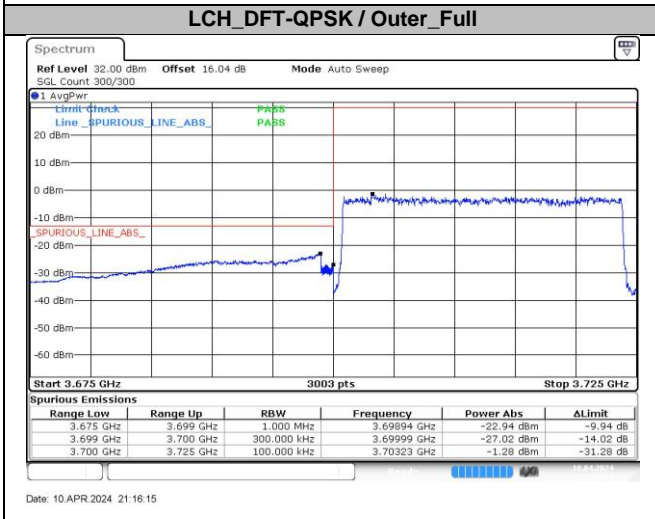
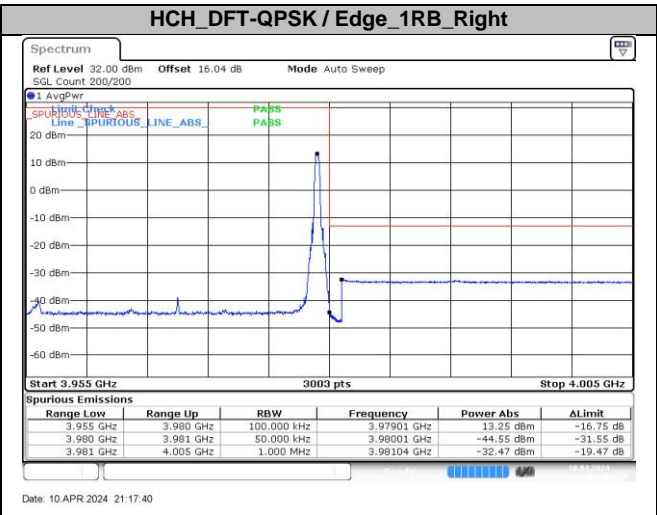
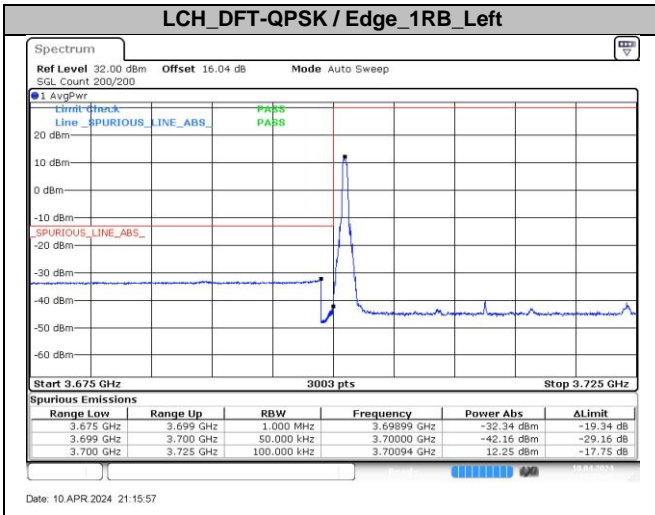
Date: 10 APR 2024 21:12:34

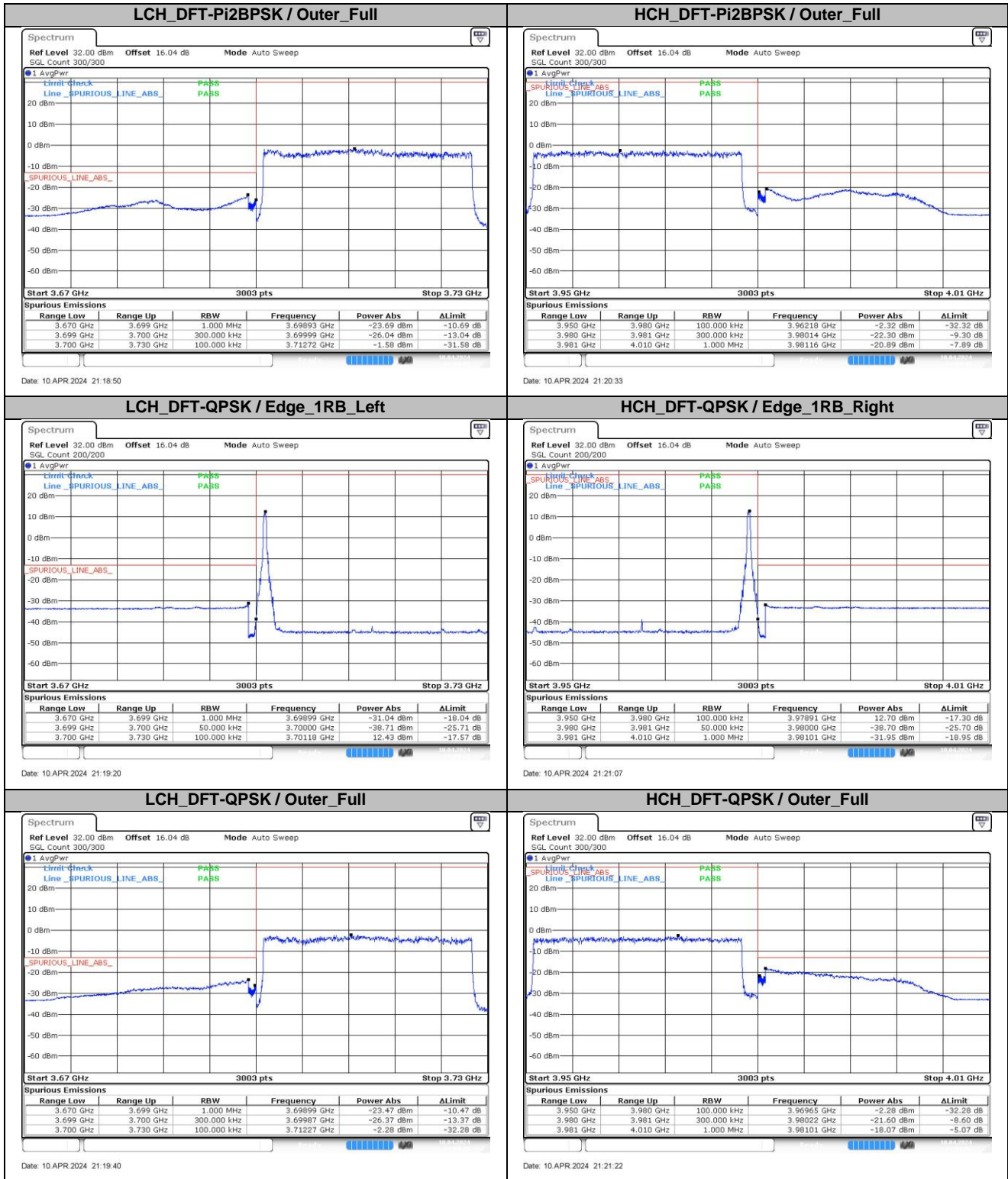
HCH_DFT-QPSK / Edge_1RB_Right



Date: 10 APR 2024 21:14:18

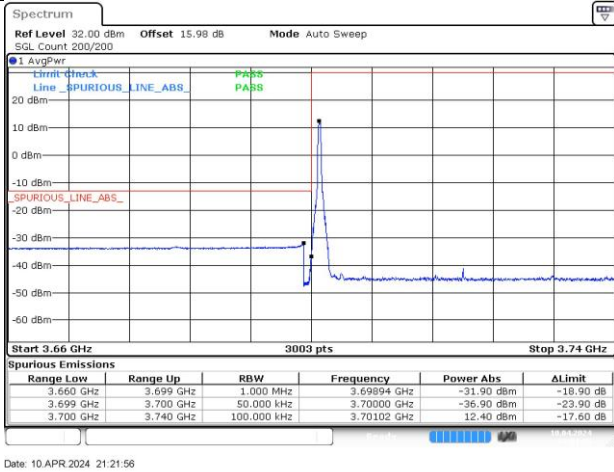




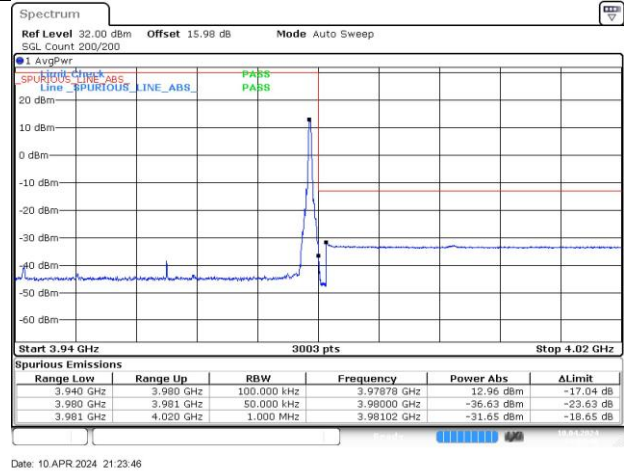


DC_7A_n77A / 30KHz / 40MHz

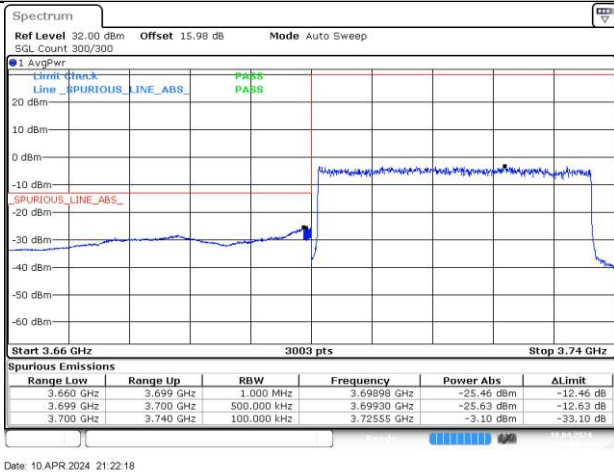
LCH_DFT-Pi2BPSK / Edge_1RB_Left



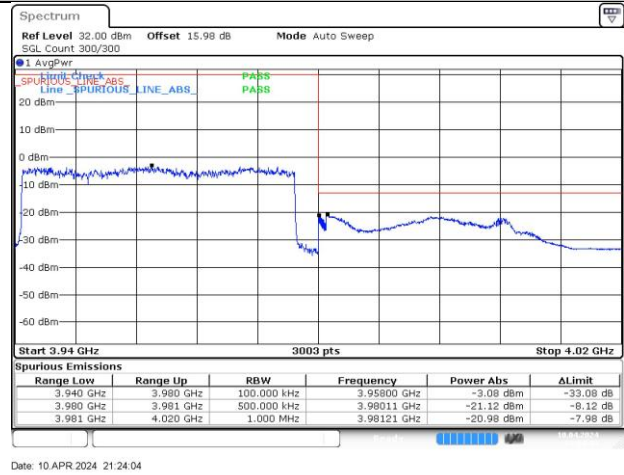
HCH_DFT-Pi2BPSK / Edge_1RB_Right



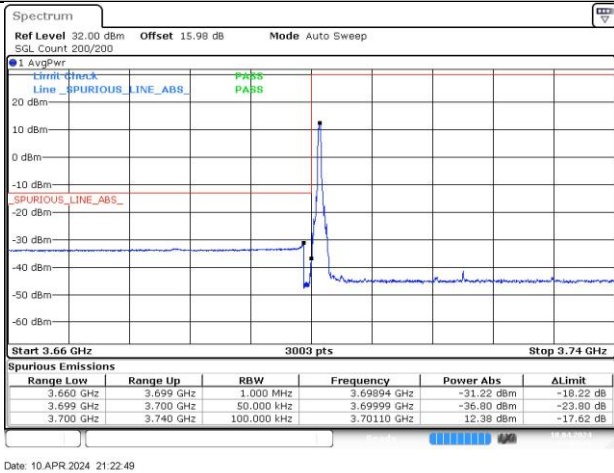
LCH_DFT-Pi2BPSK / Outer_Full



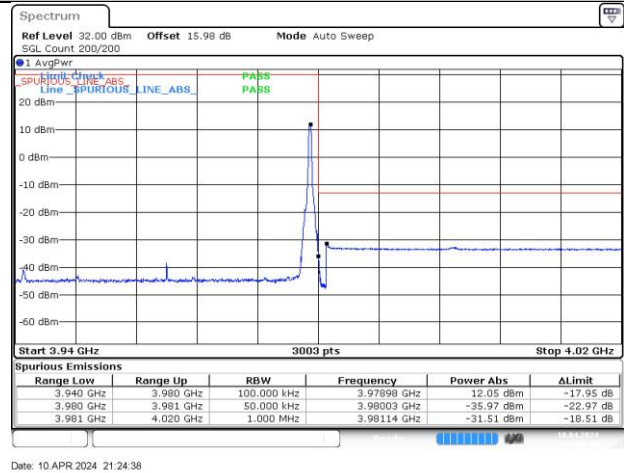
HCH_DFT-Pi2BPSK / Outer_Full

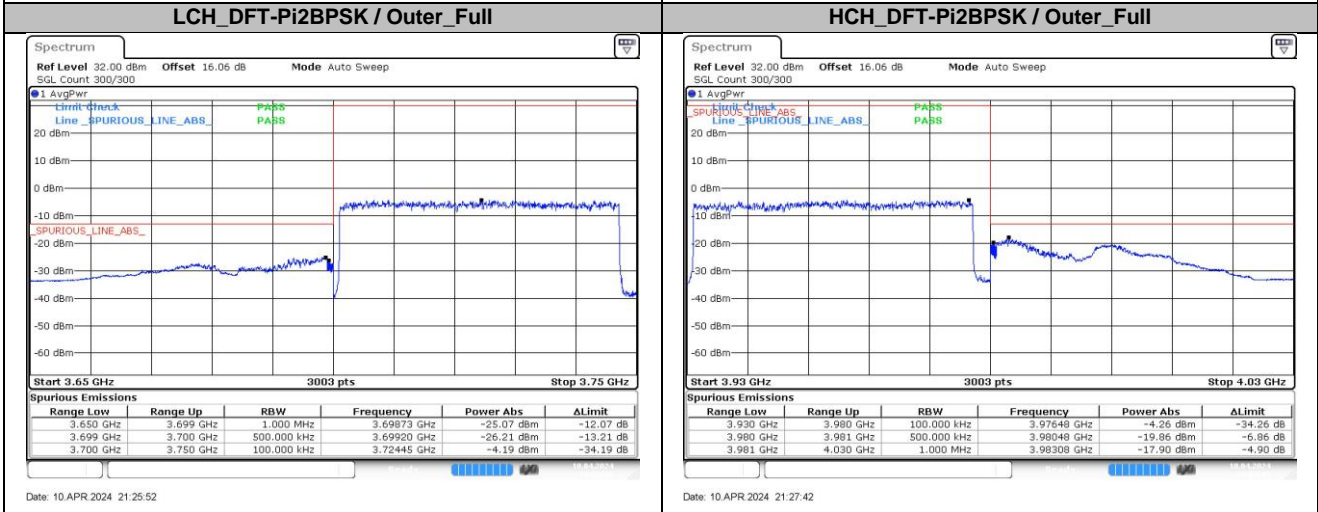
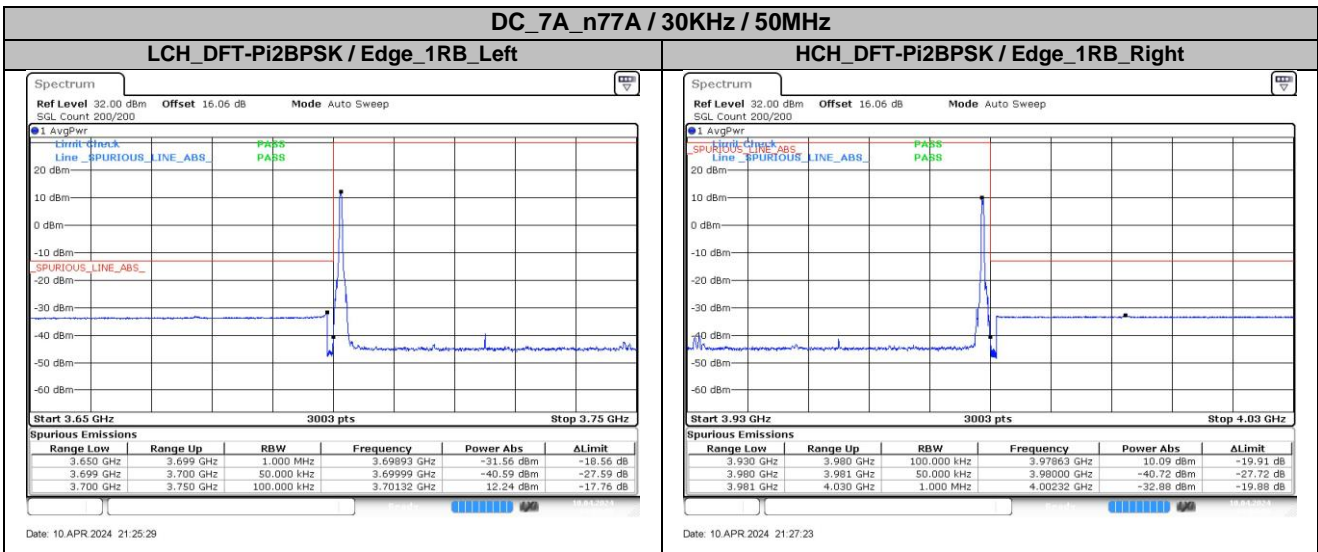
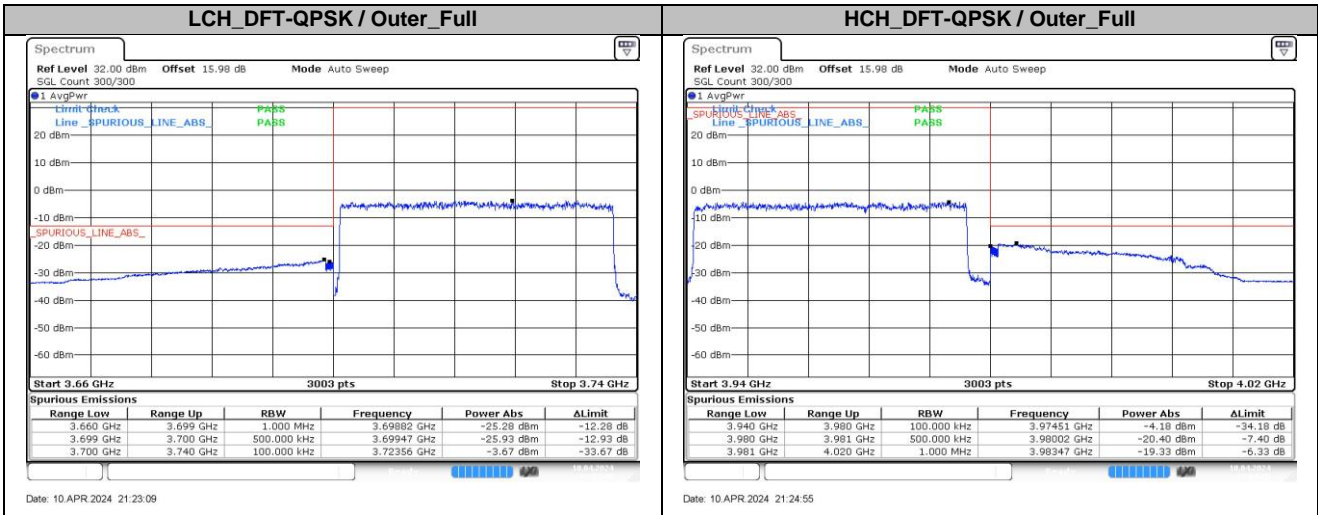


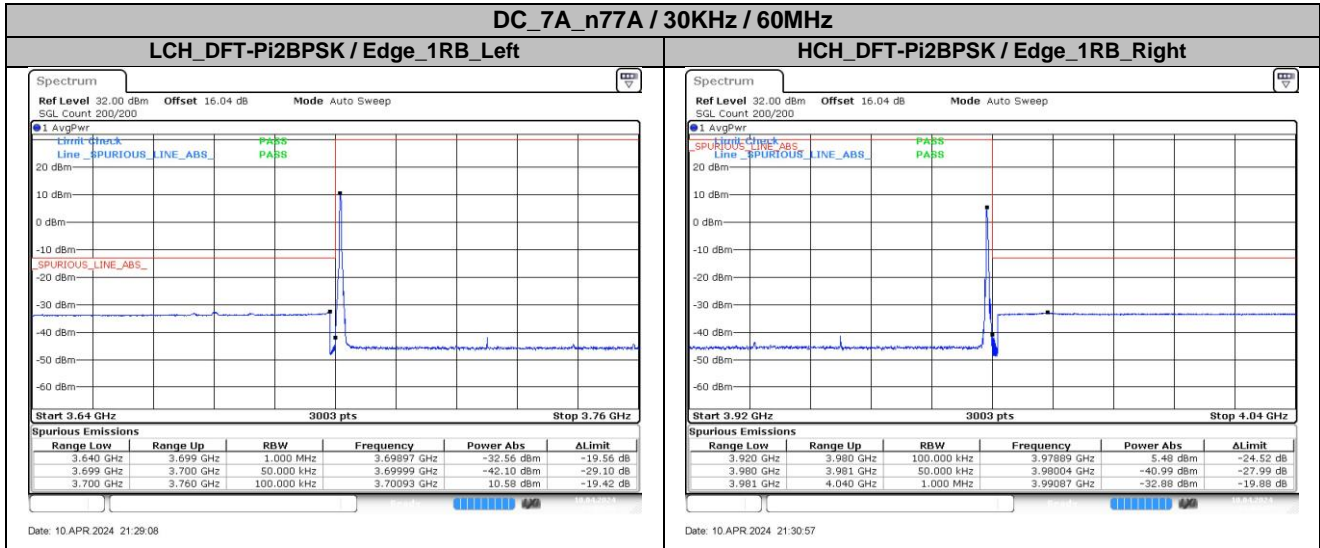
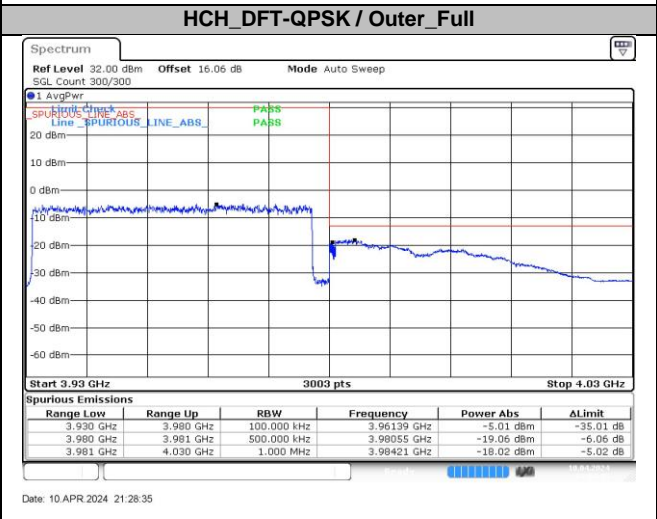
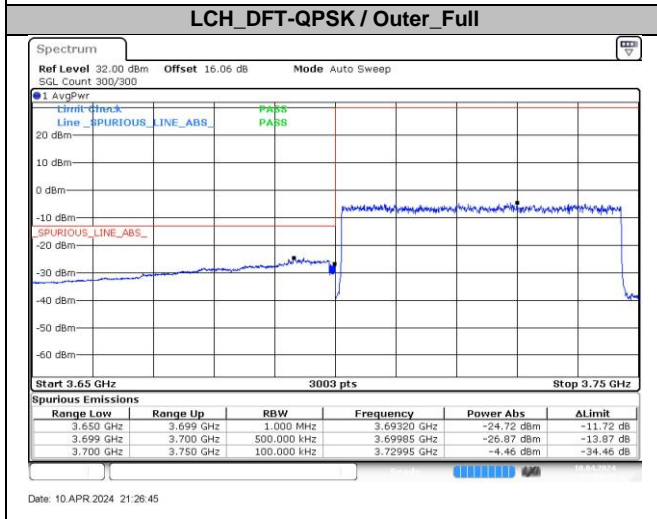
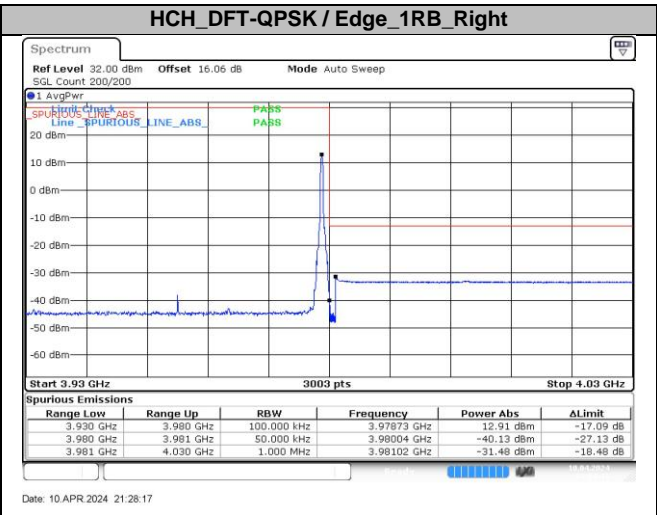
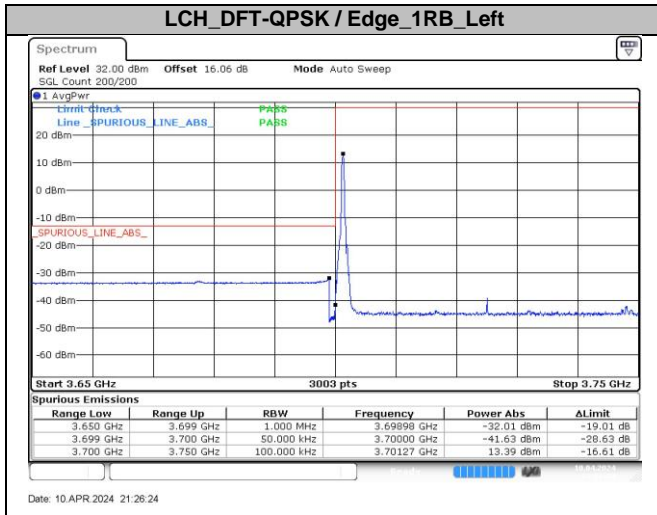
LCH_DFT-QPSK / Edge_1RB_Left

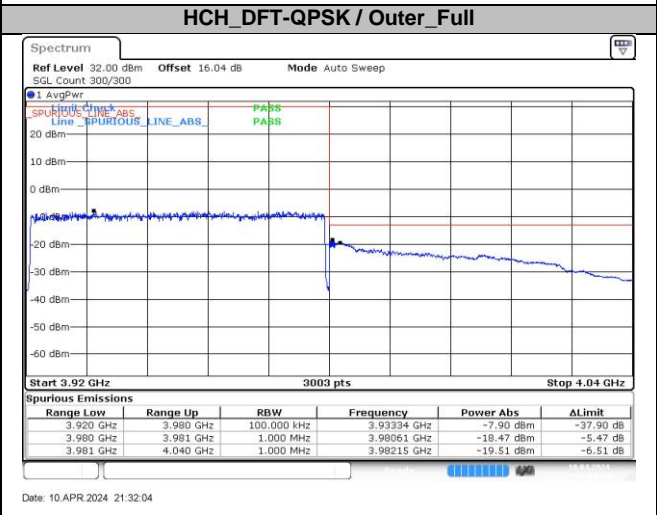
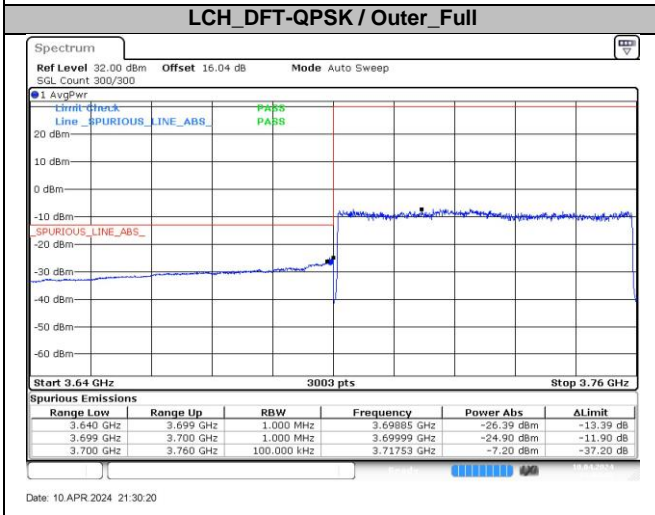
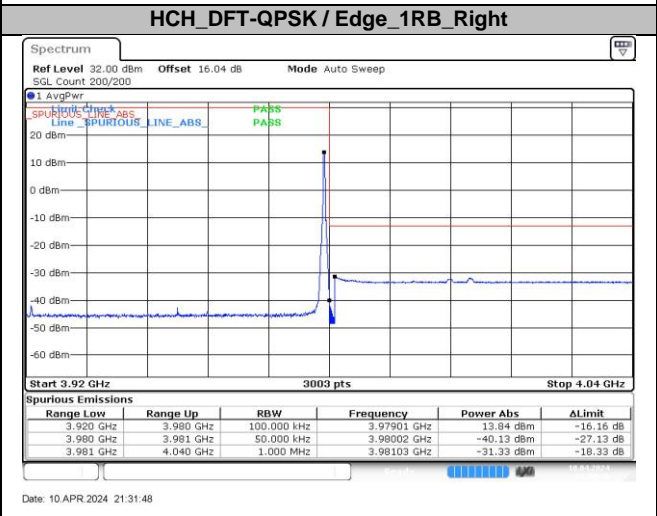
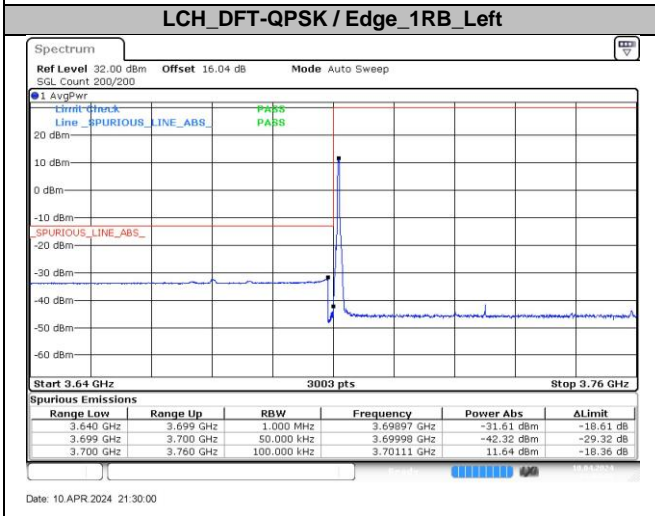
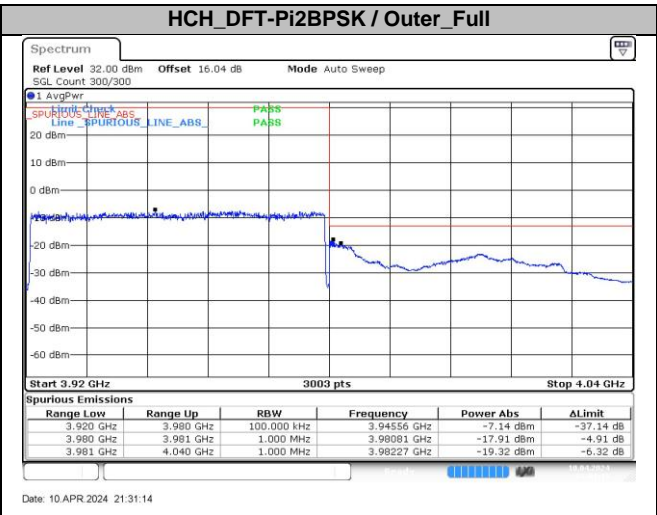
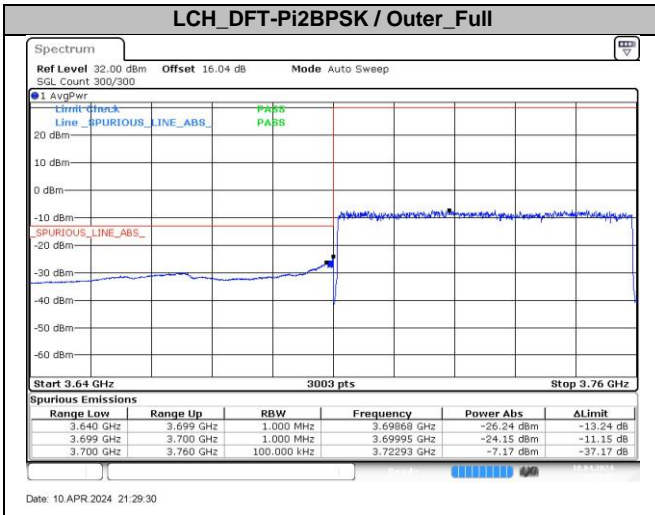


HCH_DFT-QPSK / Edge_1RB_Right



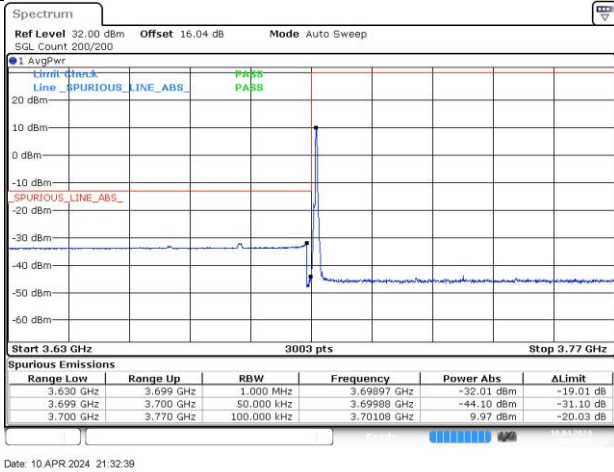






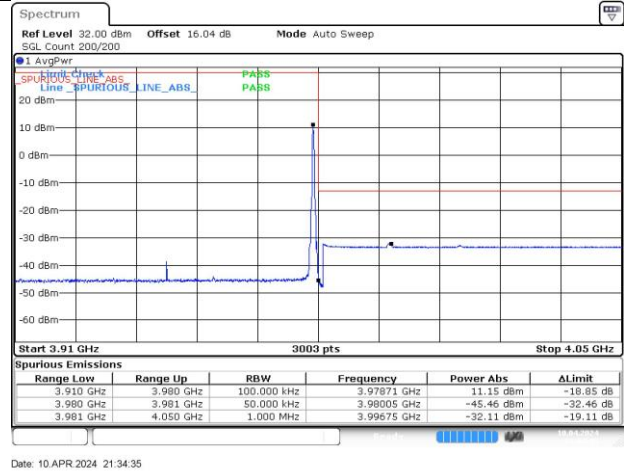
DC_7A_n77A / 30KHz / 70MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



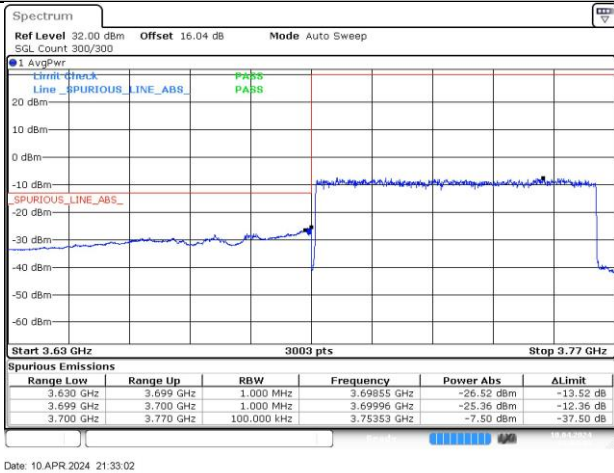
Date: 10 APR 2024 21:32:39

HCH_DFT-Pi2BPSK / Edge_1RB_Right



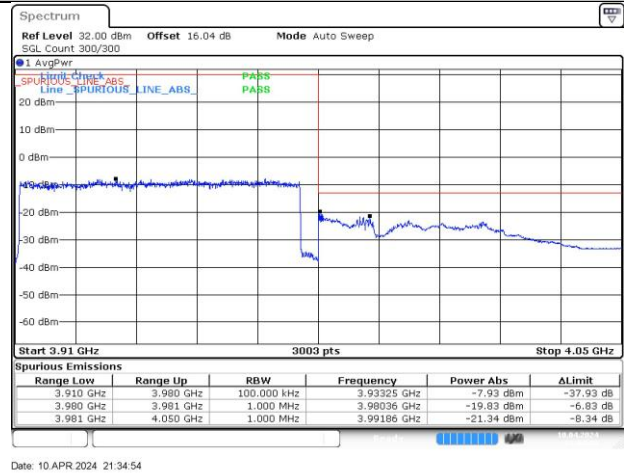
Date: 10 APR 2024 21:34:35

LCH_DFT-Pi2BPSK / Outer_Full



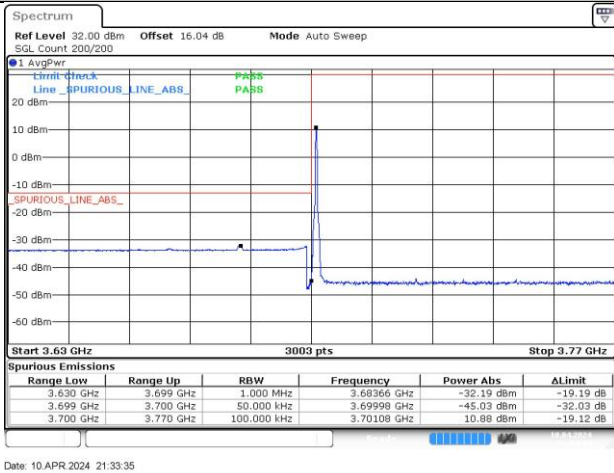
Date: 10 APR 2024 21:33:02

HCH_DFT-Pi2BPSK / Outer_Full



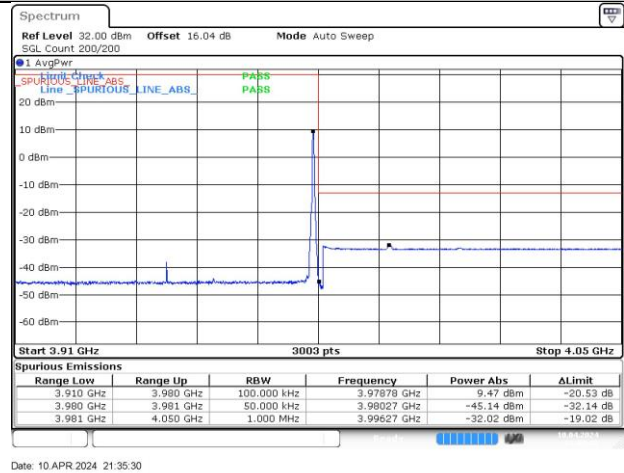
Date: 10 APR 2024 21:34:54

LCH_DFT-QPSK / Edge_1RB_Left



Date: 10 APR 2024 21:33:35

HCH_DFT-QPSK / Edge_1RB_Right



Date: 10 APR 2024 21:35:30

