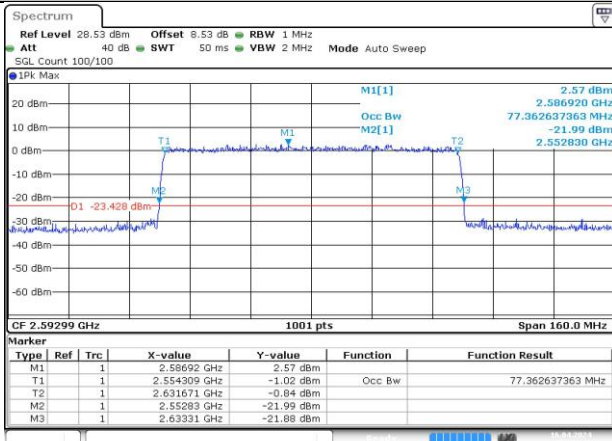


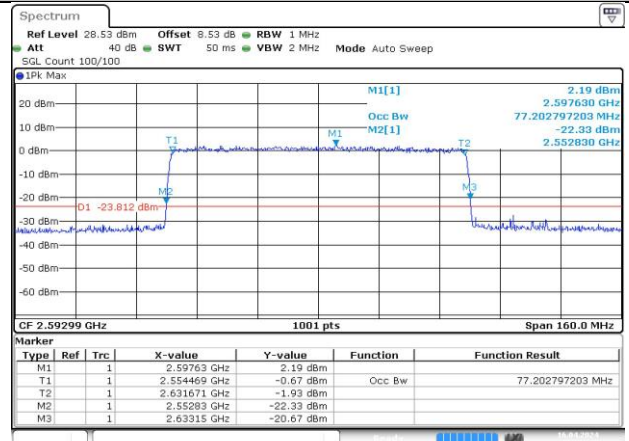
n41A / 30KHz / 80MHz

MCH / CP-QPSK / Outer_Full



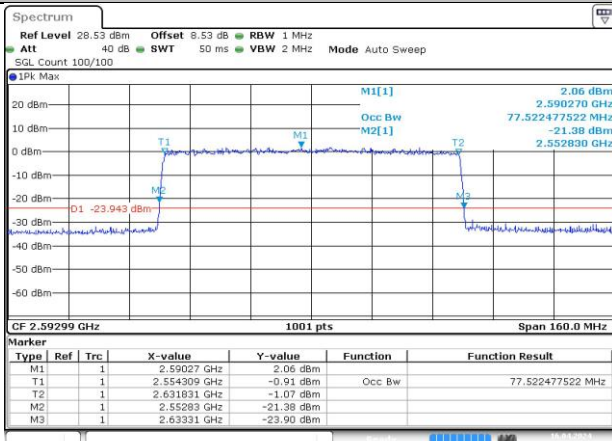
Date: 16 APR 2024 21:41:30

MCH / CP-16QAM / Outer_Full



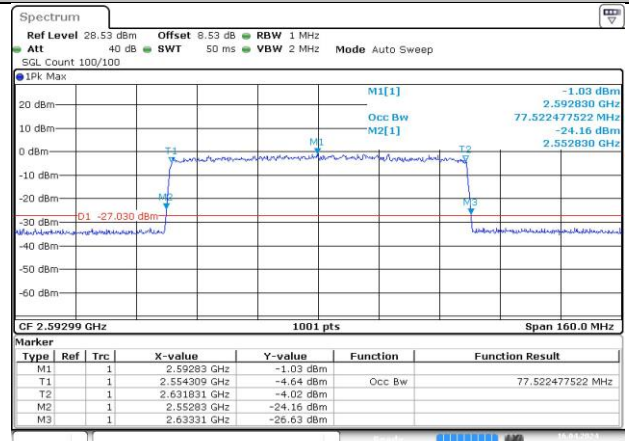
Date: 16 APR 2024 21:41:45

MCH / CP-64QAM / Outer_Full



Date: 16 APR 2024 21:42:00

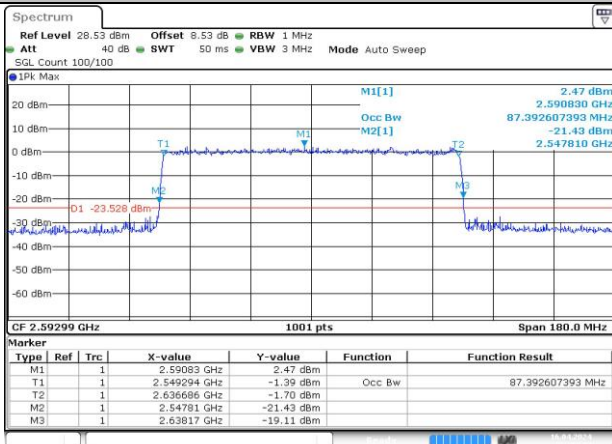
MCH / CP-256QAM / Outer_Full



Date: 16 APR 2024 21:42:16

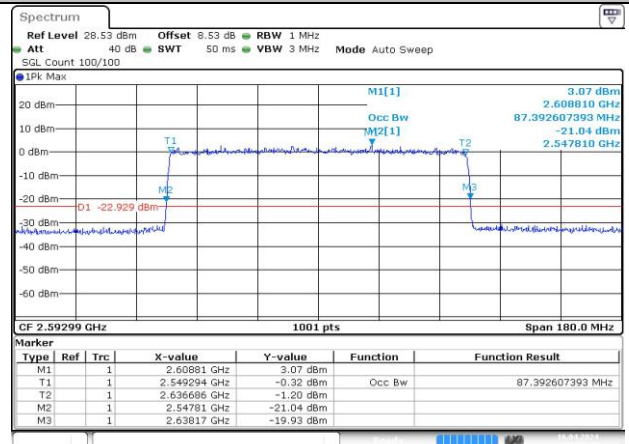
n41A / 30KHz / 90MHz

MCH / CP-QPSK / Outer_Full

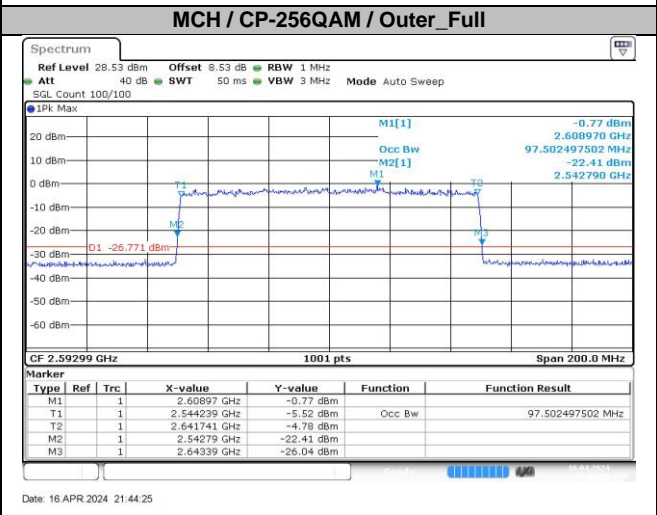
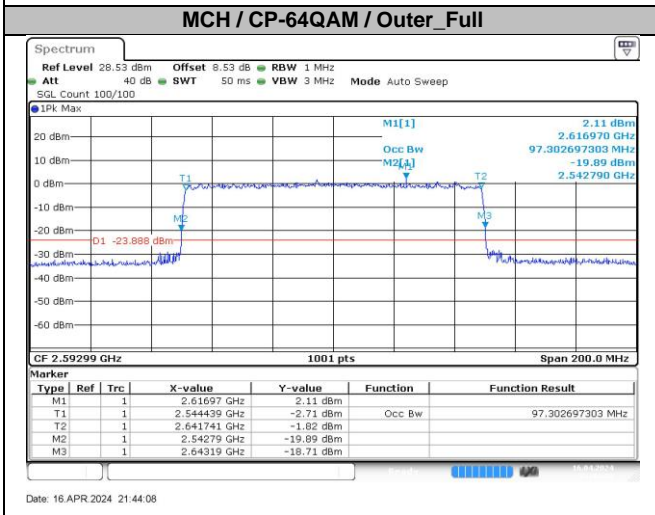
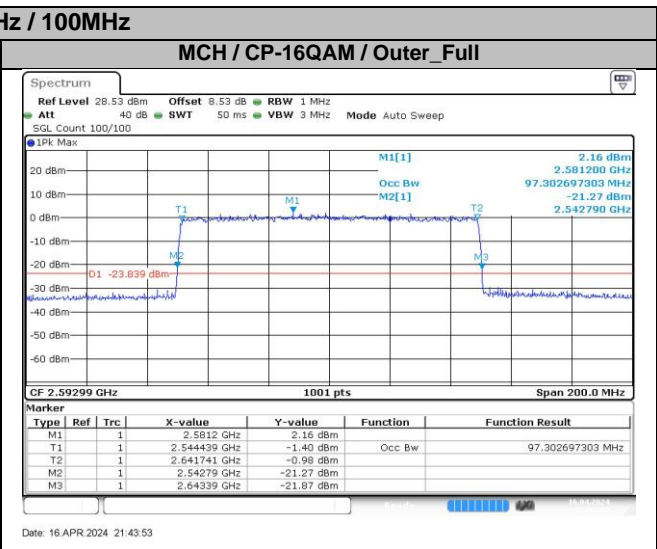
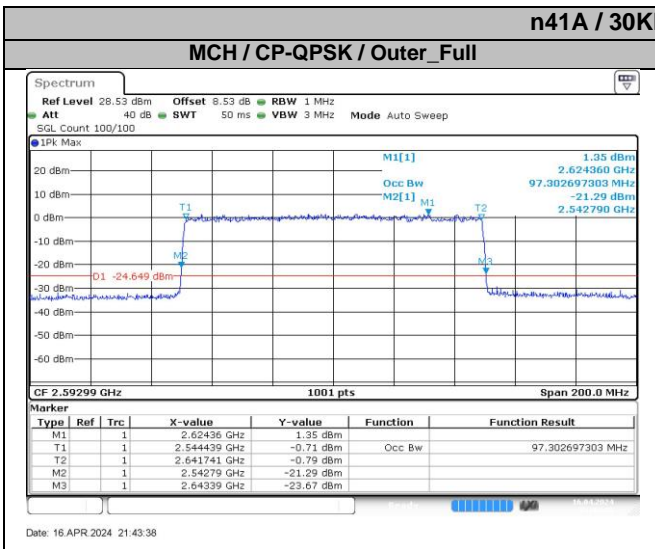
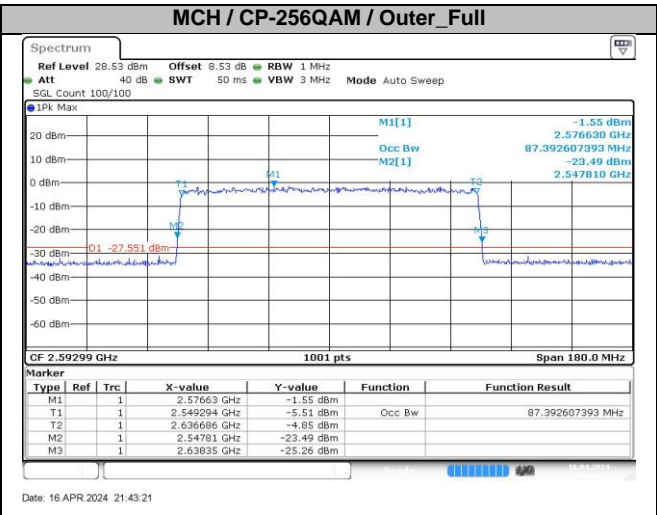
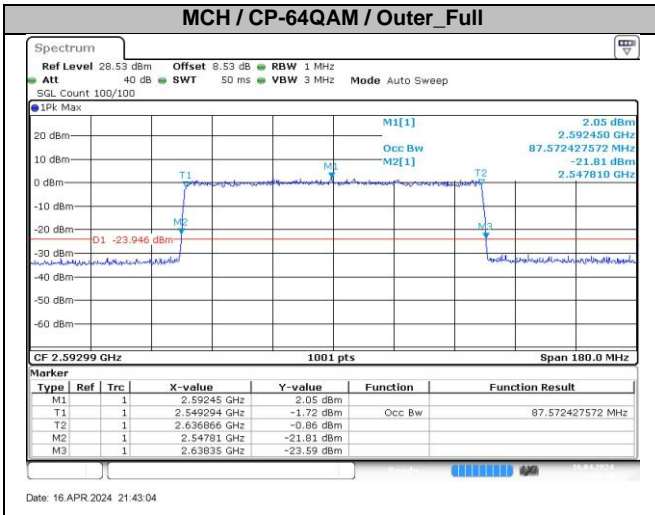


Date: 16 APR 2024 21:42:34

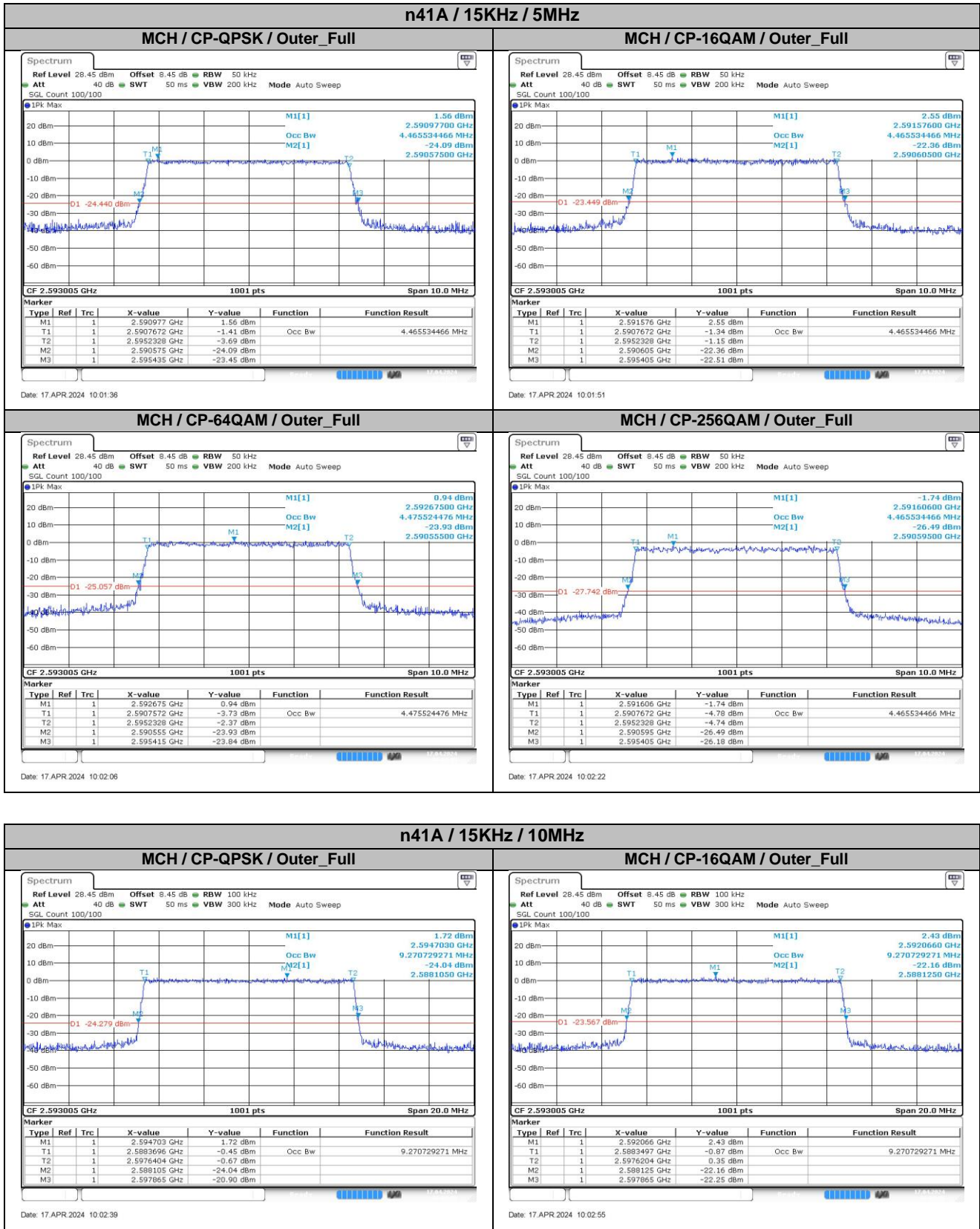
MCH / CP-16QAM / Outer_Full

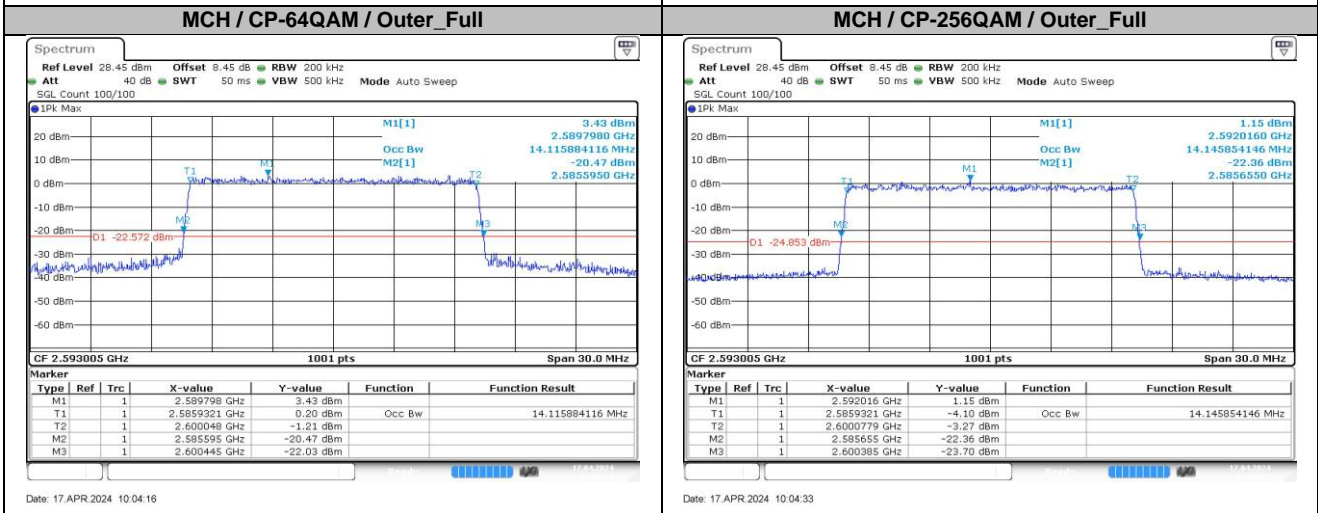
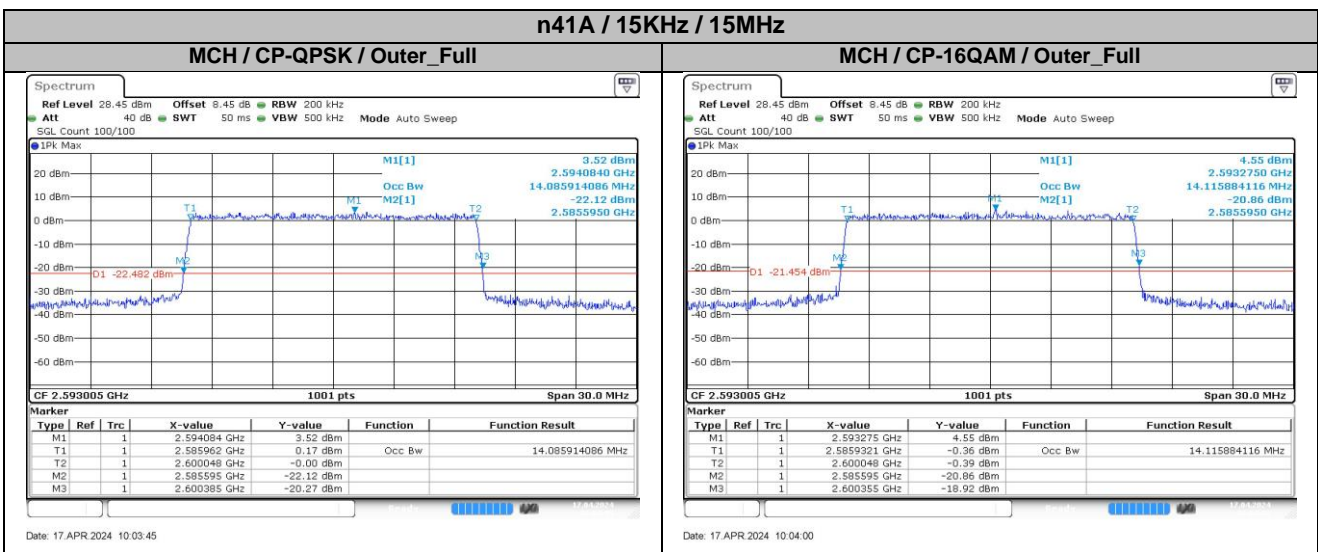
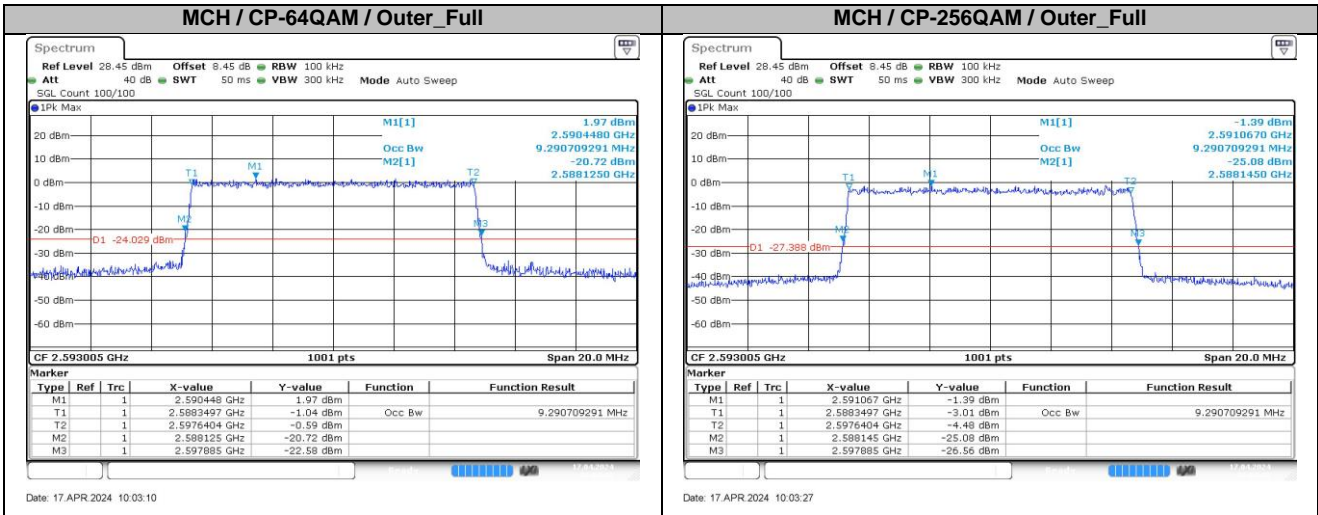


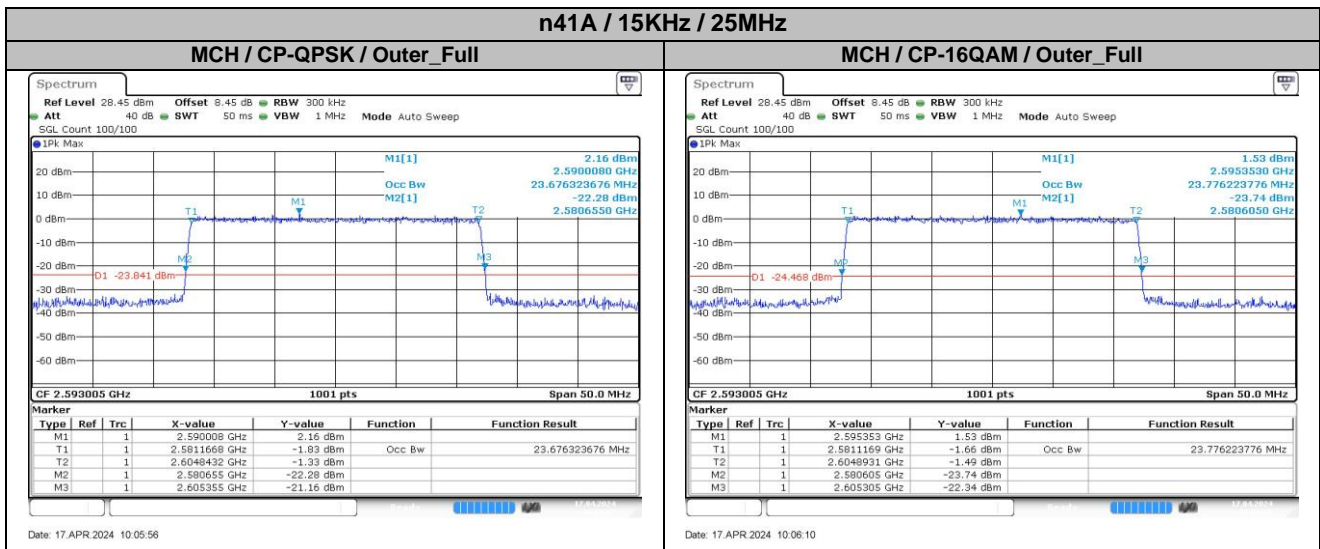
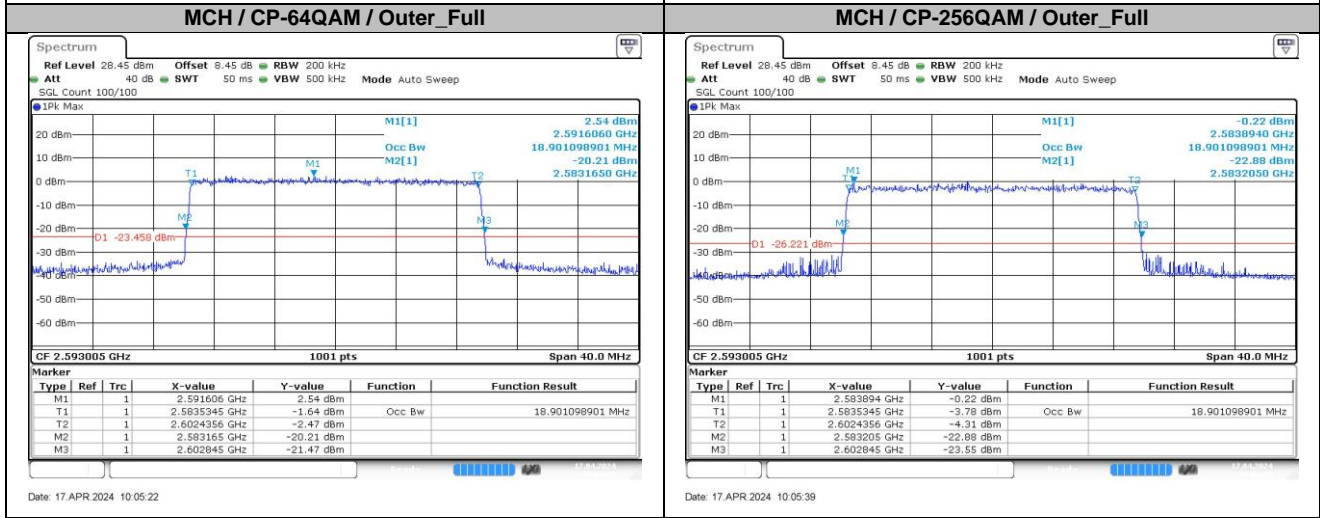
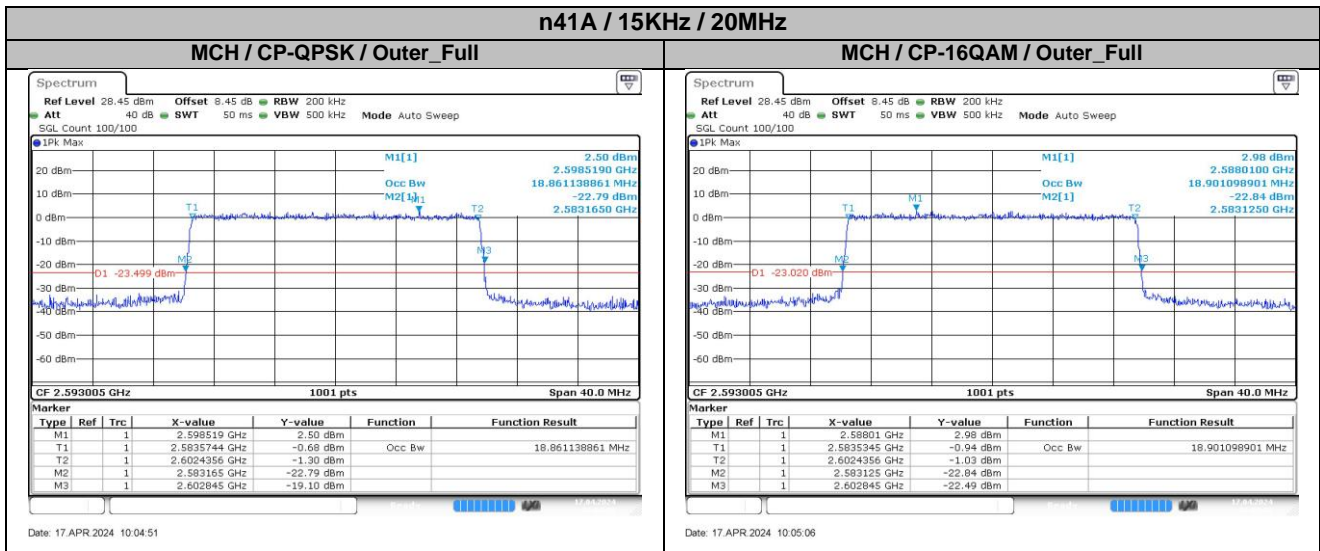
Date: 16 APR 2024 21:42:49

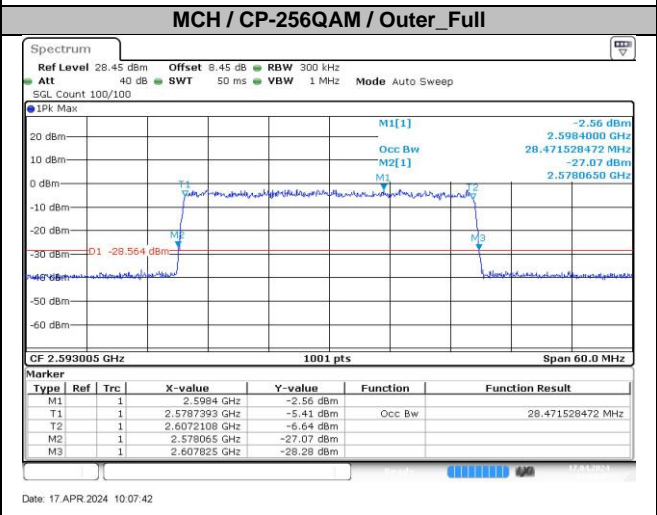
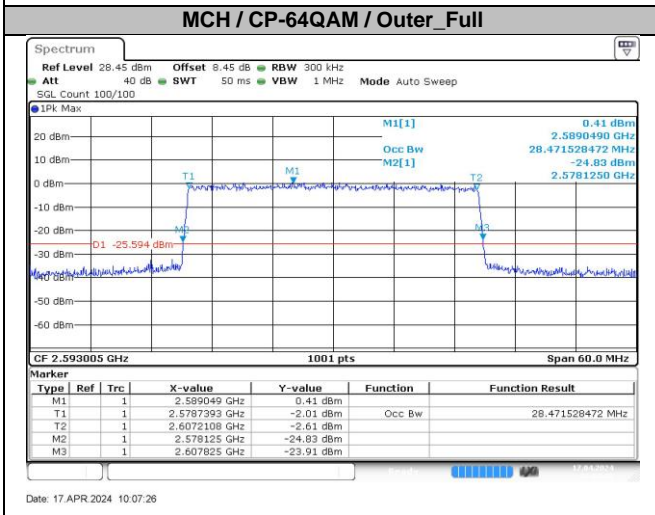
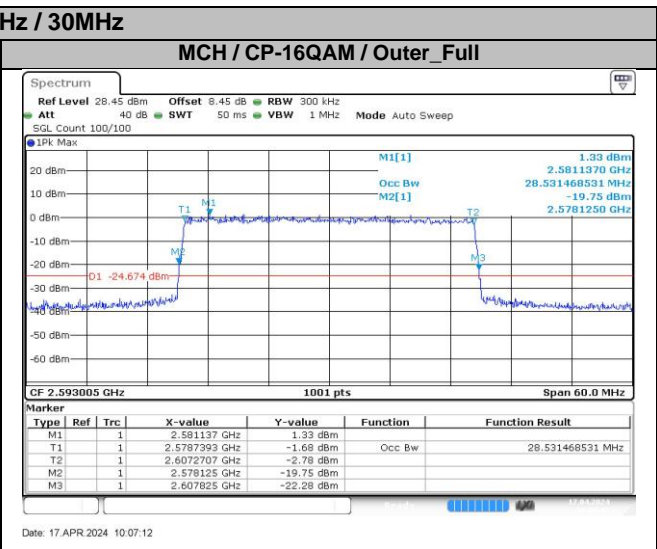
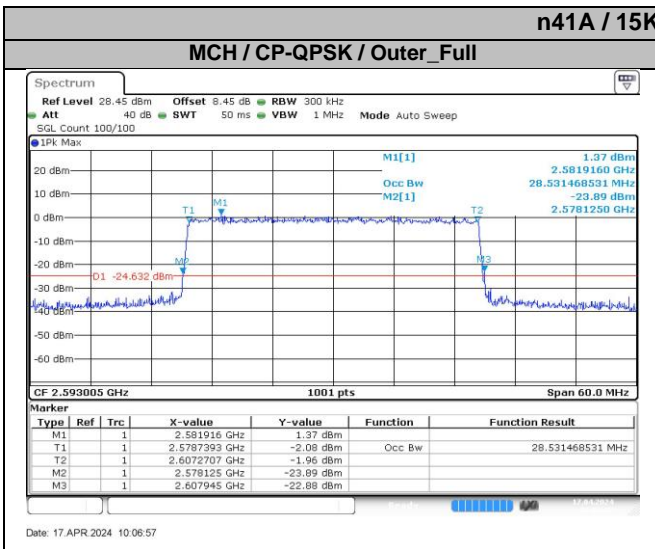
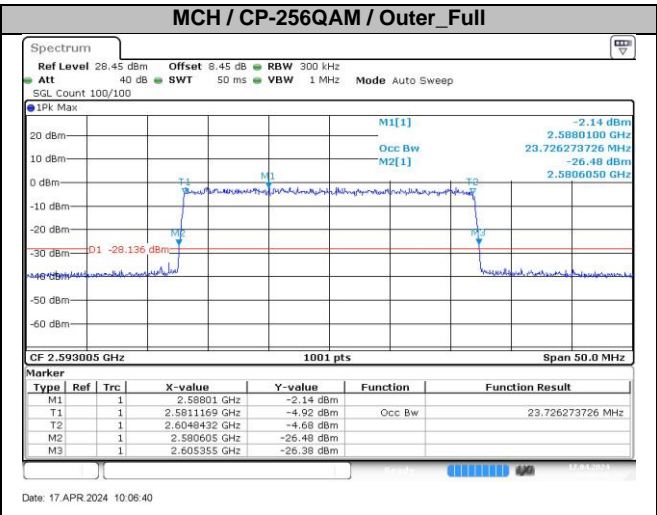
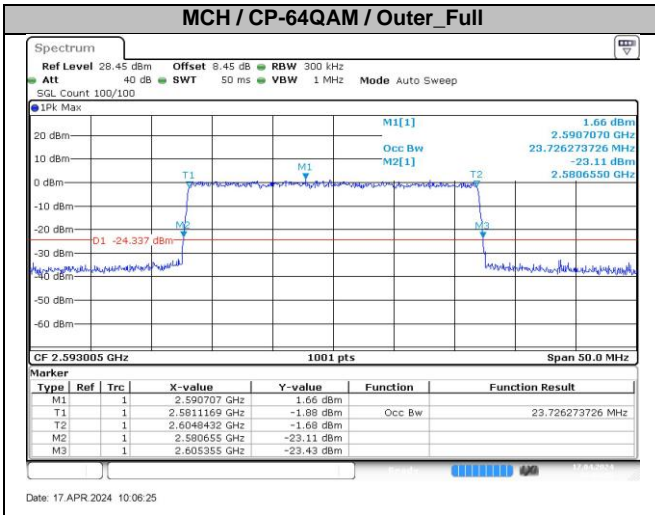


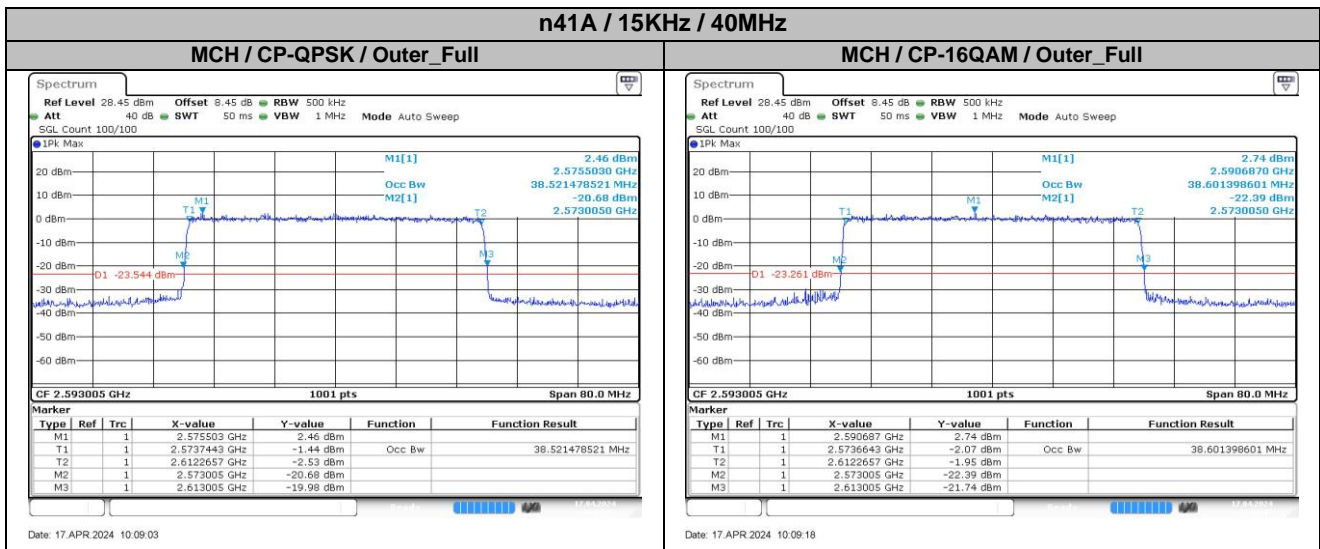
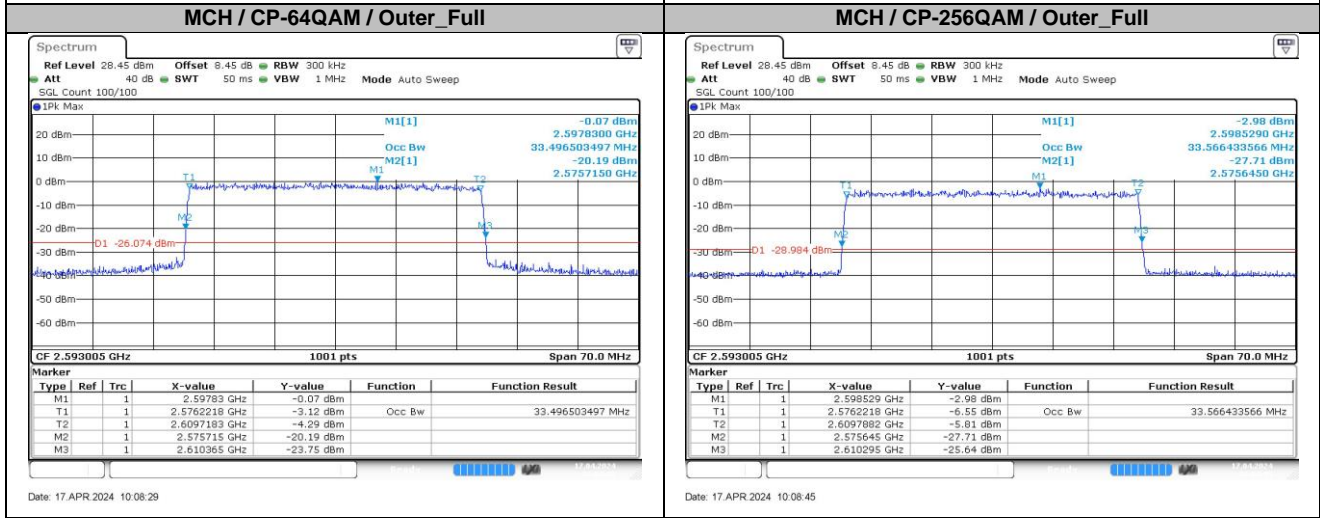
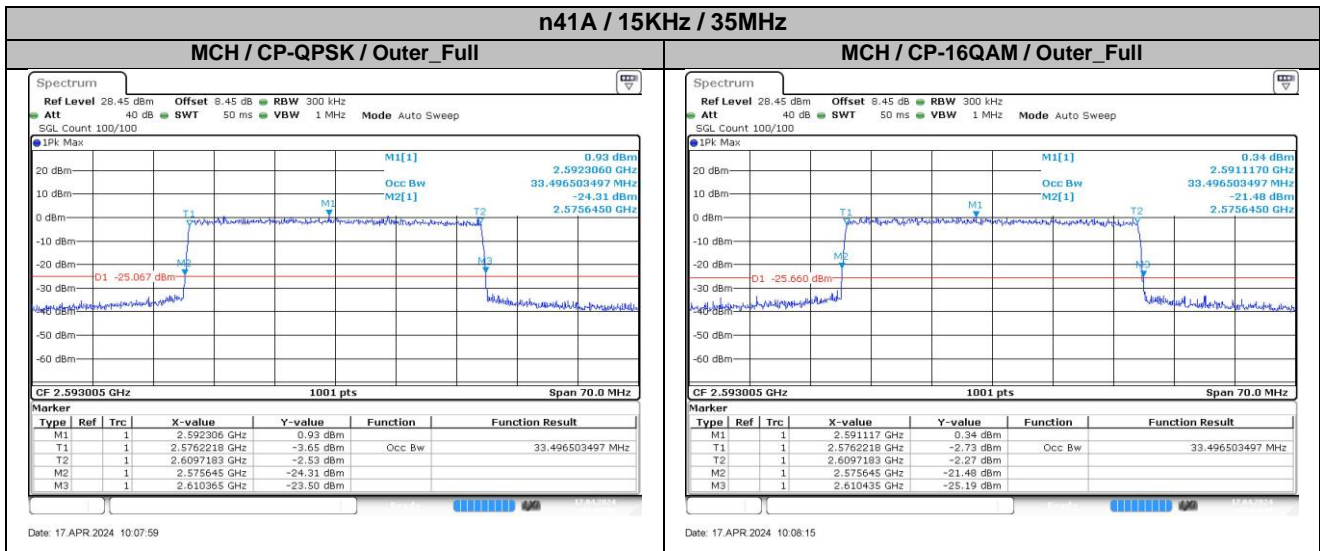
4.3. Test Plots for Ant3

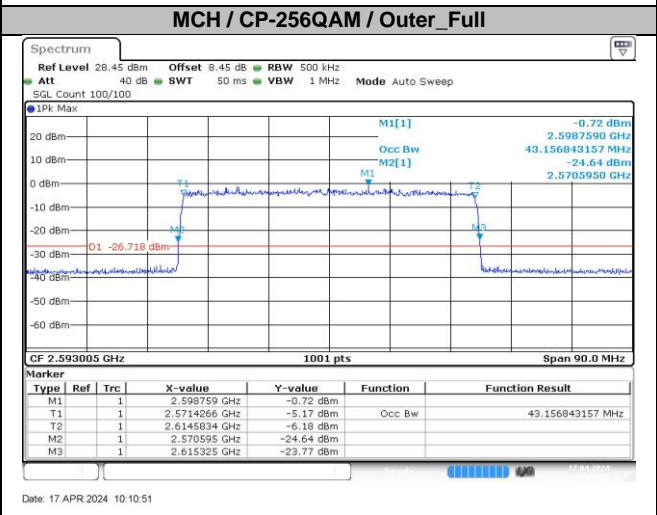
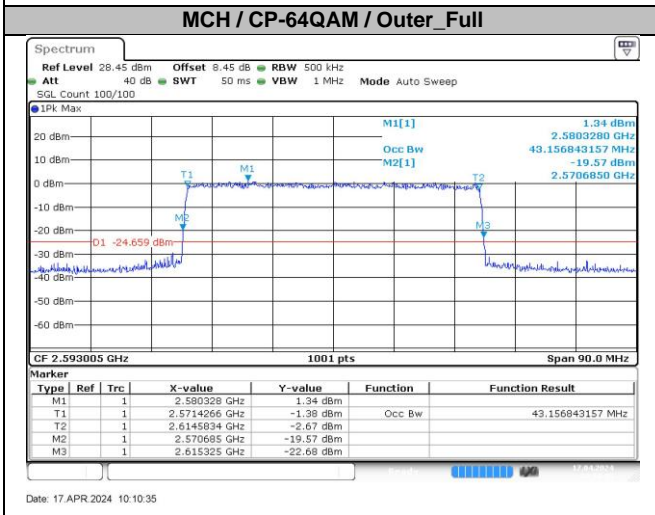
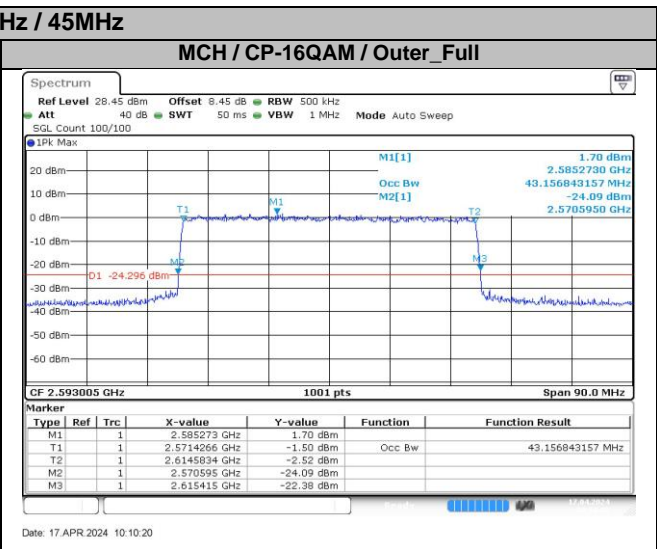
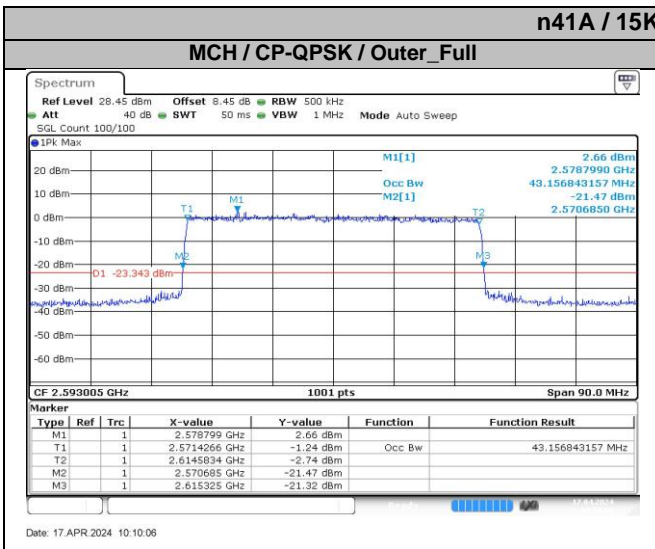
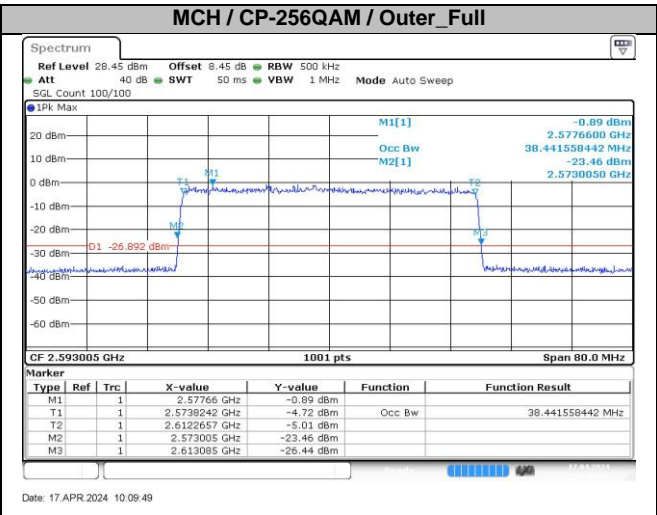
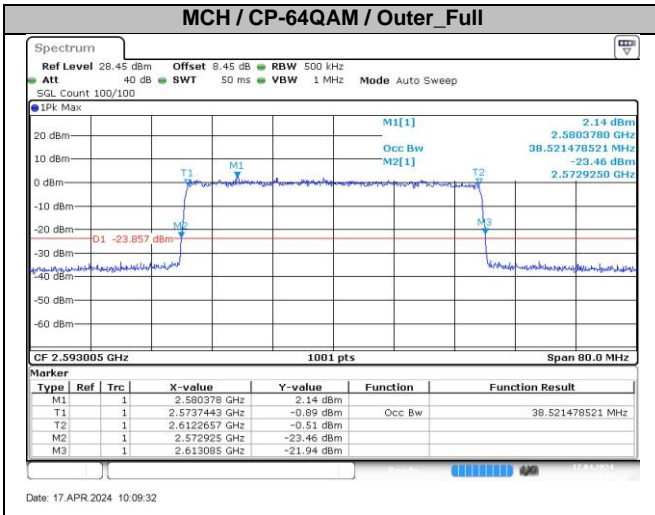


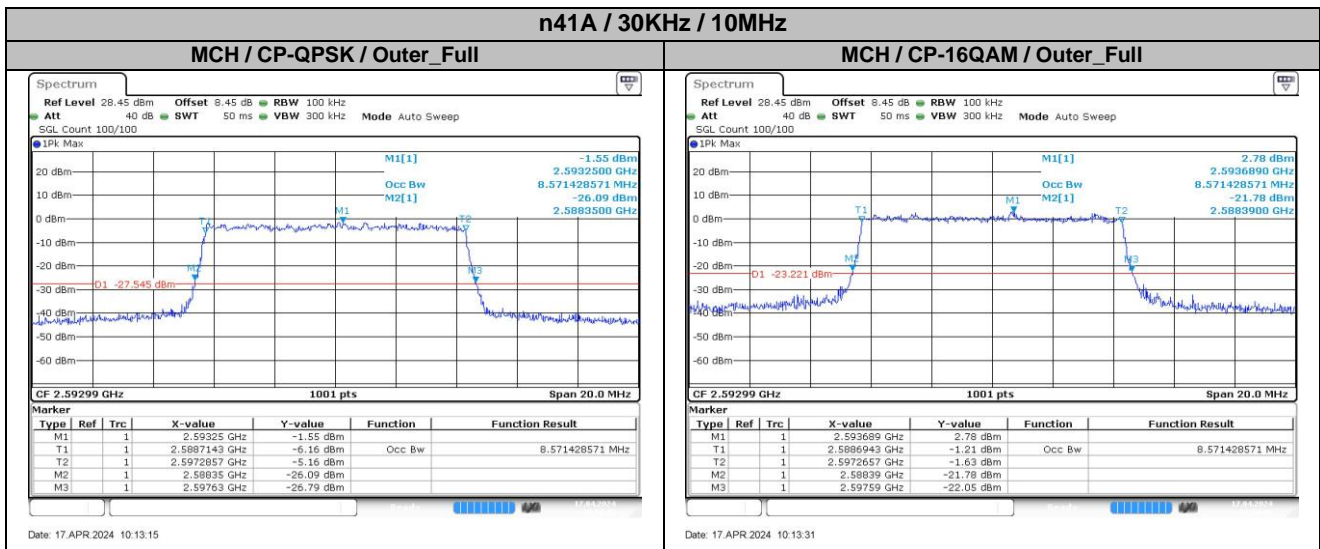
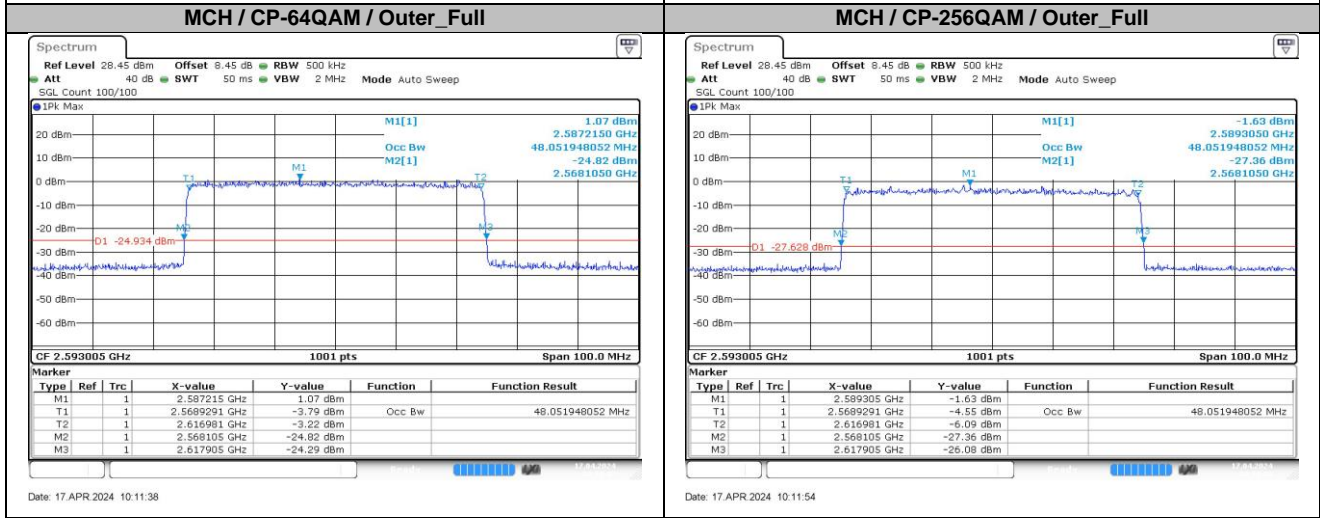
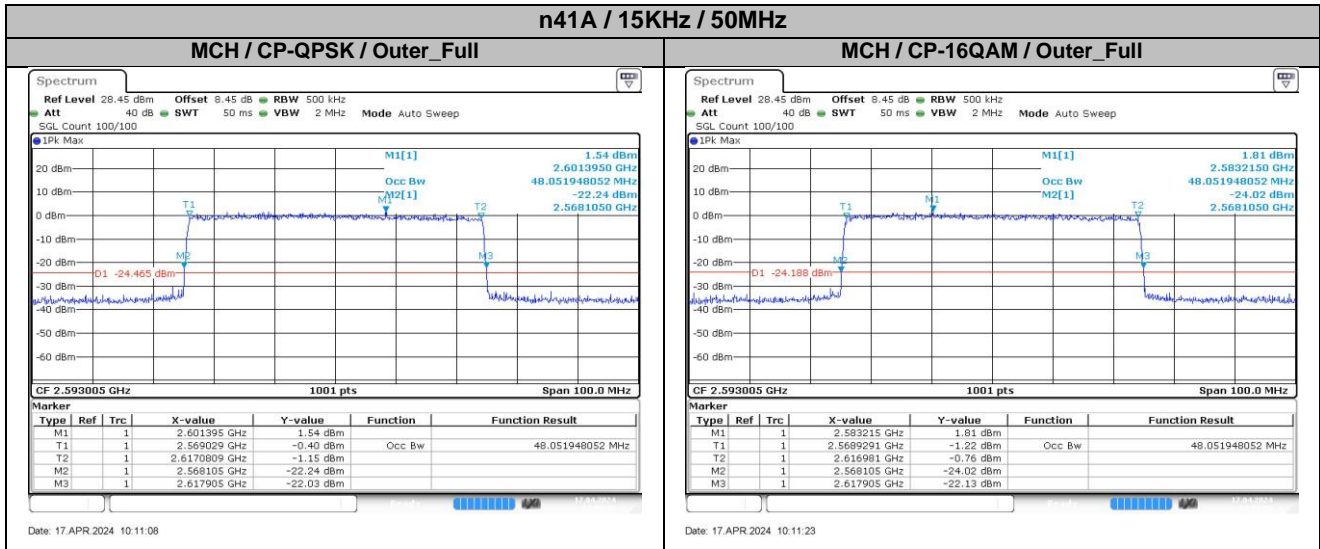


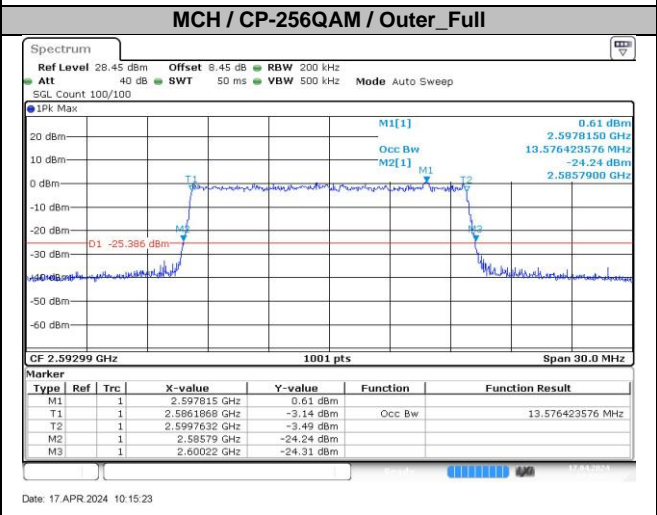
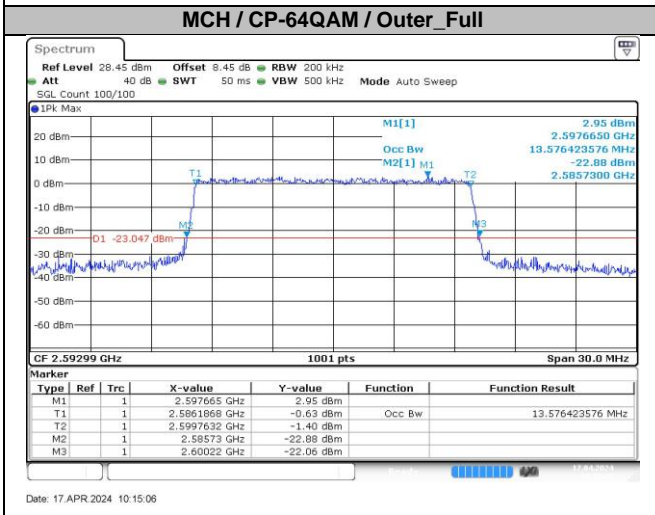
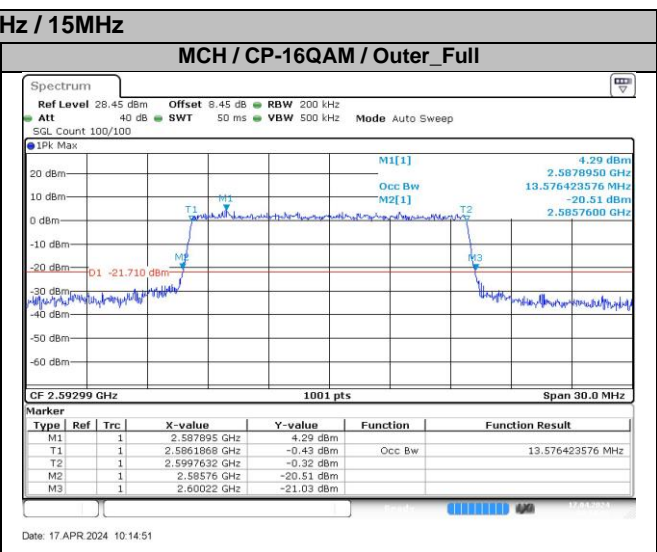
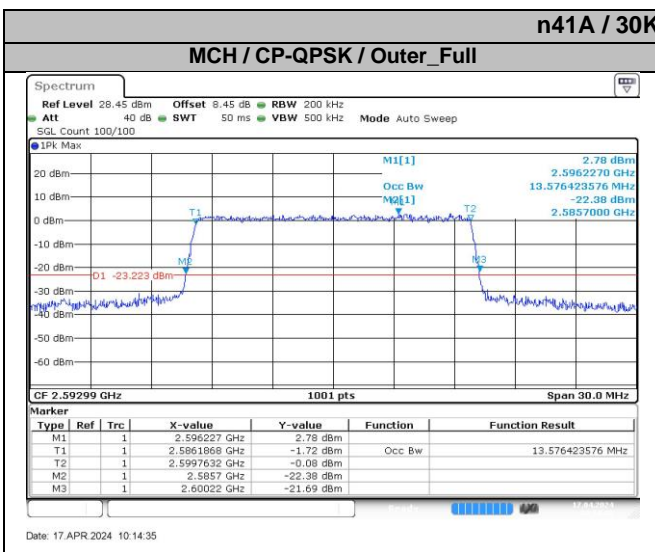
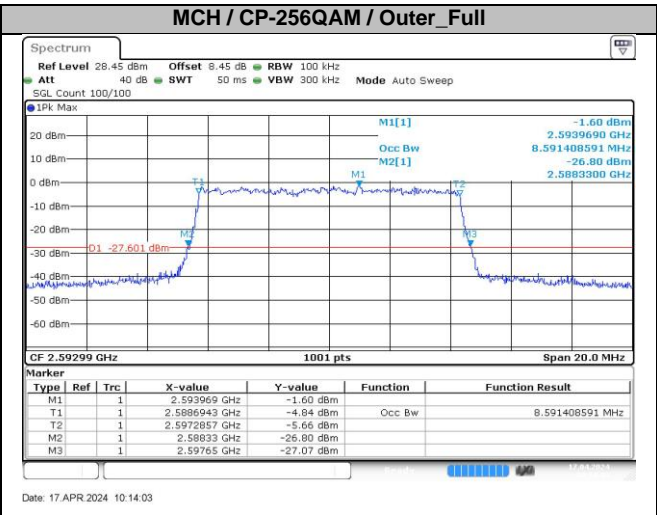
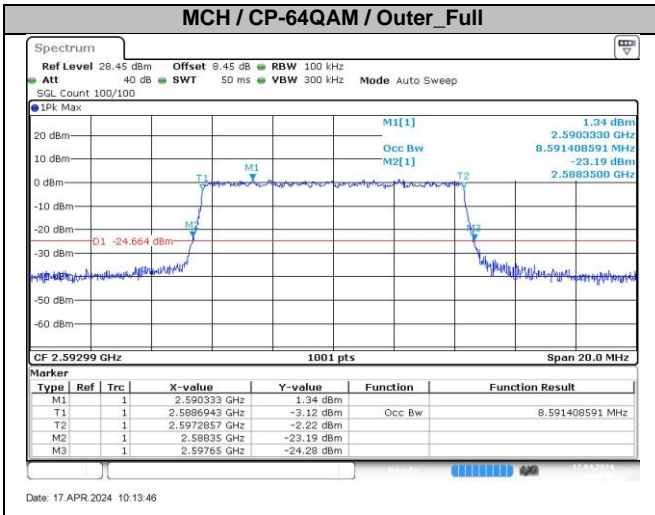






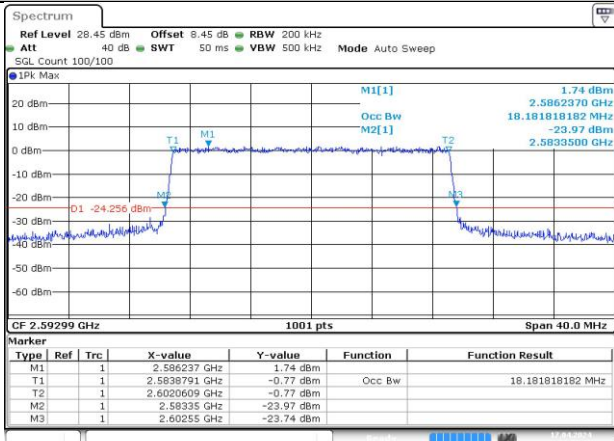






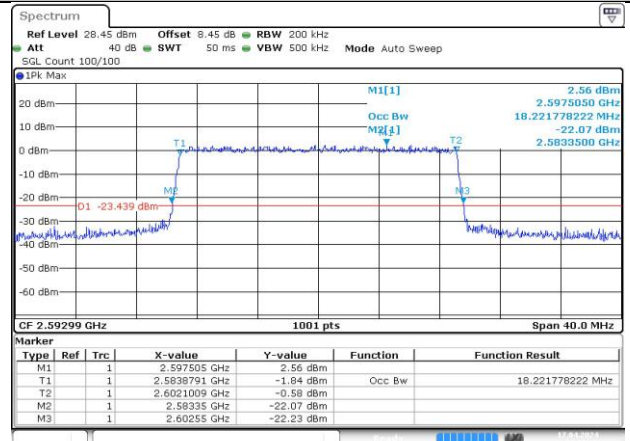
n41A / 30KHz / 20MHz

MCH / CP-QPSK / Outer_Full



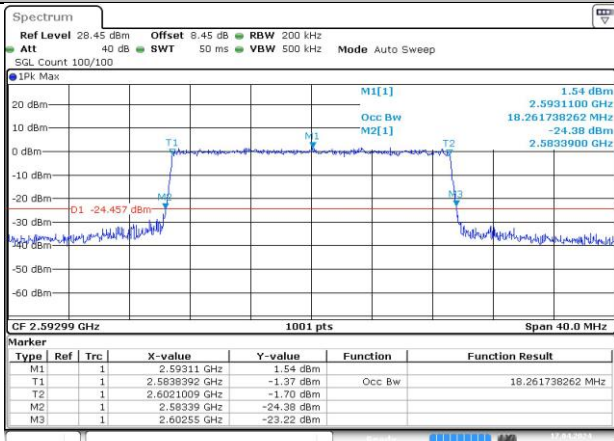
Date: 17 APR 2024 10:15:41

MCH / CP-16QAM / Outer_Full



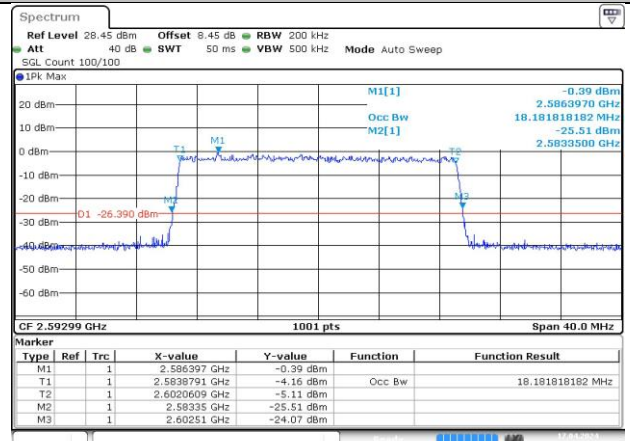
Date: 17 APR 2024 10:15:56

MCH / CP-64QAM / Outer_Full



Date: 17 APR 2024 10:16:12

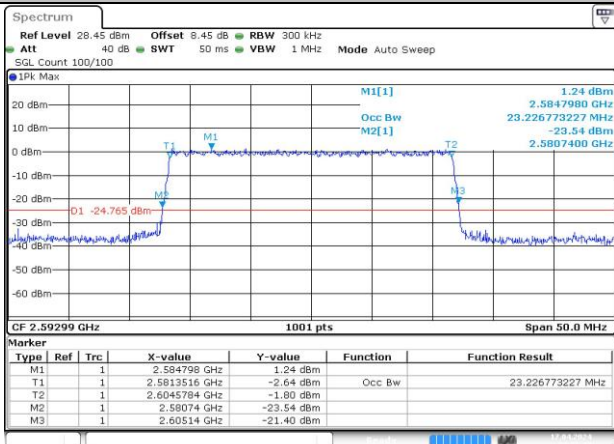
MCH / CP-256QAM / Outer_Full



Date: 17 APR 2024 10:16:28

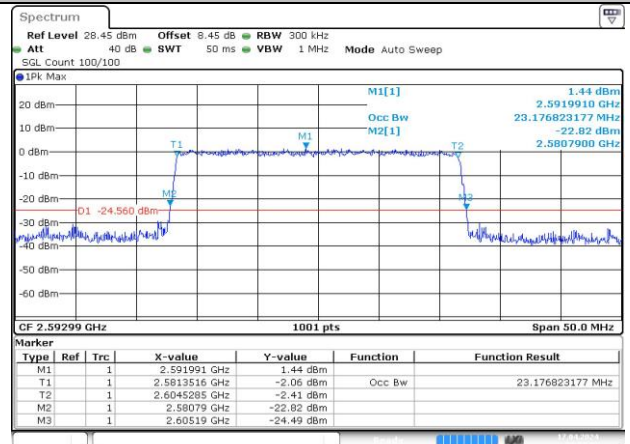
n41A / 30KHz / 25MHz

MCH / CP-QPSK / Outer_Full



Date: 17 APR 2024 10:16:45

MCH / CP-16QAM / Outer_Full



Date: 17 APR 2024 10:16:58

