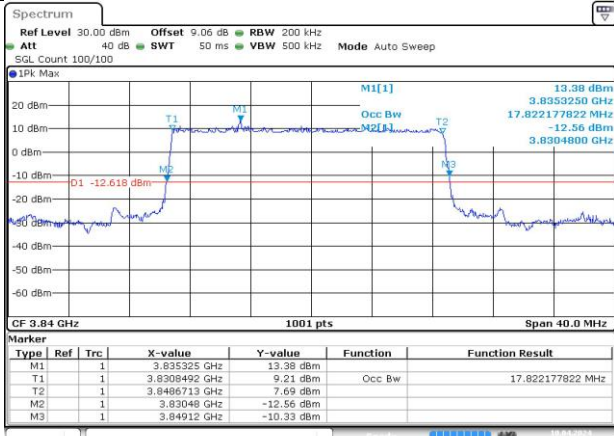


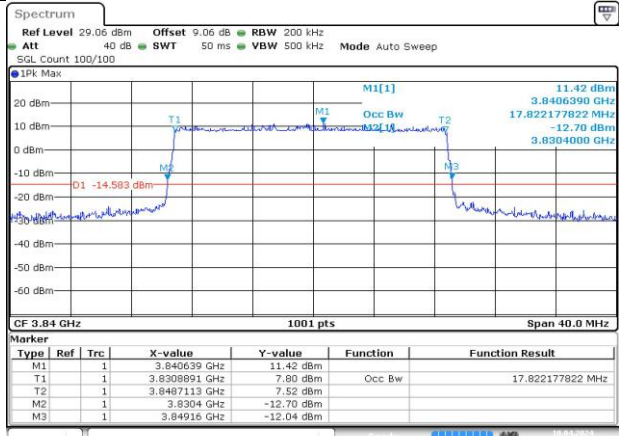
DC_7A_n77A / 30KHz / 20MHz

MCH_DFT-Pi2BPSK / Outer_Full



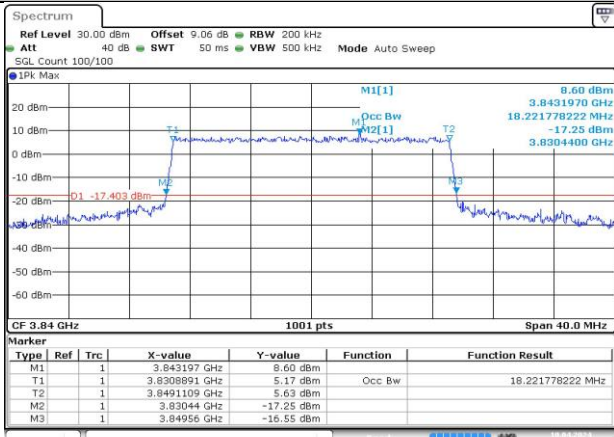
Date: 10 APR 2024 20:25:14

MCH_DFT-QPSK / Outer_Full



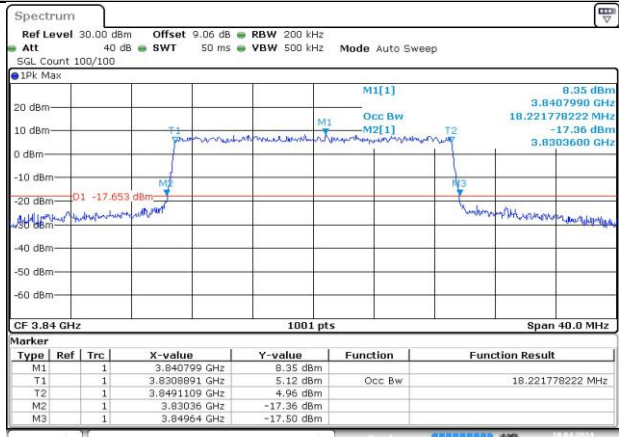
Date: 10 APR 2024 20:25:33

MCH_CP-QPSK / Outer_Full



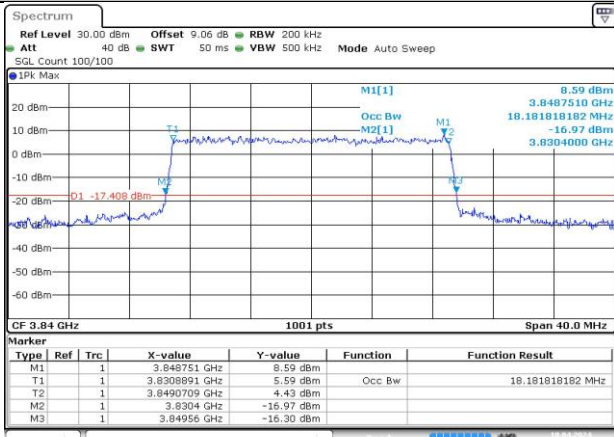
Date: 10 APR 2024 20:25:48

MCH_CP-16QAM / Outer_Full



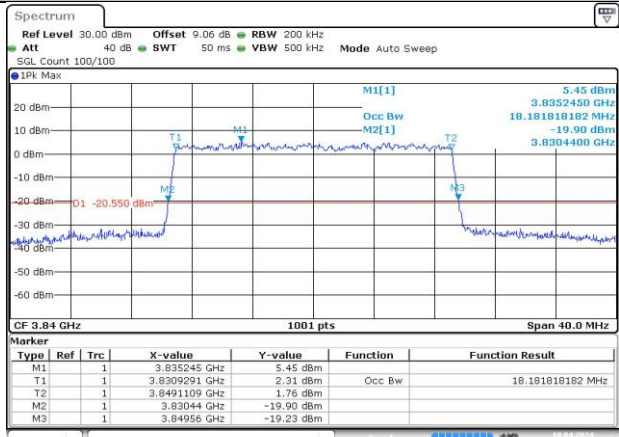
Date: 10 APR 2024 20:26:02

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:26:17

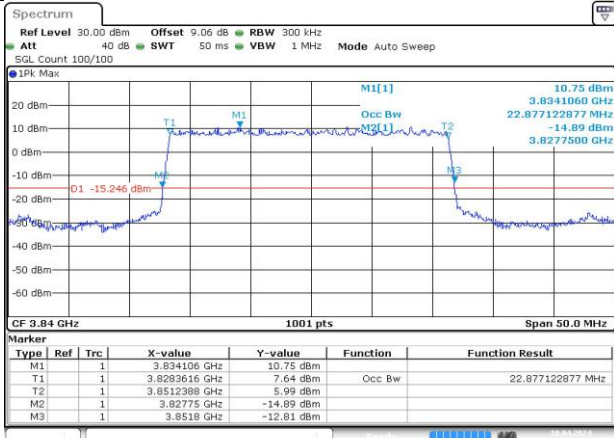
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:26:33

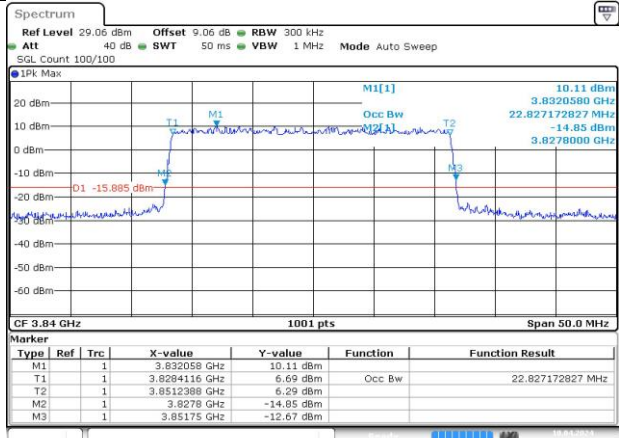
DC_7A_n77A / 30KHz / 25MHz

MCH_DFT-Pi2BPSK / Outer_Full



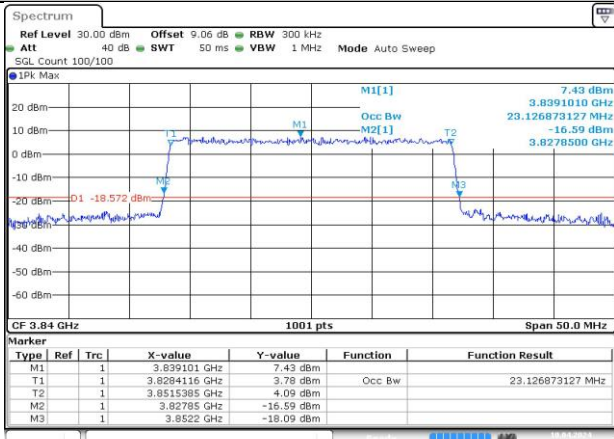
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MCH_DFT-QPSK / Outer_Full



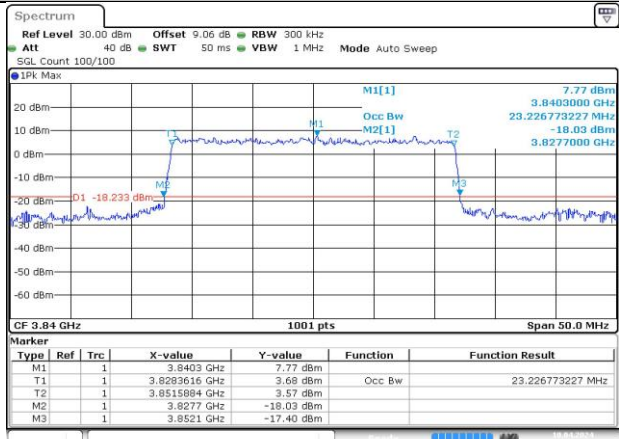
Date: 10 APR 2024 20:27:06

MCH_CP-QPSK / Outer_Full



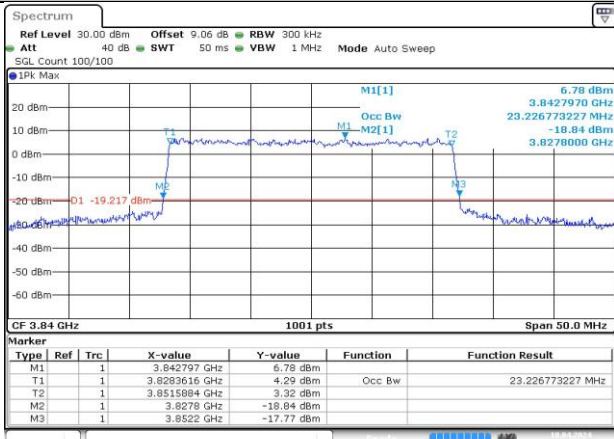
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MCH_CP-16QAM / Outer_Full



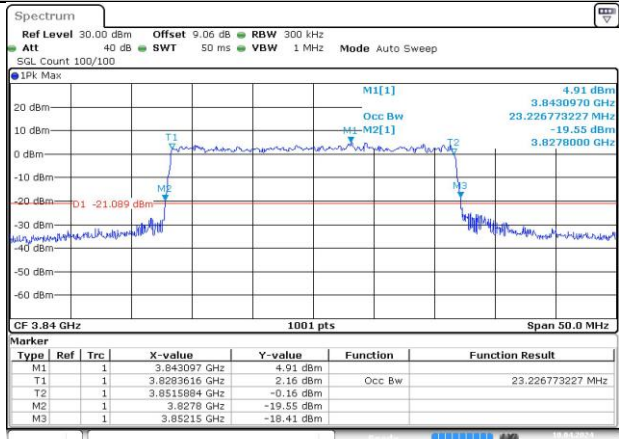
Date: 10 APR 2024 20:27:32

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:27:45

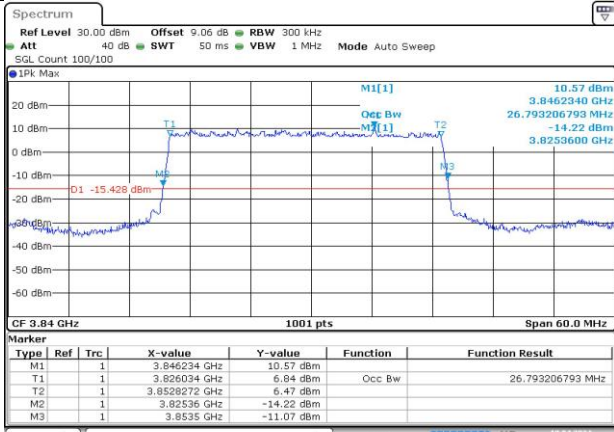
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:27:59

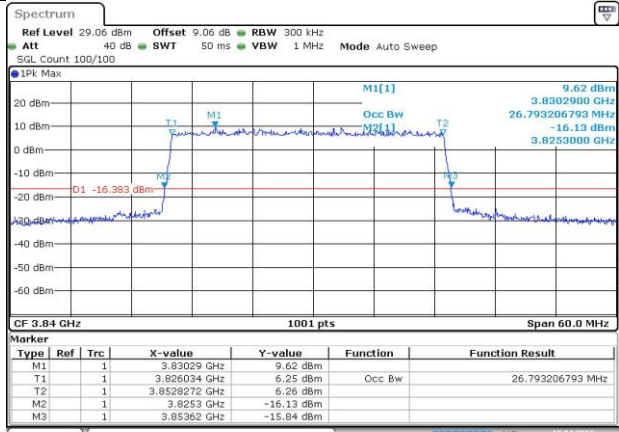
DC_7A_n77A / 30KHz / 30MHz

MCH_DFT-Pi2BPSK / Outer_Full



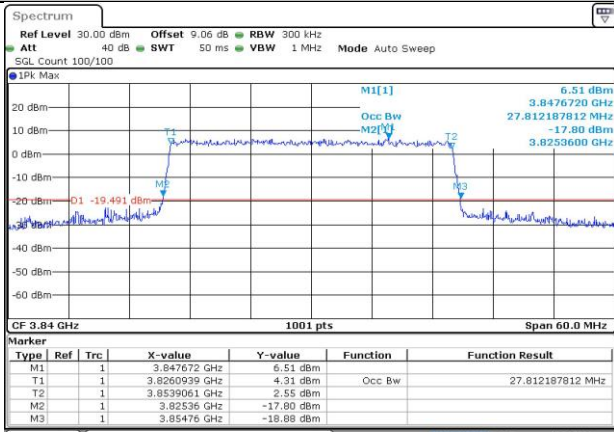
Date: 10 APR 2024 20:28:16

MCH_DFT-QPSK / Outer_Full



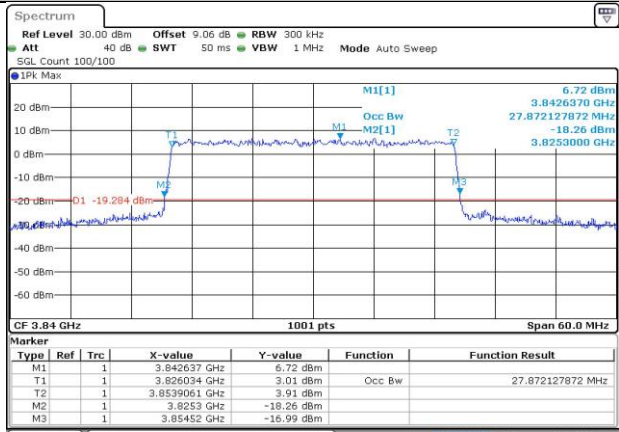
Date: 10 APR 2024 20:28:33

MCH_CP-QPSK / Outer_Full



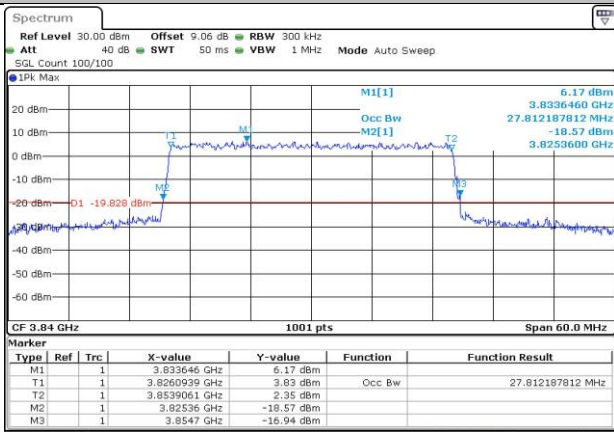
Date: 10 APR 2024 20:28:46

MCH_CP-16QAM / Outer_Full



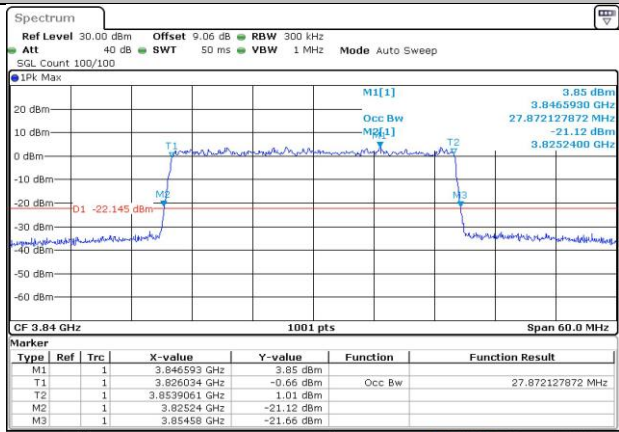
Date: 10 APR 2024 20:28:59

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:29:12

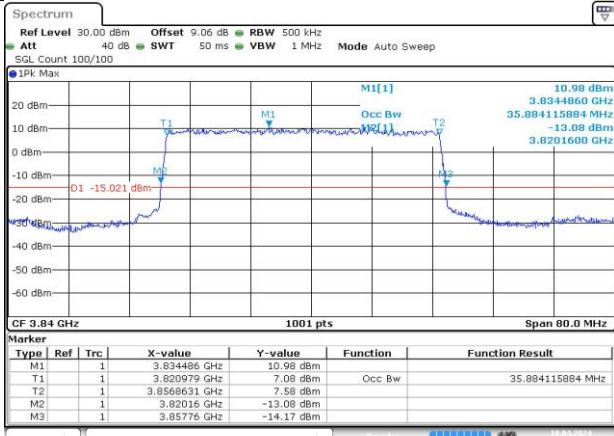
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:29:26

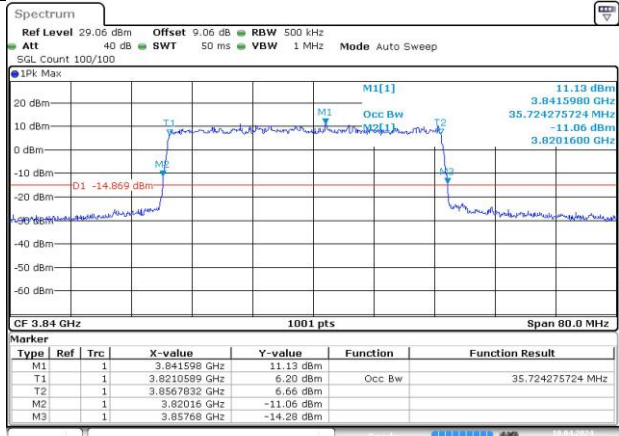
DC_7A_n77A / 30KHz / 40MHz

MCH_DFT-Pi2BPSK / Outer_Full



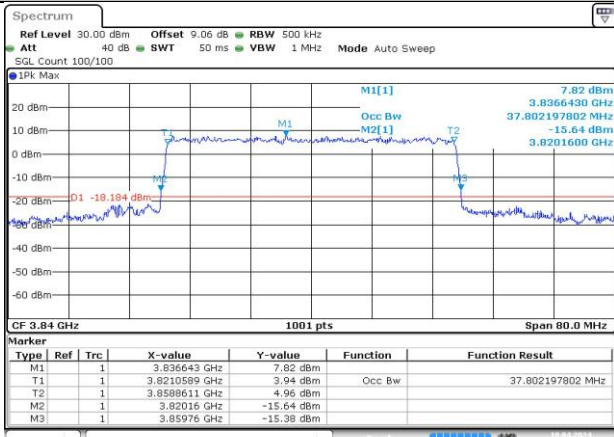
Date: 10 APR 2024 20:29:43

MCH_DFT-QPSK / Outer_Full



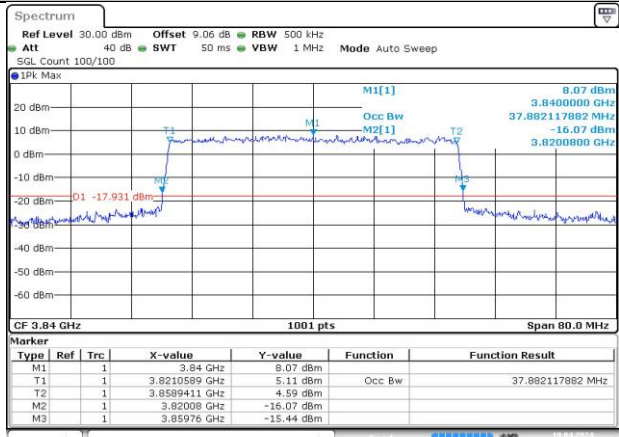
Date: 10 APR 2024 20:29:59

MCH_CP-QPSK / Outer_Full



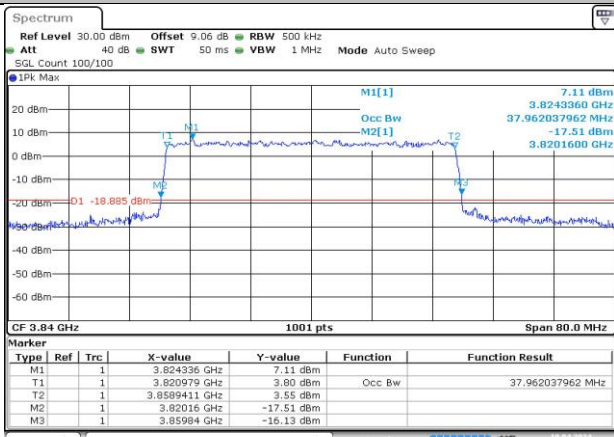
Date: 10 APR 2024 20:30:11

MCH_CP-16QAM / Outer_Full



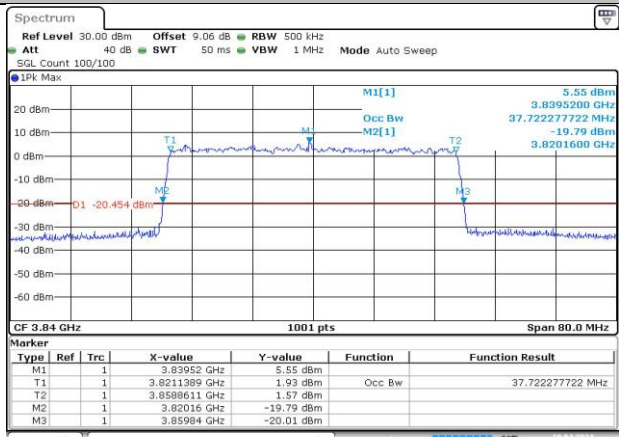
Date: 10 APR 2024 20:30:23

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:30:36

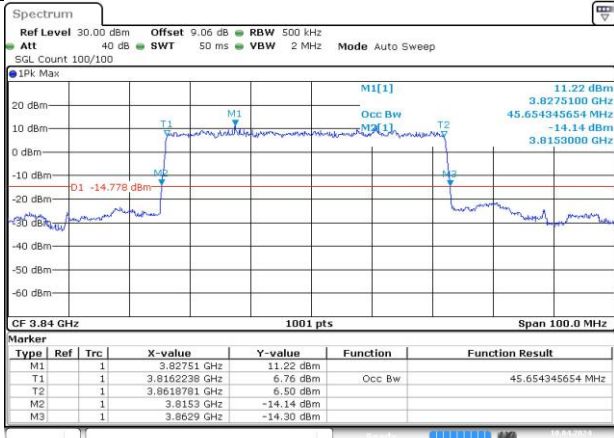
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:30:50

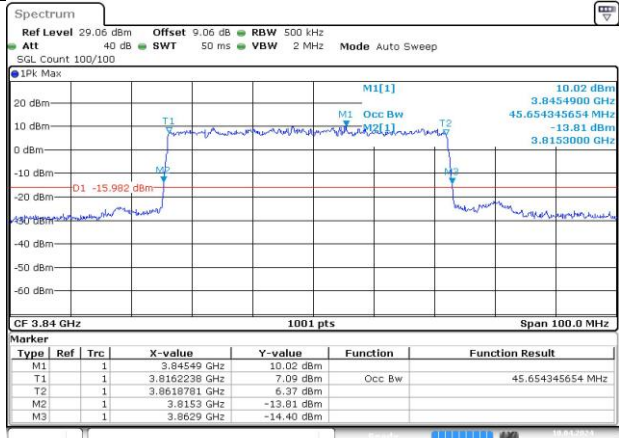
DC_7A_n77A / 30KHz / 50MHz

MCH_DFT-Pi2BPSK / Outer_Full



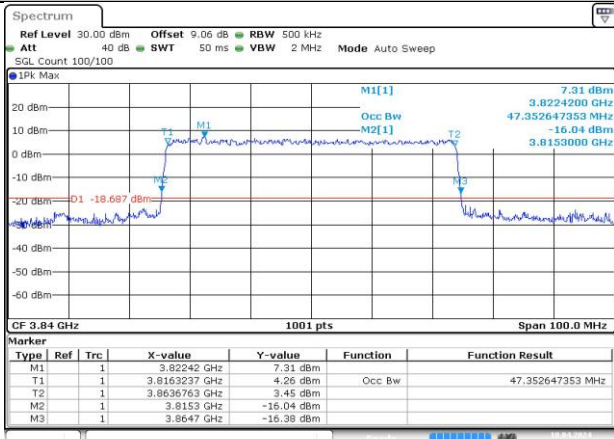
Date: 10 APR 2024 20:31:06

MCH_DFT-QPSK / Outer_Full



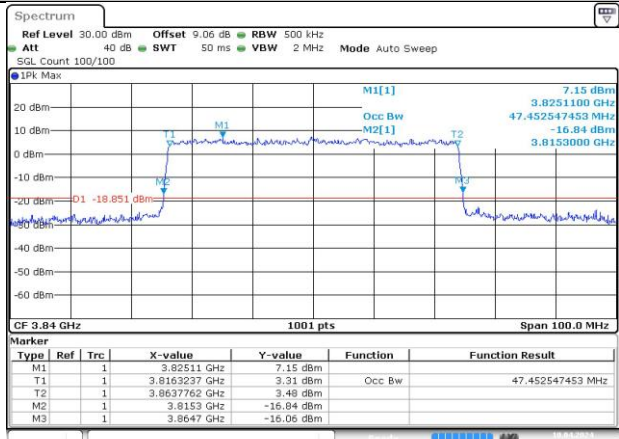
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MCH_CP-QPSK / Outer_Full



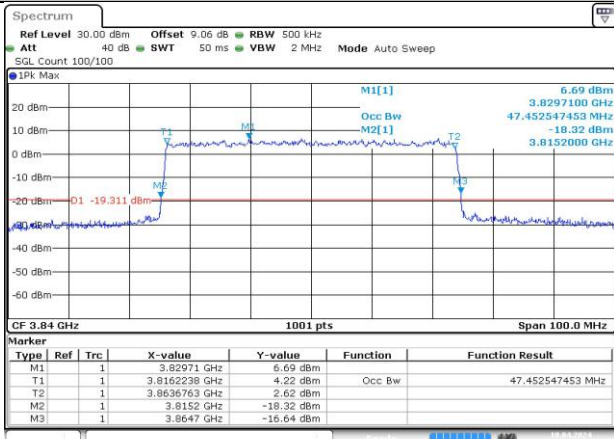
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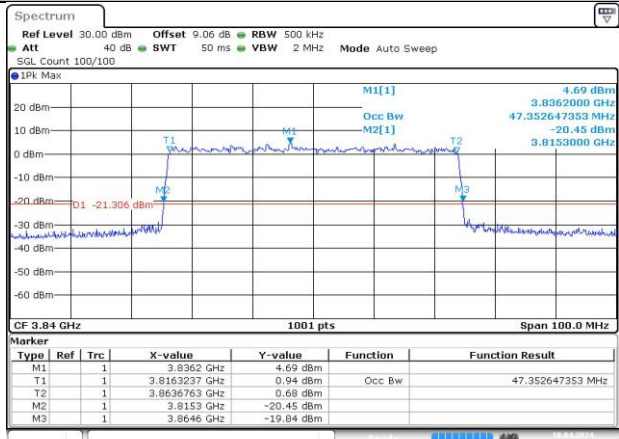
Date: 10 APR 2024 20:31:48

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:32:01

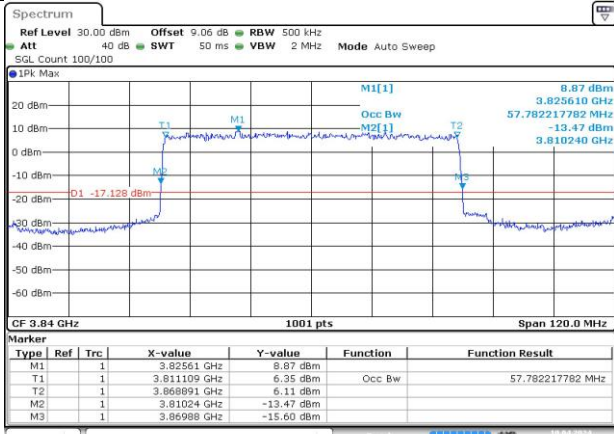
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:32:15

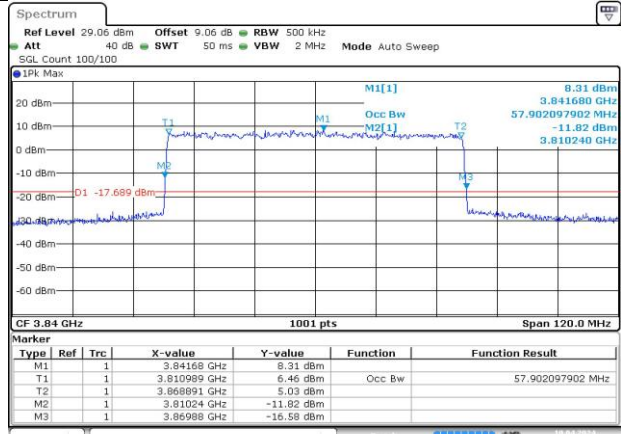
DC_7A_n77A / 30KHz / 60MHz

MCH_DFT-Pi2BPSK / Outer_Full



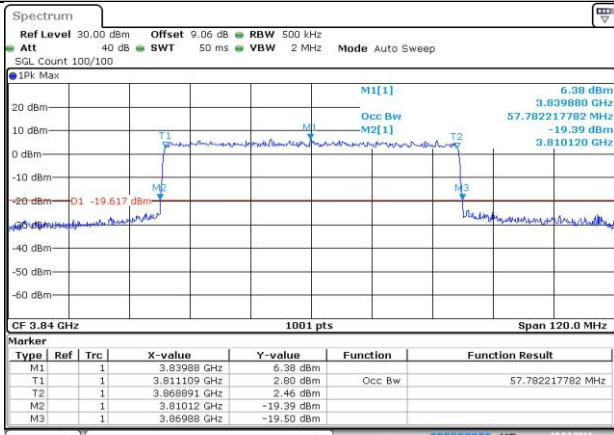
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MCH_DFT-QPSK / Outer_Full



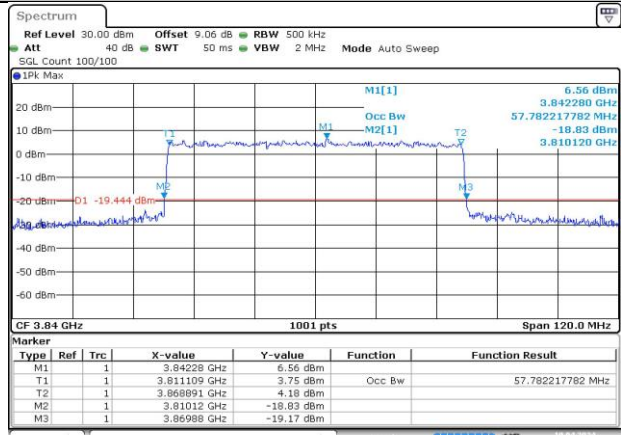
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MCH_CP-QPSK / Outer_Full



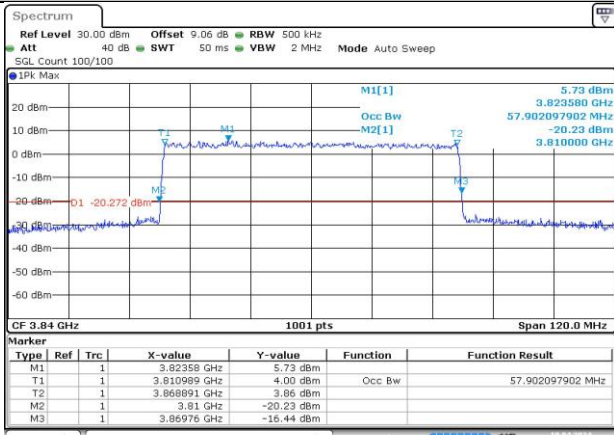
Date: 10 APR 2024 20:33:00

MCH_CP-16QAM / Outer_Full



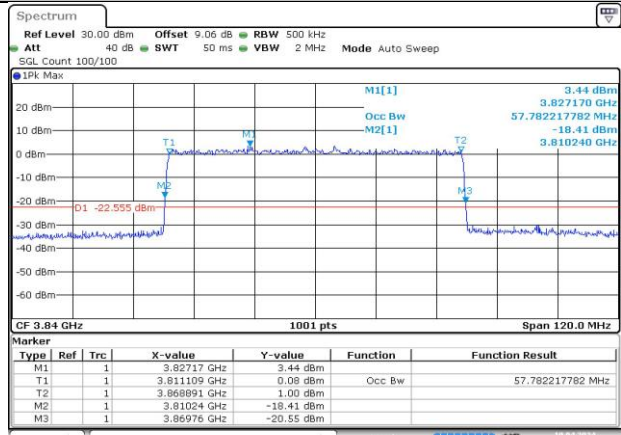
Date: 10 APR 2024 20:33:13

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:33:26

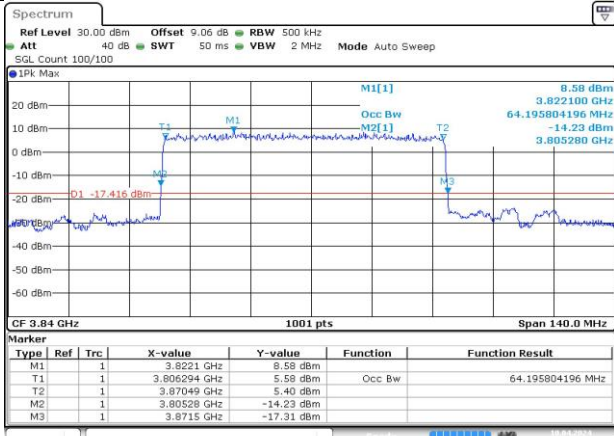
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:33:39

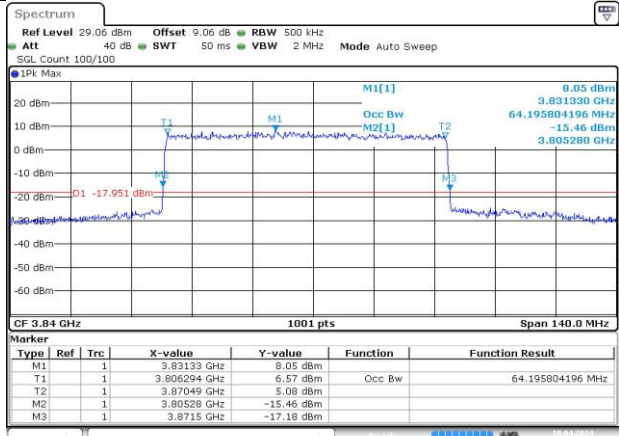
DC_7A_n77A / 30KHz / 70MHz

MCH_DFT-Pi2BPSK / Outer_Full



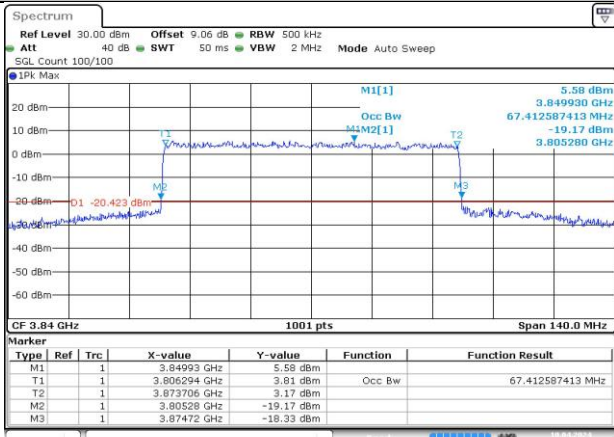
Date: 10 APR 2024 20:33:56

MCH_DFT-QPSK / Outer_Full



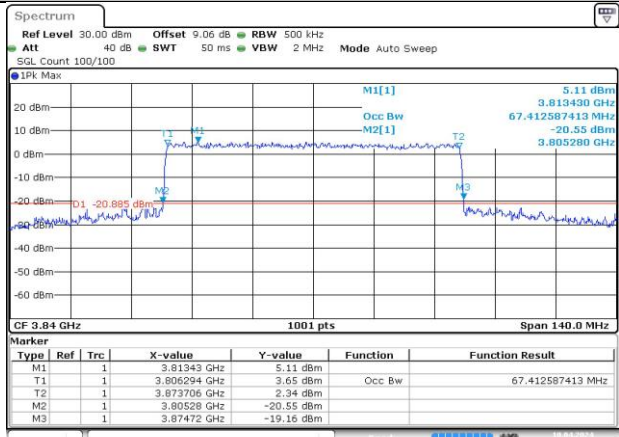
Date: 10 APR 2024 20:34:13

MCH_CP-QPSK / Outer_Full



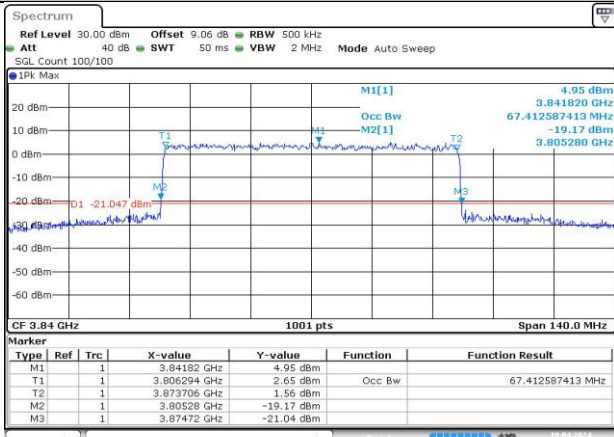
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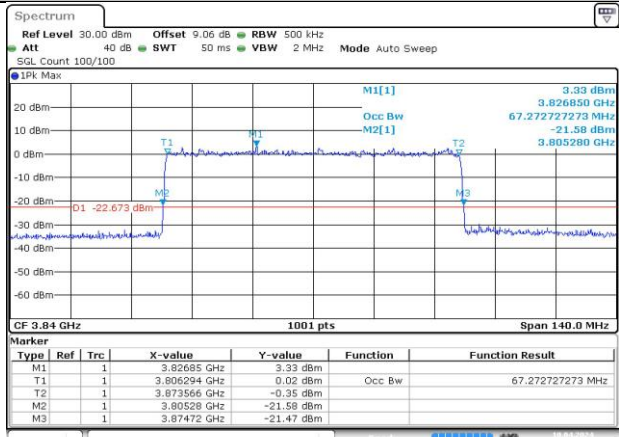
Date: 10 APR 2024 20:34:38

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:34:51

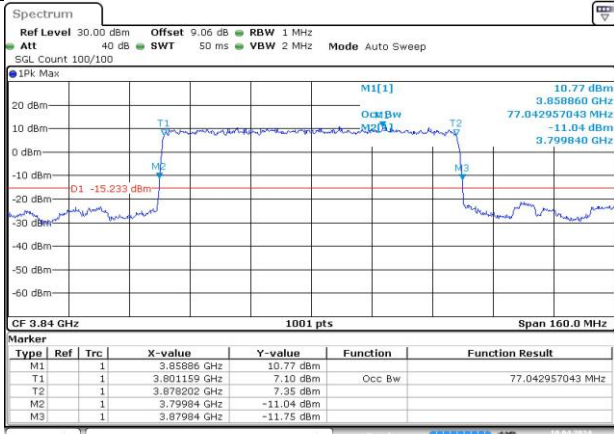
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:35:04

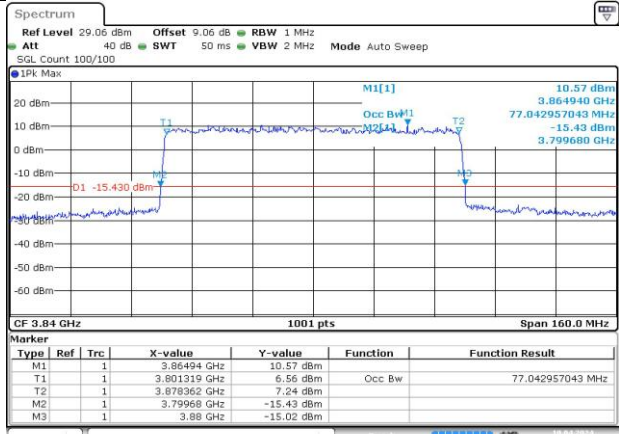
DC_7A_n77A / 30KHz / 80MHz

MCH_DFT-Pi2BPSK / Outer_Full



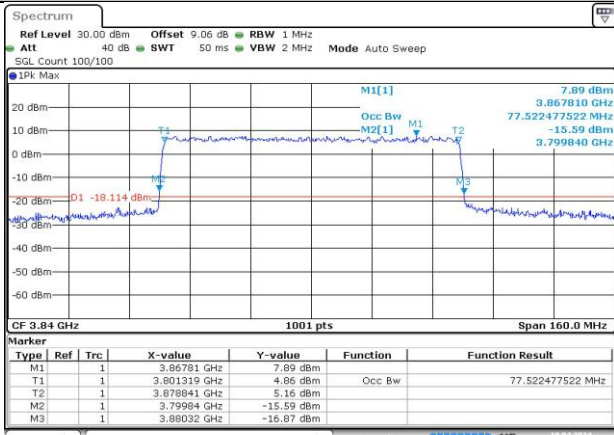
Date: 10 APR 2024 20:35:21

MCH_DFT-QPSK / Outer_Full



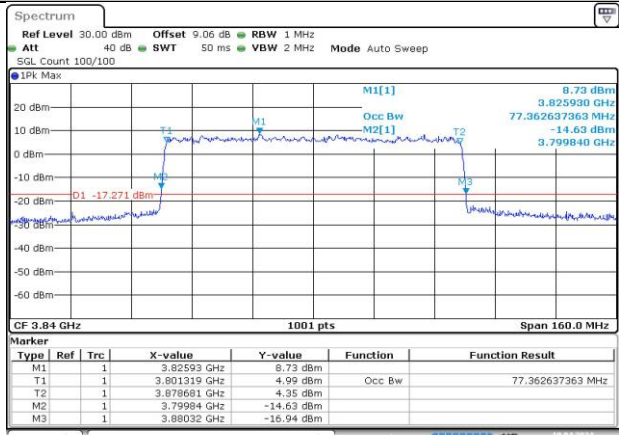
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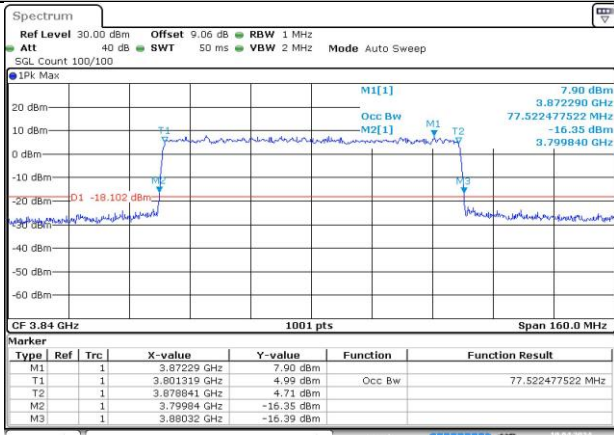
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MCH_CP-16QAM / Outer_Full



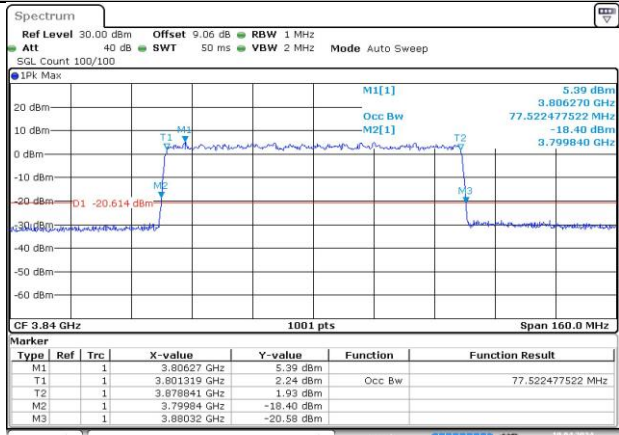
Date: 10 APR 2024 20:36:03

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:36:16

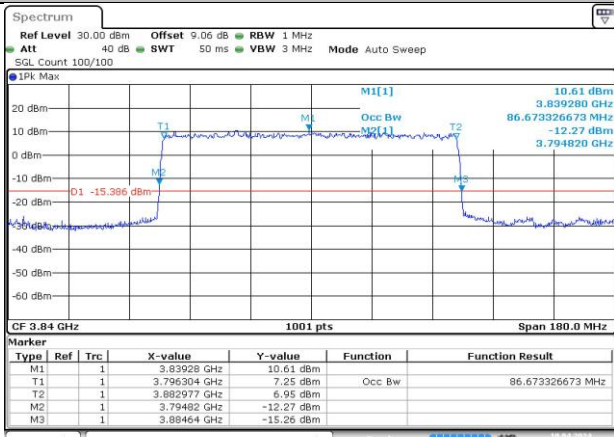
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 20:36:30

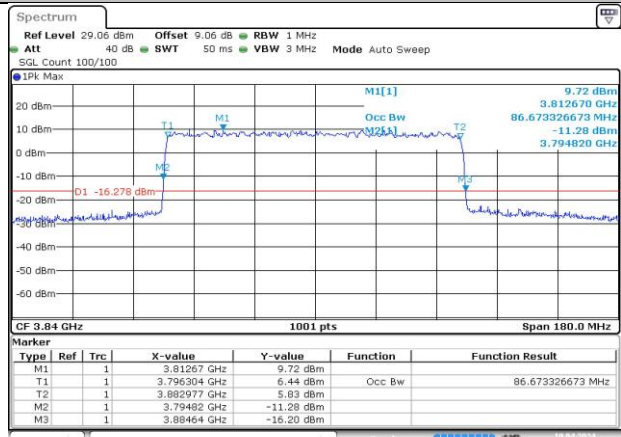
DC_7A_n77A / 30KHz / 90MHz

MCH_DFT-Pi2BPSK / Outer_Full



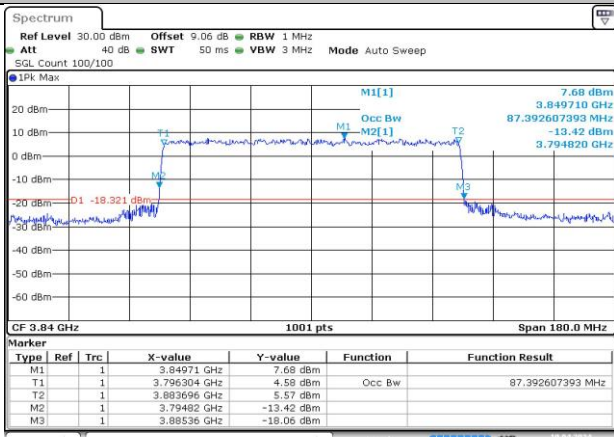
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MCH_DFT-QPSK / Outer_Full



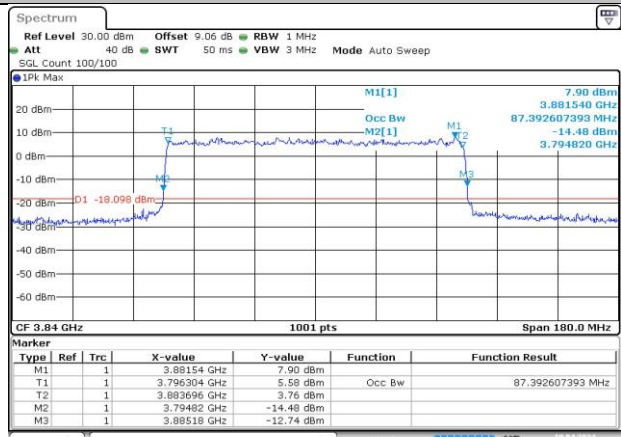
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MCH_CP-QPSK / Outer_Full



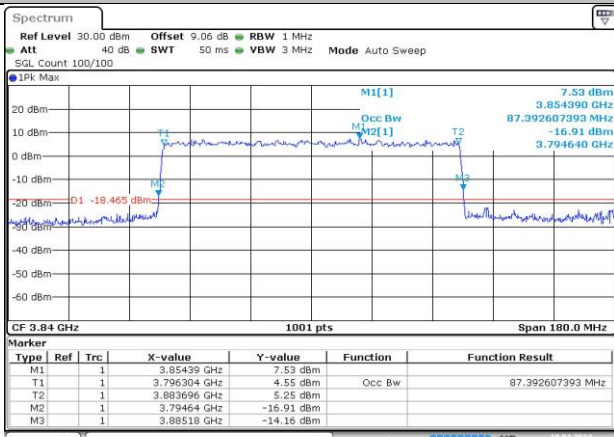
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MCH_CP-16QAM / Outer_Full



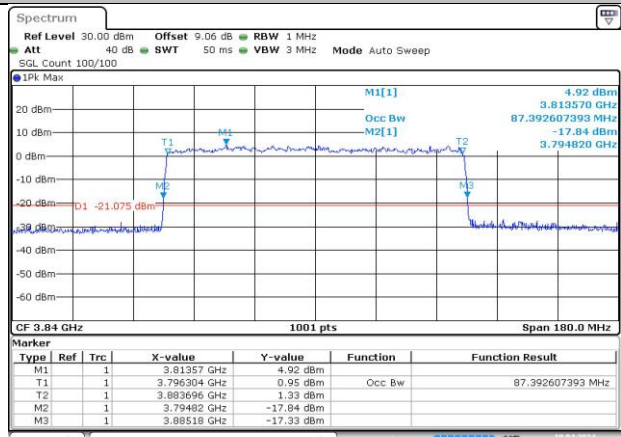
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MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:37:41

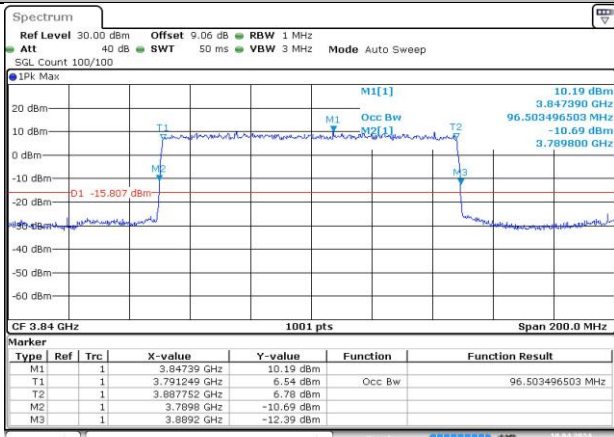
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Date: 10 APR 2024 20:37:55

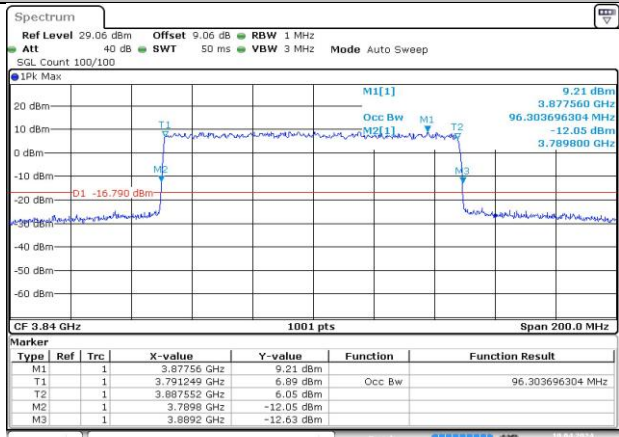
DC_7A_n77A / 30KHz / 100MHz

MCH_DFT-Pi2BPSK / Outer_Full



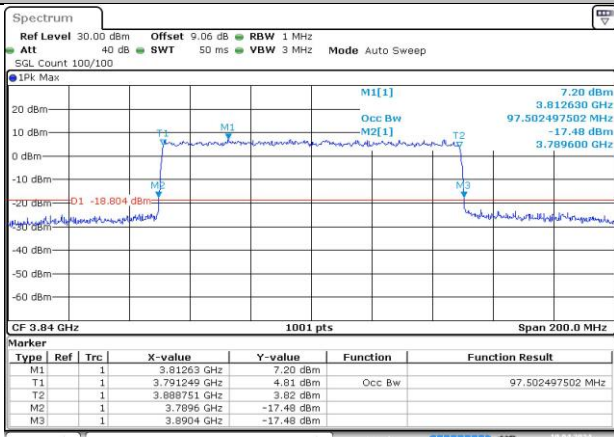
Date: 10 APR 2024 20:38:11

MCH_DFT-QPSK / Outer_Full



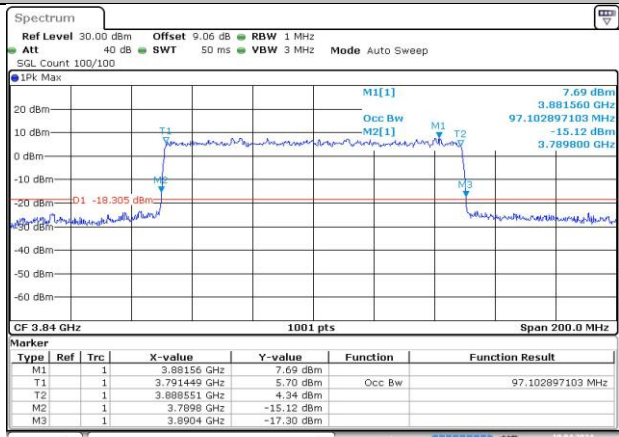
Date: 10 APR 2024 20:38:29

MCH_CP-QPSK / Outer_Full



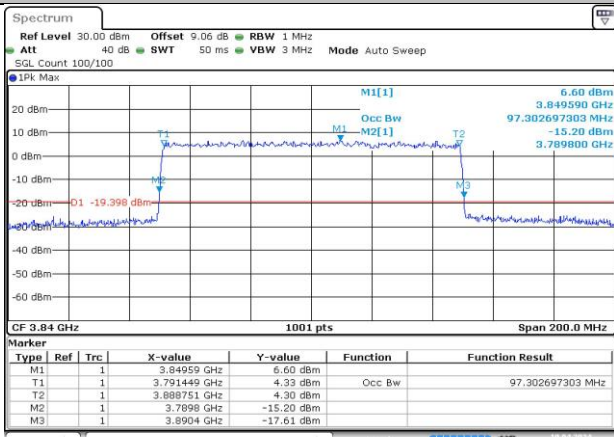
Date: 10 APR 2024 20:38:41

MCH_CP-16QAM / Outer_Full



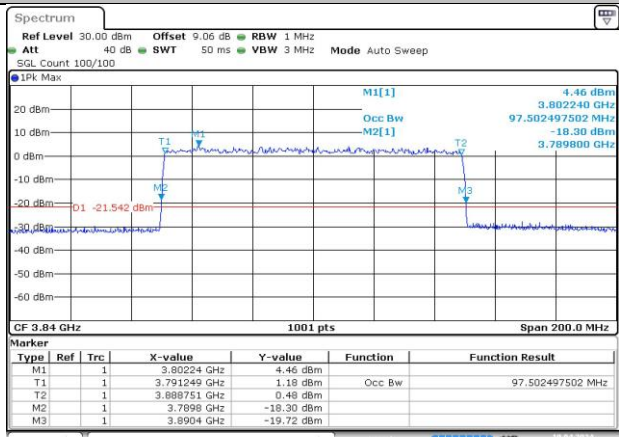
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MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 20:38:07

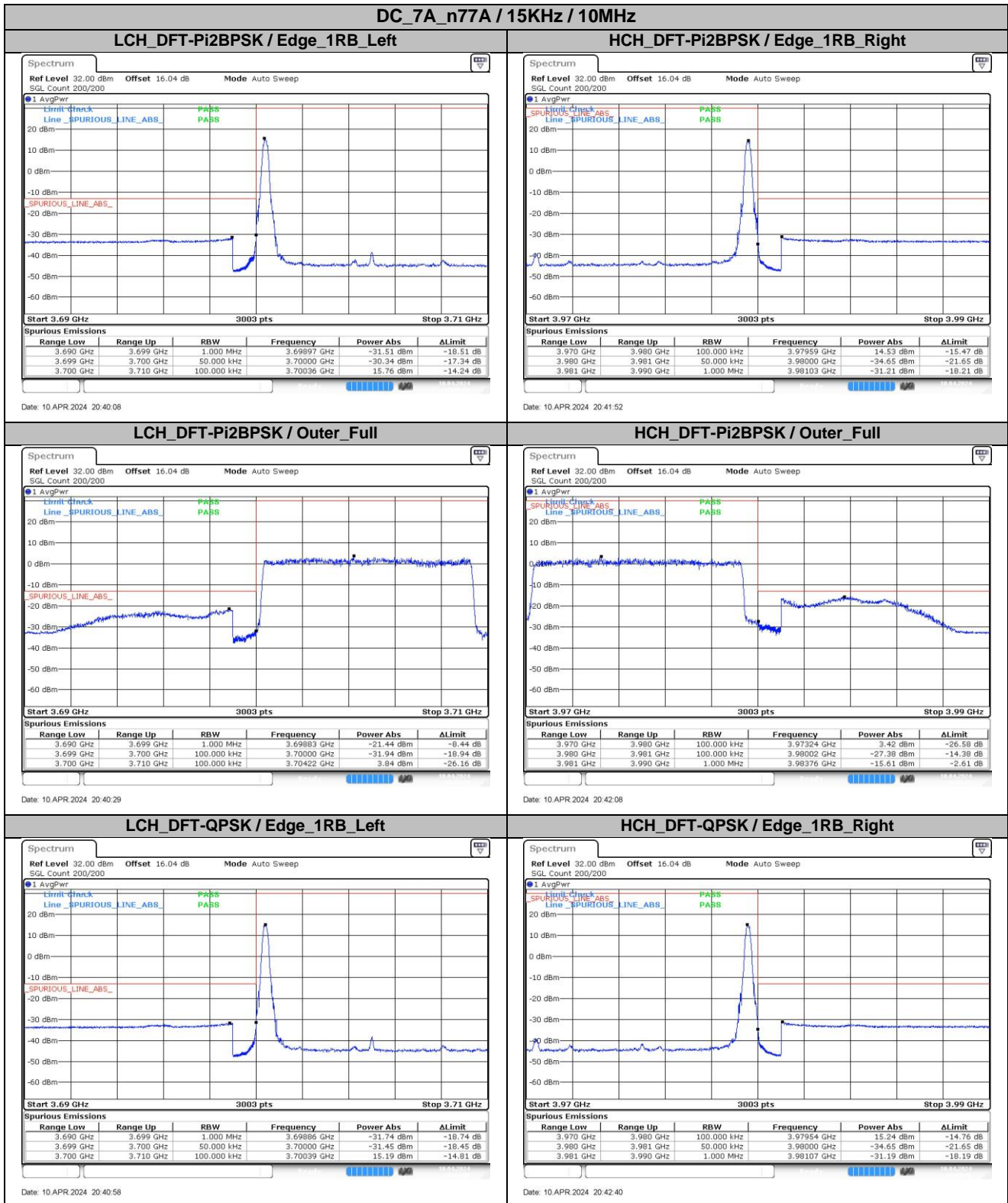
MCH_CP-256QAM / Outer_Full

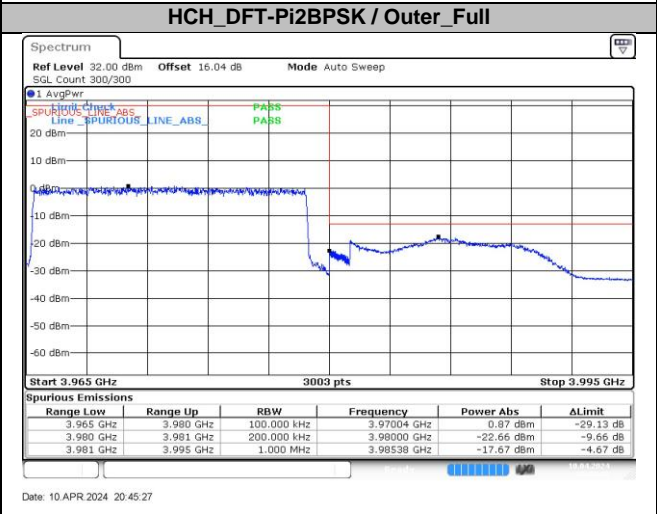
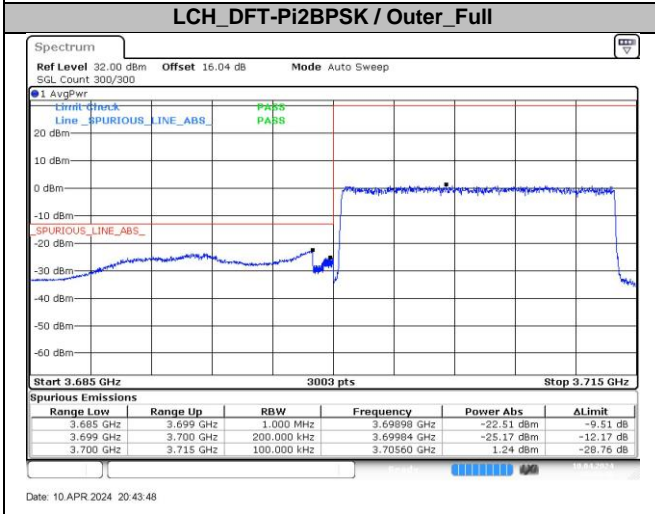
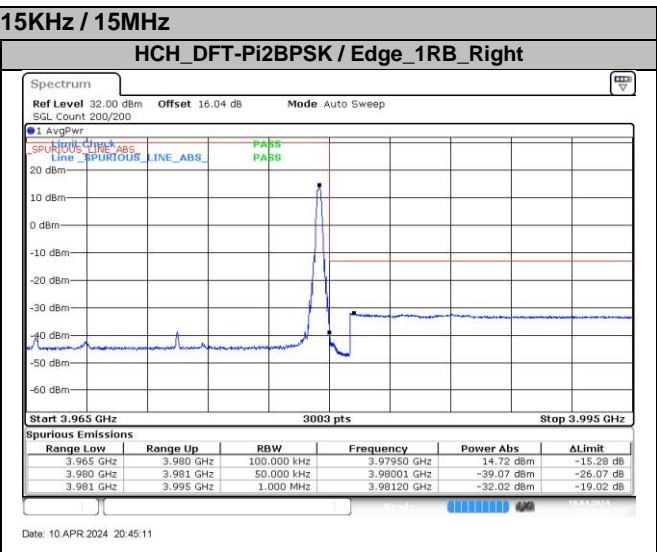
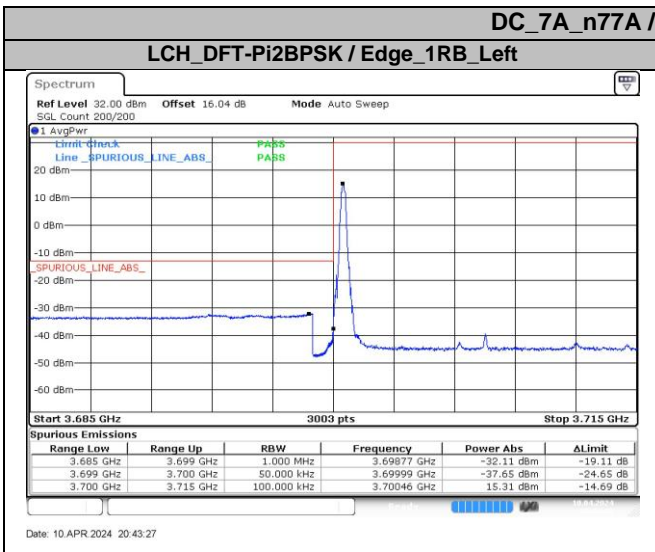
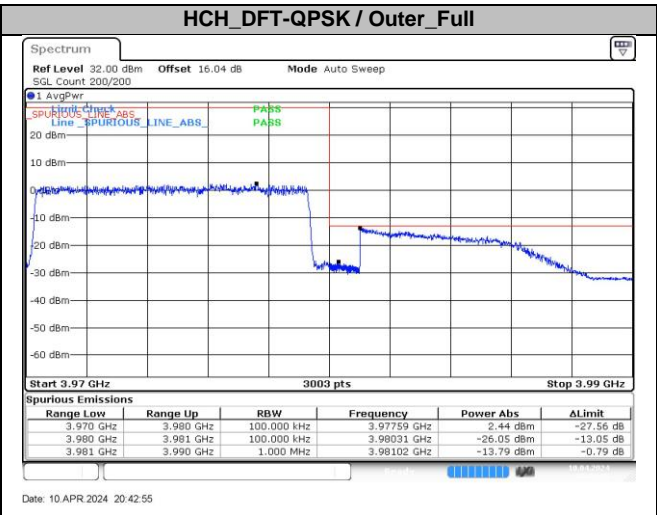
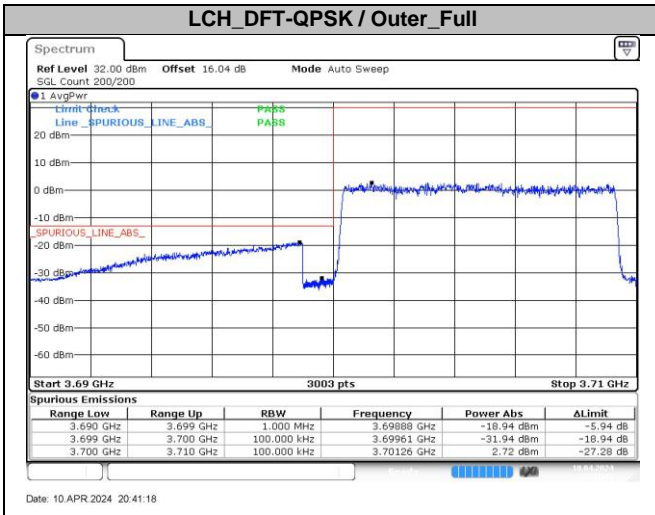


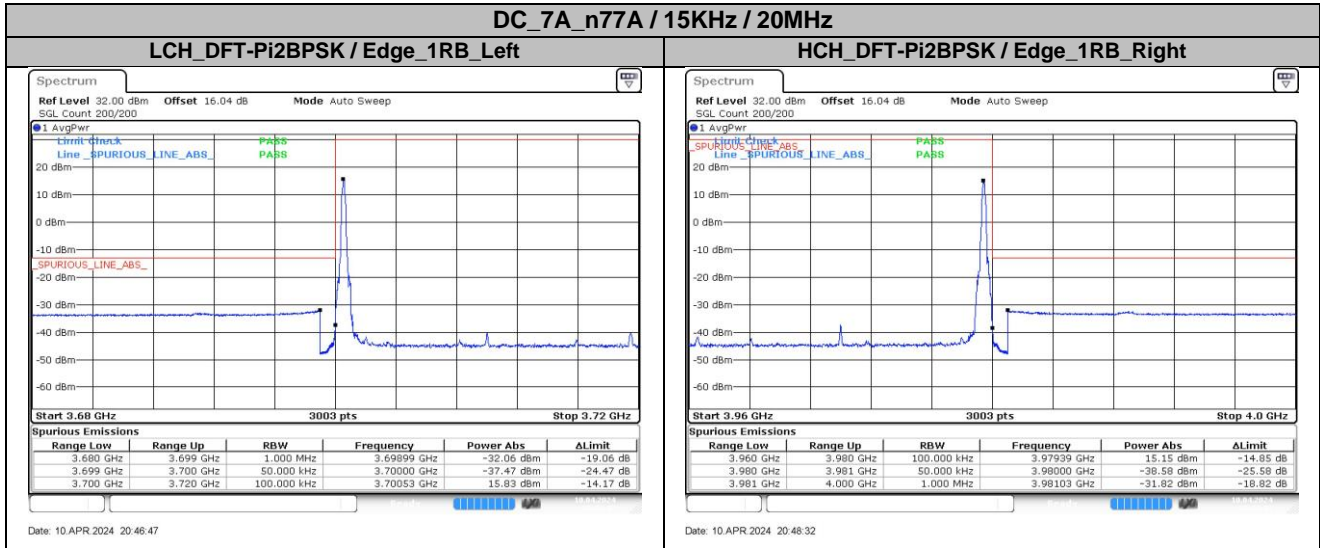
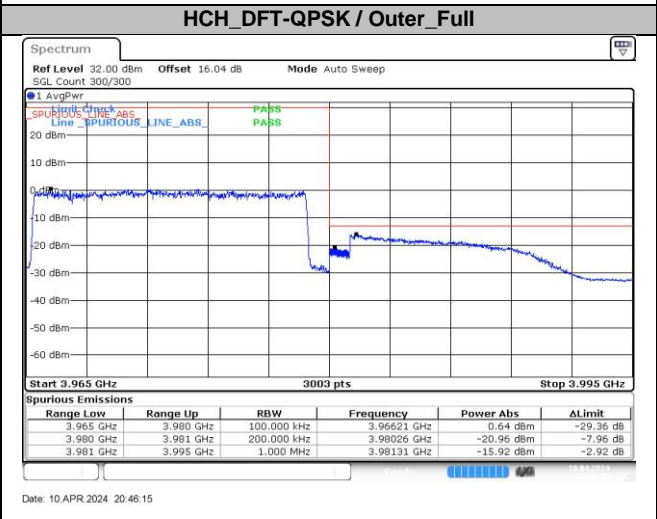
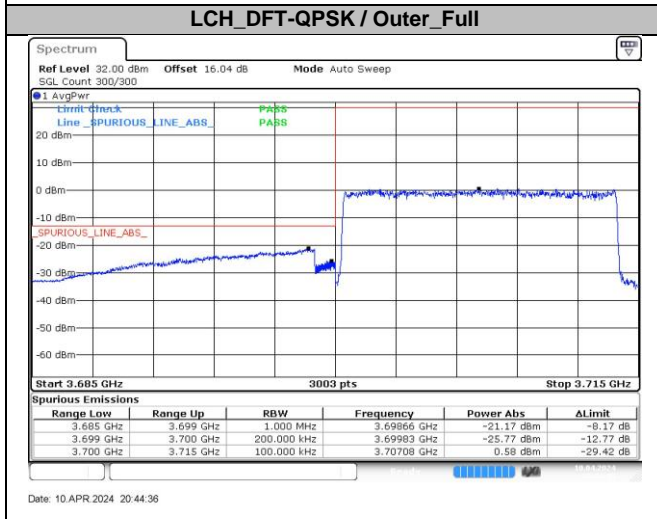
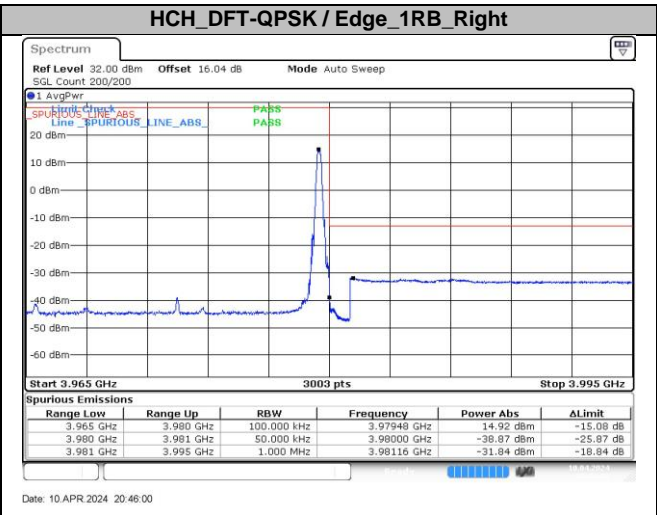
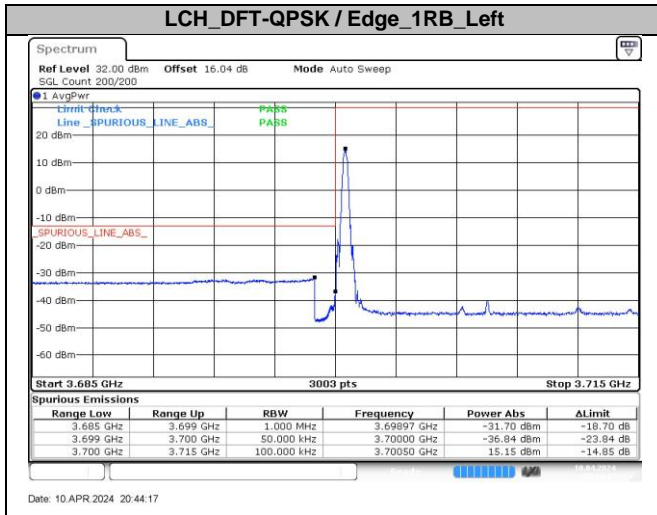
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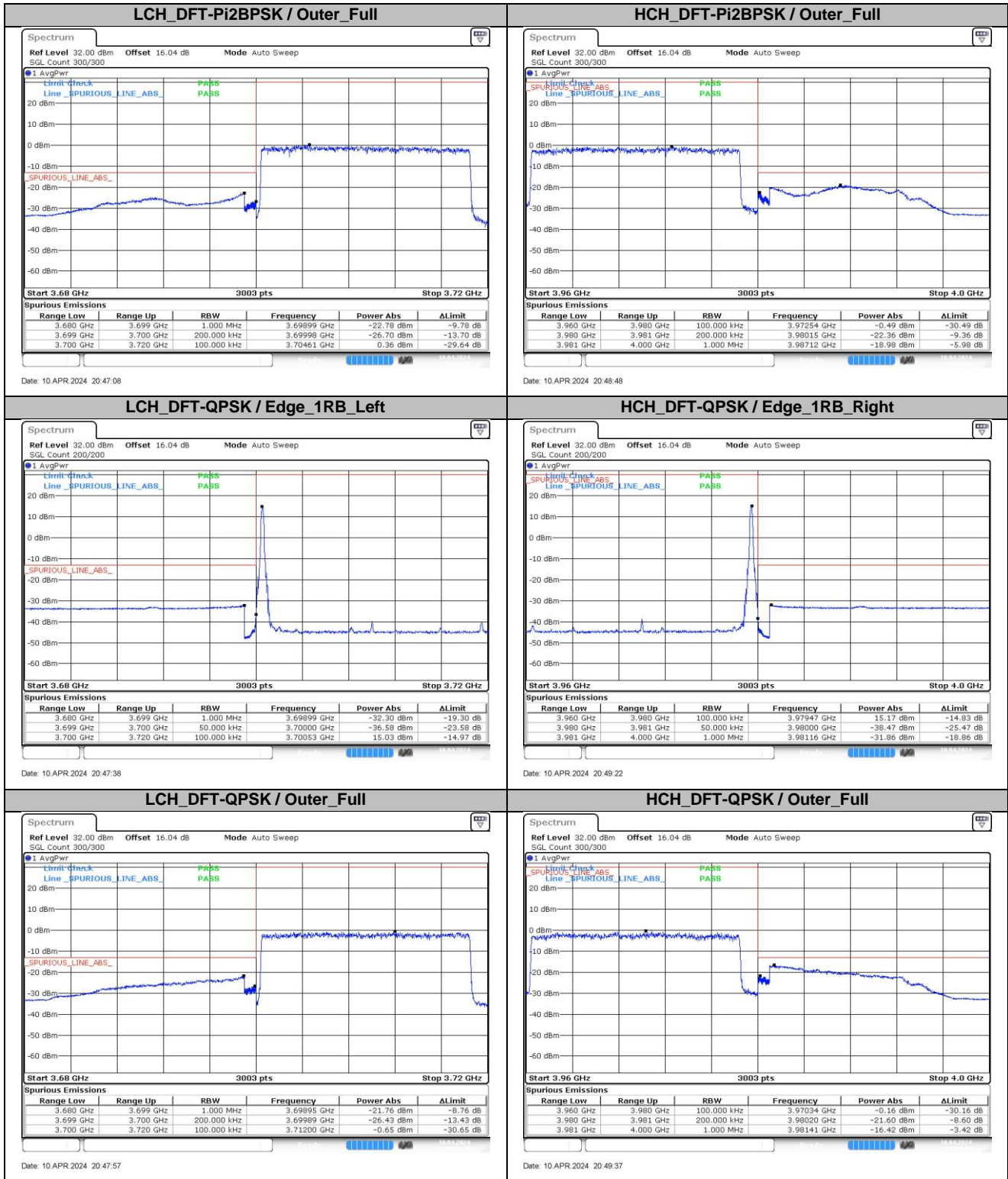
5. Conducted Band Edges

5.1. Test Plots



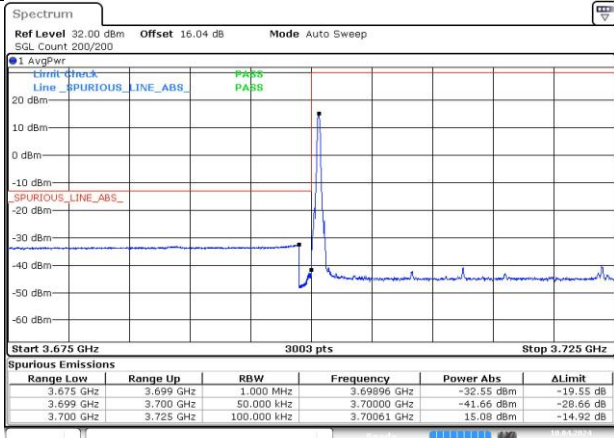






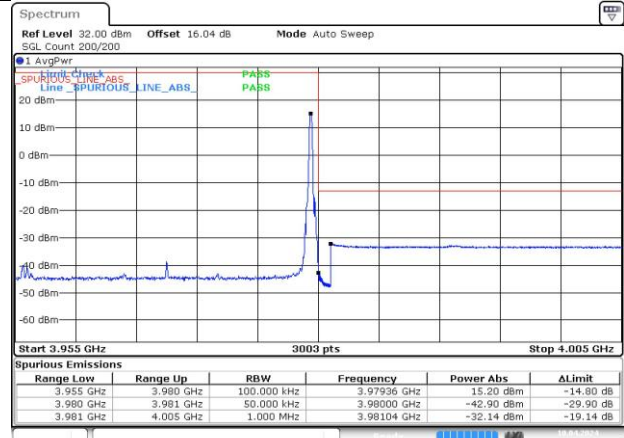
DC_7A_n77A / 15KHz / 25MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 10 APR 2024 20:50:10

HCH_DFT-Pi2BPSK / Edge_1RB_Right



Date: 10 APR 2024 20:52:41

LCH_DFT-Pi2BPSK / Outer_Full



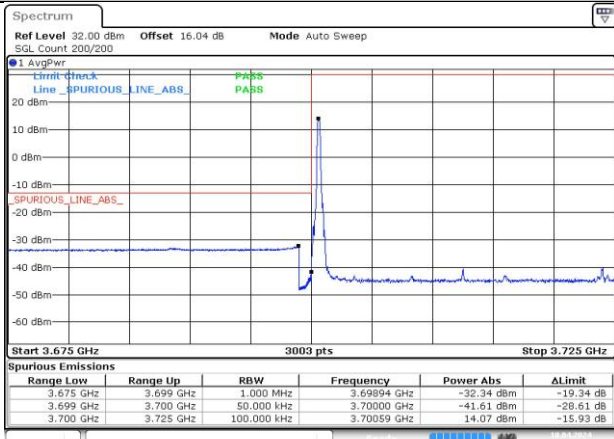
Date: 10 APR 2024 20:50:31

HCH_DFT-Pi2BPSK / Outer_Full



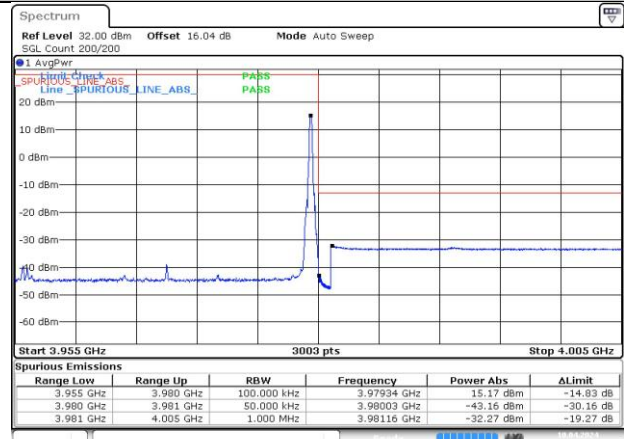
Date: 10 APR 2024 20:52:57

LCH_DFT-QPSK / Edge_1RB_Left

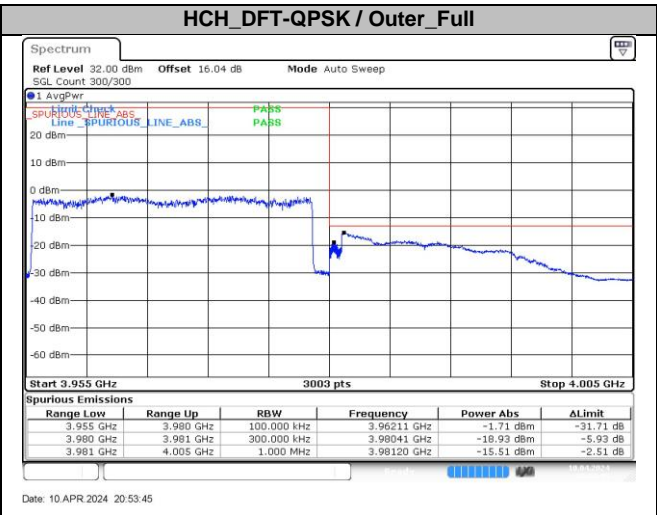
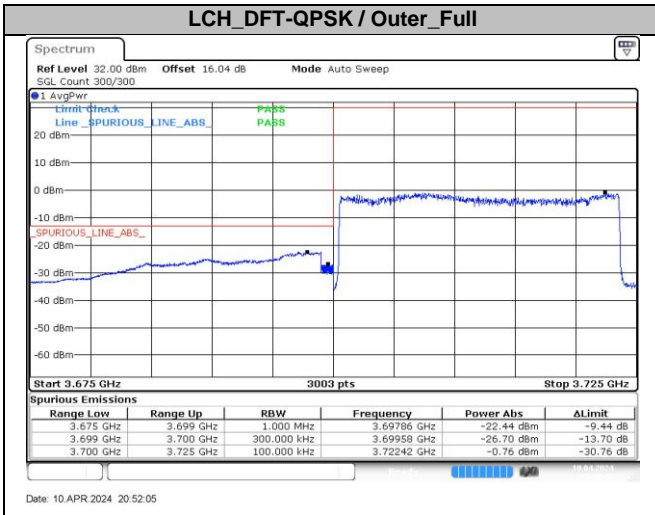


Date: 10 APR 2024 20:51:01

HCH_DFT-QPSK / Edge_1RB_Right



Date: 10 APR 2024 20:53:30



DC_7A_n77A / 15KHz / 30MHz

