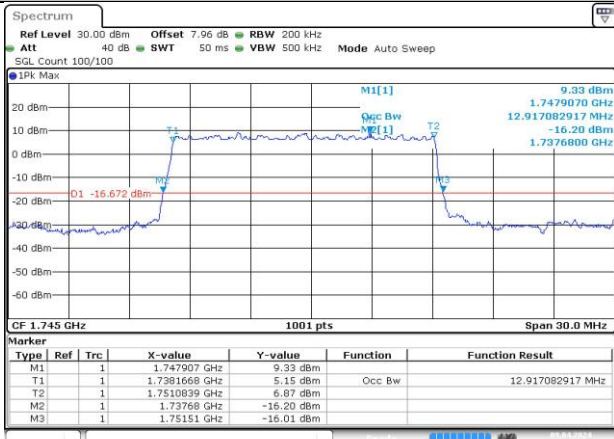


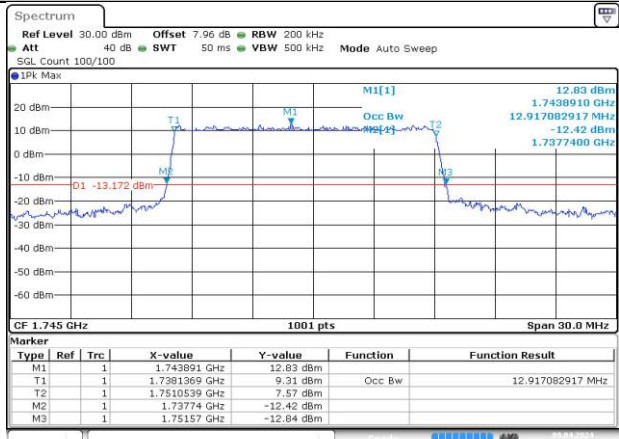
DC_7A_n66A / 30KHz / 15MHz

MCH_DFT-Pi2BPSK / Outer_Full



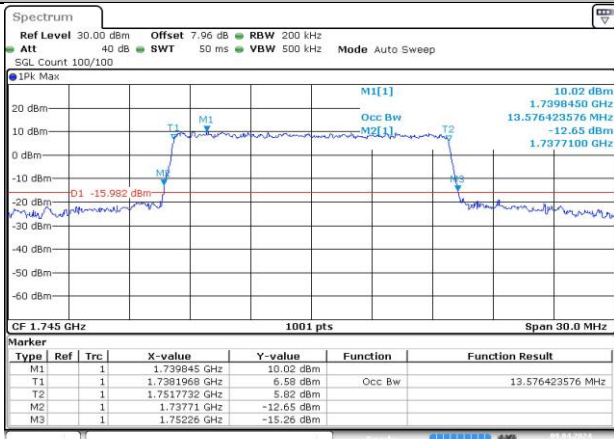
Date: 9 APR 2024 10:44:08

MCH_DFT-QPSK / Outer_Full



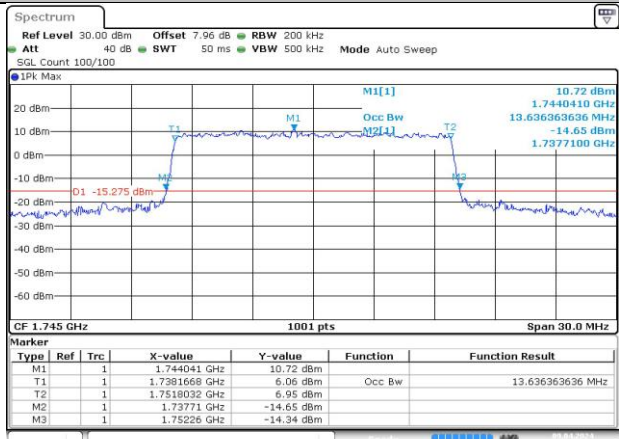
Date: 9 APR 2024 10:44:21

MCH_CP-QPSK / Outer_Full



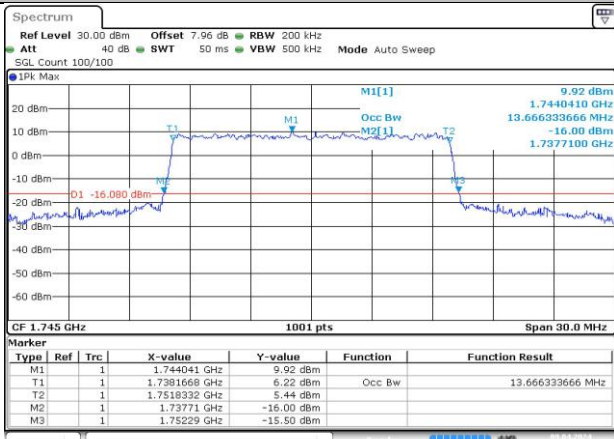
Date: 9 APR 2024 10:44:35

MCH_CP-16QAM / Outer_Full



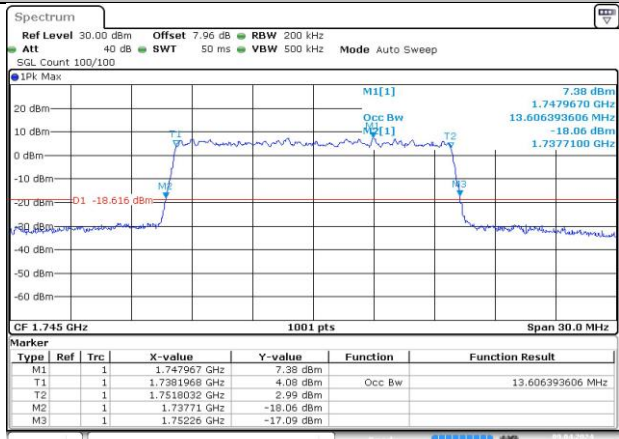
Date: 9 APR 2024 10:44:48

MCH_CP-64QAM / Outer_Full



Date: 9 APR 2024 10:45:02

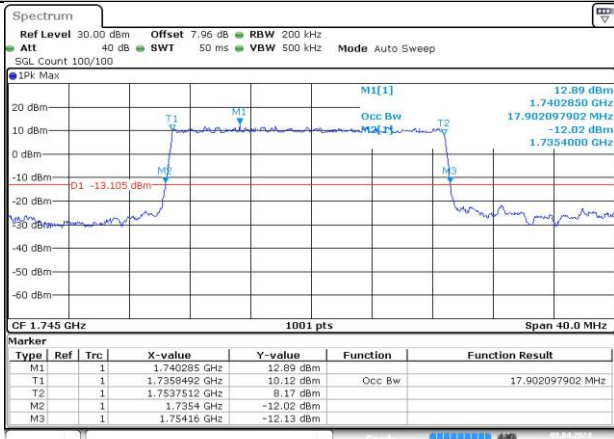
MCH_CP-256QAM / Outer_Full



Date: 9 APR 2024 10:45:15

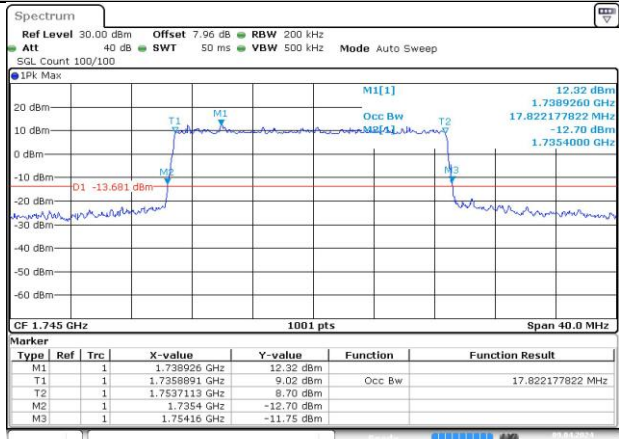
DC_7A_n66A / 30KHz / 20MHz

MCH_DFT-Pi2BPSK / Outer_Full



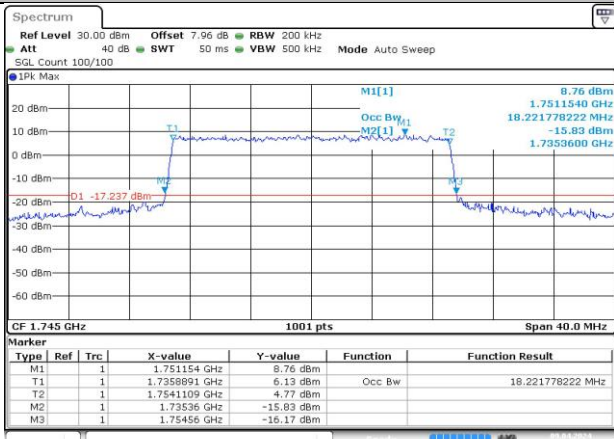
Date: 9 APR 2024 10:45:33

MCH_DFT-QPSK / Outer_Full



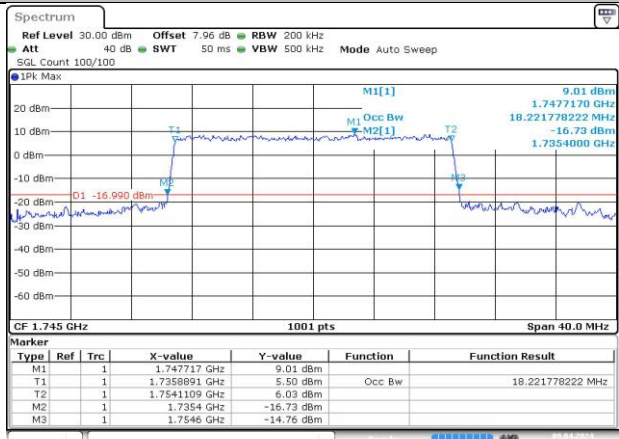
Date: 9 APR 2024 10:45:47

MCH_CP-QPSK / Outer_Full



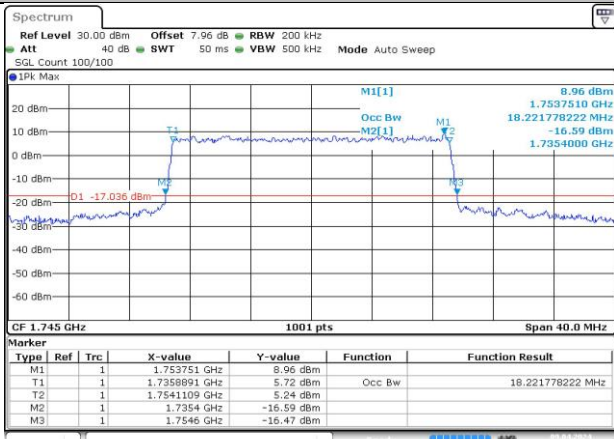
Date: 9 APR 2024 10:46:01

MCH_CP-16QAM / Outer_Full



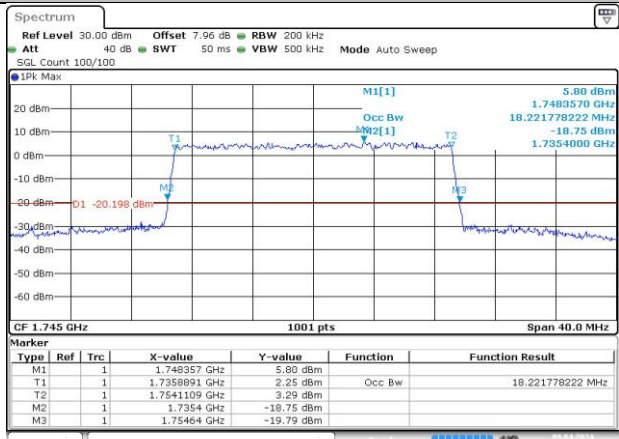
Date: 9 APR 2024 10:46:14

MCH_CP-64QAM / Outer_Full



Date: 9 APR 2024 10:46:28

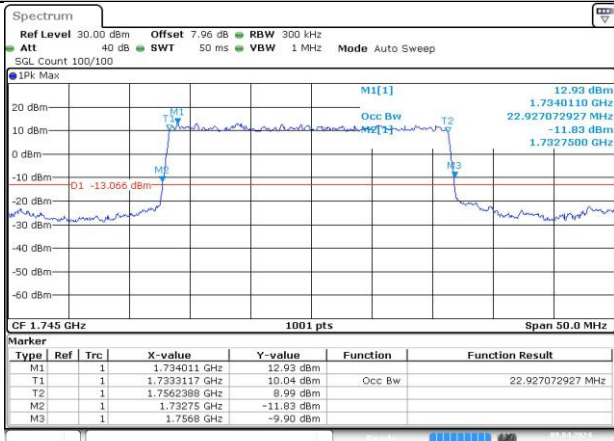
MCH_CP-256QAM / Outer_Full



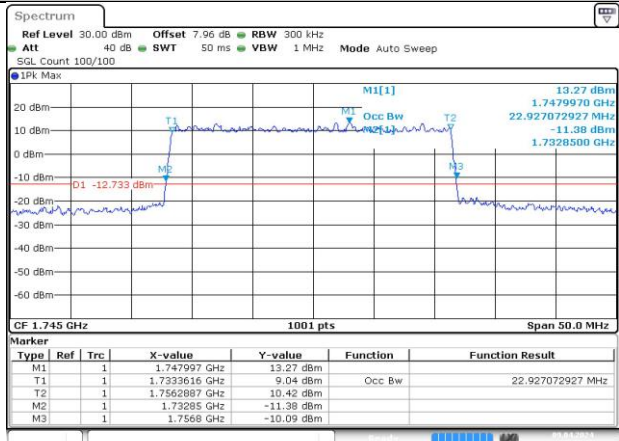
Date: 9 APR 2024 10:46:42

DC_7A_n66A / 30KHz / 25MHz

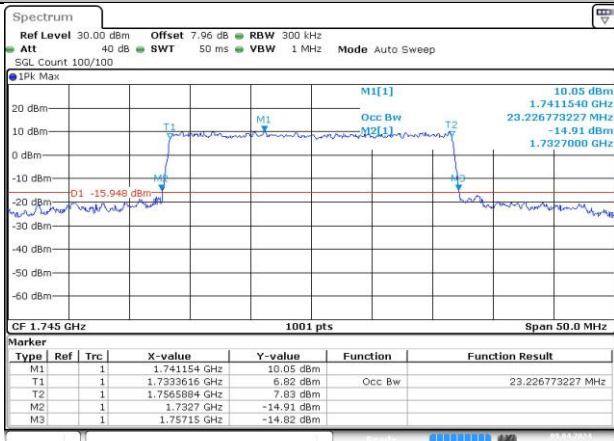
MCH_DFT-Pi2BPSK / Outer_Full



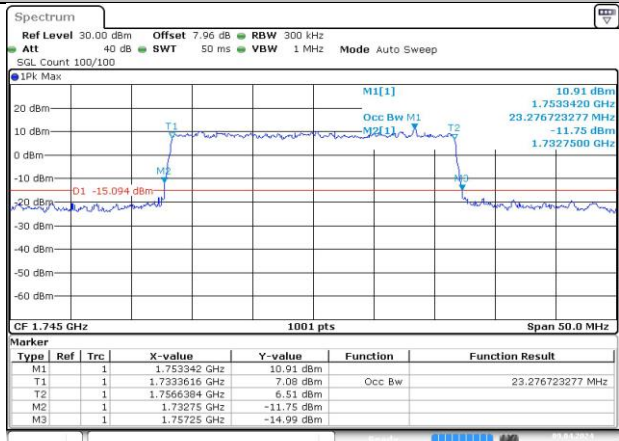
MCH_DFT-QPSK / Outer_Full



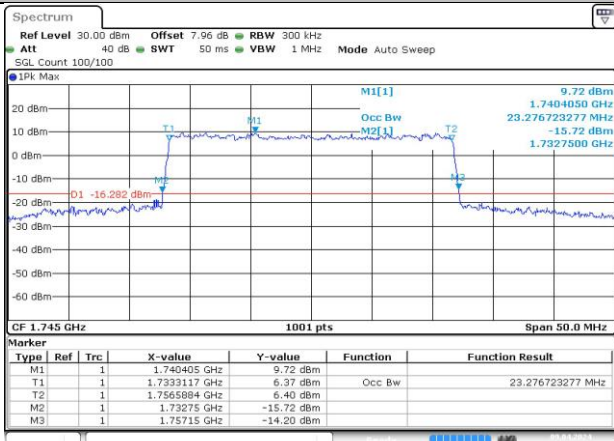
MCH_CP-QPSK / Outer_Full



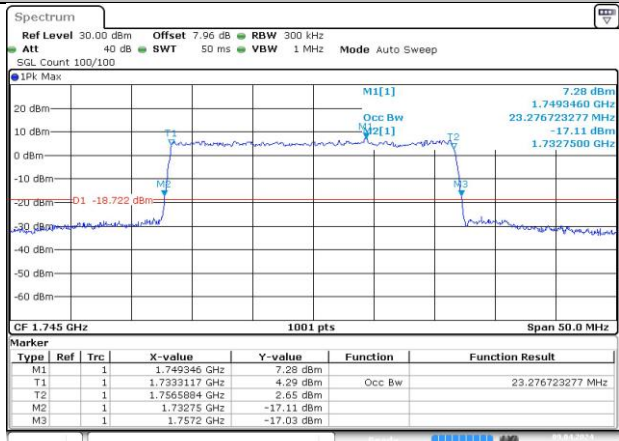
MCH_CP-16QAM / Outer_Full

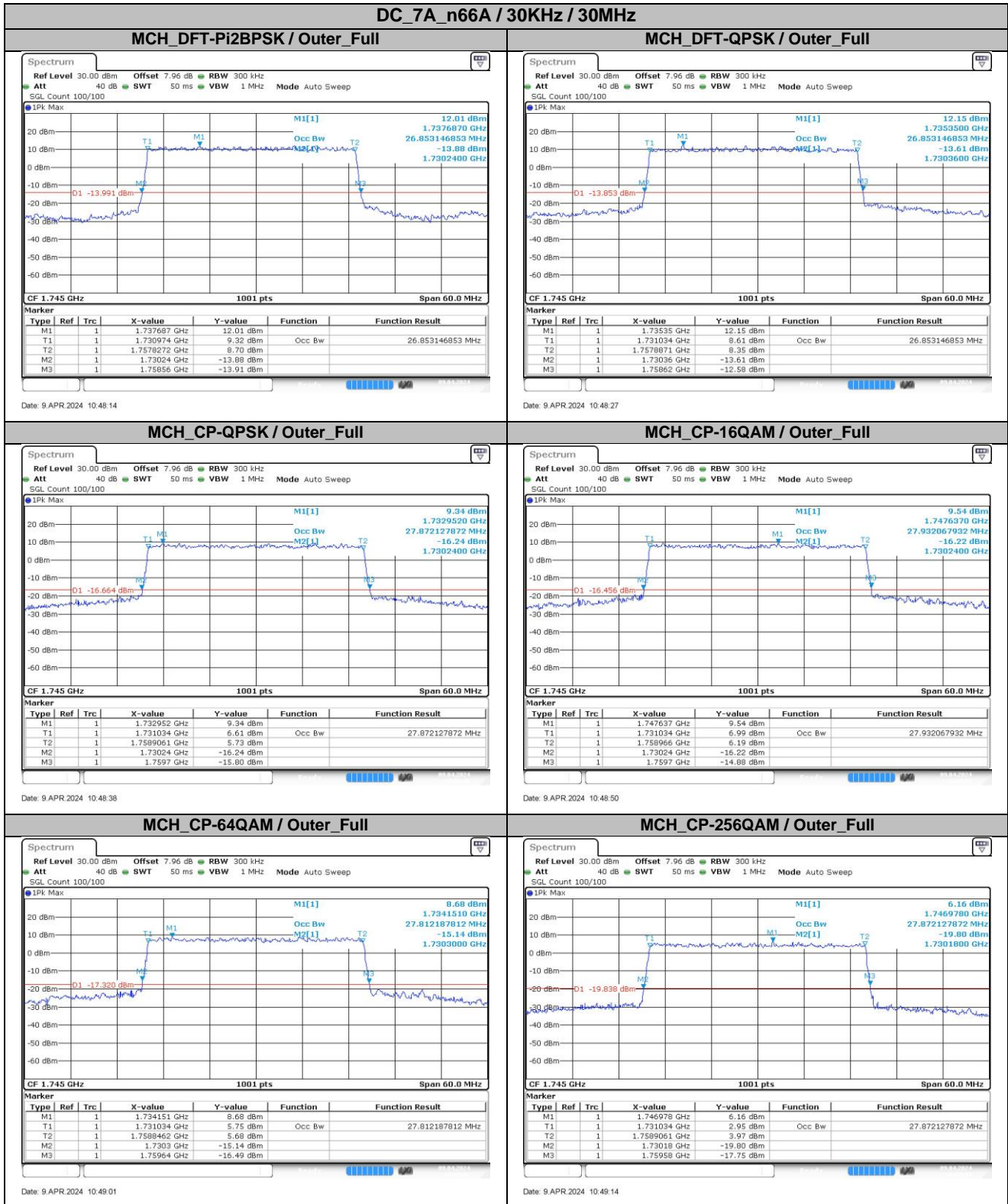


MCH_CP-64QAM / Outer_Full



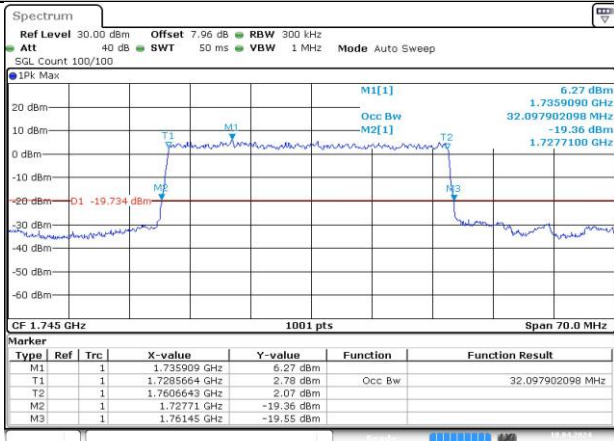
MCH_CP-256QAM / Outer_Full





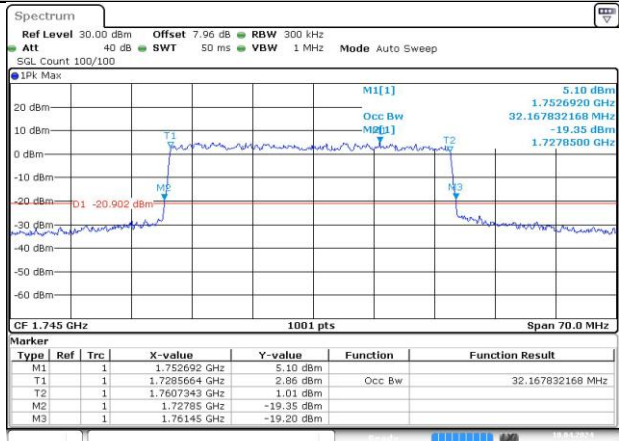
DC_7A_n66A / 30KHz / 35MHz

MCH_DFT-Pi2BPSK / Outer_Full



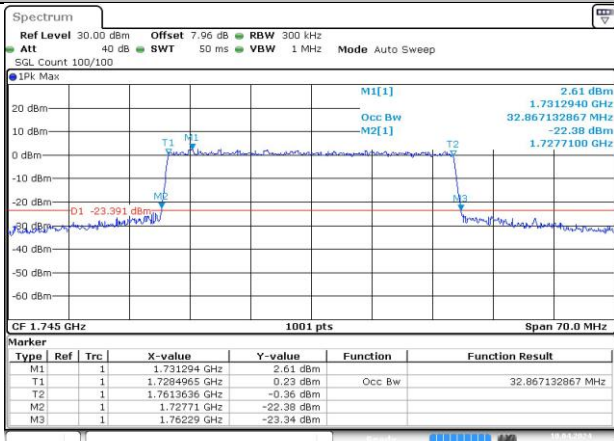
Date: 10 APR 2024 09:29:24

MCH_DFT-QPSK / Outer_Full



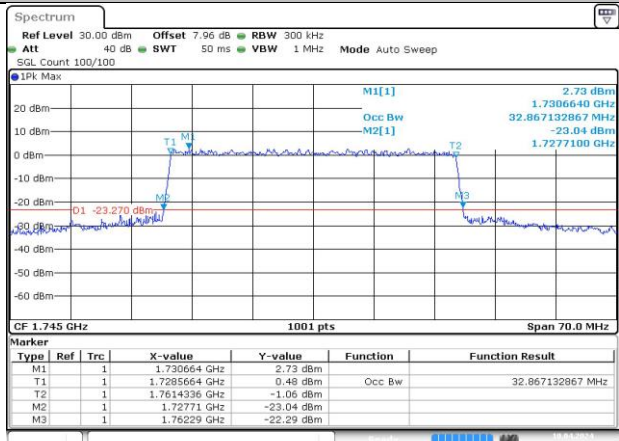
Date: 10 APR 2024 09:29:37

MCH_CP-QPSK / Outer_Full



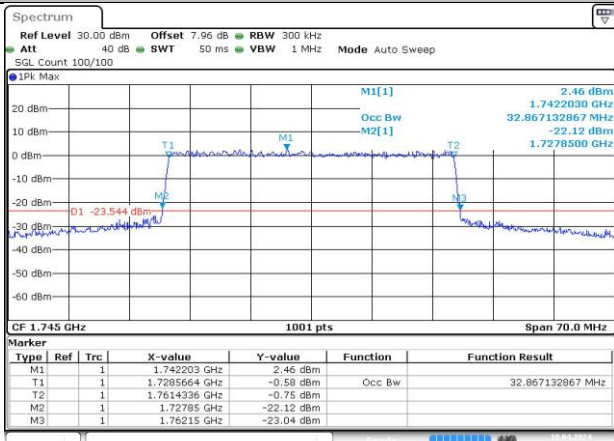
Date: 10 APR 2024 09:29:49

MCH_CP-16QAM / Outer_Full



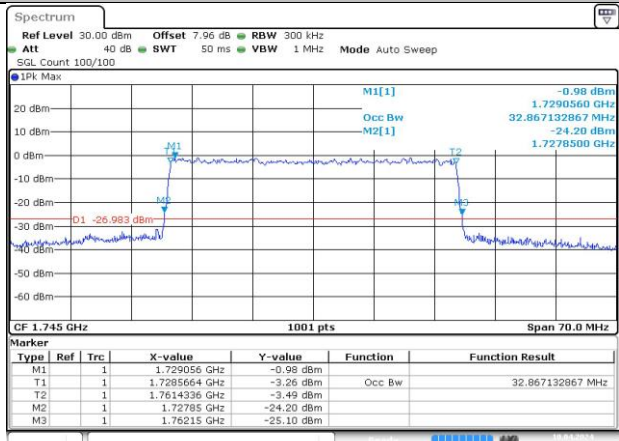
Date: 10 APR 2024 09:30:01

MCH_CP-64QAM / Outer_Full



Date: 10 APR 2024 09:30:14

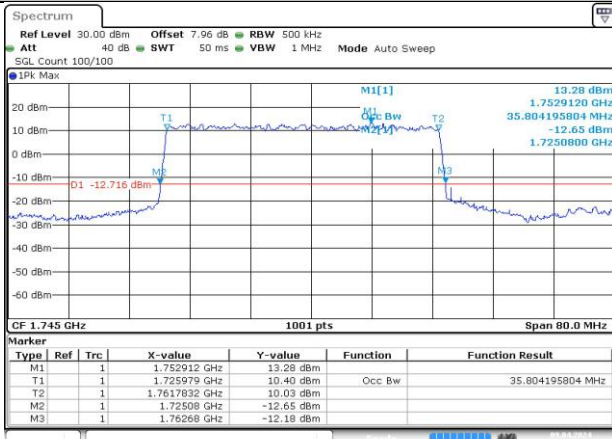
MCH_CP-256QAM / Outer_Full



Date: 10 APR 2024 09:30:28

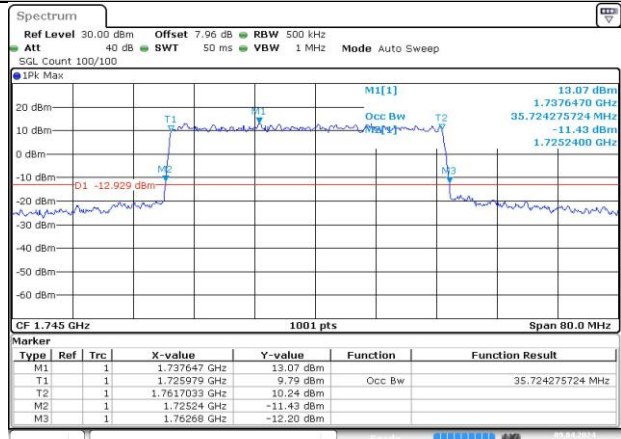
DC_7A_n66A / 30KHz / 40MHz

MCH_DFT-Pi2BPSK / Outer_Full



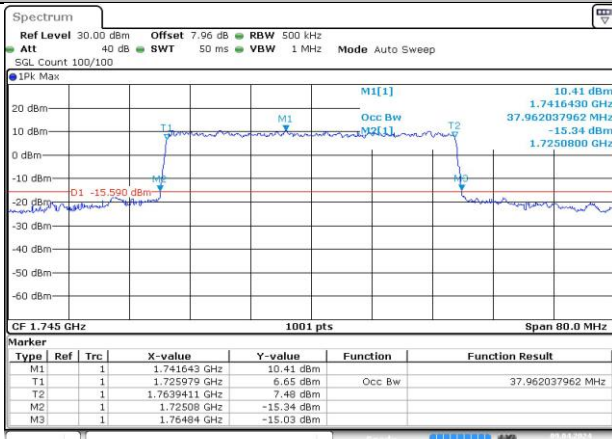
Date: 9 APR 2024 10:49:29

MCH_DFT-QPSK / Outer_Full



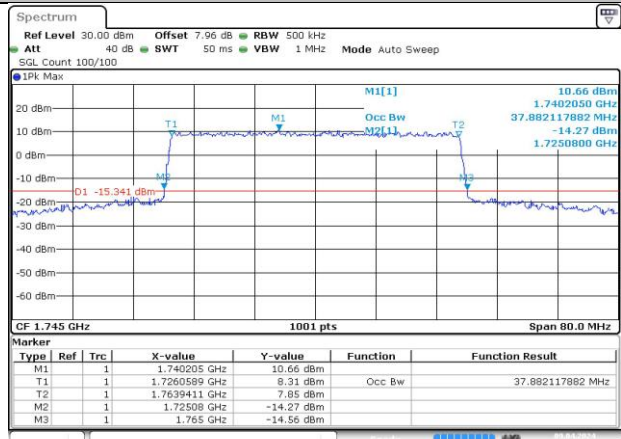
Date: 9 APR 2024 10:49:42

MCH_CP-QPSK / Outer_Full



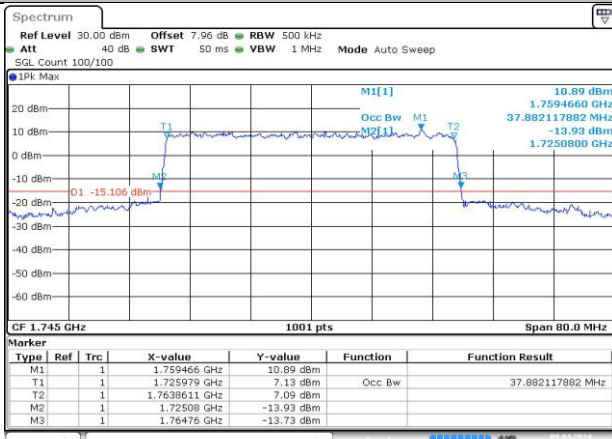
Date: 9 APR 2024 10:49:55

MCH_CP-16QAM / Outer_Full



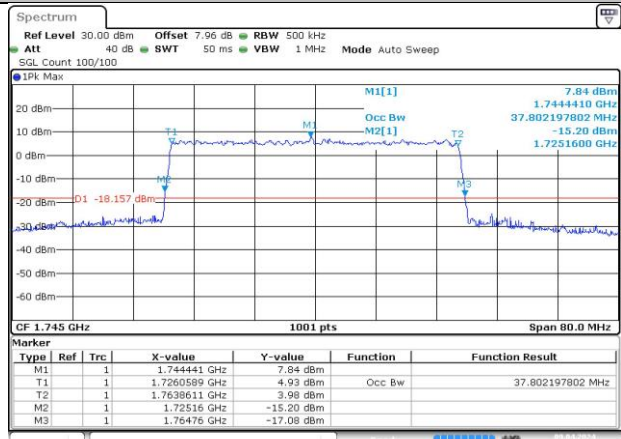
Date: 9 APR 2024 10:50:07

MCH_CP-64QAM / Outer_Full



Date: 9 APR 2024 10:50:19

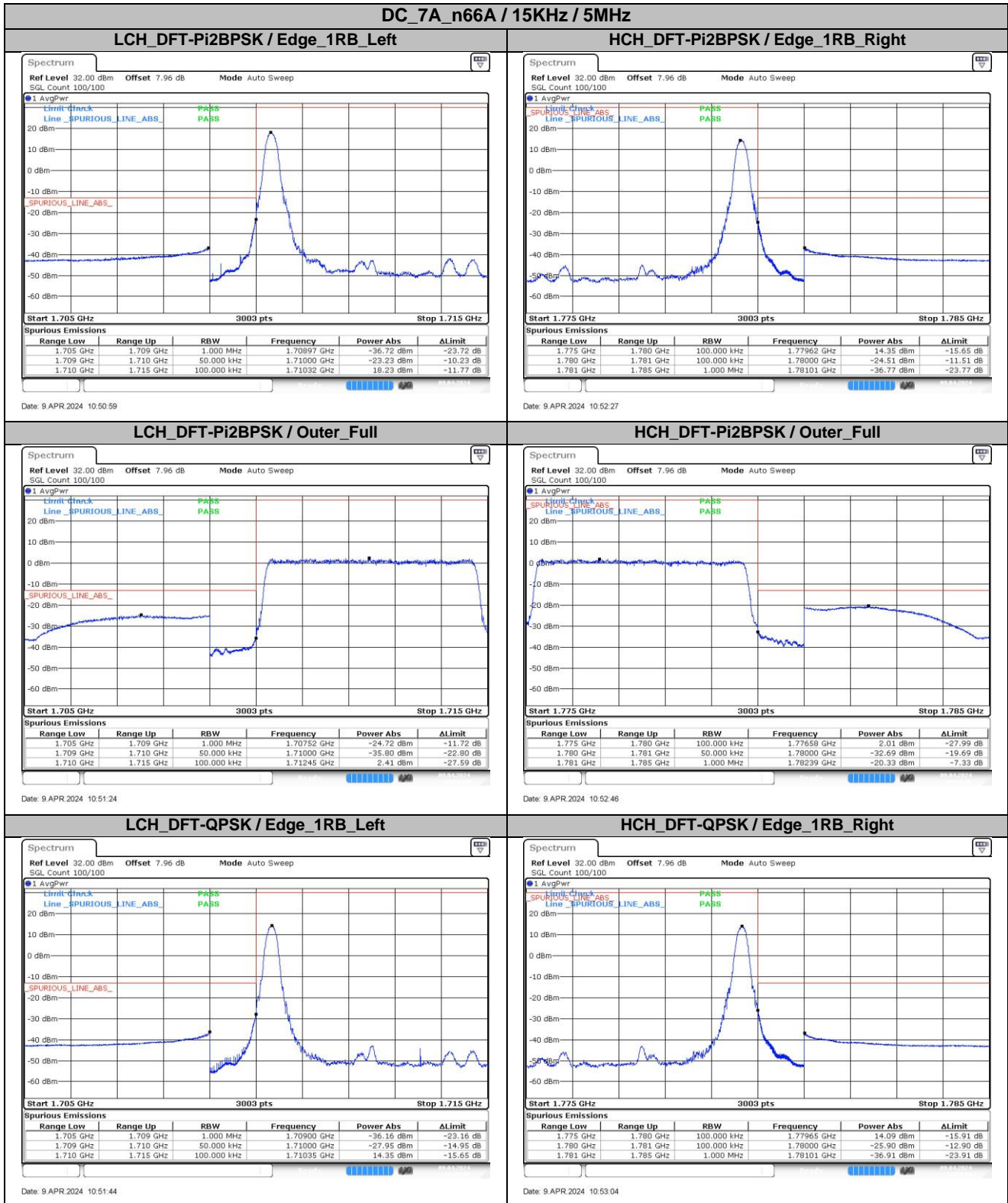
MCH_CP-256QAM / Outer_Full

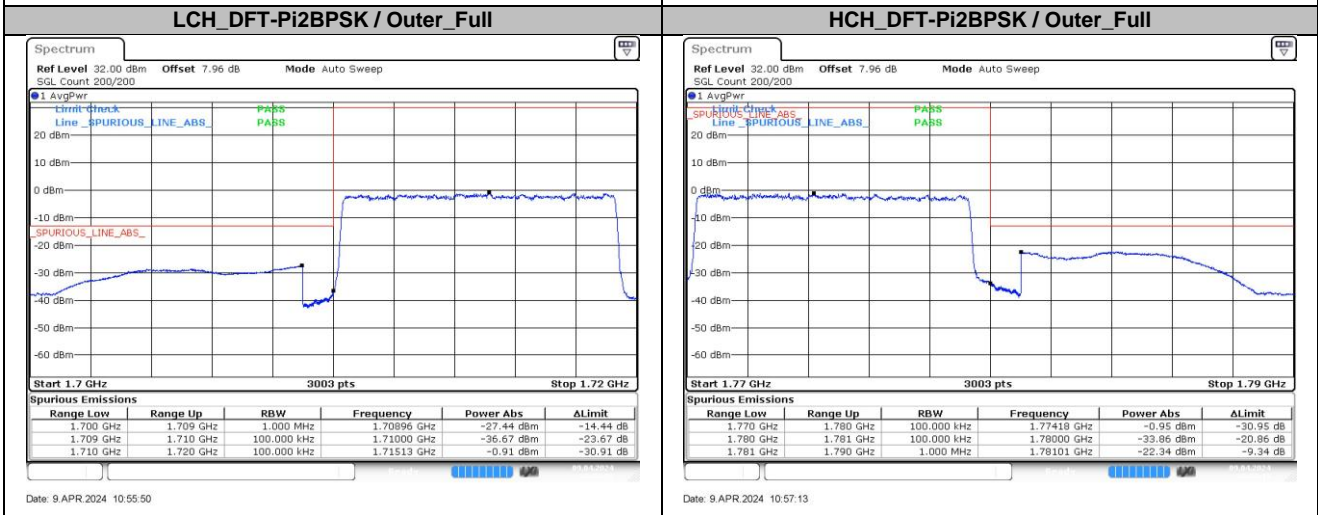
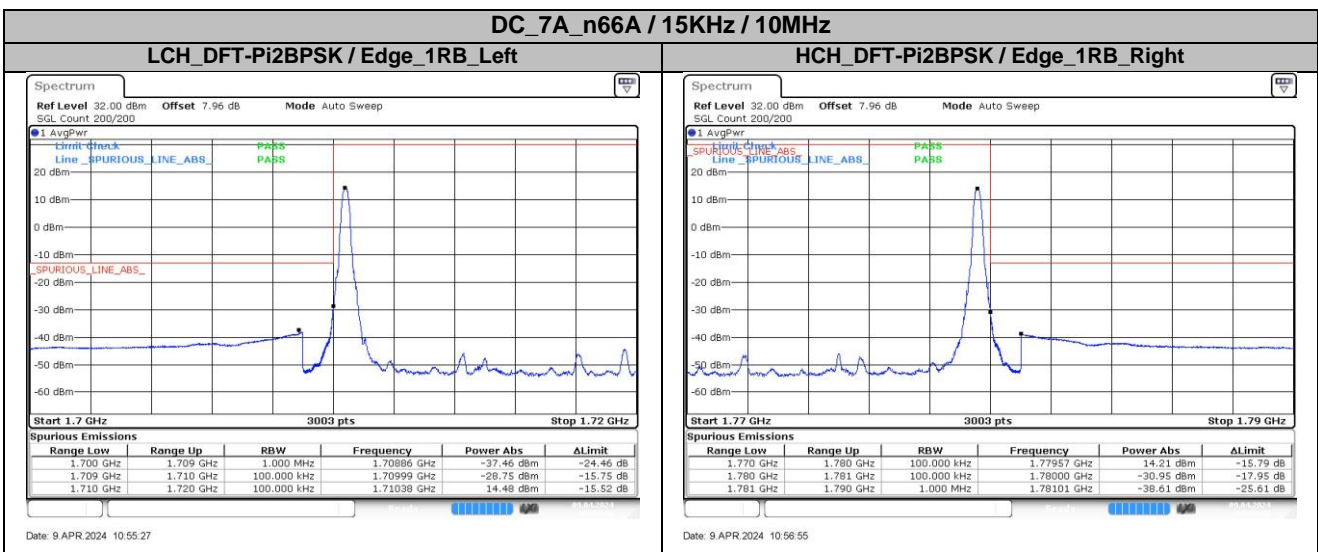
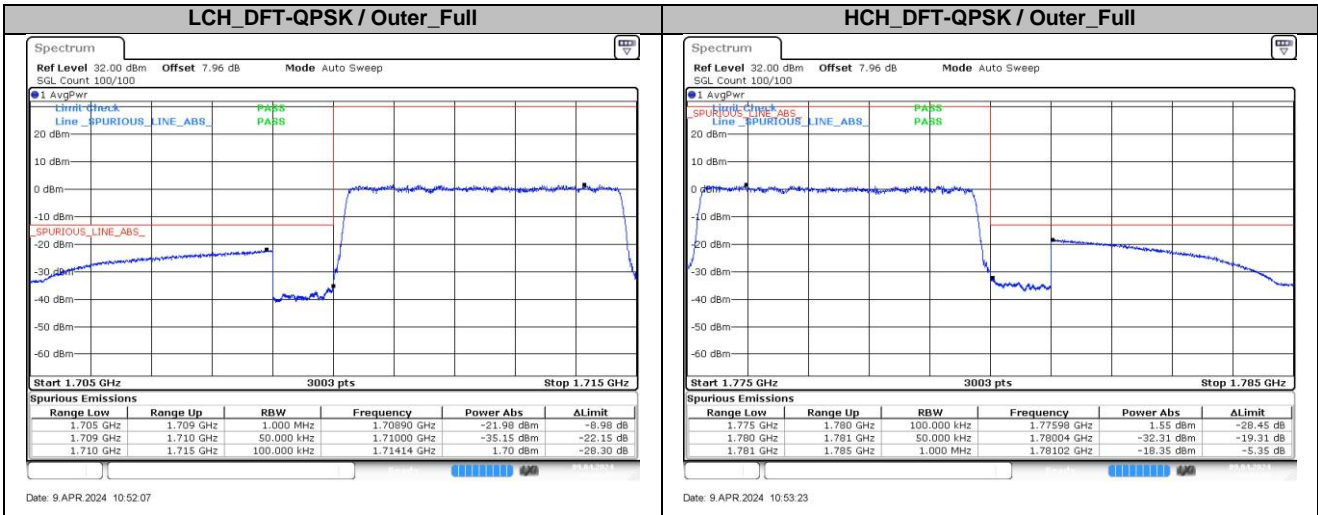


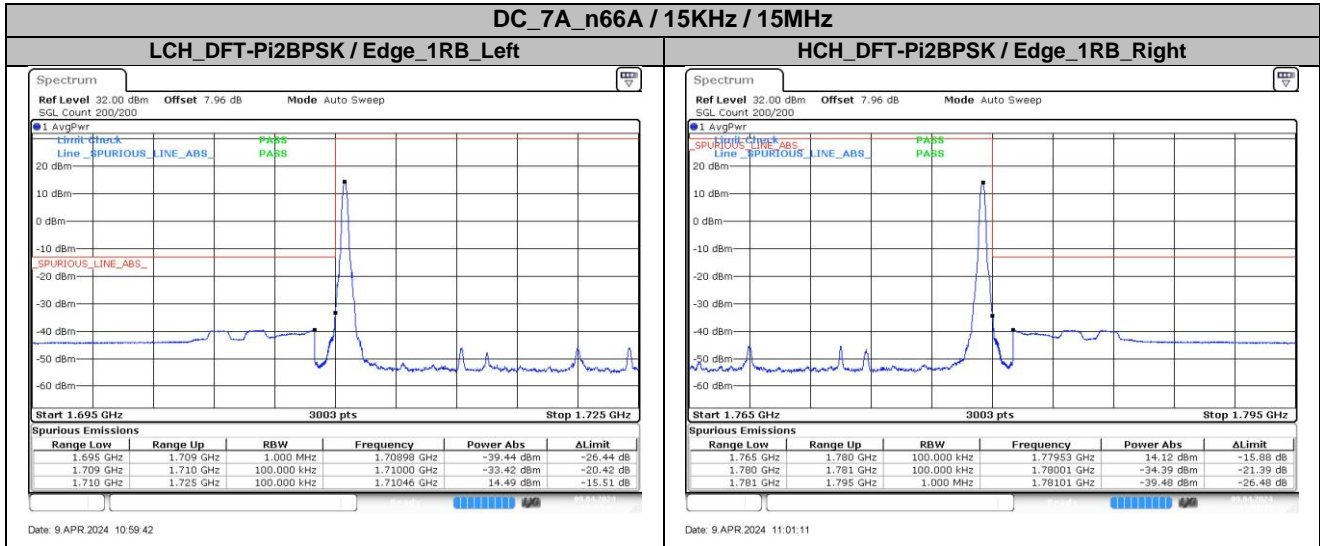
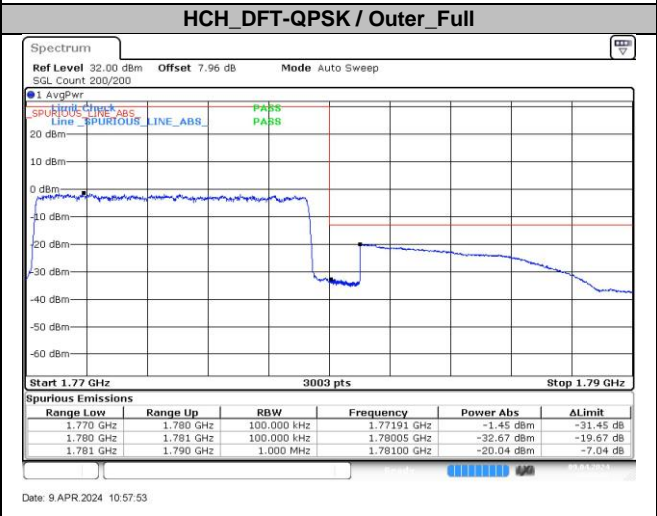
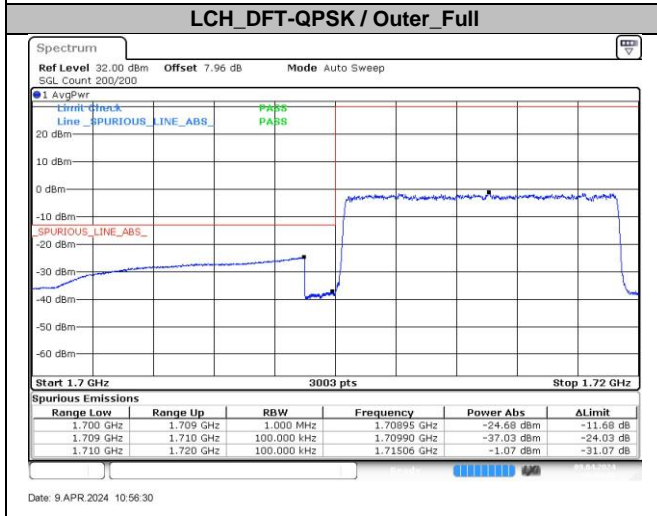
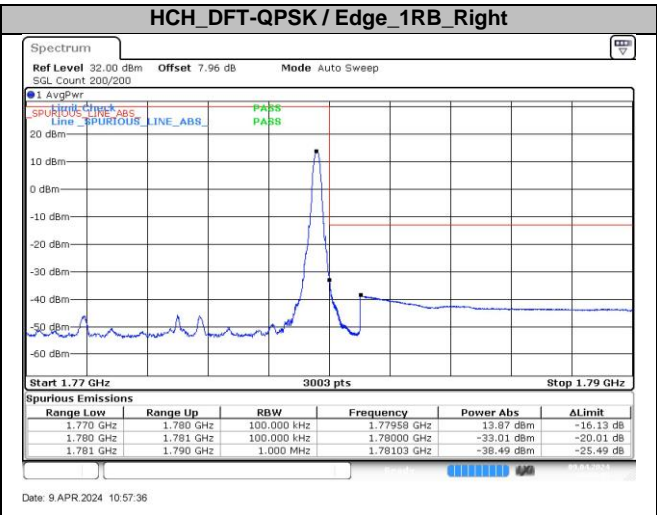
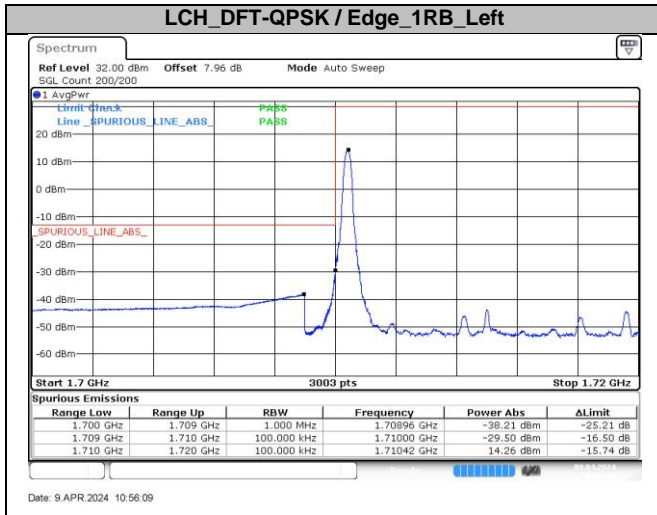
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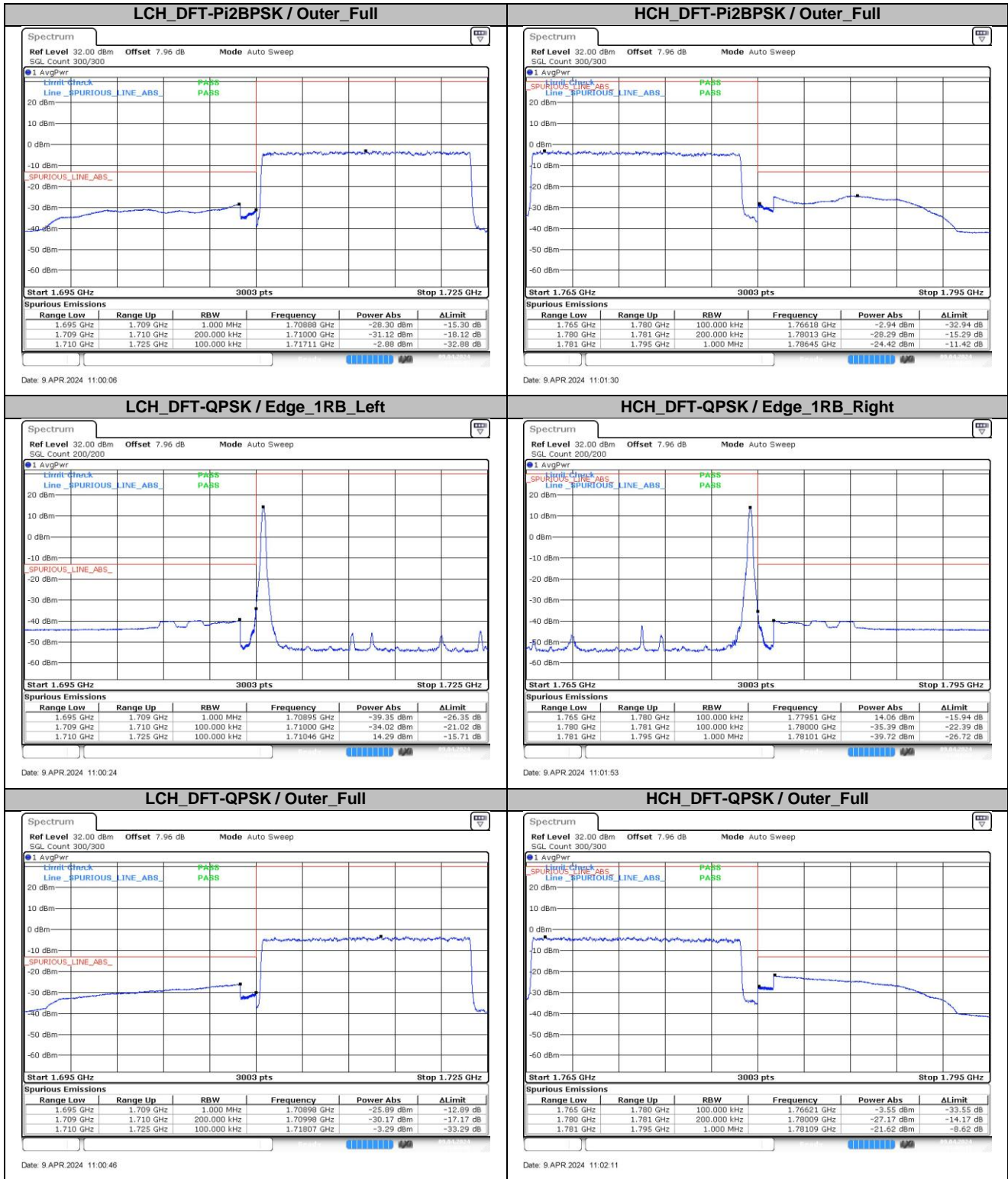
5. Conducted Band Edges

5.1. Test Plots



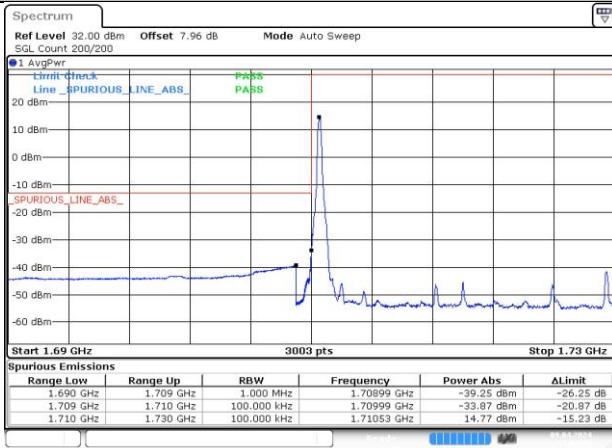






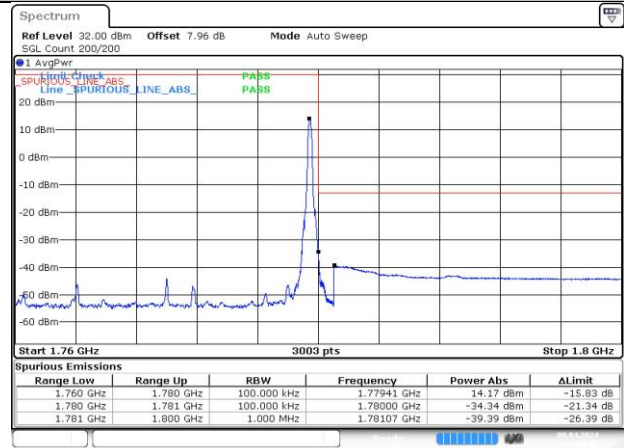
DC_7A_n66A / 15KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



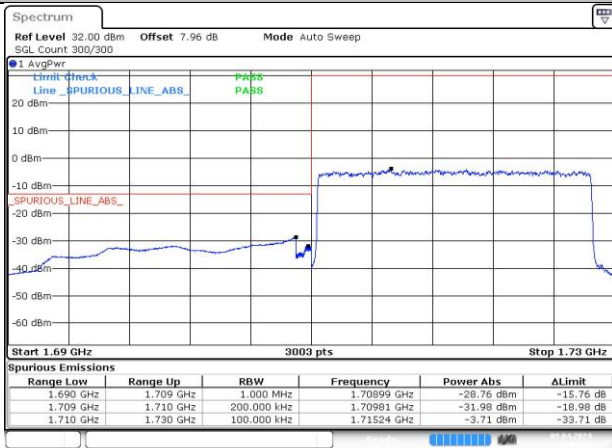
Date: 9 APR 2024 11:04:01

HCH_DFT-Pi2BPSK / Edge_1RB_Right



Date: 9 APR 2024 11:05:35

LCH_DFT-Pi2BPSK / Outer_Full



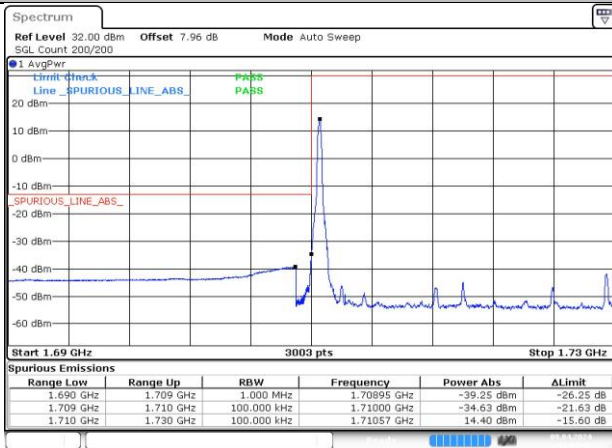
Date: 9 APR 2024 11:04:26

HCH_DFT-Pi2BPSK / Outer_Full



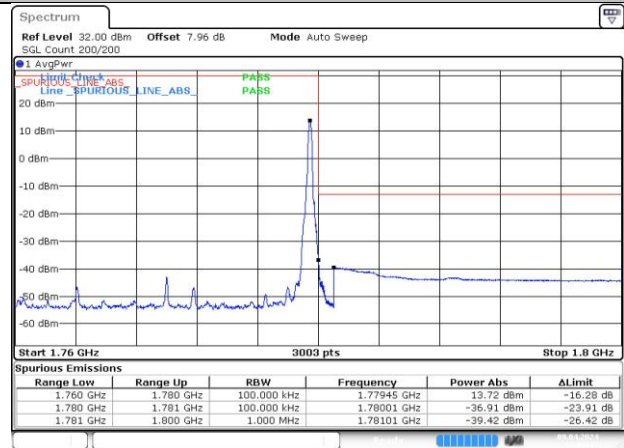
Date: 9 APR 2024 11:05:54

LCH_DFT-QPSK / Edge_1RB_Left

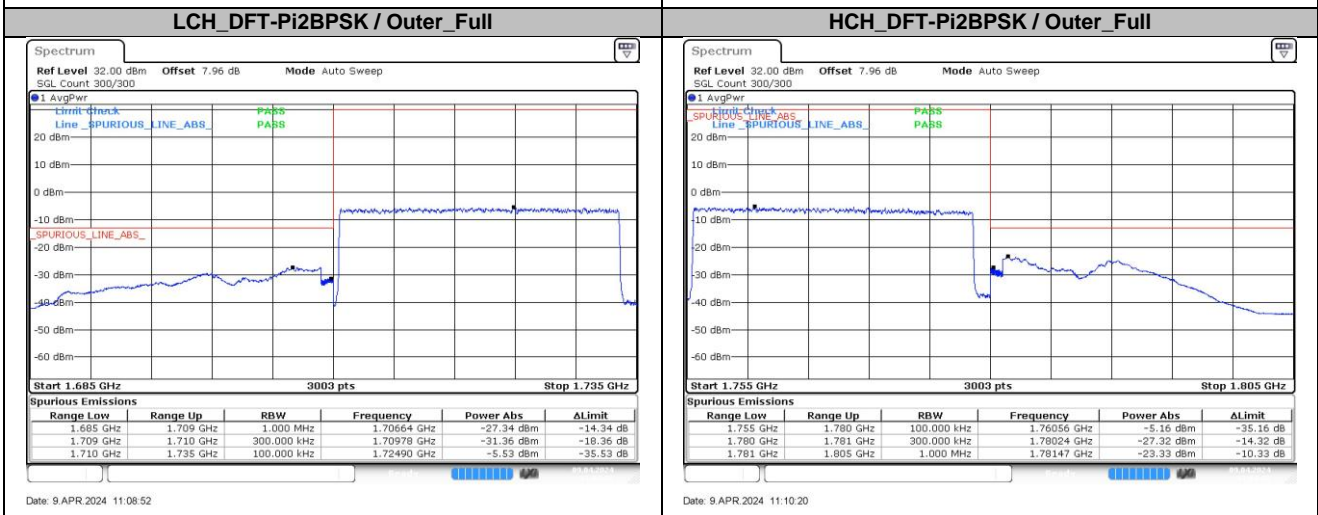
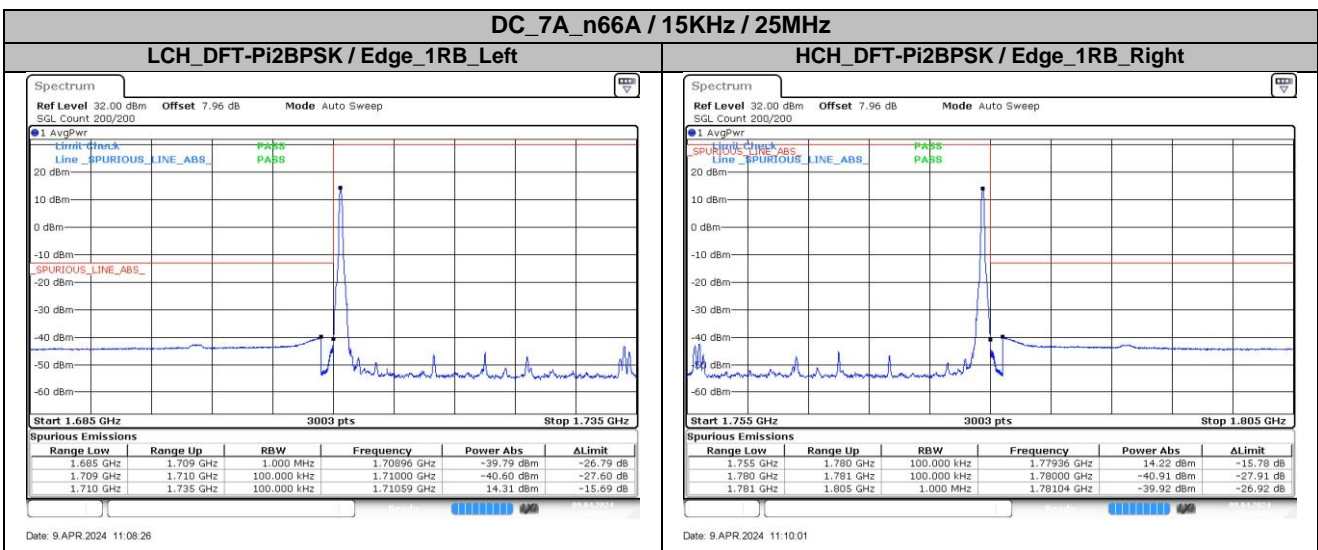
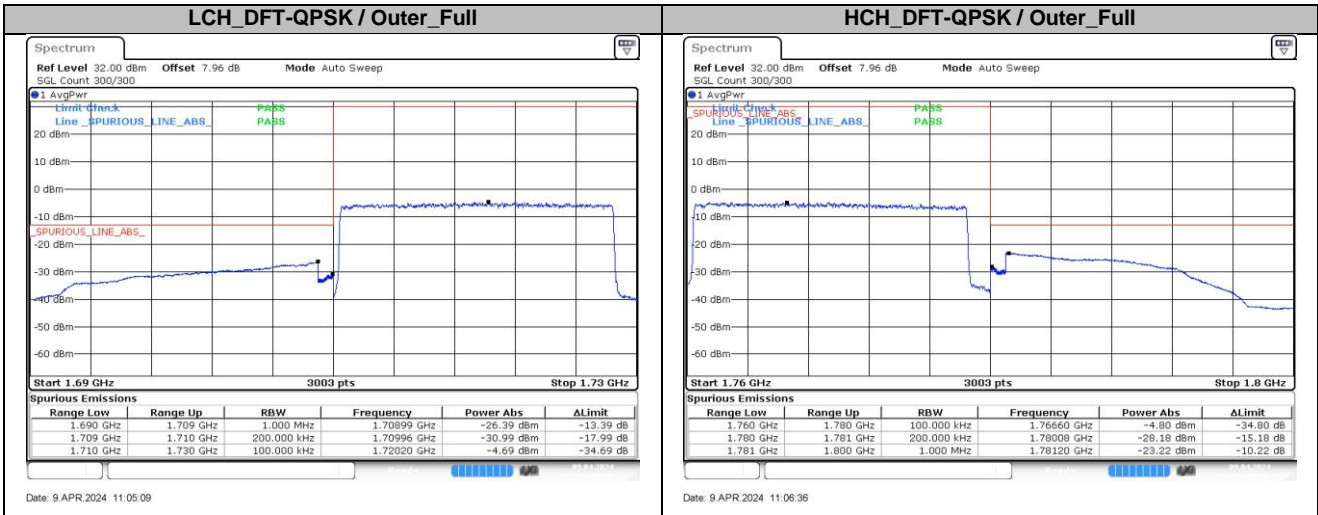


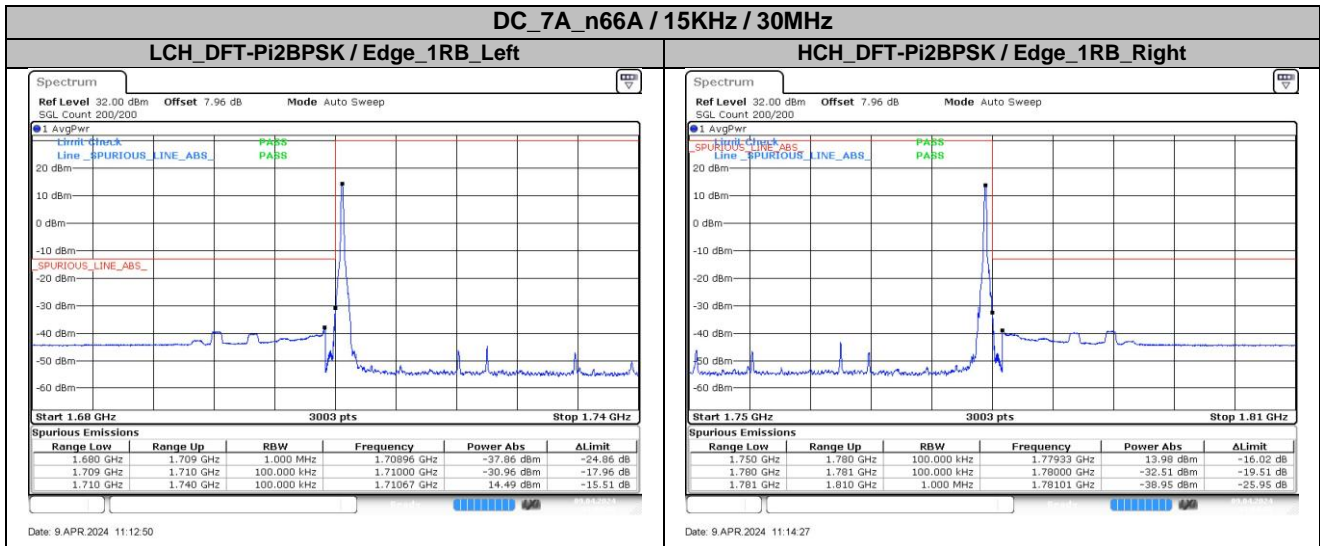
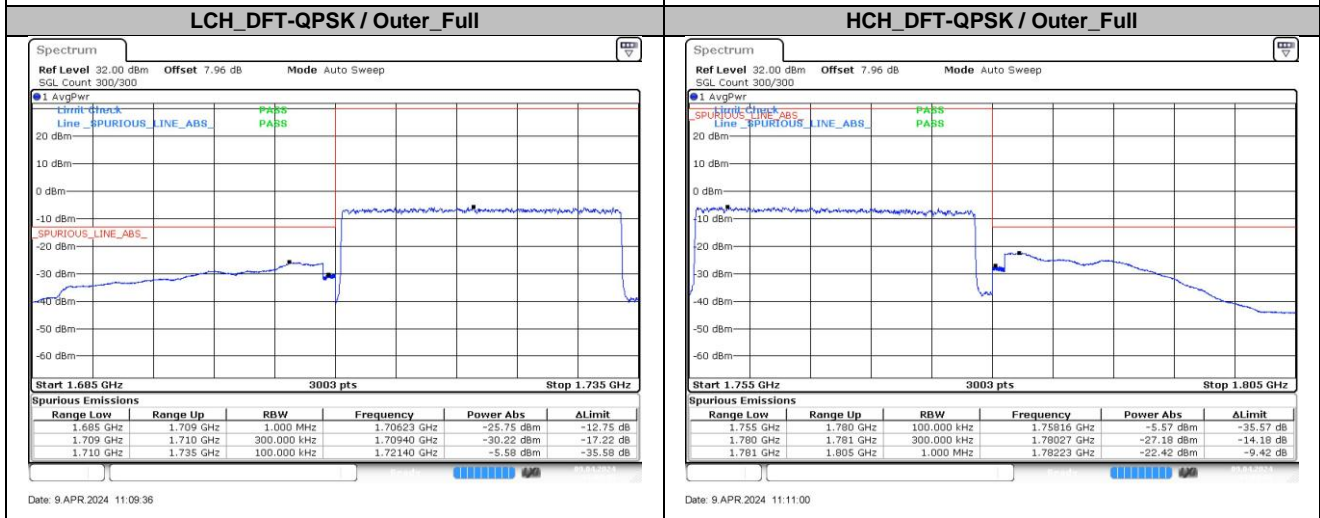
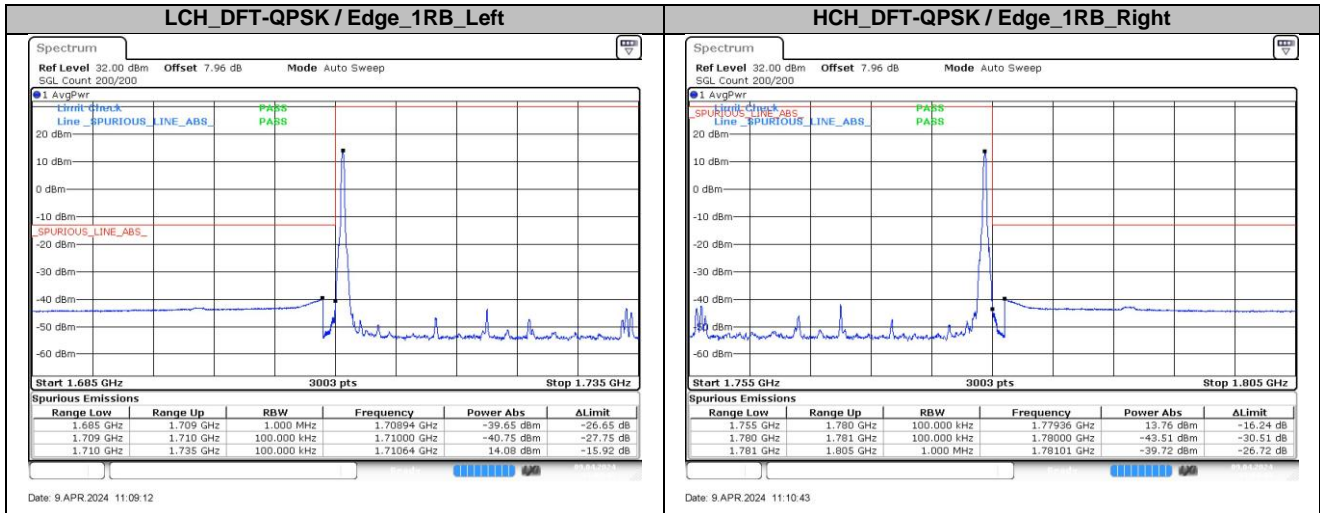
Date: 9 APR 2024 11:04:46

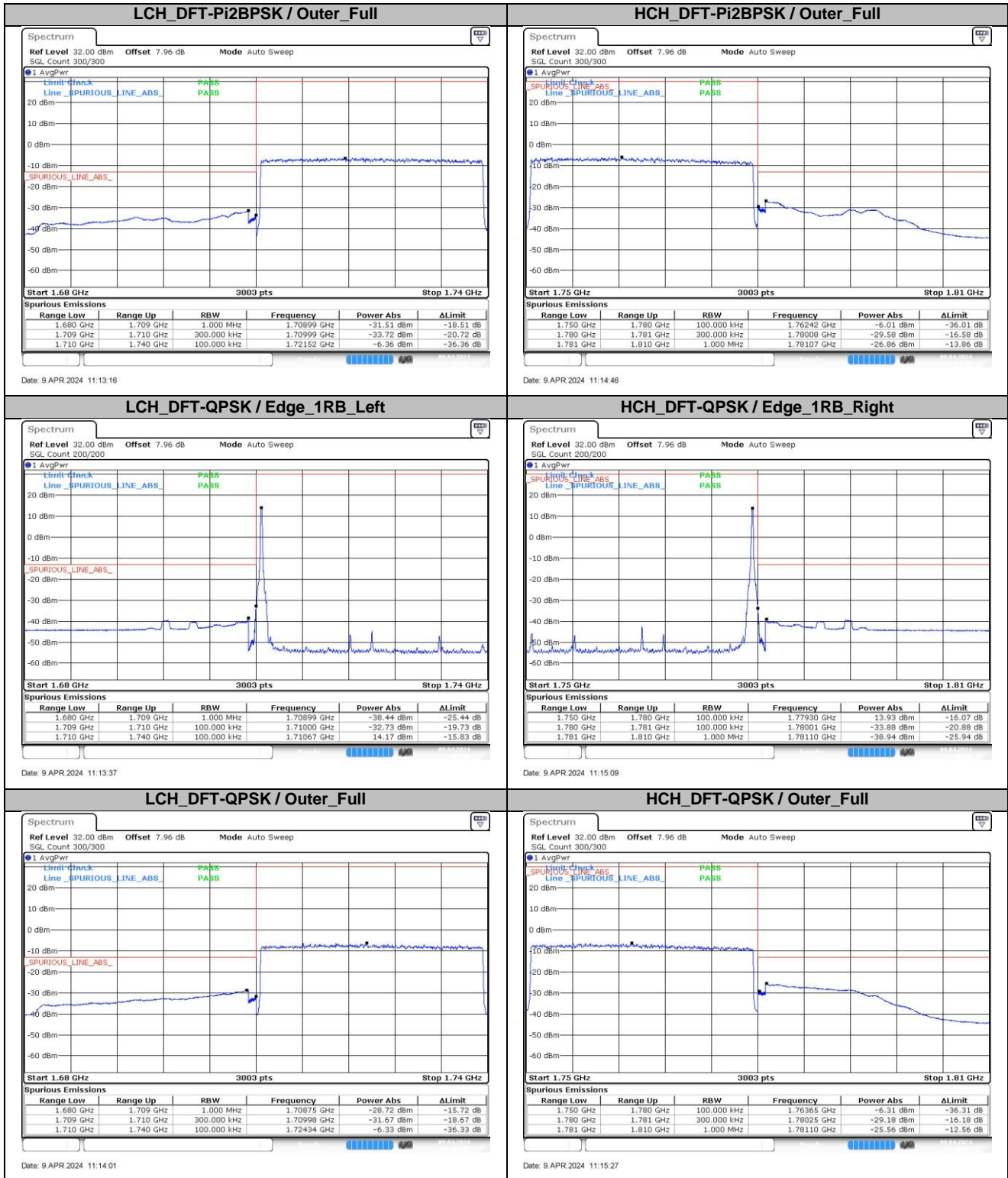
HCH_DFT-QPSK / Edge_1RB_Right



Date: 9 APR 2024 11:06:17

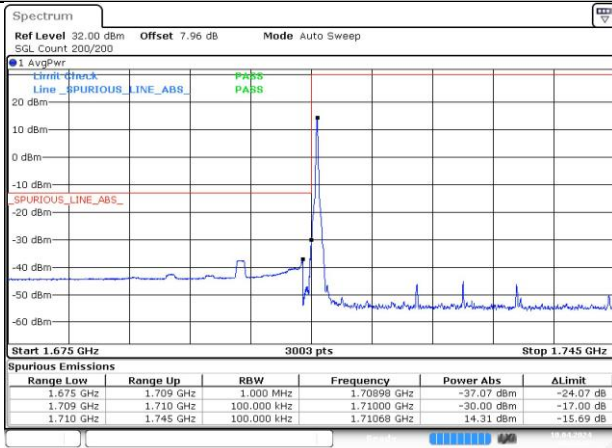






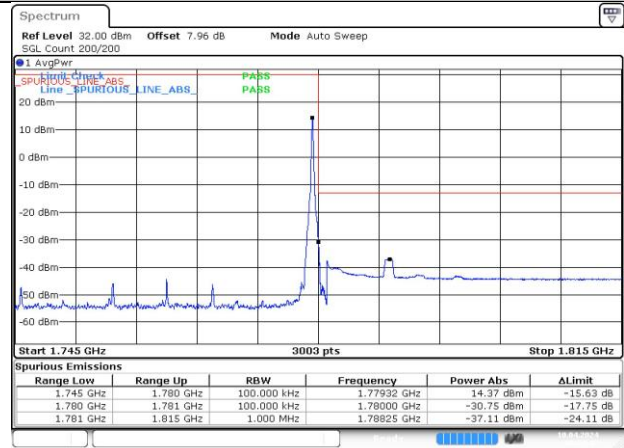
DC_7A_n66A / 15KHz / 35MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



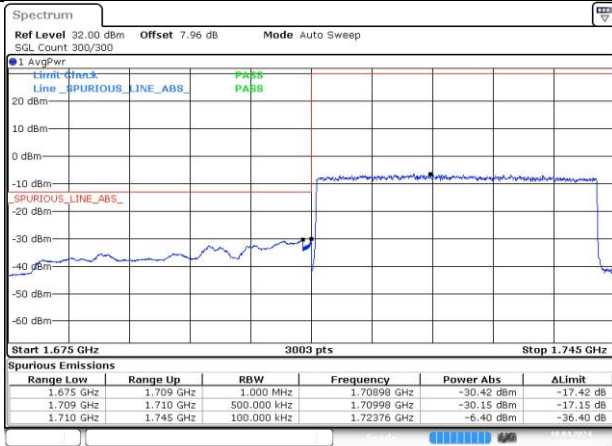
Date: 10 APR 2024 09:30:57

HCH_DFT-Pi2BPSK / Edge_1RB_Right



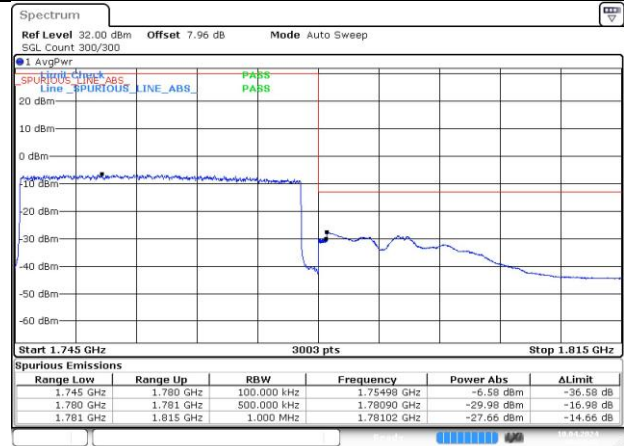
Date: 10 APR 2024 09:32:37

LCH_DFT-Pi2BPSK / Outer_Full



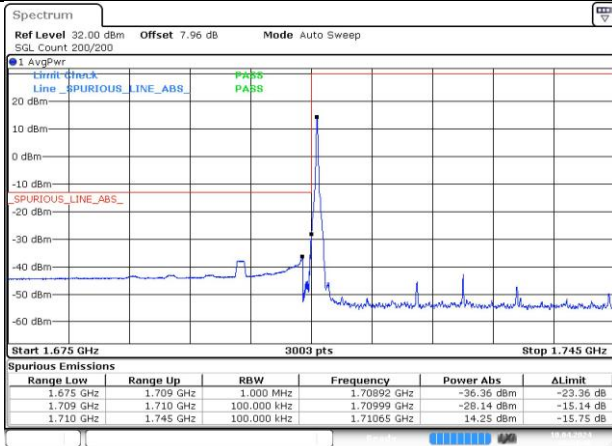
Date: 10 APR 2024 09:31:24

HCH_DFT-Pi2BPSK / Outer_Full



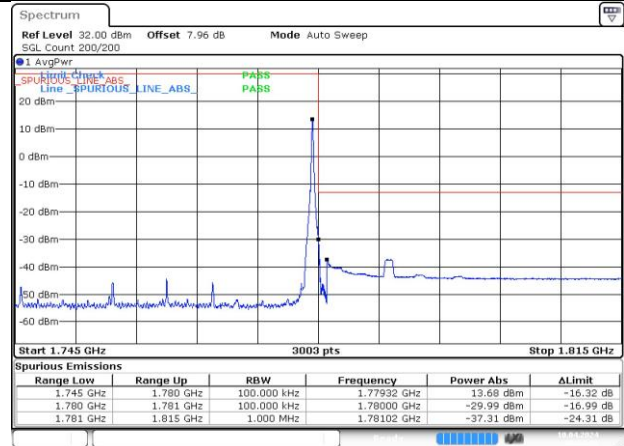
Date: 10 APR 2024 09:32:56

LCH_DFT-QPSK / Edge_1RB_Left



Date: 10 APR 2024 09:31:46

HCH_DFT-QPSK / Edge_1RB_Right



Date: 10 APR 2024 09:33:21

