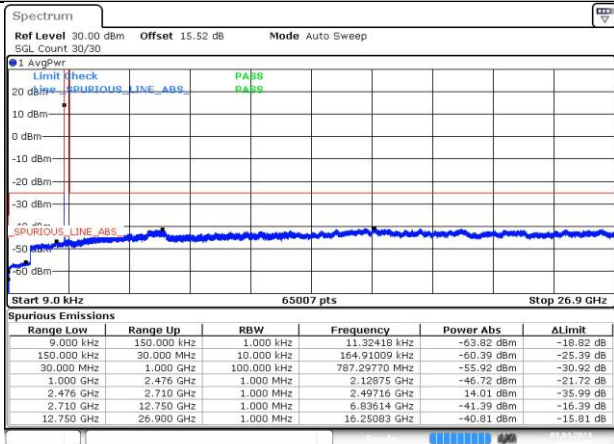


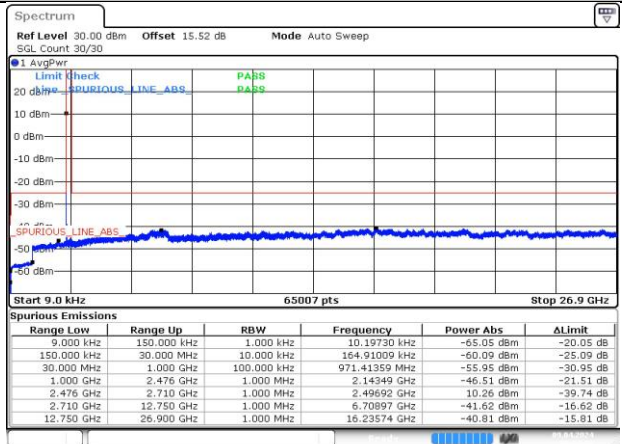
DC_66A_n41A / 30KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



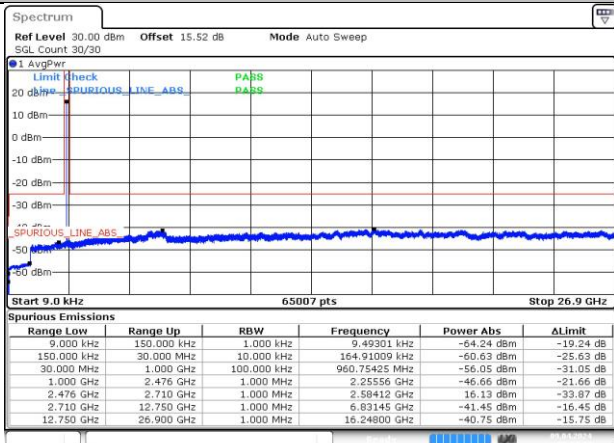
Date: 9 APR 2024 08:08:27

LCH_DFT-QPSK / Edge_1RB_Left



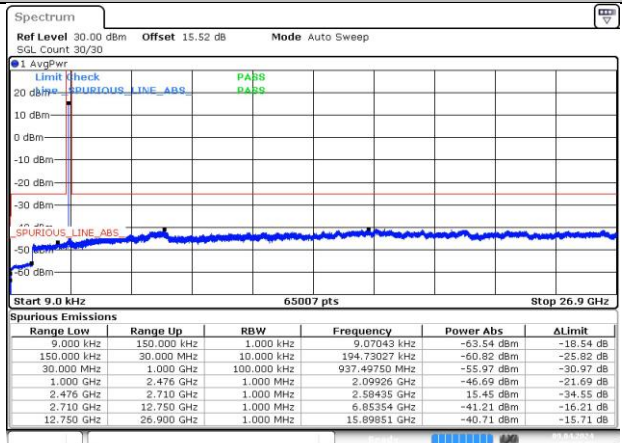
Date: 9 APR 2024 08:08:50

MCH_DFT-Pi2BPSK / Edge_1RB_Left



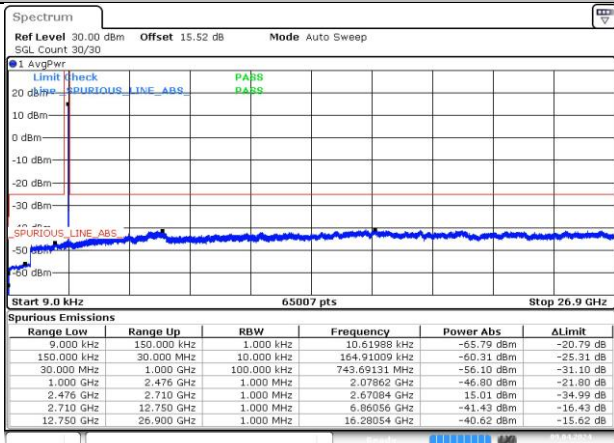
Date: 9 APR 2024 08:09:14

MCH_DFT-QPSK / Edge_1RB_Left



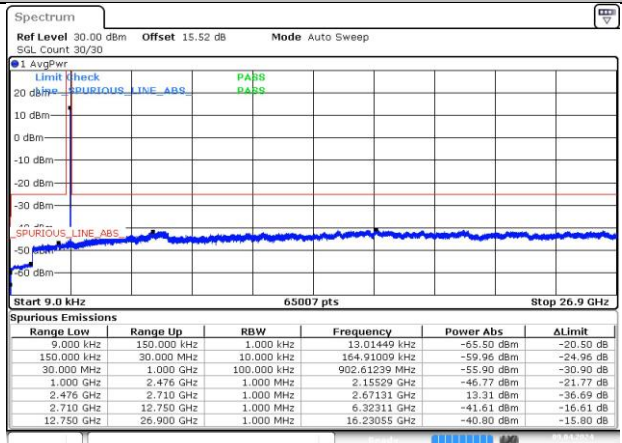
Date: 9 APR 2024 08:09:37

HCH_DFT-Pi2BPSK / Edge_1RB_Left

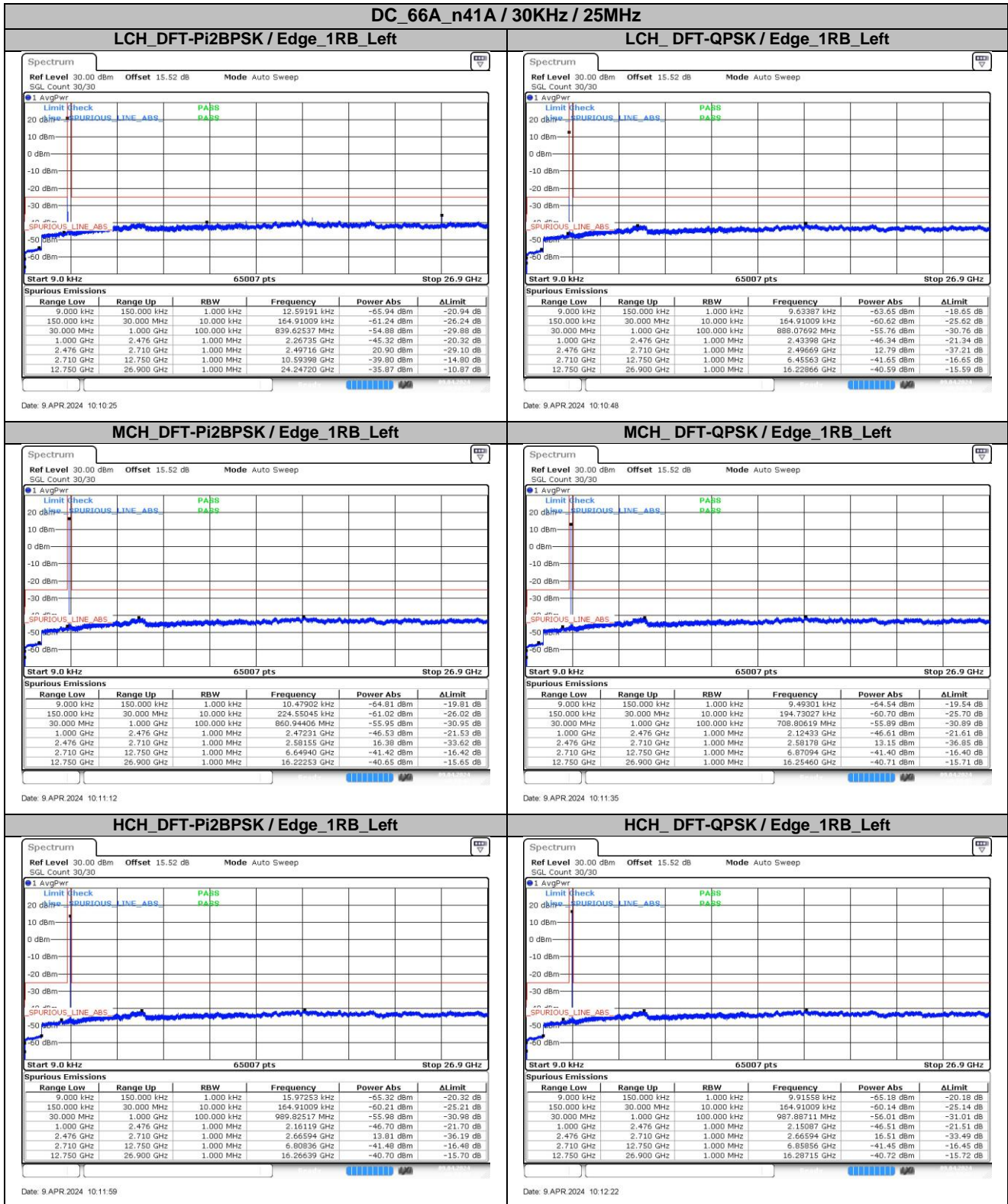


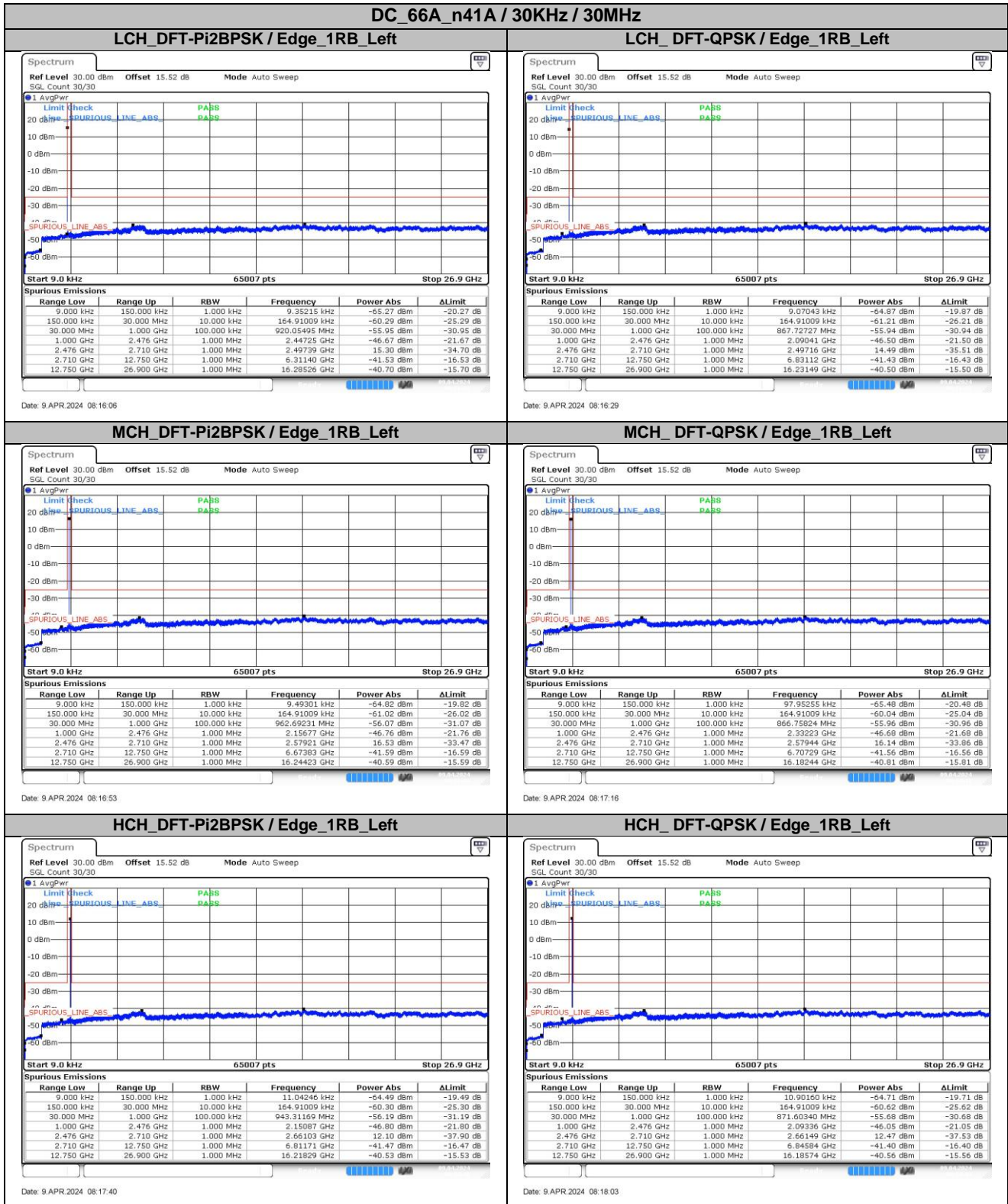
Date: 9 APR 2024 08:10:01

HCH_DFT-QPSK / Edge_1RB_Left



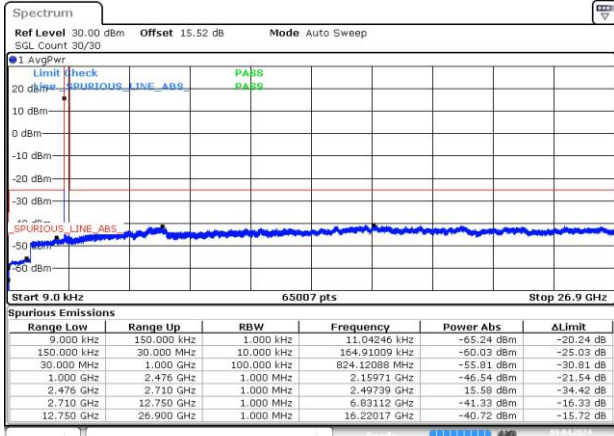
Date: 9 APR 2024 08:10:24





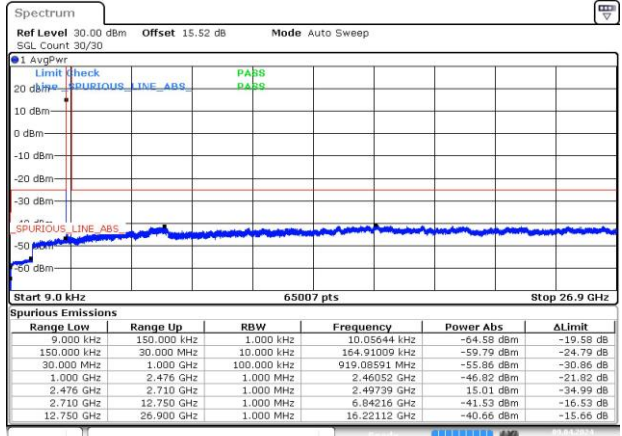
DC_66A_n41A / 30KHz / 35MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



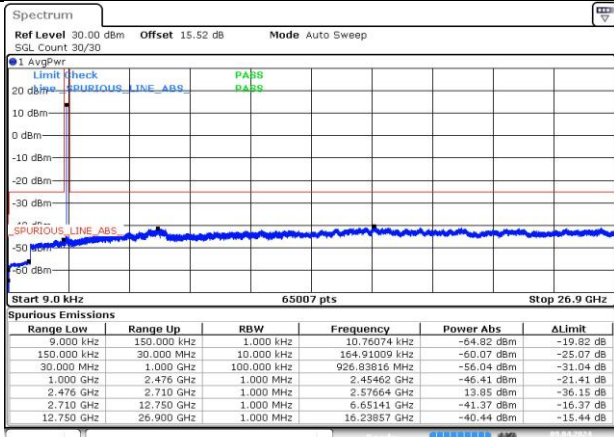
Date: 9 APR 2024 10:13:48

LCH_DFT-QPSK / Edge_1RB_Left



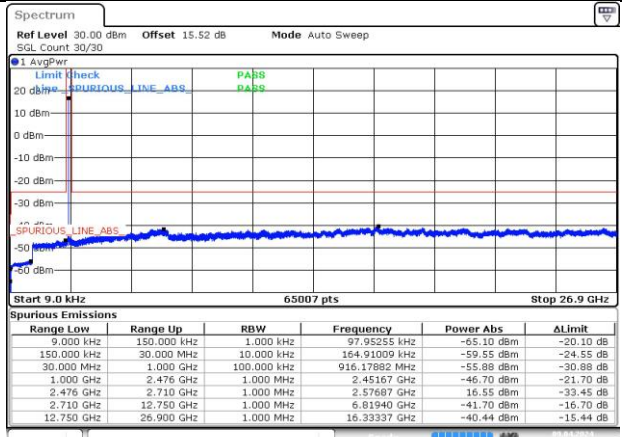
Date: 9 APR 2024 10:14:11

MCH_DFT-Pi2BPSK / Edge_1RB_Left



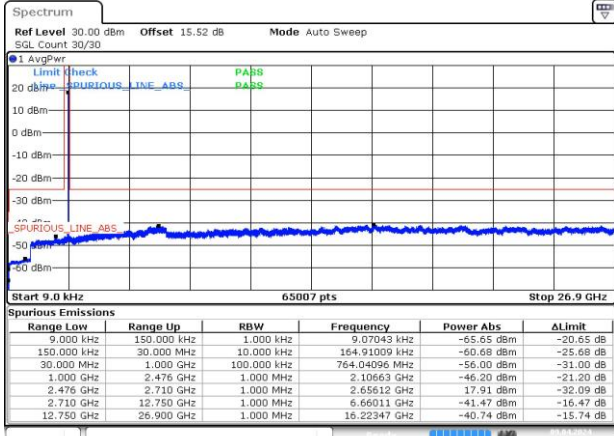
Date: 9 APR 2024 10:14:35

MCH_DFT-QPSK / Edge_1RB_Left



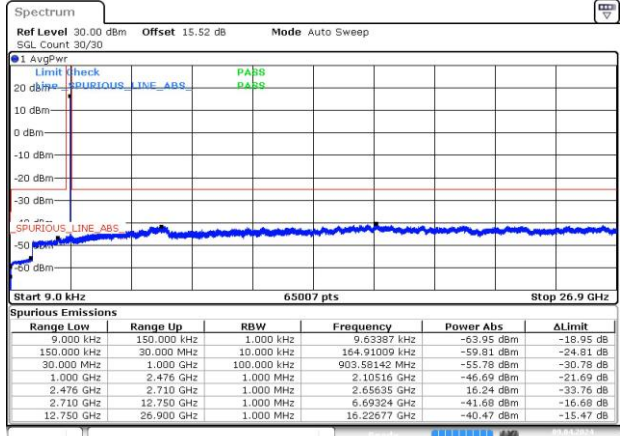
Date: 9 APR 2024 10:14:58

HCH_DFT-Pi2BPSK / Edge_1RB_Left

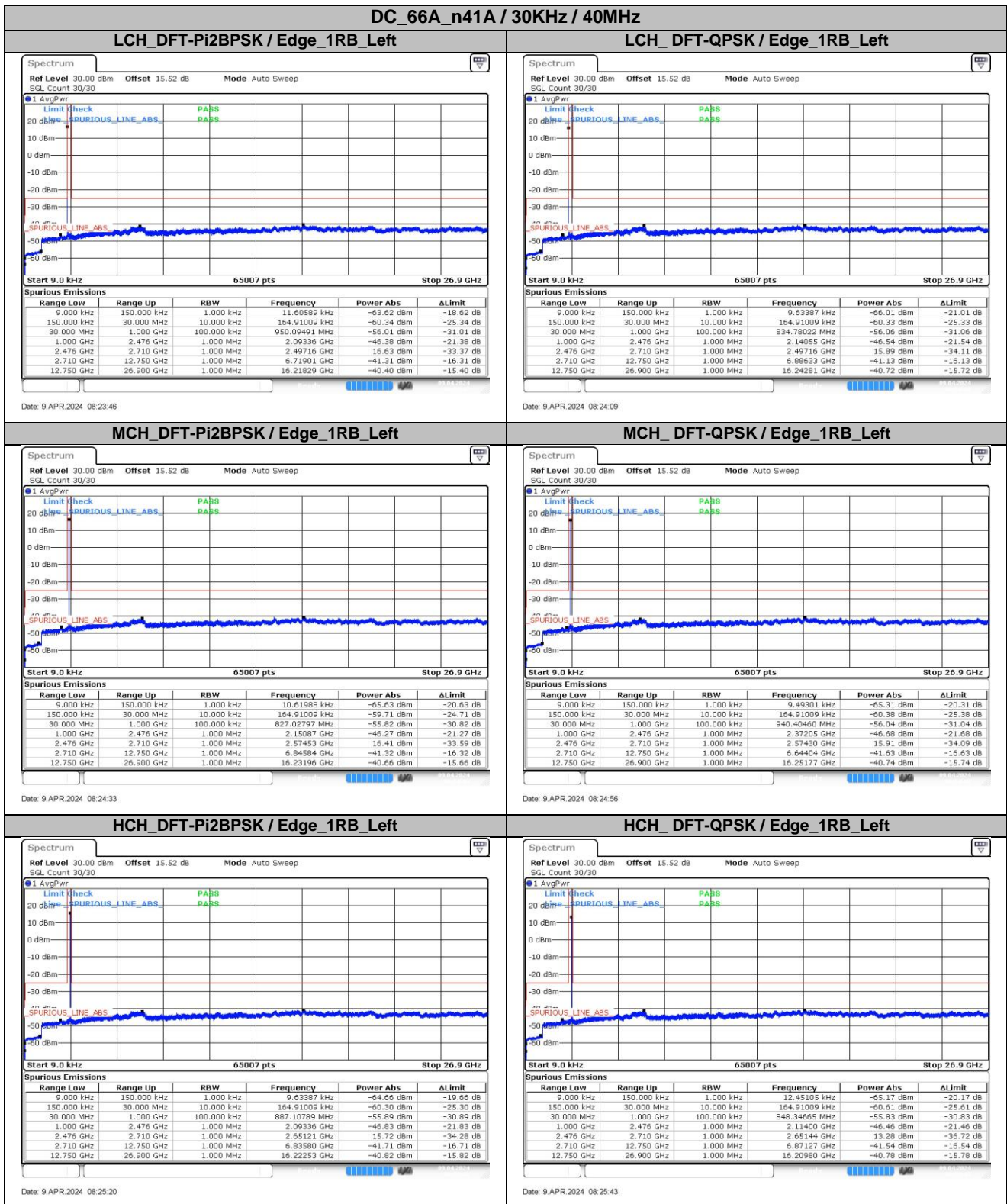


Date: 9 APR 2024 10:15:22

HCH_DFT-QPSK / Edge_1RB_Left

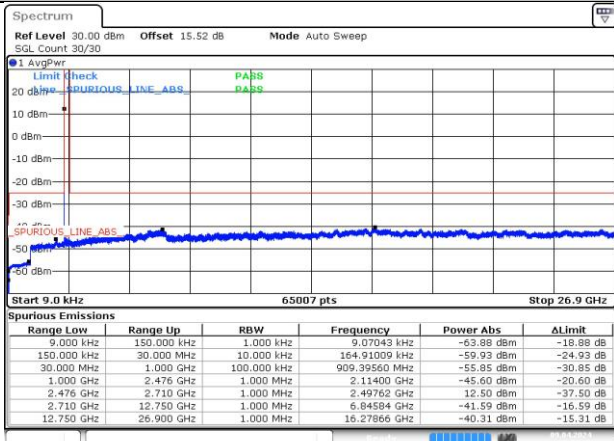


Date: 9 APR 2024 10:15:45



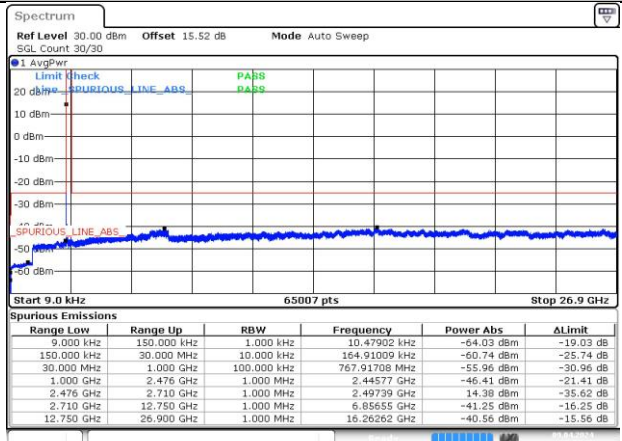
DC_66A_n41A / 30KHz / 45MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



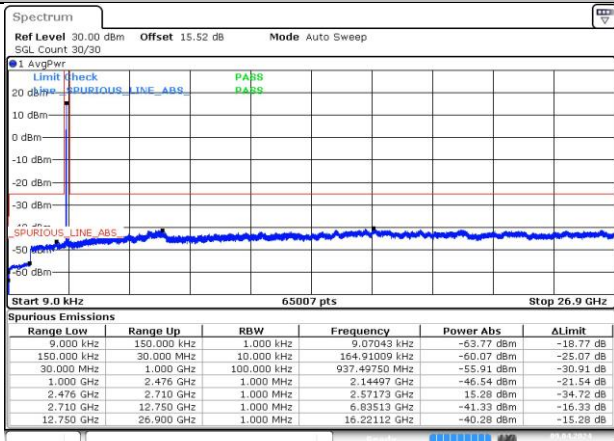
Date: 9 APR 2024 10:17:56

LCH_DFT-QPSK / Edge_1RB_Left



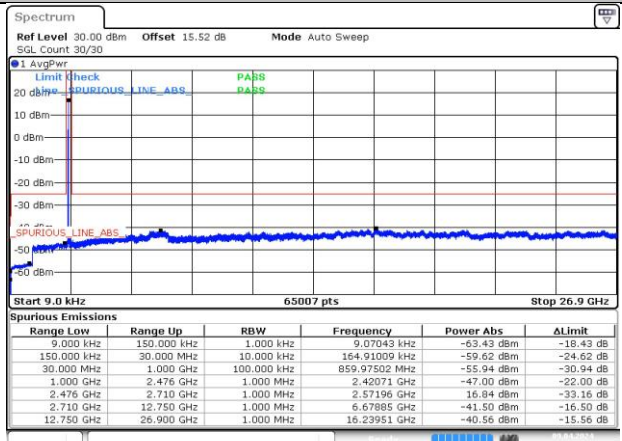
Date: 9 APR 2024 10:18:19

MCH_DFT-Pi2BPSK / Edge_1RB_Left



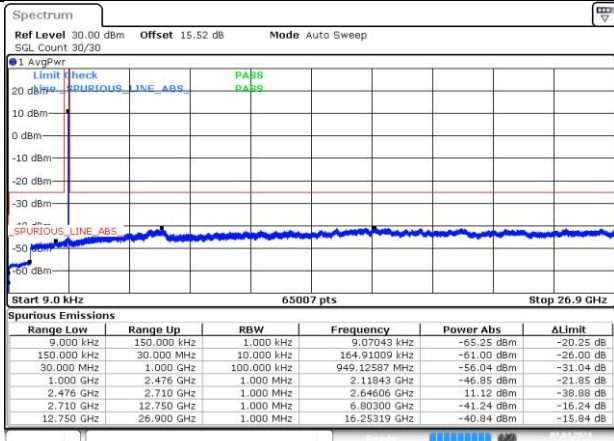
Date: 9 APR 2024 10:18:44

MCH_DFT-QPSK / Edge_1RB_Left



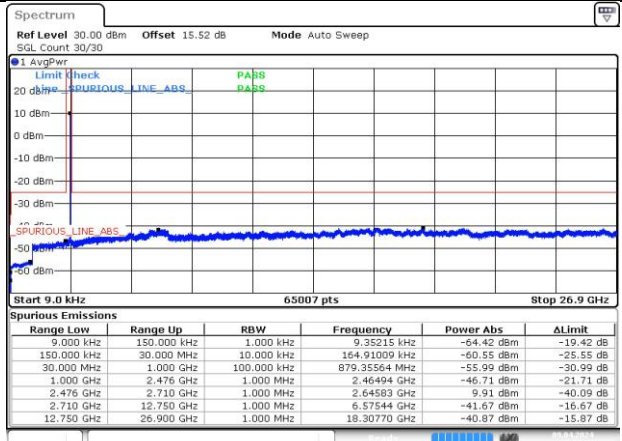
Date: 9 APR 2024 10:19:07

HCH_DFT-Pi2BPSK / Edge_1RB_Left

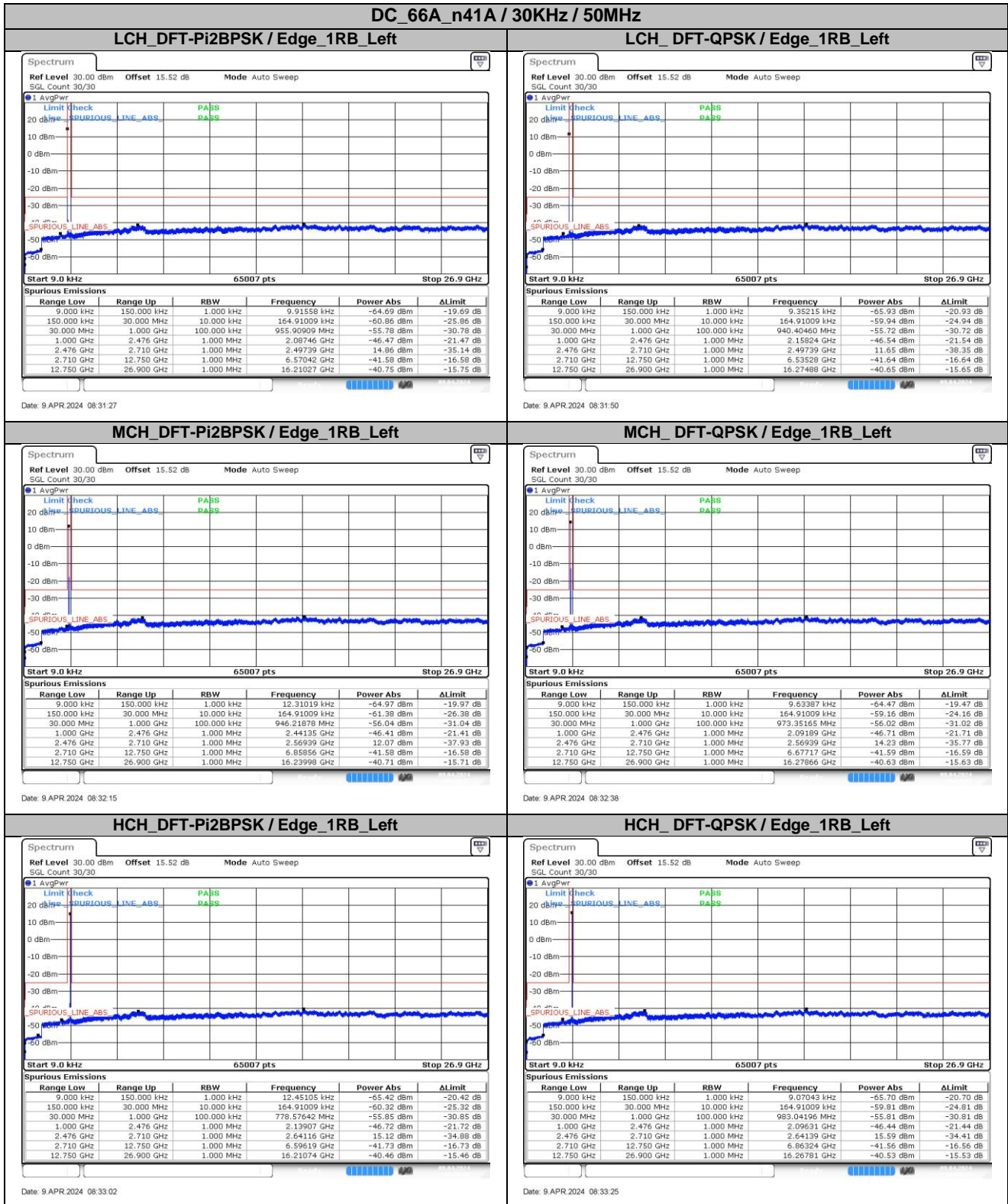


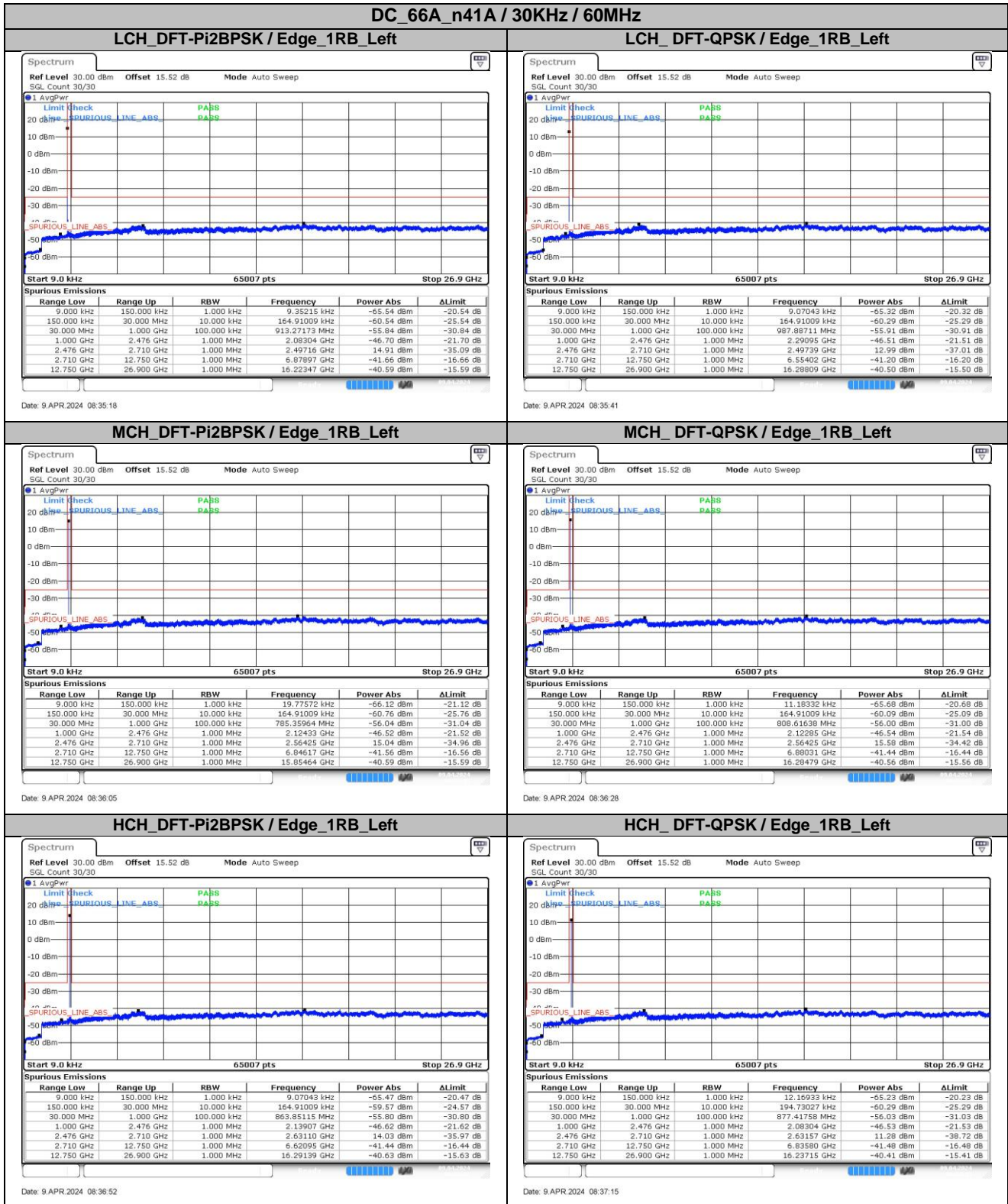
Date: 9 APR 2024 10:19:31

HCH_DFT-QPSK / Edge_1RB_Left



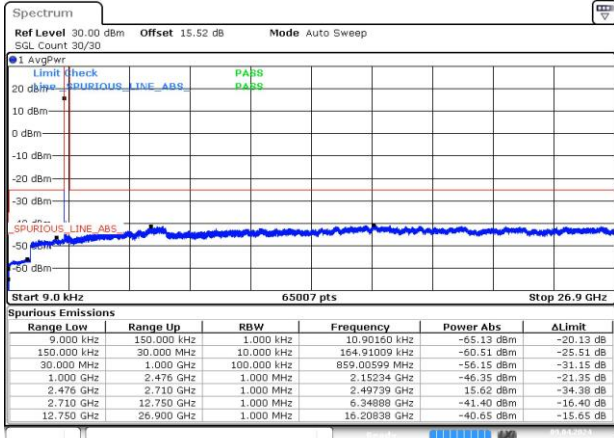
Date: 9 APR 2024 10:19:54





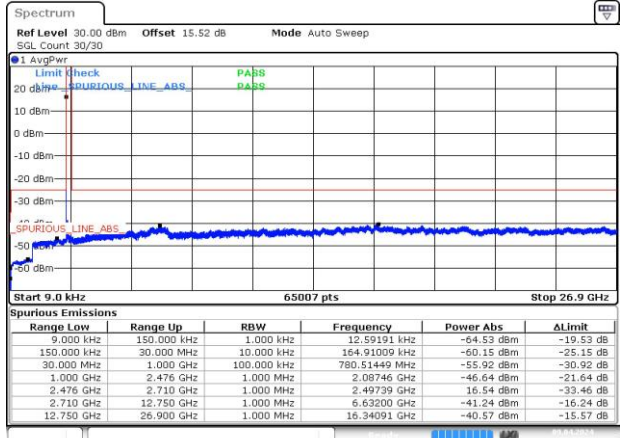
DC_66A_n41A / 30KHz / 70MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



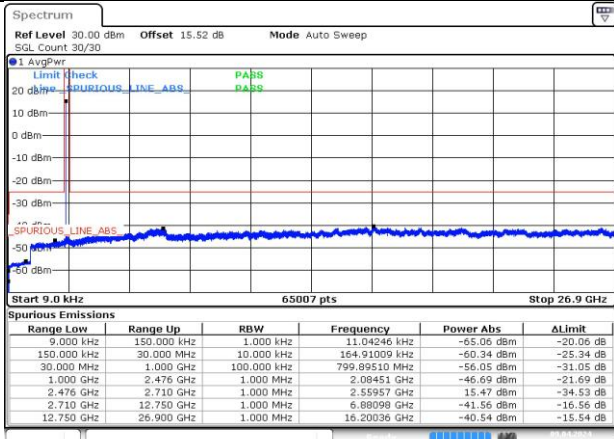
Date: 9 APR 2024 08:38:10

LCH_DFT-QPSK / Edge_1RB_Left



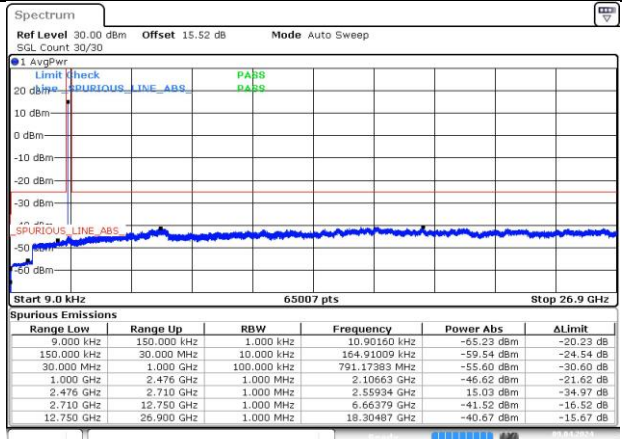
Date: 9 APR 2024 08:38:34

MCH_DFT-Pi2BPSK / Edge_1RB_Left



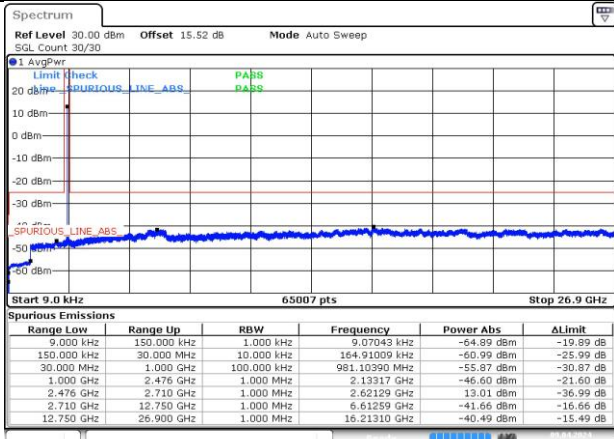
Date: 9 APR 2024 08:38:59

MCH_DFT-QPSK / Edge_1RB_Left



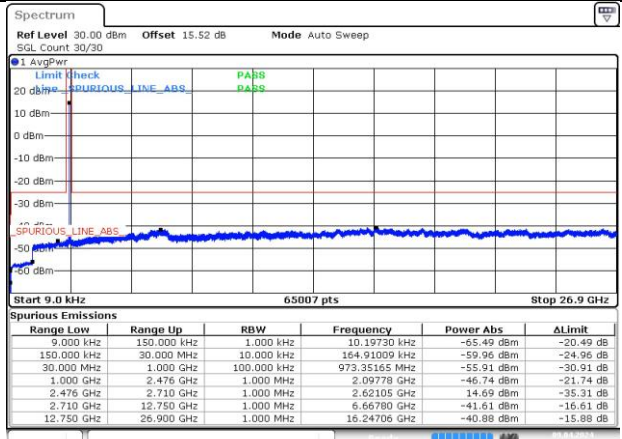
Date: 9 APR 2024 08:40:22

HCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 9 APR 2024 08:40:47

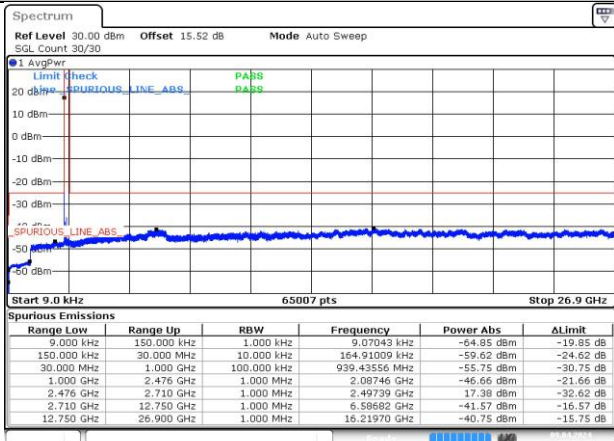
HCH_DFT-QPSK / Edge_1RB_Left



Date: 9 APR 2024 08:41:12

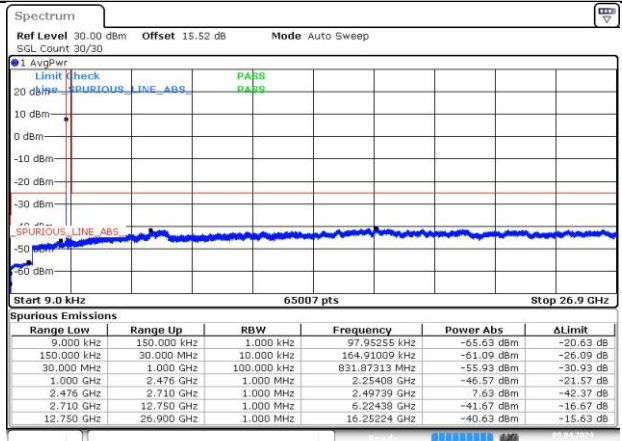
DC_66A_n41A / 30KHz / 80MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



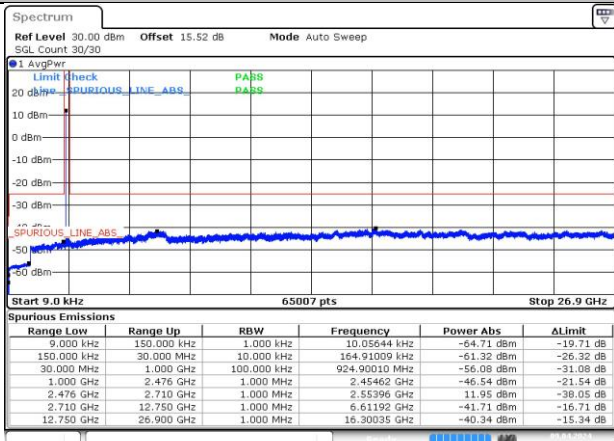
Date: 9 APR 2024 08:43:05

LCH_DFT-QPSK / Edge_1RB_Left



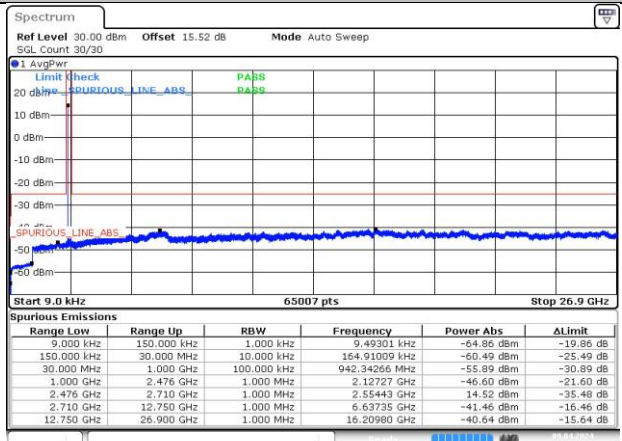
Date: 9 APR 2024 08:43:29

MCH_DFT-Pi2BPSK / Edge_1RB_Left



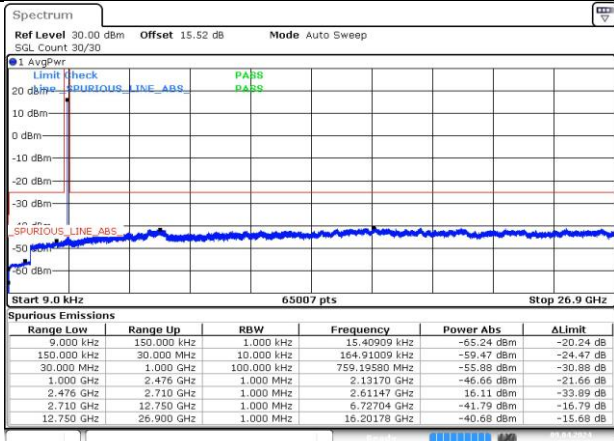
Date: 9 APR 2024 08:43:53

MCH_DFT-QPSK / Edge_1RB_Left



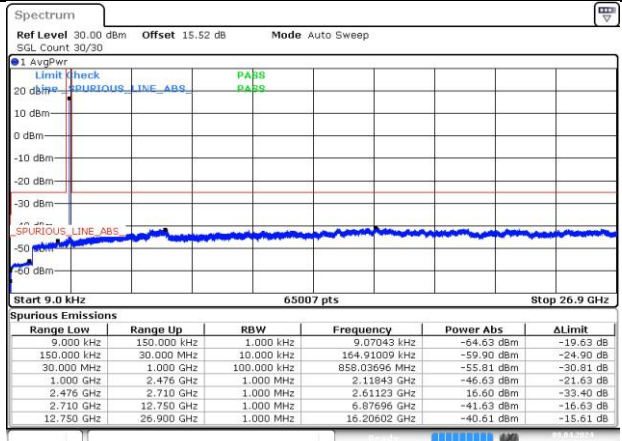
Date: 9 APR 2024 08:44:17

HCH_DFT-Pi2BPSK / Edge_1RB_Left

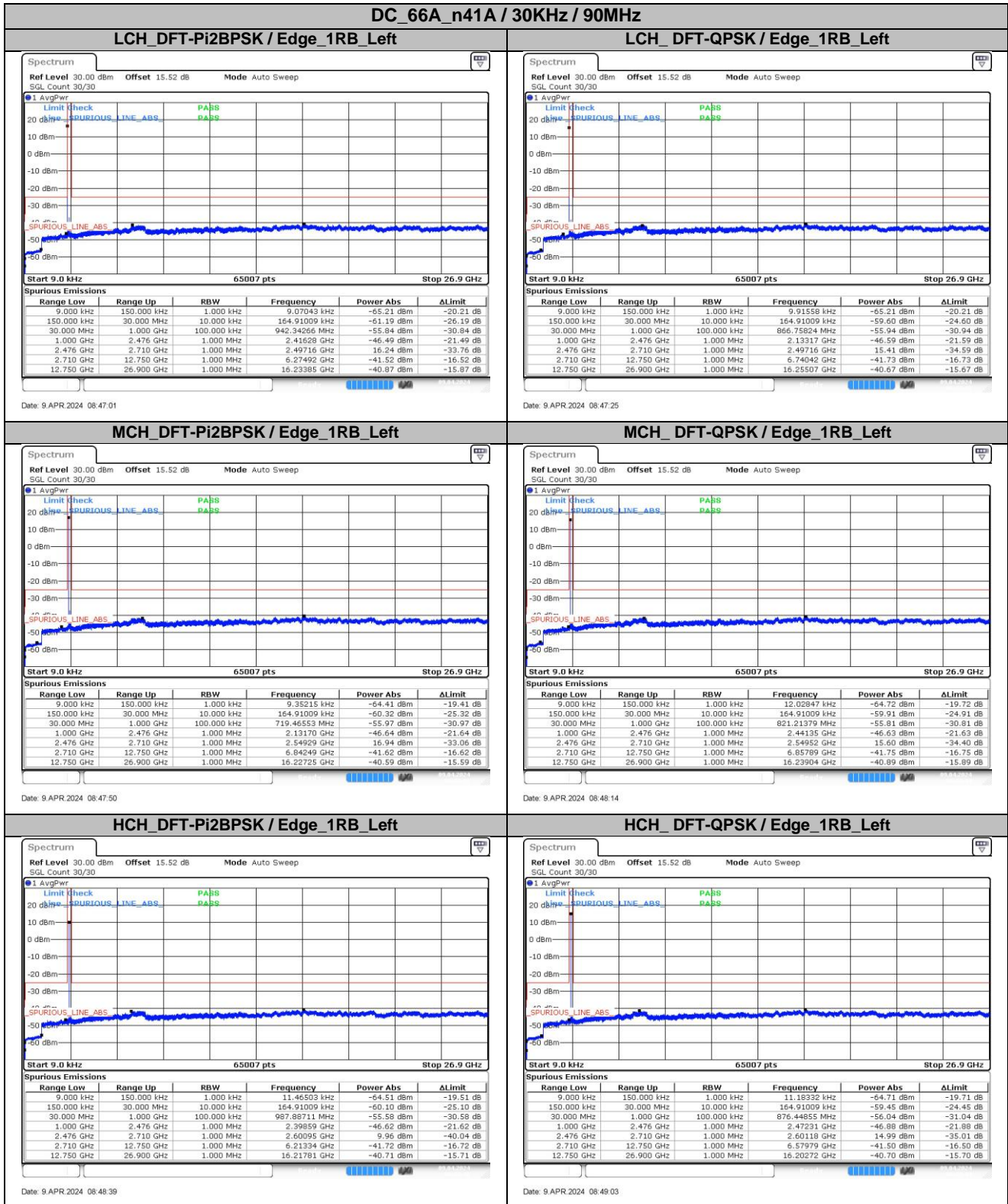


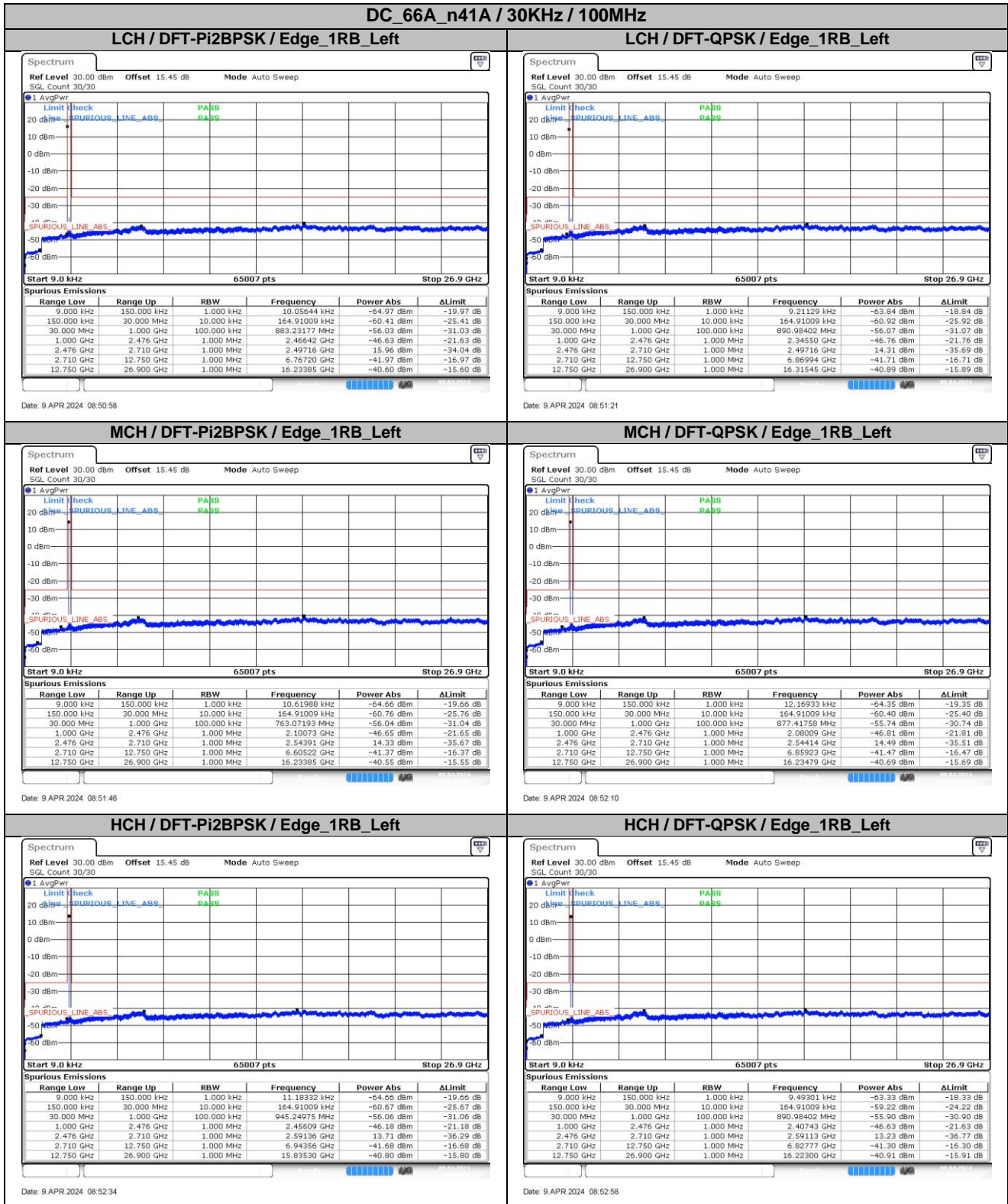
Date: 9 APR 2024 08:44:43

HCH_DFT-QPSK / Edge_1RB_Left



Date: 9 APR 2024 08:45:07





7. Frequency Stability

7.1. Test Results

7.1.1. Frequency Error Vs Voltage

| SCS | Bandwidth | Channel | RB Config | Modulation | Temperature | Voltage | Deviation Result | | Verdict |
|-------|-----------|---------|------------|------------|-------------|---------|---|---|---------|
| | | | | | | | (Hz) | (ppm) | |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | NT | LV | -4.20 | -0.001620 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | NT | NV | -4.40 | -0.001697 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | NT | HV | -1.30 | -0.000501 | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | NT | LV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | NT | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | NT | HV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |

7.1.2. Frequency Error Vs Temperature

| SCS | Bandwidth | Channel | RB Config | Modulation | Temperature | Voltage | Deviation Result | | Verdict |
|-------|-----------|---------|------------|------------|-------------|---------|---|---|---------|
| | | | | | | | (Hz) | (ppm) | |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | -30°C | NV | -2.20 | -0.000848 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | -20°C | NV | -1.40 | -0.000540 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | -10°C | NV | -4.80 | -0.001851 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | 0°C | NV | 0.80 | 0.000309 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | 10°C | NV | -4.90 | -0.001890 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | 20°C | NV | -2.60 | -0.001003 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | 30°C | NV | -0.80 | -0.000309 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | 40°C | NV | -1.40 | -0.000540 | Pass |
| 15KHz | 50MHz | MCH | Outer_Full | DFT-QPSK | 50°C | NV | -3.50 | -0.001350 | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | -30°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | -20°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | -10°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | 0°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | 10°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | 20°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | 30°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | 40°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |
| 30KHz | 100MHz | MCH | Outer_Full | DFT-QPSK | 50°C | NV | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | <small>NTV2_FreqSprr1_DCF_80A_401A_C0A 3.300Hz_100MHz_MCH_DFT- QPSK_Cover_Full_Avds_Rst</small> | Pass |

The End