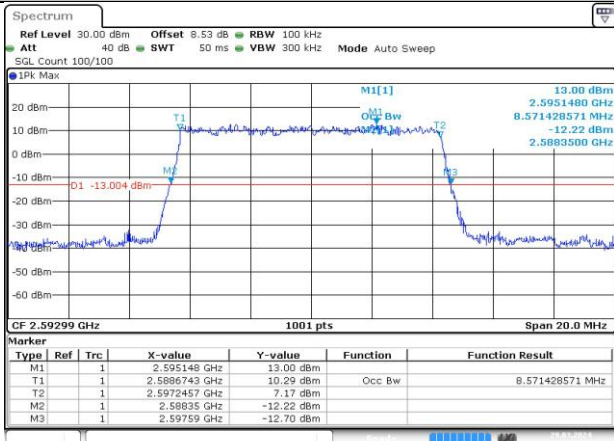
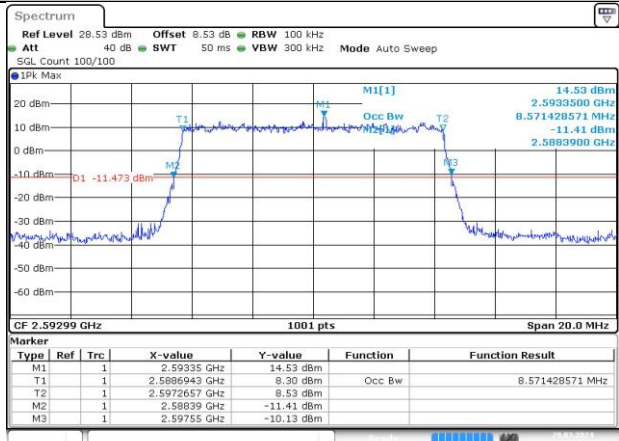


DC_66A_n41A / 30KHz / 10MHz

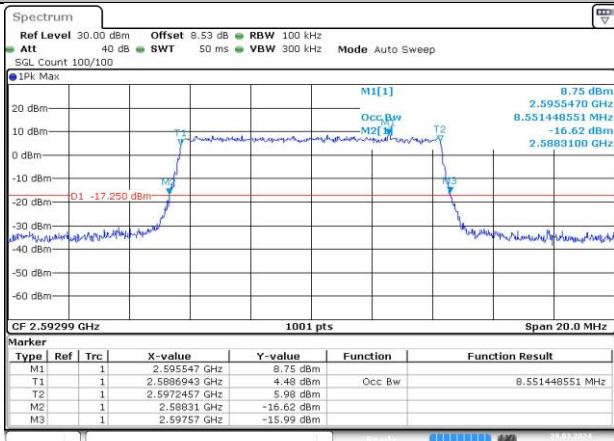
MCH_DFT-Pi2BPSK / Outer_Full



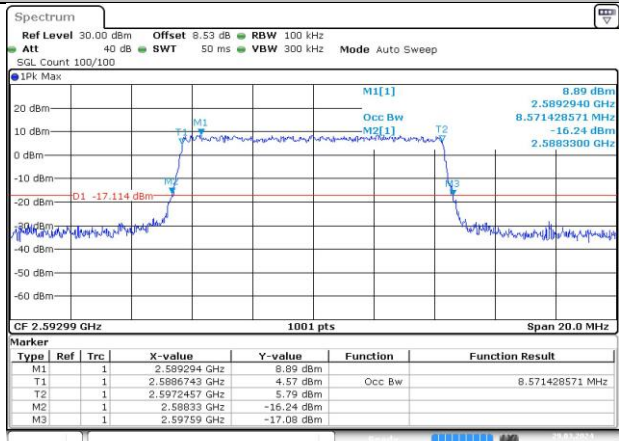
MCH_DFT-QPSK / Outer_Full



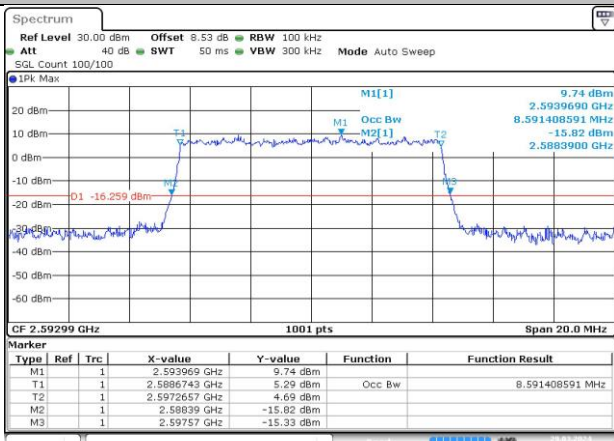
MCH_CP-QPSK / Outer_Full



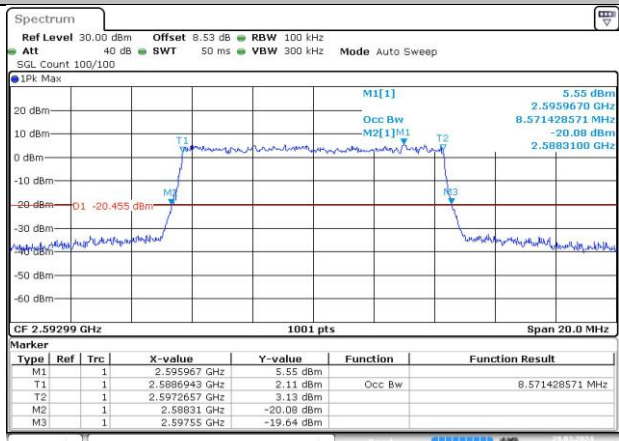
MCH_CP-16QAM / Outer_Full



MCH_CP-64QAM / Outer_Full

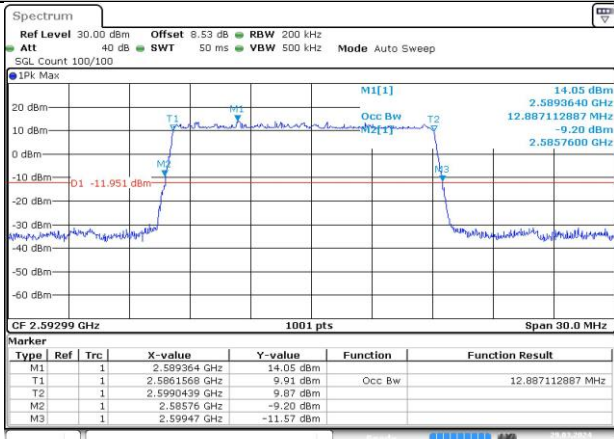


MCH_CP-256QAM / Outer_Full



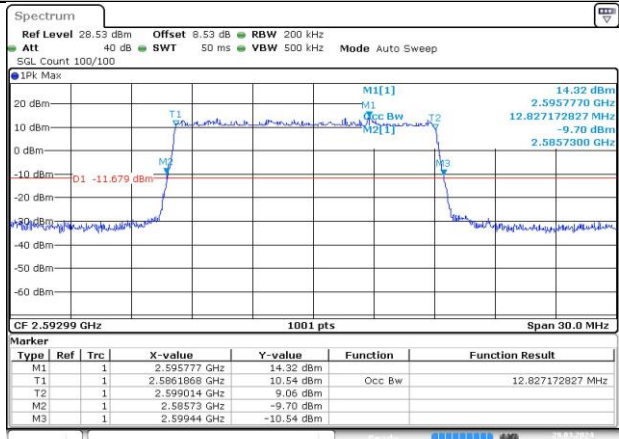
DC_66A_n41A / 30KHz / 15MHz

MCH_DFT-Pi2BPSK / Outer_Full



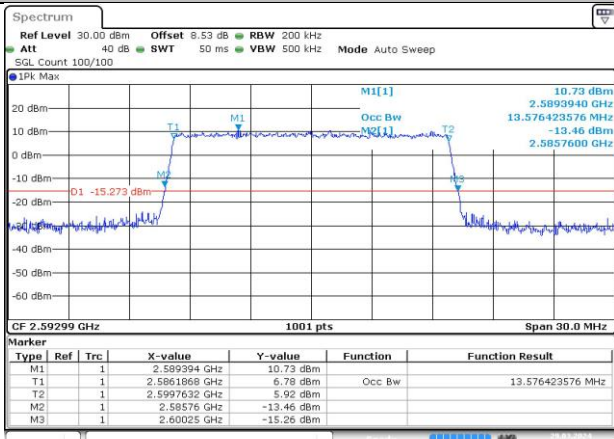
Date: 29 MAR 2024 18:24:24

MCH_DFT-QPSK / Outer_Full



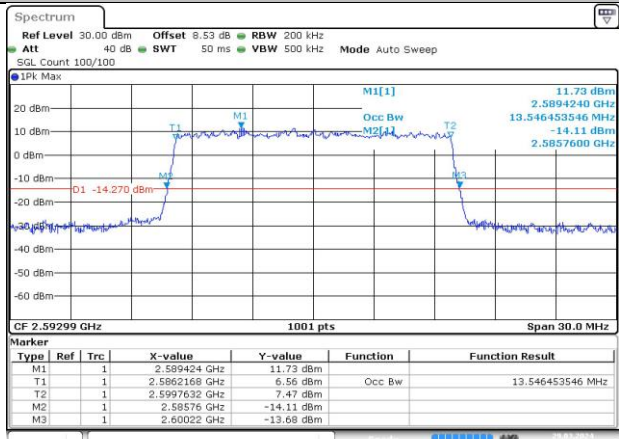
Date: 29 MAR 2024 18:24:47

MCH_CP-QPSK / Outer_Full



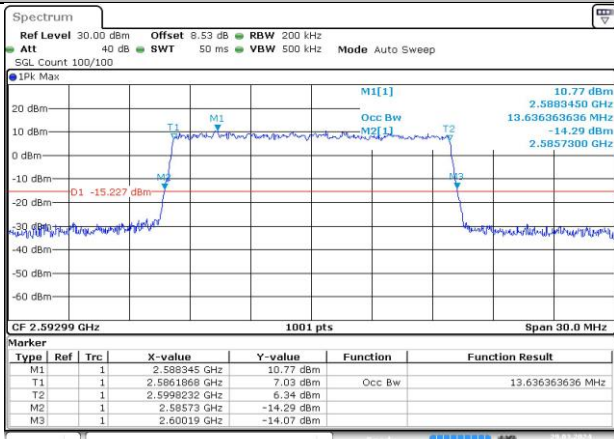
Date: 29 MAR 2024 18:25:03

MCH_CP-16QAM / Outer_Full



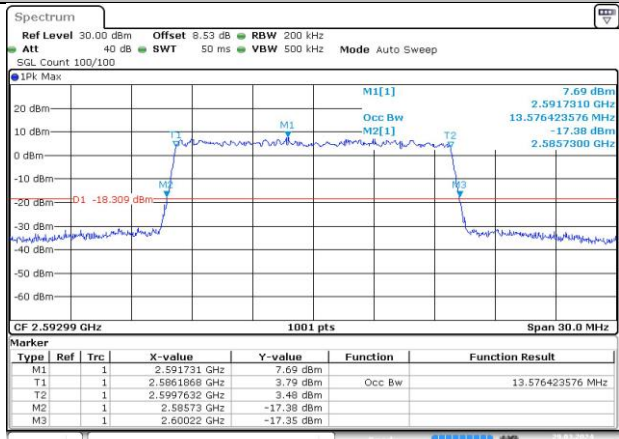
Date: 29 MAR 2024 18:25:19

MCH_CP-64QAM / Outer_Full



Date: 29 MAR 2024 18:25:38

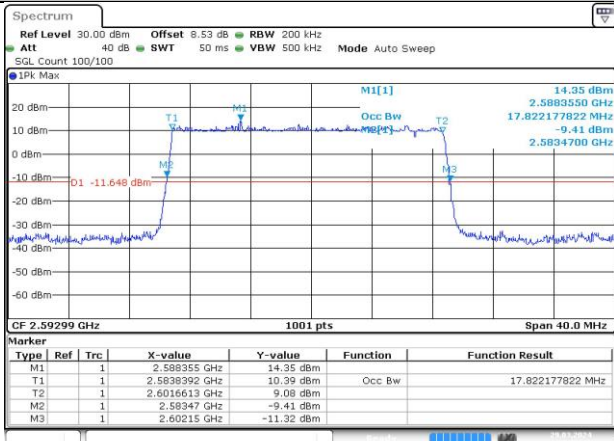
MCH_CP-256QAM / Outer_Full



Date: 29 MAR 2024 18:25:55

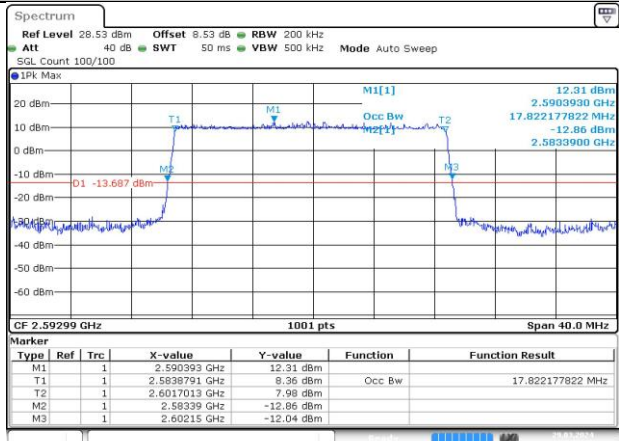
DC_66A_n41A / 30KHz / 20MHz

MCH_DFT-Pi2BPSK / Outer_Full



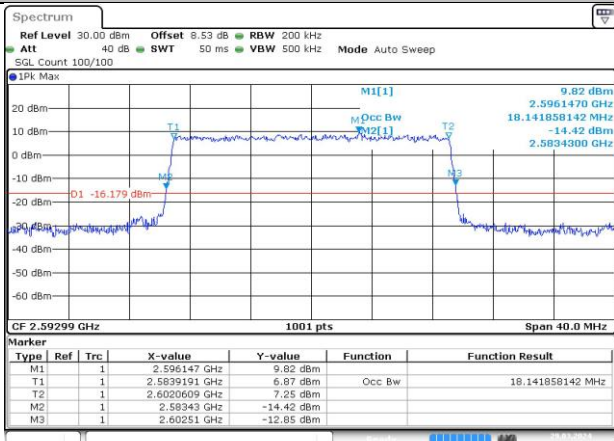
Date: 29 MAR 2024 18:26:15

MCH_DFT-QPSK / Outer_Full



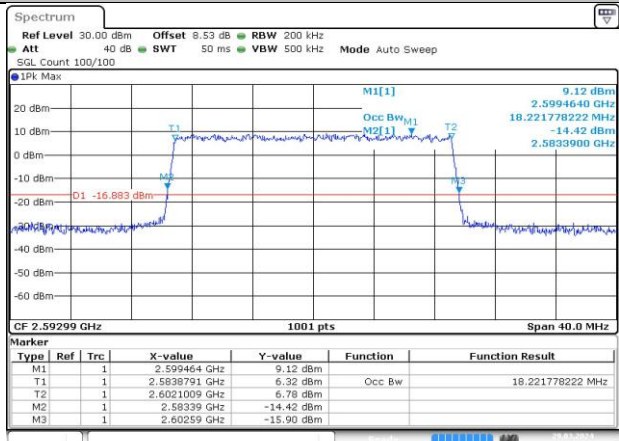
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MCH_CP-QPSK / Outer_Full



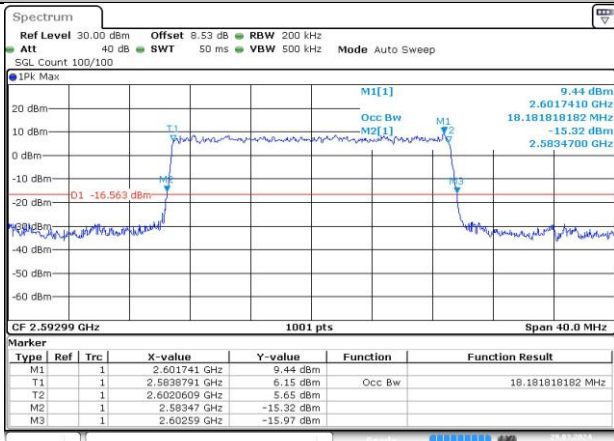
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MCH_CP-16QAM / Outer_Full



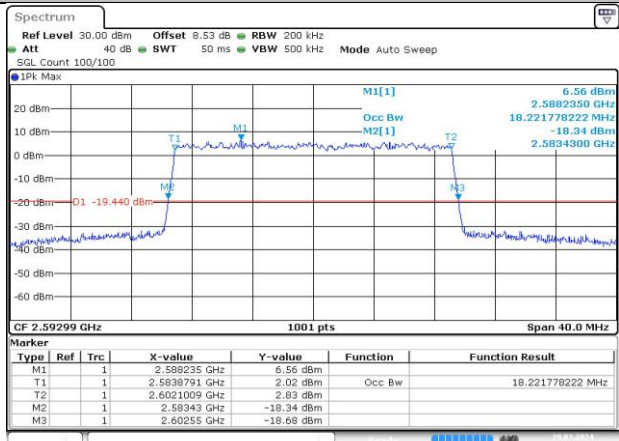
Date: 29 MAR 2024 18:27:11

MCH_CP-64QAM / Outer_Full

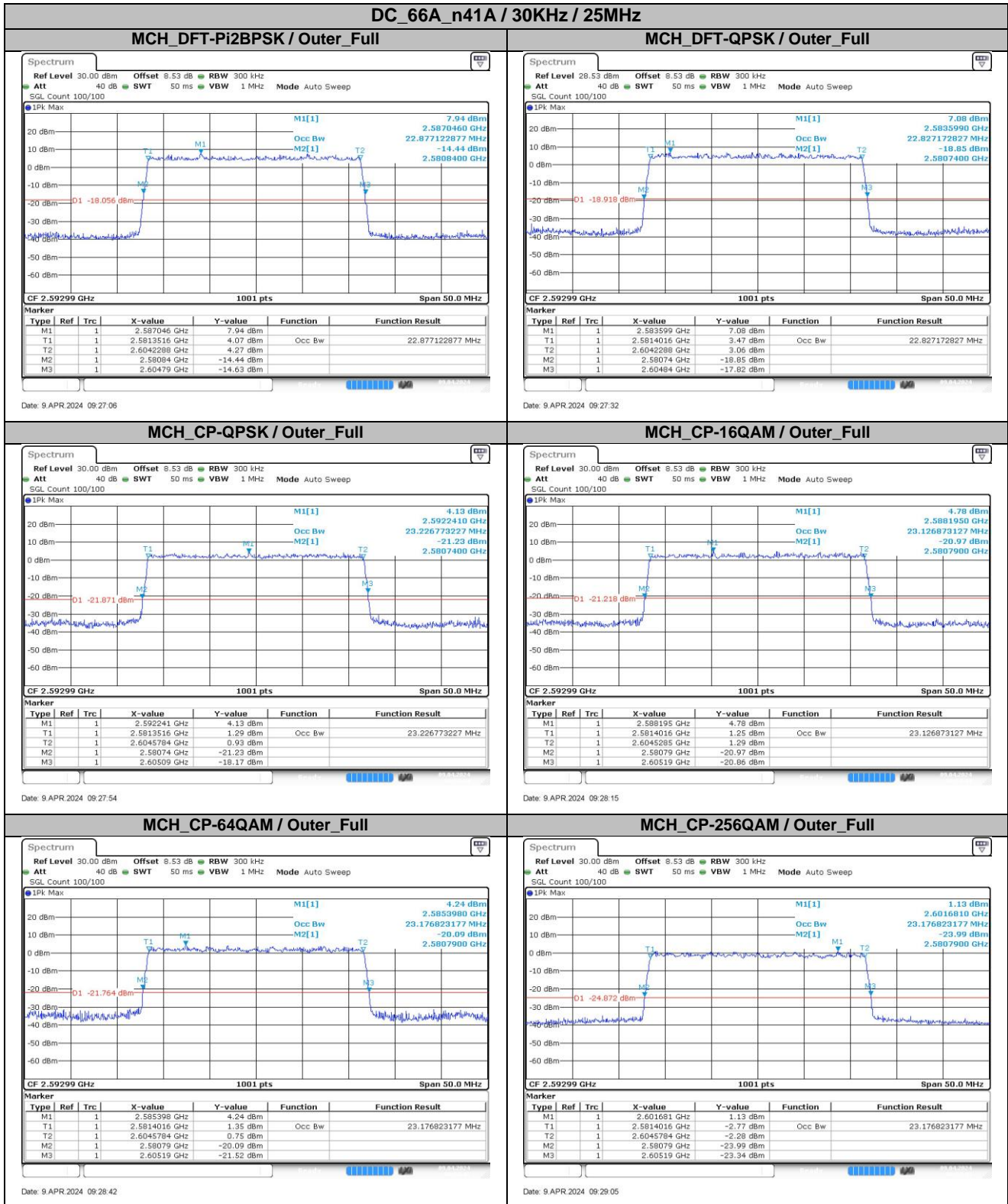


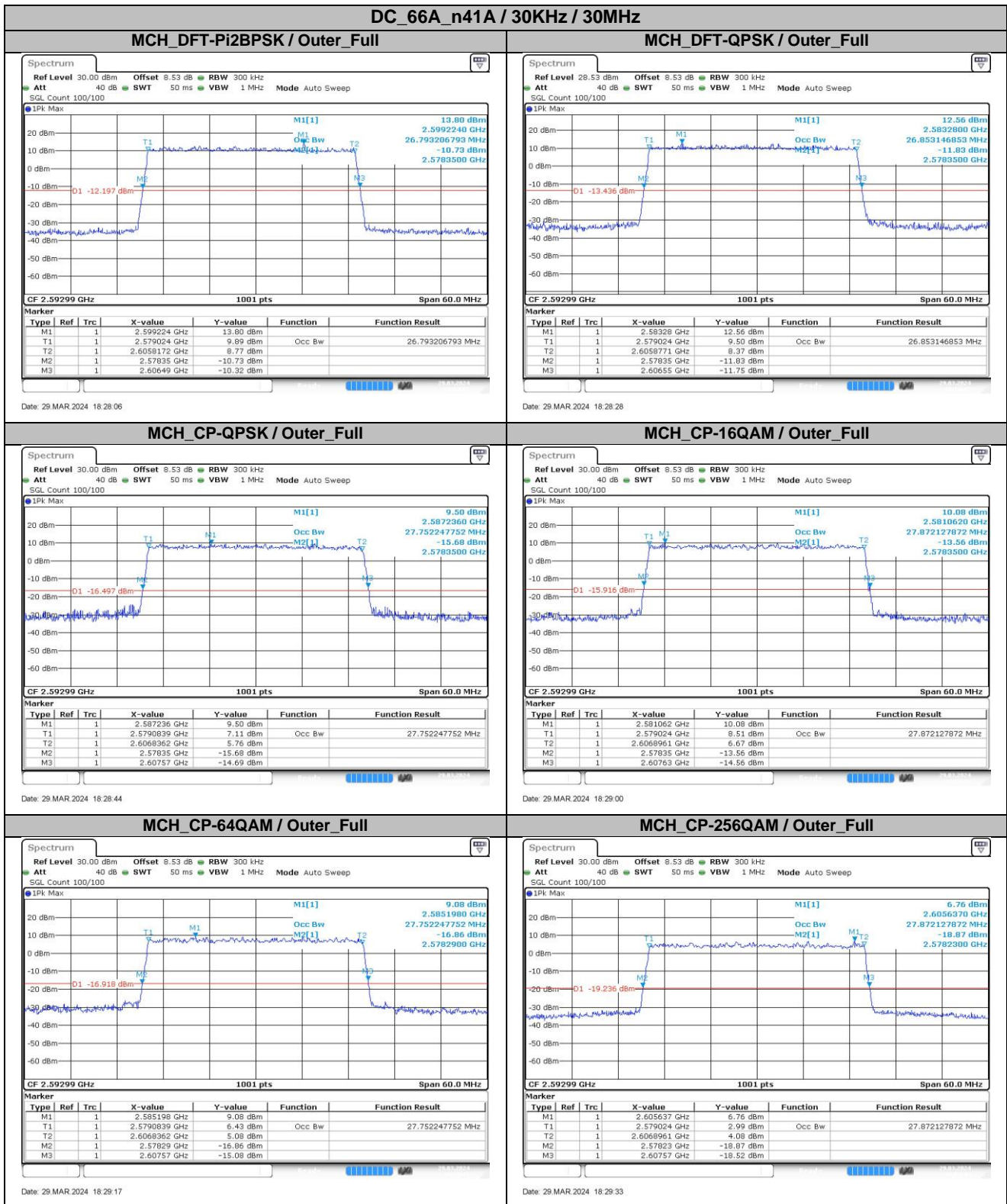
Date: 29 MAR 2024 18:27:29

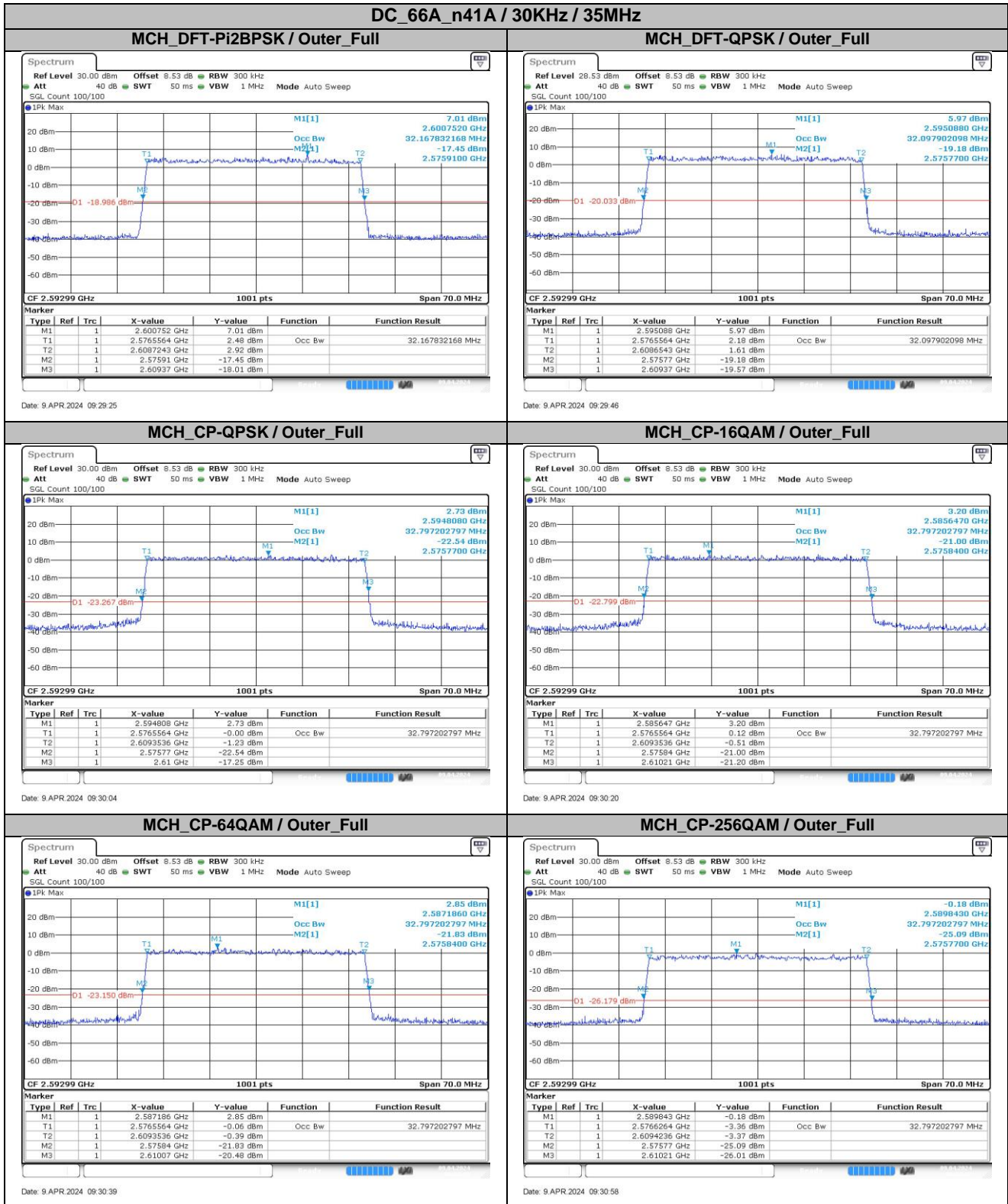
MCH_CP-256QAM / Outer_Full

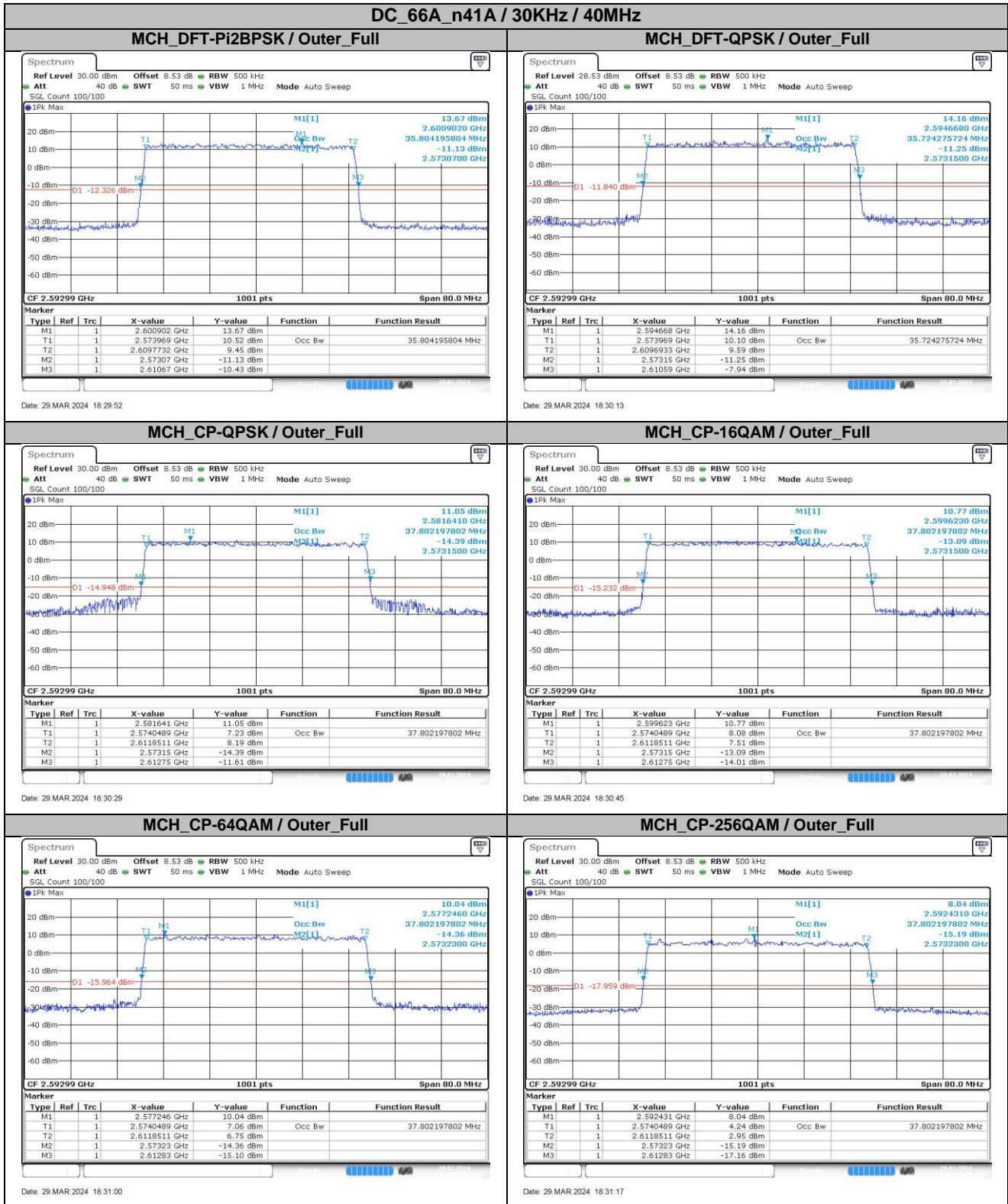


Date: 29 MAR 2024 18:27:47



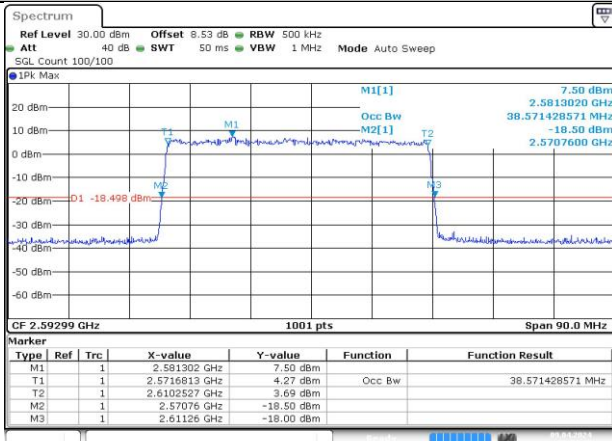






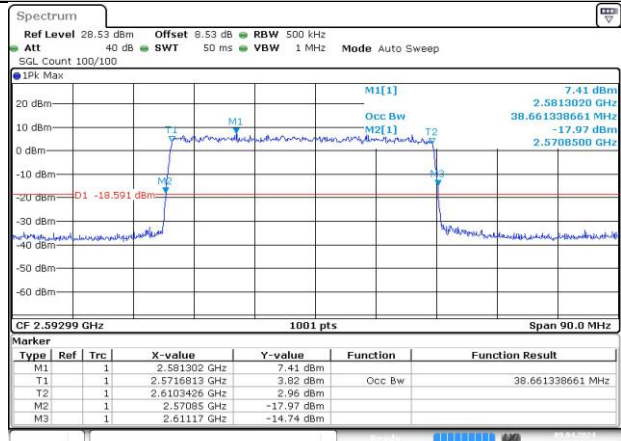
DC_66A_n41A / 30KHz / 45MHz

MCH_DFT-Pi2BPSK / Outer_Full



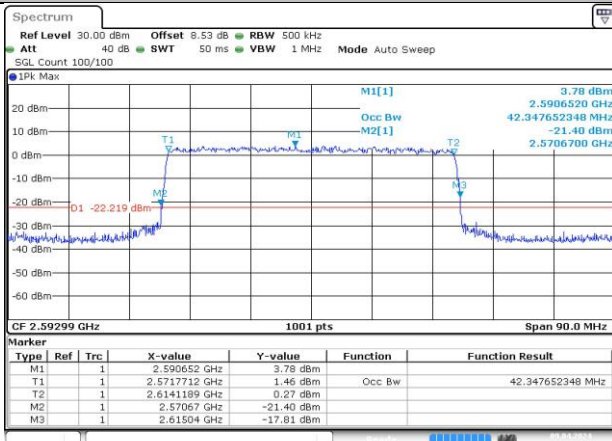
Date: 9 APR 2024 09:31:17

MCH_DFT-QPSK / Outer_Full



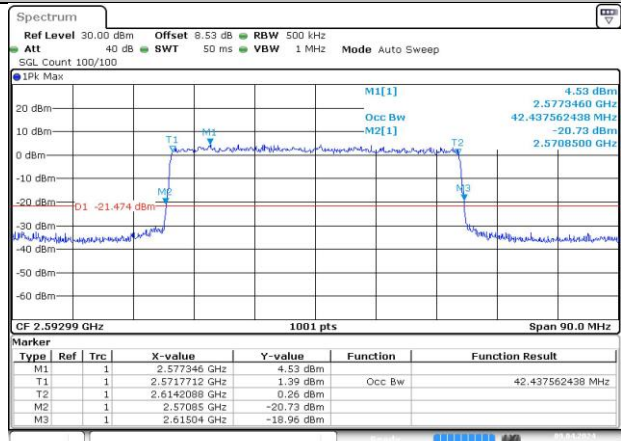
Date: 9 APR 2024 09:31:38

MCH_CP-QPSK / Outer_Full



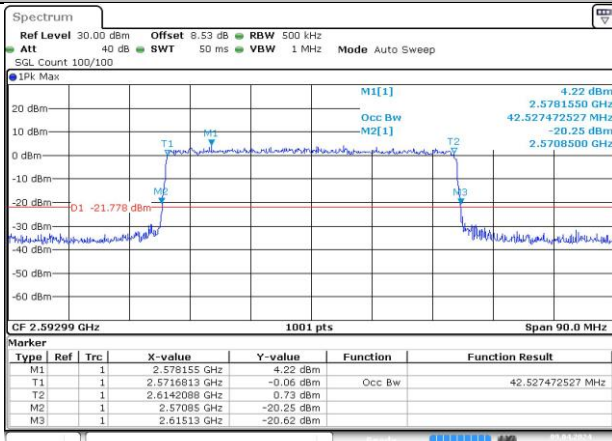
Date: 9 APR 2024 09:31:56

MCH_CP-16QAM / Outer_Full



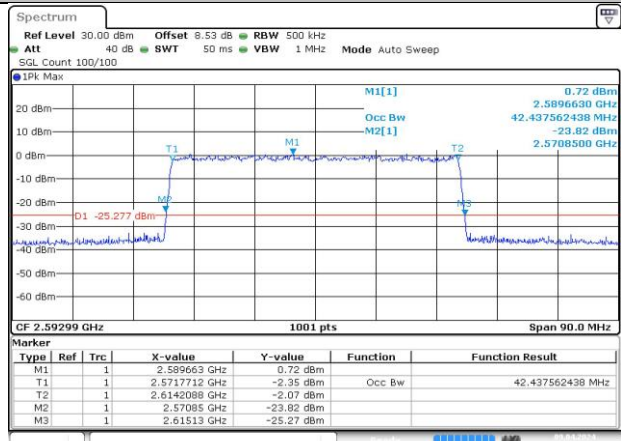
Date: 9 APR 2024 09:32:12

MCH_CP-64QAM / Outer_Full



Date: 9 APR 2024 09:32:30

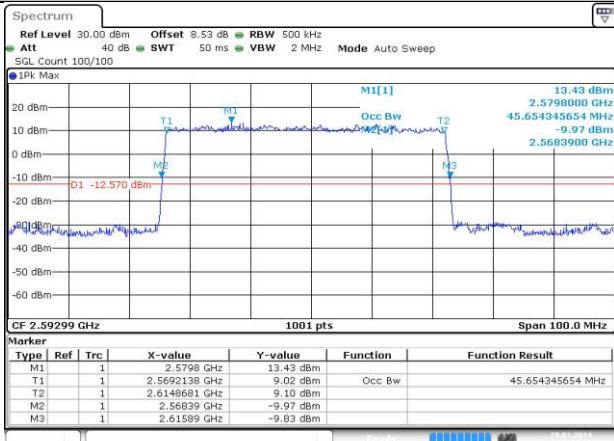
MCH_CP-256QAM / Outer_Full



Date: 9 APR 2024 09:32:49

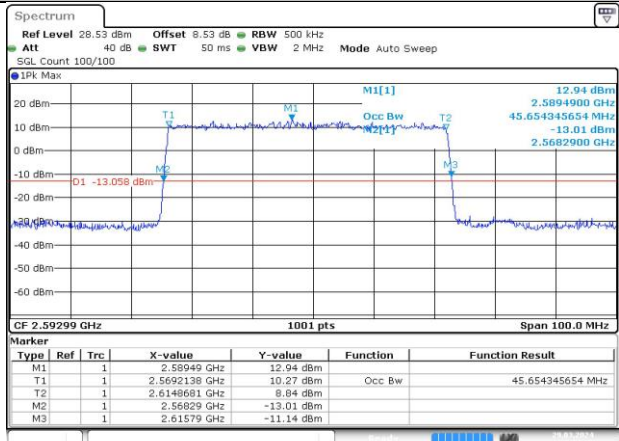
DC_66A_n41A / 30KHz / 50MHz

MCH_DFT-Pi2BPSK / Outer_Full



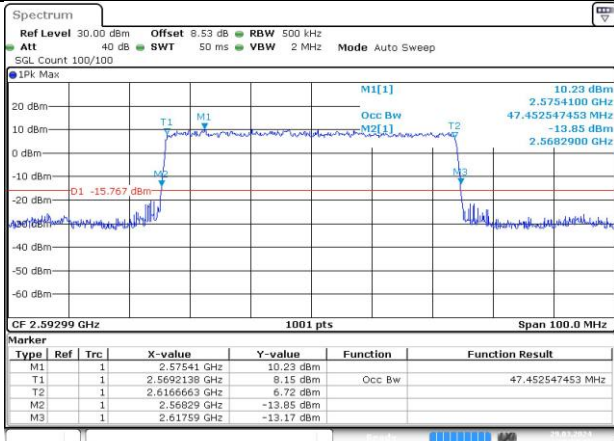
Date: 29 MAR 2024 18:31:36

MCH_DFT-QPSK / Outer_Full



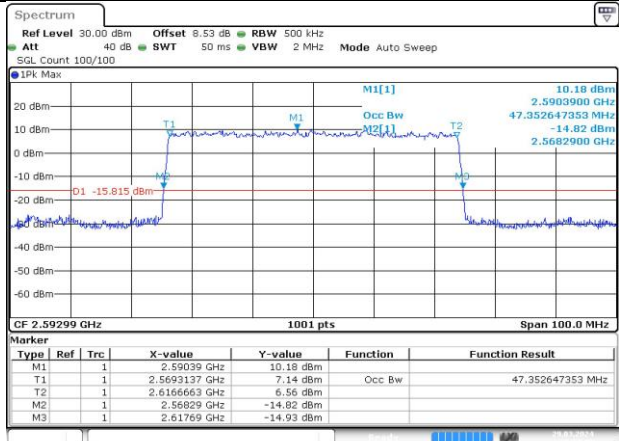
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MCH_CP-QPSK / Outer_Full



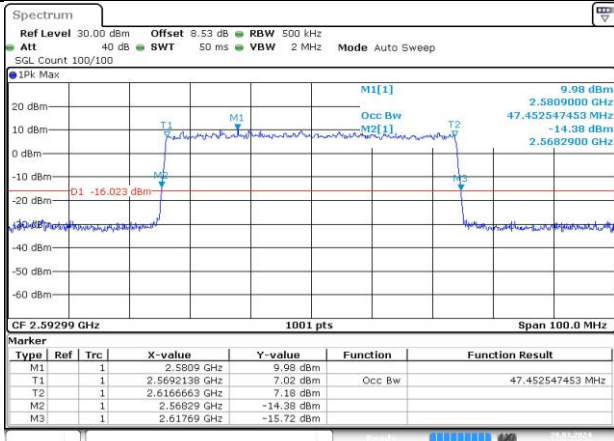
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MCH_CP-16QAM / Outer_Full



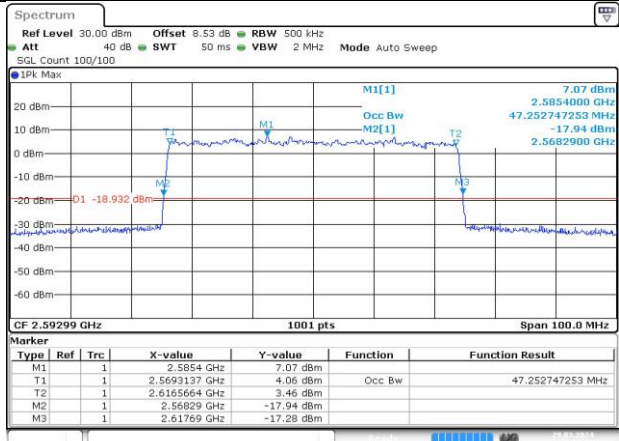
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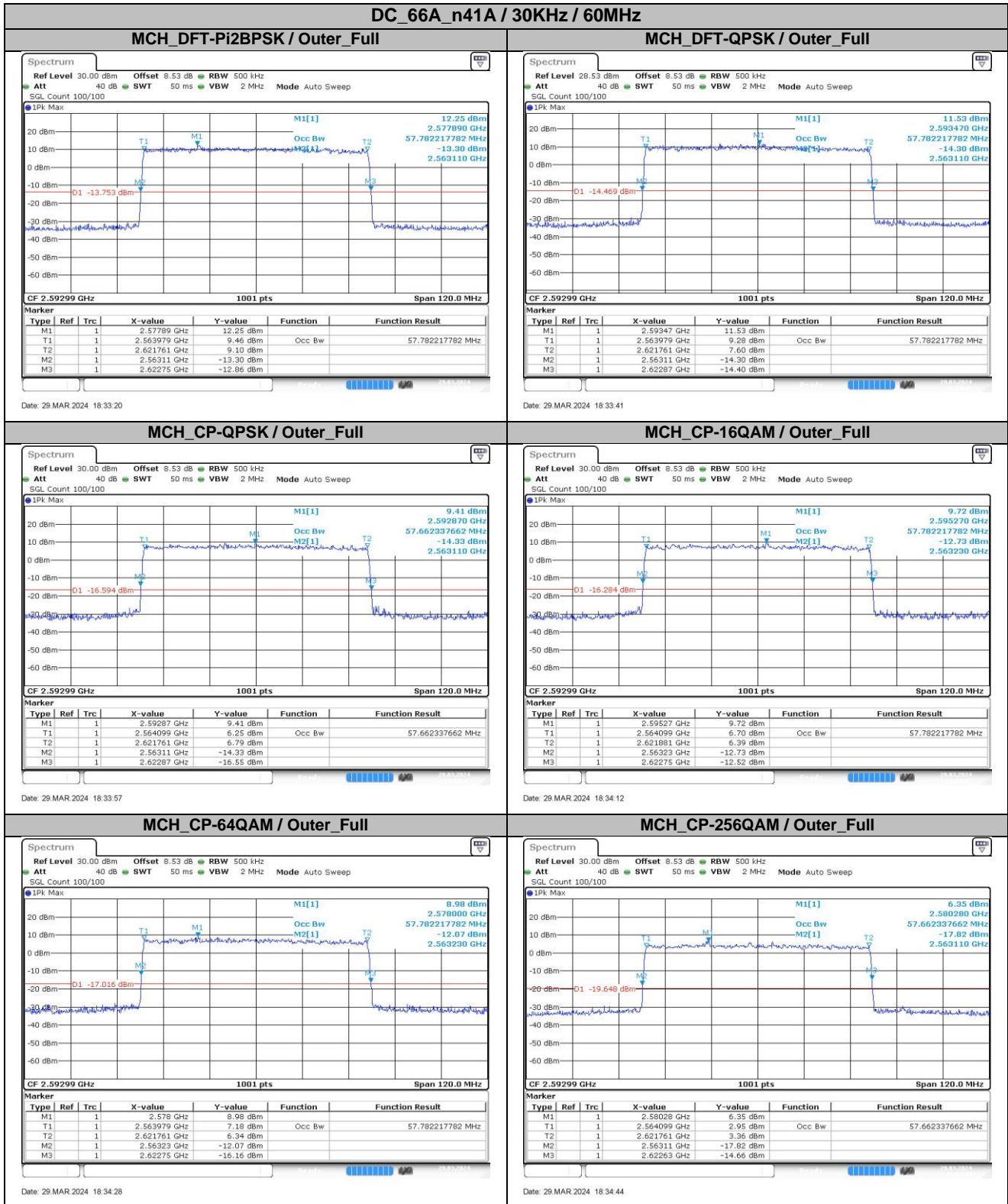


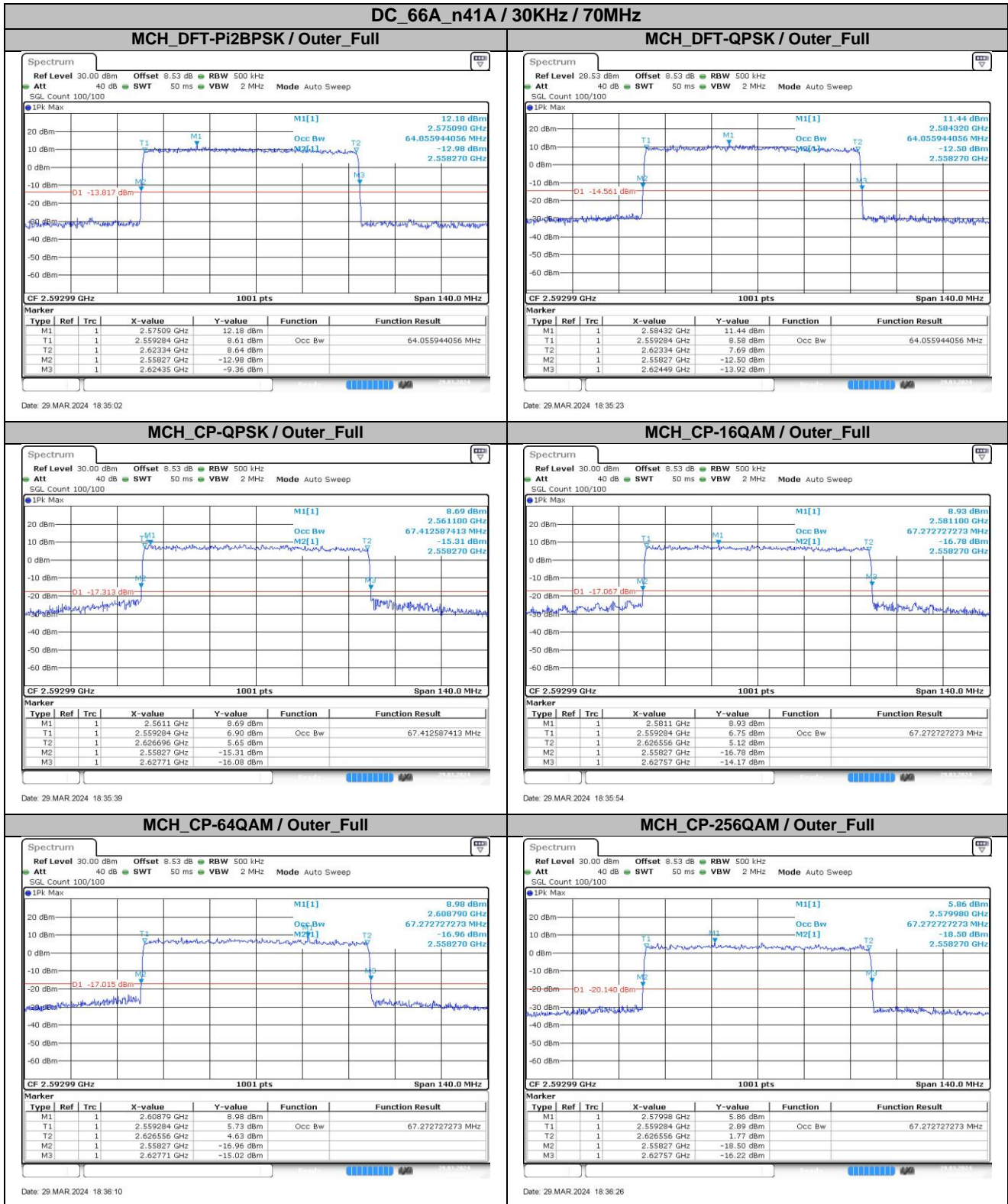
Date: 29 MAR 2024 18:32:45

MCH_CP-256QAM / Outer_Full



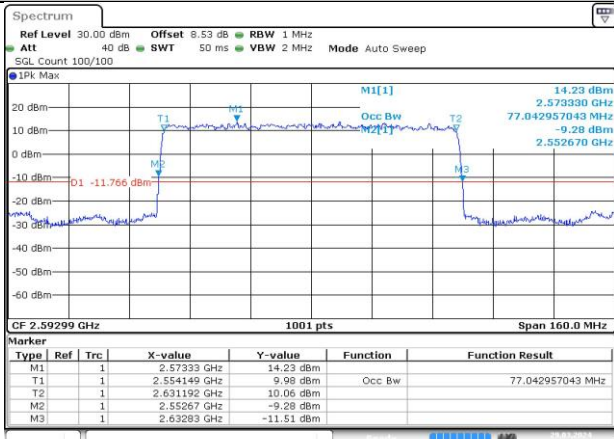
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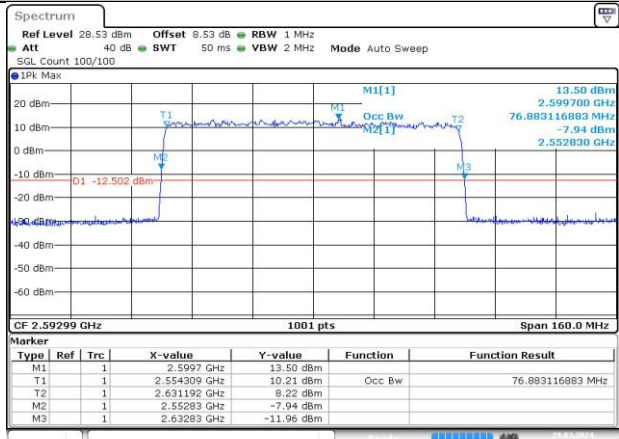
DC_66A_n41A / 30KHz / 80MHz

MCH_DFT-Pi2BPSK / Outer_Full



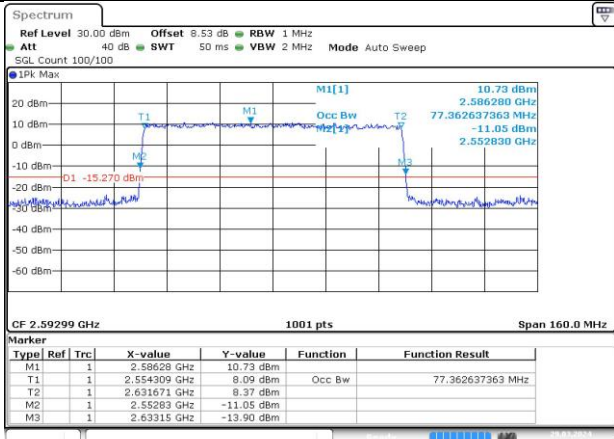
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MCH_DFT-QPSK / Outer_Full



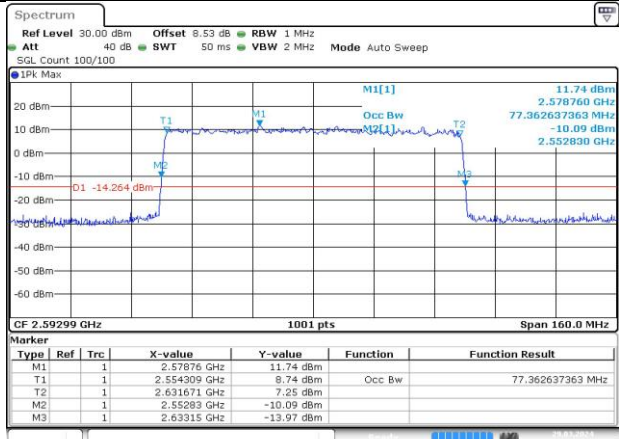
Date: 29 MAR 2024 18:37:06

MCH_CP-QPSK / Outer_Full



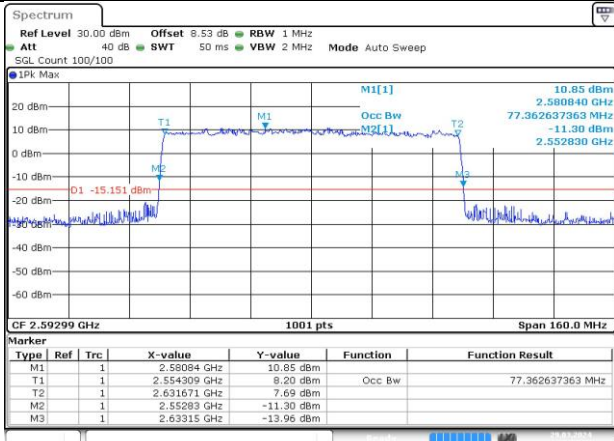
Date: 29 MAR 2024 18:37:22

MCH_CP-16QAM / Outer_Full



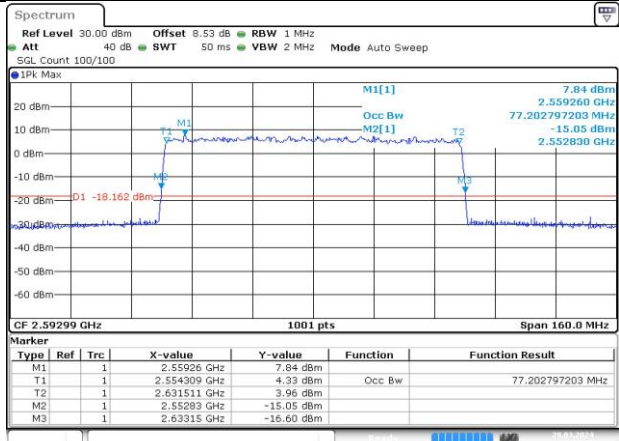
Date: 29 MAR 2024 18:37:38

MCH_CP-64QAM / Outer_Full



Date: 29 MAR 2024 18:37:54

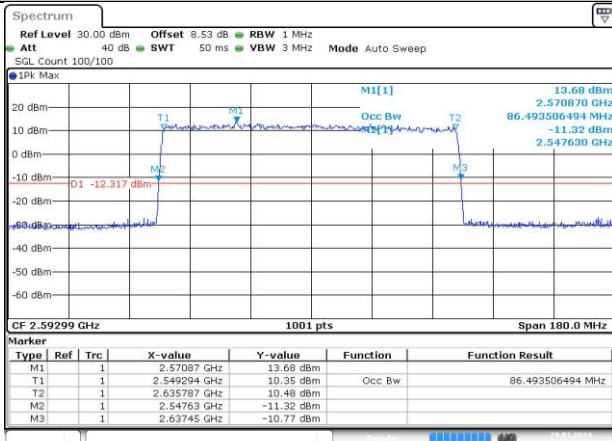
MCH_CP-256QAM / Outer_Full



Date: 29 MAR 2024 18:38:11

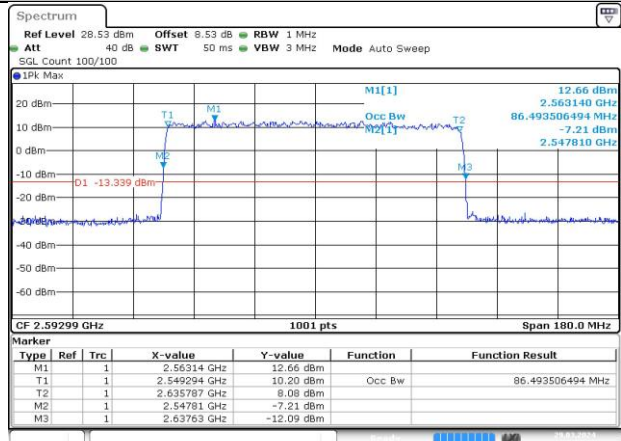
DC_66A_n41A / 30KHz / 90MHz

MCH_DFT-Pi2BPSK / Outer_Full



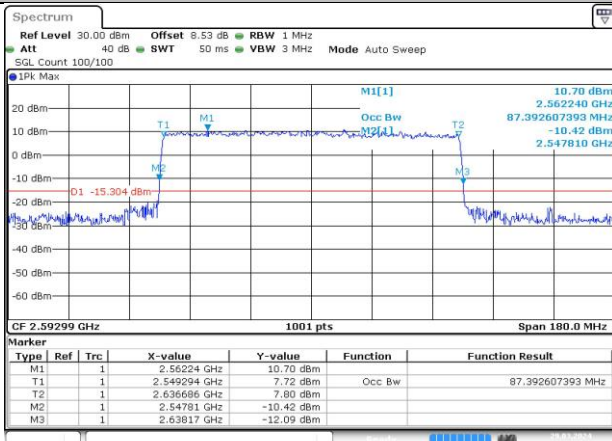
Date: 29 MAR 2024 18:38:30

MCH_DFT-QPSK / Outer_Full



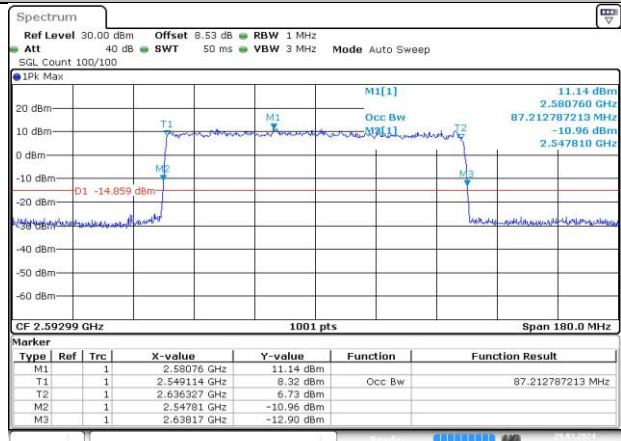
Date: 29 MAR 2024 18:38:51

MCH_CP-QPSK / Outer_Full



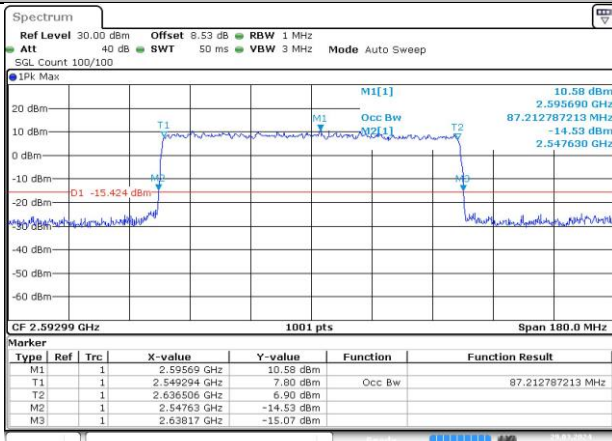
Date: 29 MAR 2024 18:39:07

MCH_CP-16QAM / Outer_Full



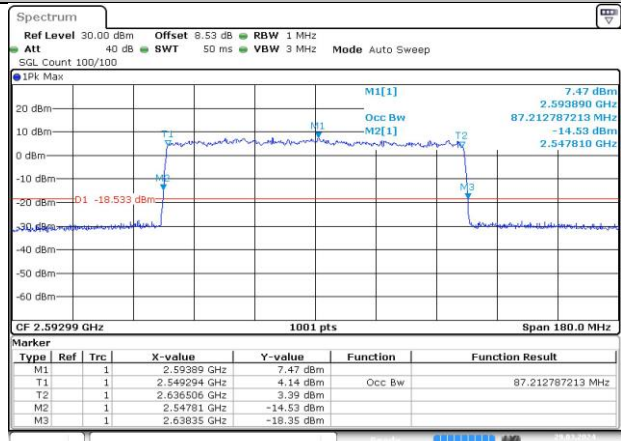
Date: 29 MAR 2024 18:39:23

MCH_CP-64QAM / Outer_Full



Date: 29 MAR 2024 18:39:39

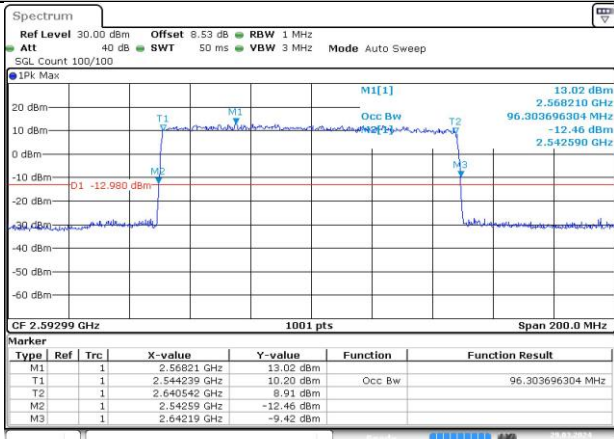
MCH_CP-256QAM / Outer_Full



Date: 29 MAR 2024 18:39:56

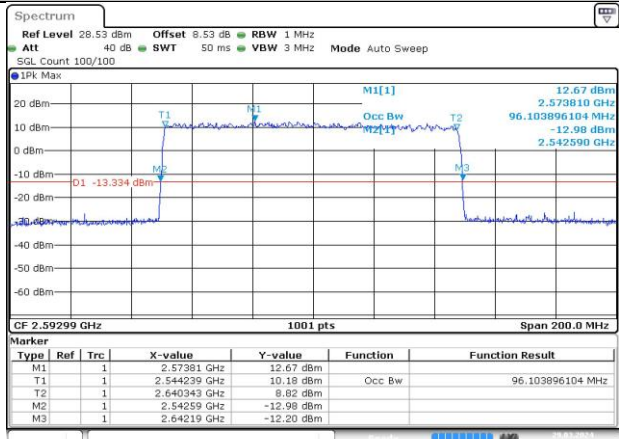
DC_66A_n41A / 30KHz / 100MHz

MCH_DFT-Pi2BPSK / Outer_Full



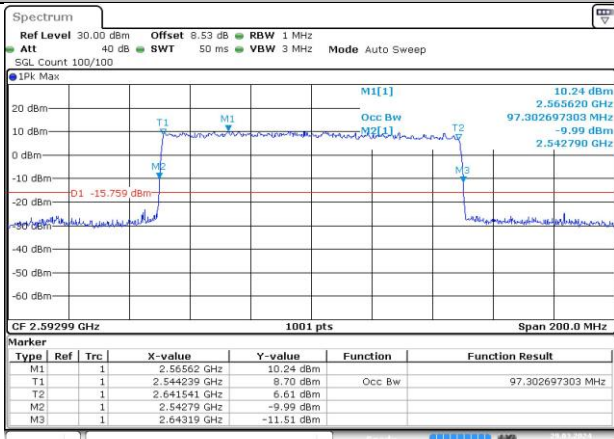
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MCH_DFT-QPSK / Outer_Full



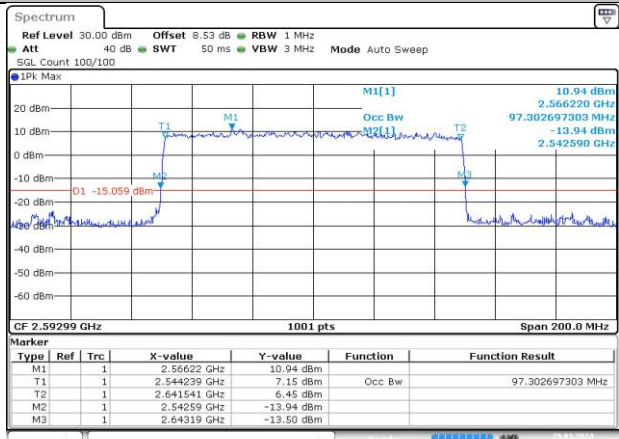
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MCH_CP-QPSK / Outer_Full



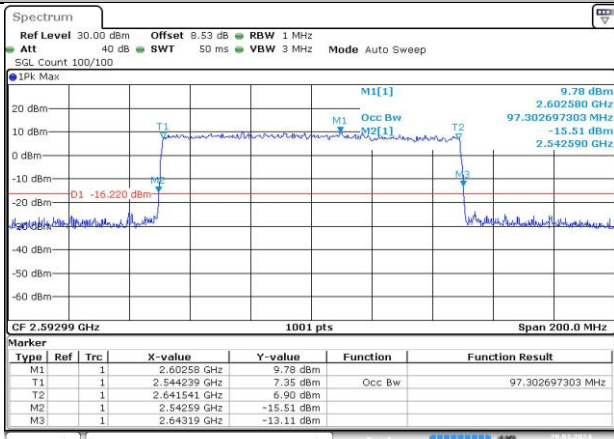
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MCH_CP-16QAM / Outer_Full



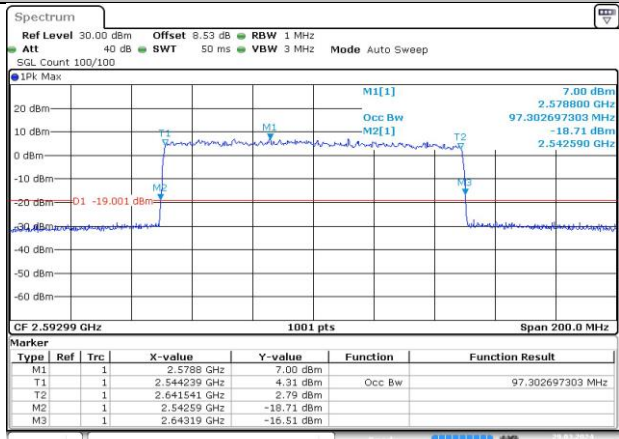
Date: 29 MAR 2024 18:41:08

MCH_CP-64QAM / Outer_Full



Date: 29 MAR 2024 18:41:24

MCH_CP-256QAM / Outer_Full



Date: 29 MAR 2024 18:41:41