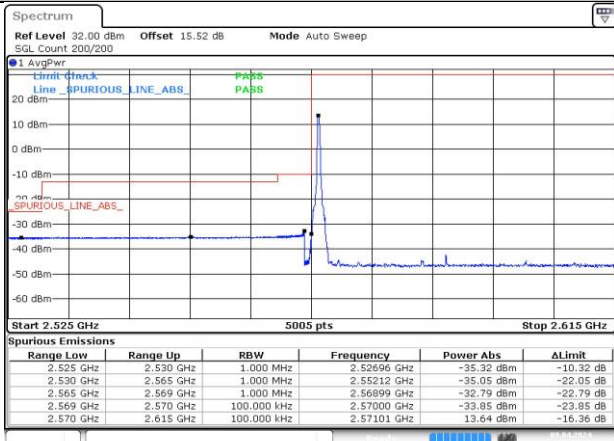


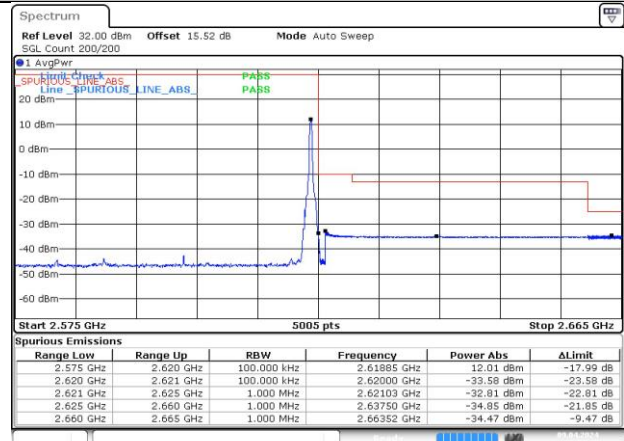
DC\_4A\_n38A / 30KHz / 40MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



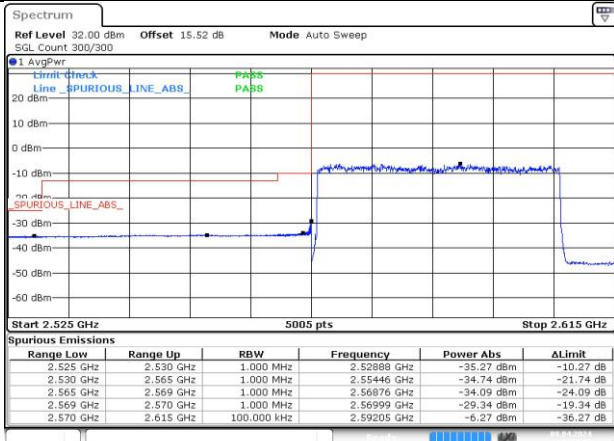
Date: 9 APR 2024 16:18:16

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Right



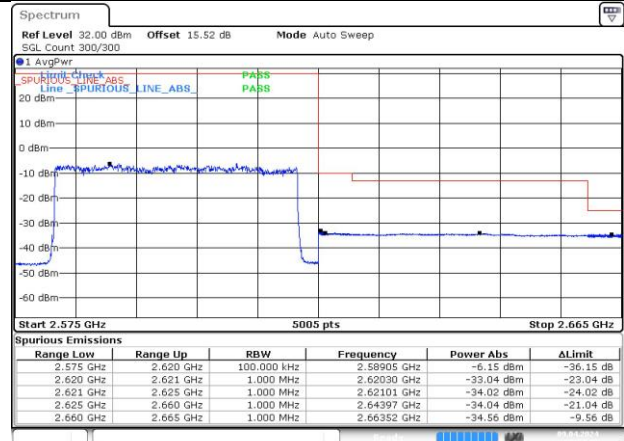
Date: 9 APR 2024 16:20:06

LCH\_DFT-Pi2BPSK / Outer\_Full



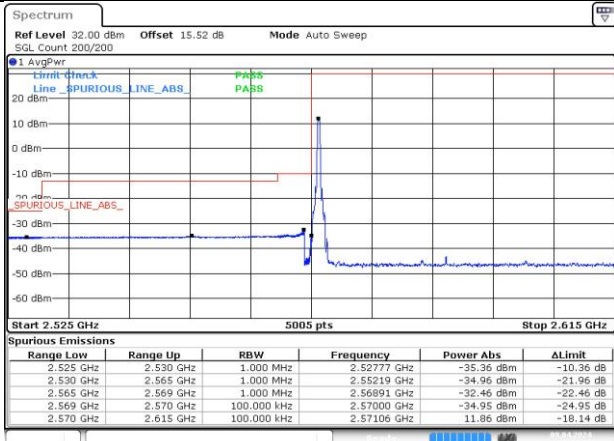
Date: 9 APR 2024 16:18:46

HCH\_DFT-Pi2BPSK / Outer\_Full



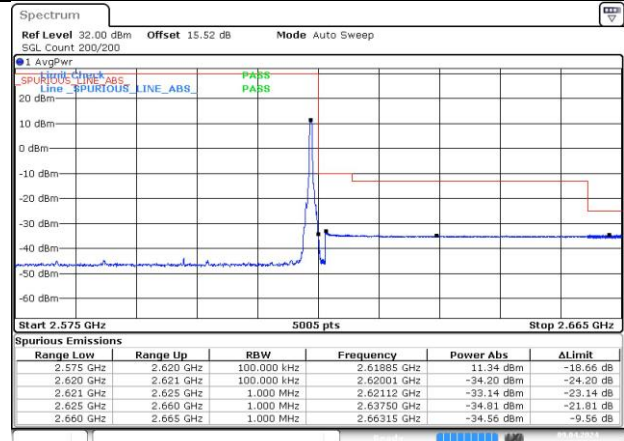
Date: 9 APR 2024 16:20:30

LCH\_DFT-QPSK / Edge\_1RB\_Left

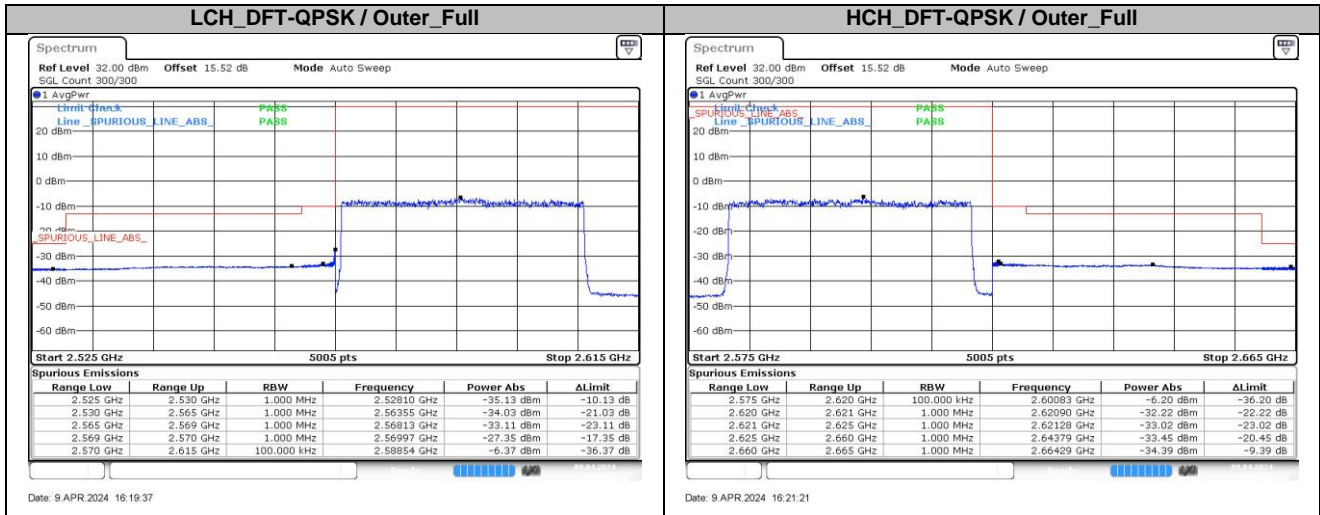


Date: 9 APR 2024 16:19:09

HCH\_DFT-QPSK / Edge\_1RB\_Right



Date: 9 APR 2024 16:20:57



## 6. Conducted Spurious Emission

### 6.1. Test Plots

