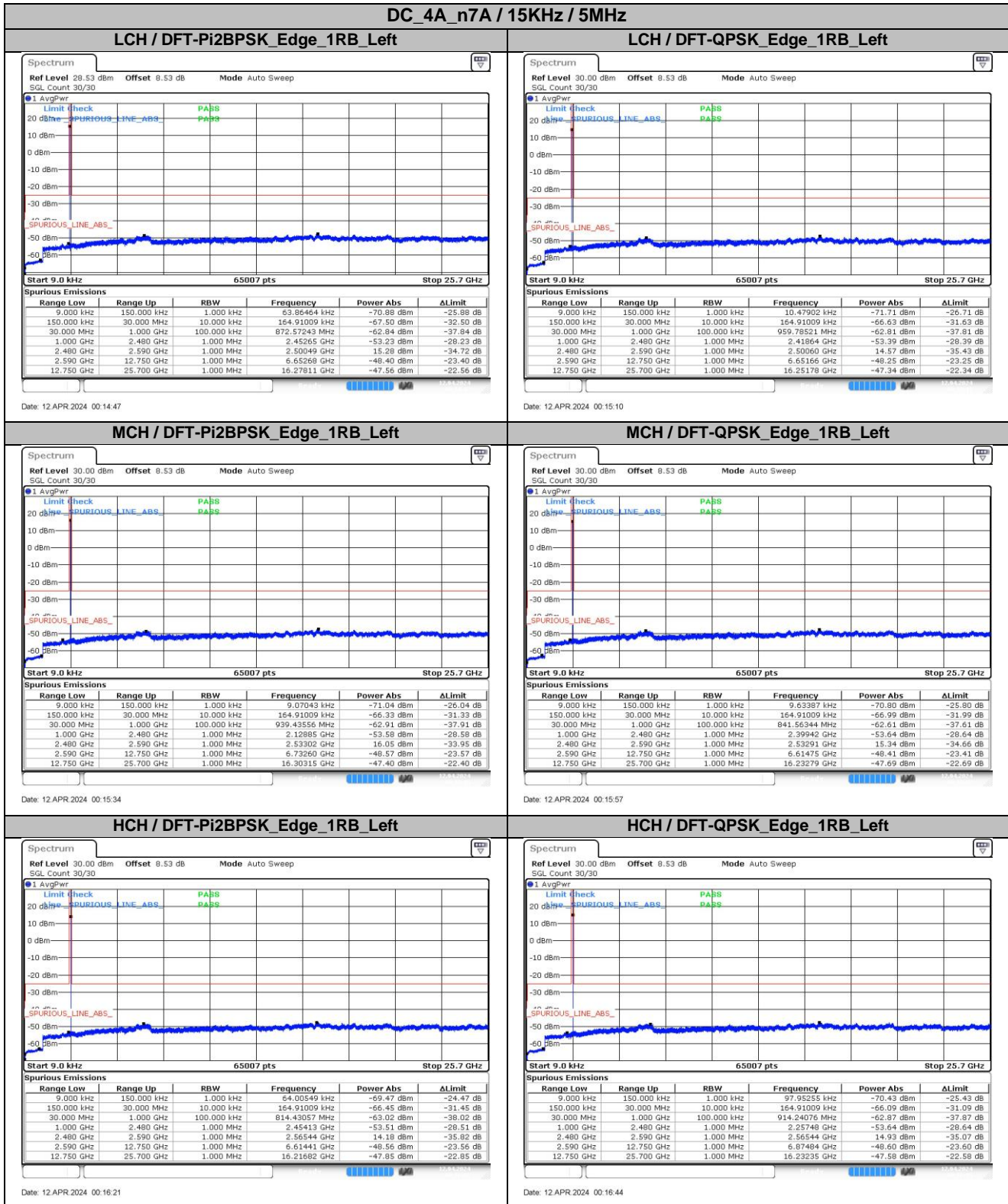


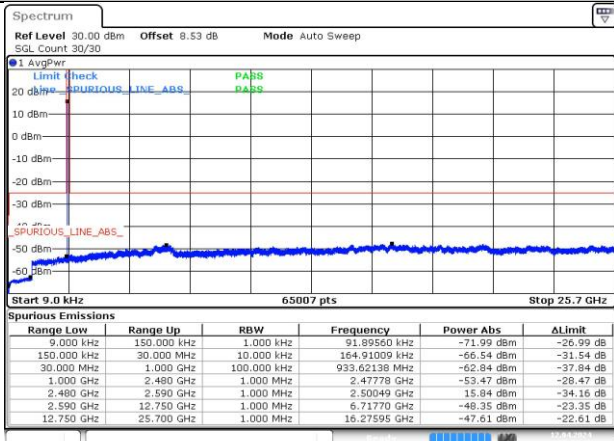
## 6. Conducted Spurious Emission

### 6.1. Test Plots



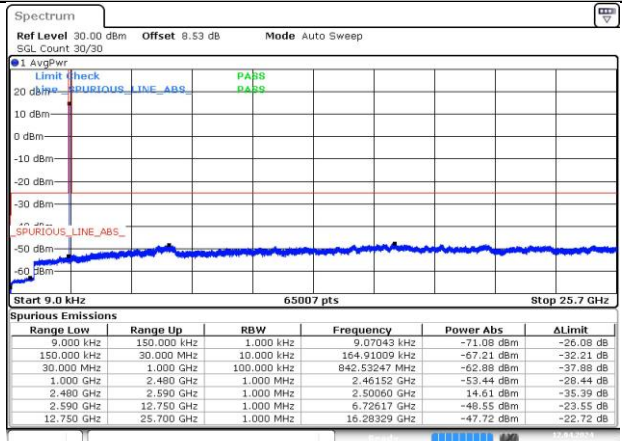
DC\_4A\_n7A / 15KHz / 10MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



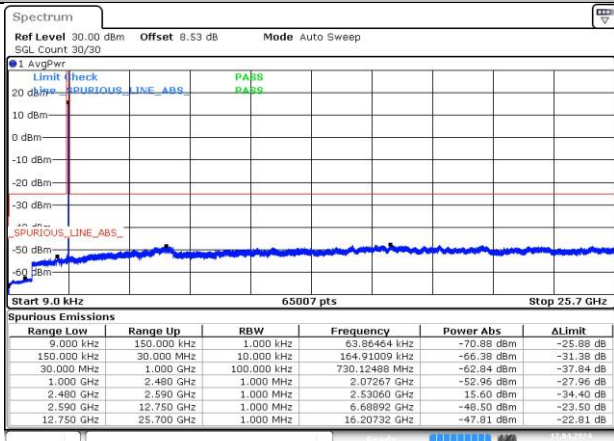
Date: 12 APR 2024 00:18:36

LCH\_DFT-QPSK / Edge\_1RB\_Left



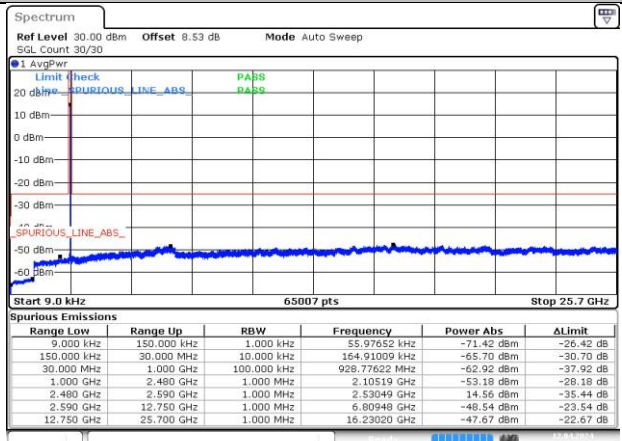
Date: 12 APR 2024 00:18:59

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



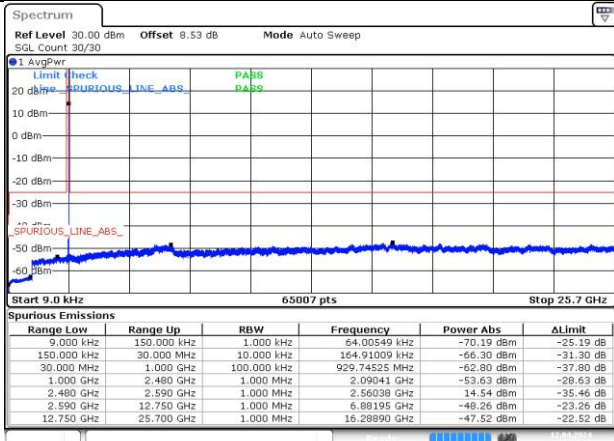
Date: 12 APR 2024 00:19:23

MCH\_DFT-QPSK / Edge\_1RB\_Left



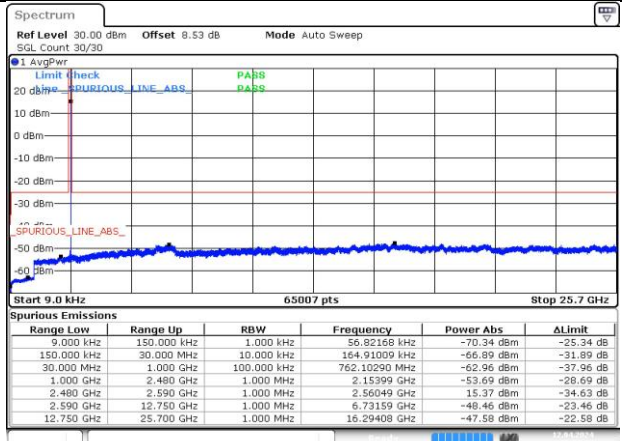
Date: 12 APR 2024 00:19:46

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:20:10

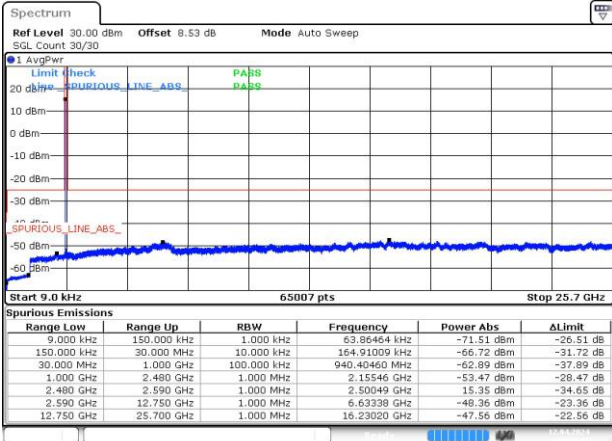
HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:20:33

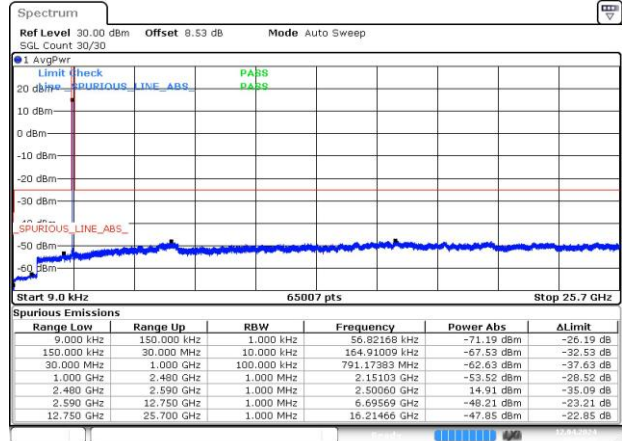
DC\_4A\_n7A / 15KHz / 15MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



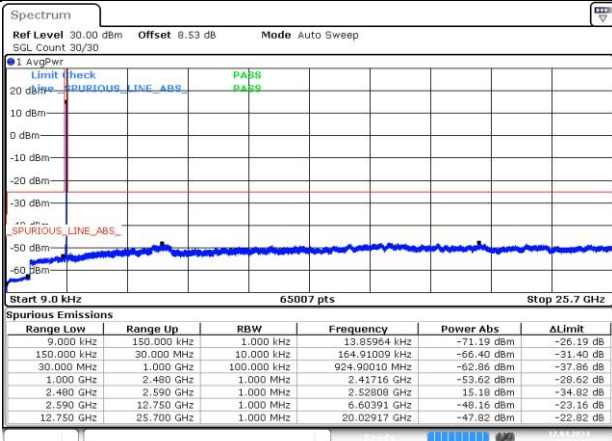
Date: 12 APR 2024 00:22:25

LCH\_DFT-QPSK / Edge\_1RB\_Left



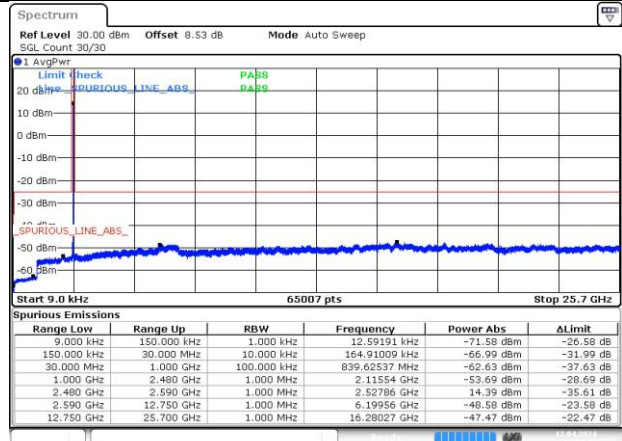
Date: 12 APR 2024 00:22:48

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



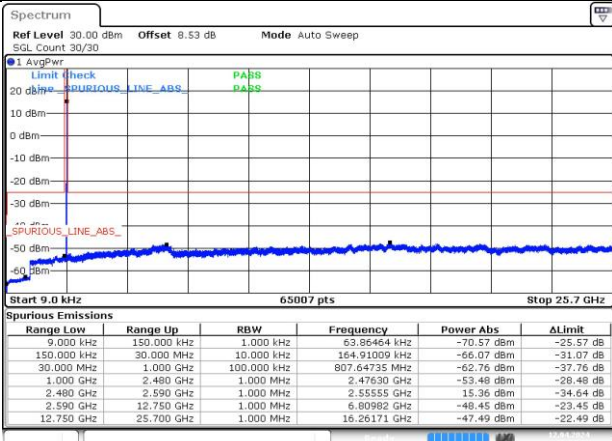
Date: 12 APR 2024 00:23:12

MCH\_DFT-QPSK / Edge\_1RB\_Left



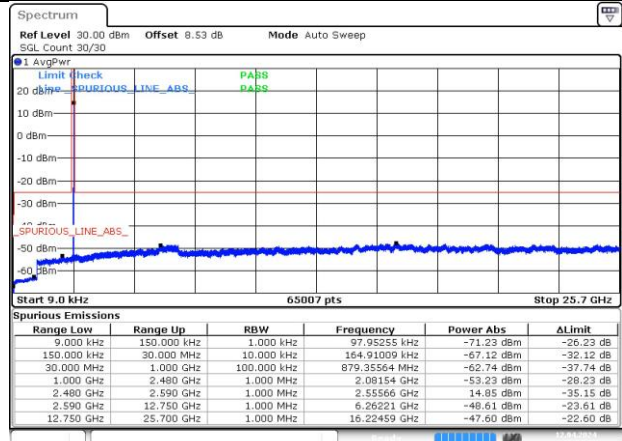
Date: 12 APR 2024 00:23:36

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:24:00

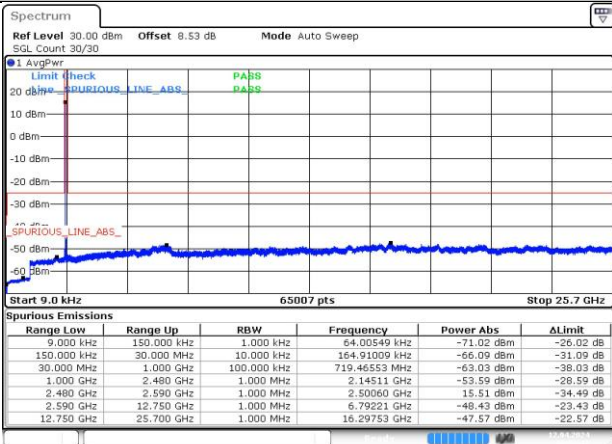
HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:24:23

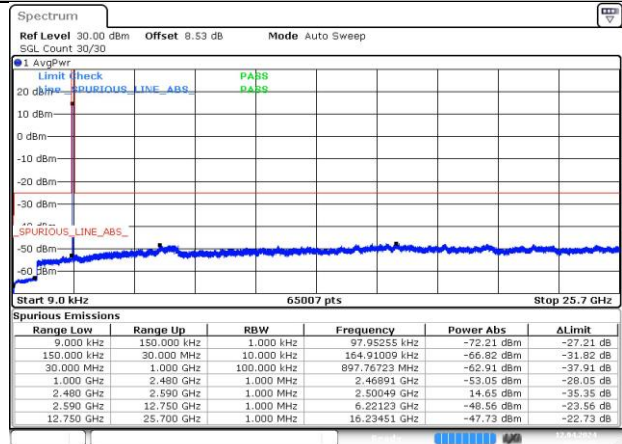
DC\_4A\_n7A / 15KHz / 20MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



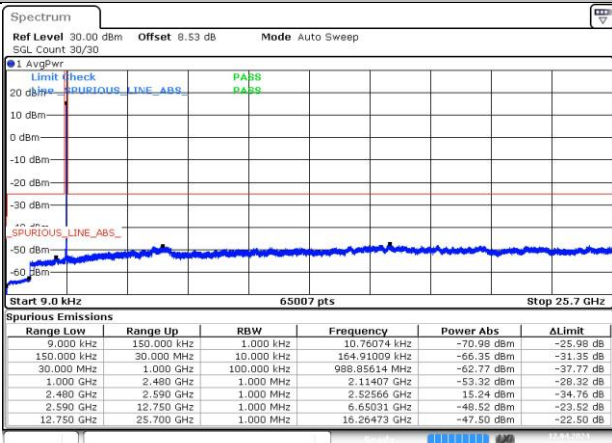
Date: 12 APR 2024 00:26:15

LCH\_DFT-QPSK / Edge\_1RB\_Left



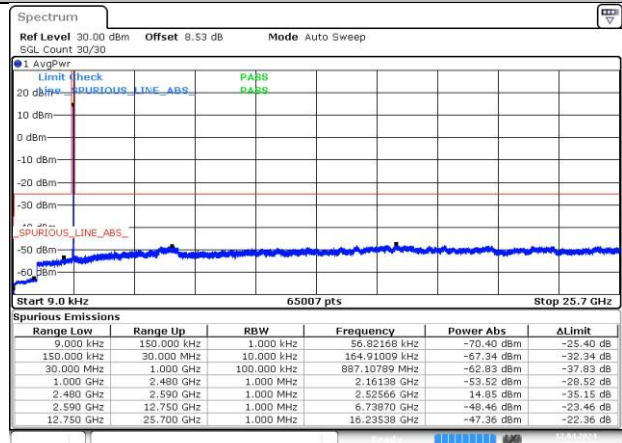
Date: 12 APR 2024 00:26:38

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



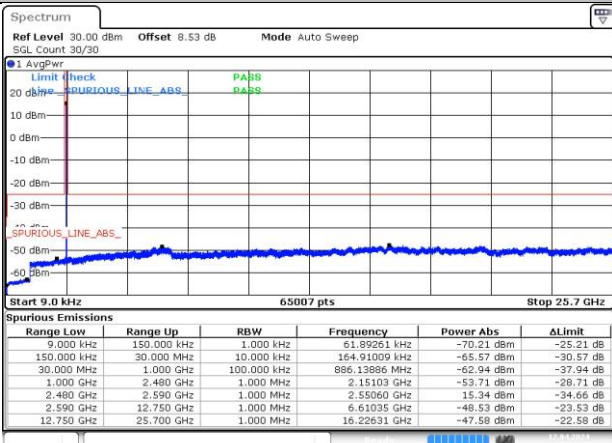
Date: 12 APR 2024 00:27:02

MCH\_DFT-QPSK / Edge\_1RB\_Left



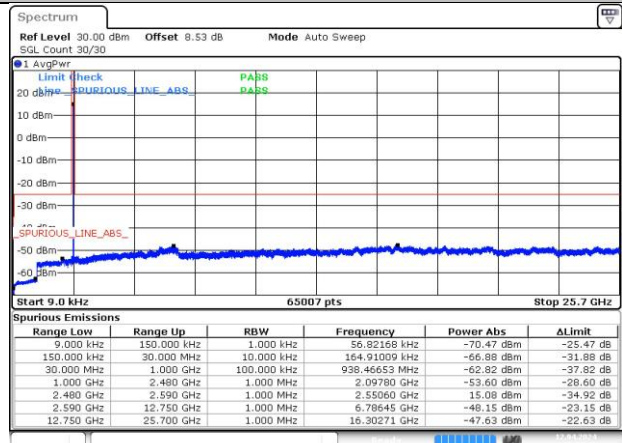
Date: 12 APR 2024 00:27:25

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



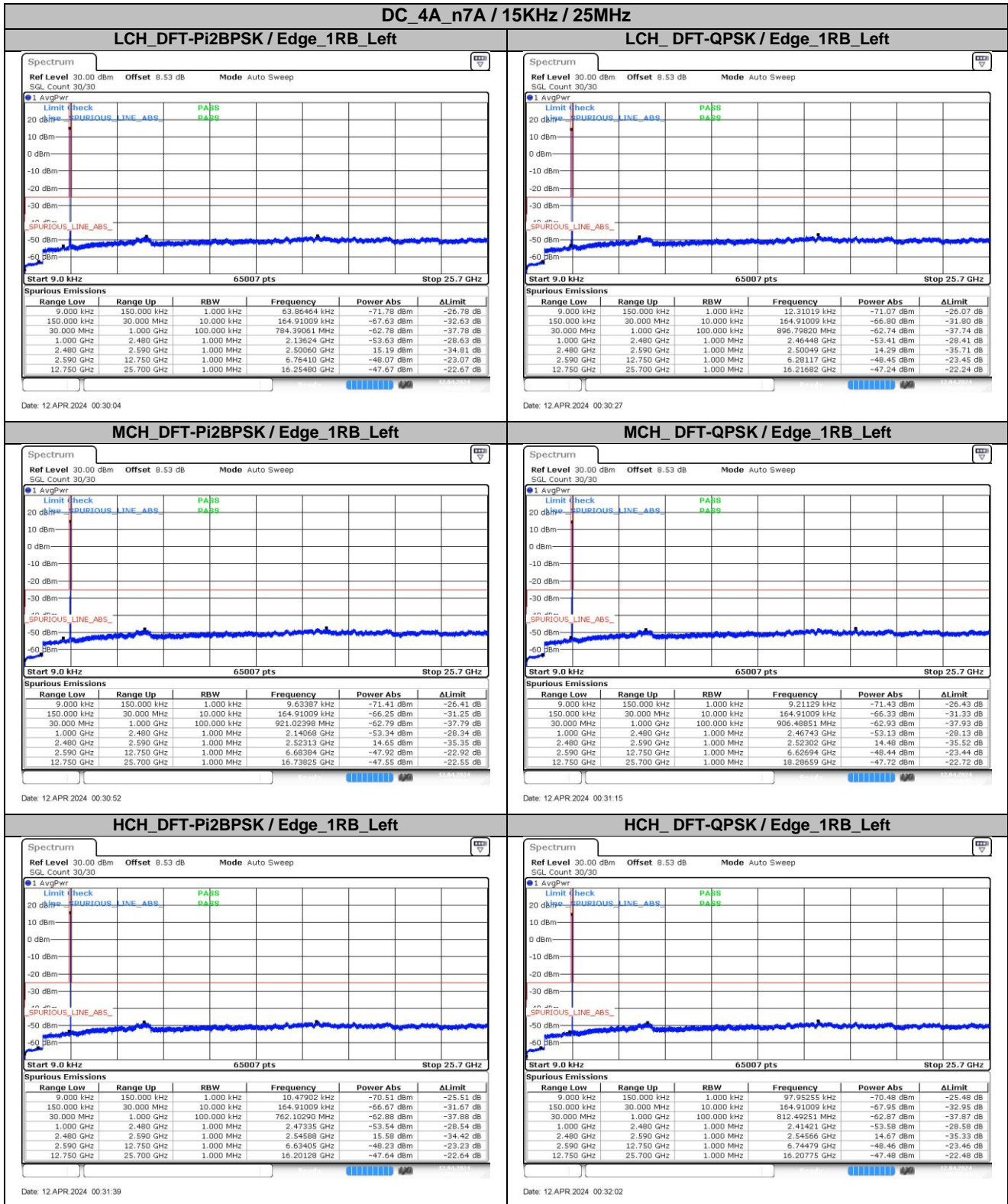
Date: 12 APR 2024 00:27:49

HCH\_DFT-QPSK / Edge\_1RB\_Left



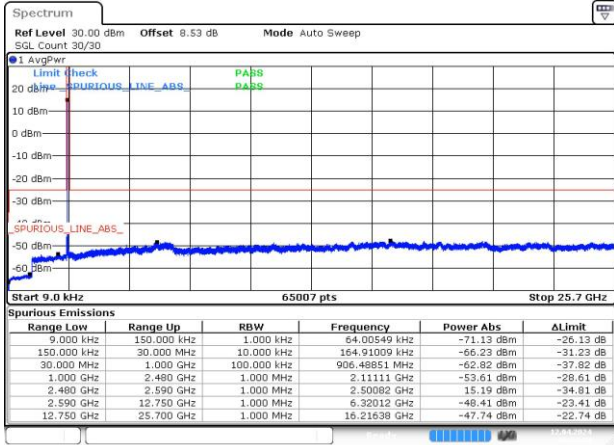
Date: 12 APR 2024 00:28:12





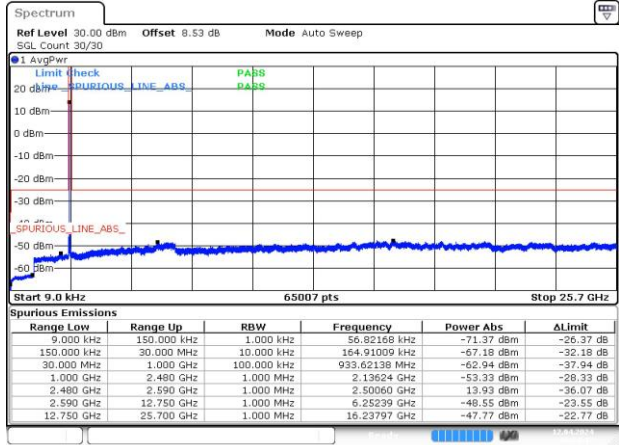
DC\_4A\_n7A / 15KHz / 30MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



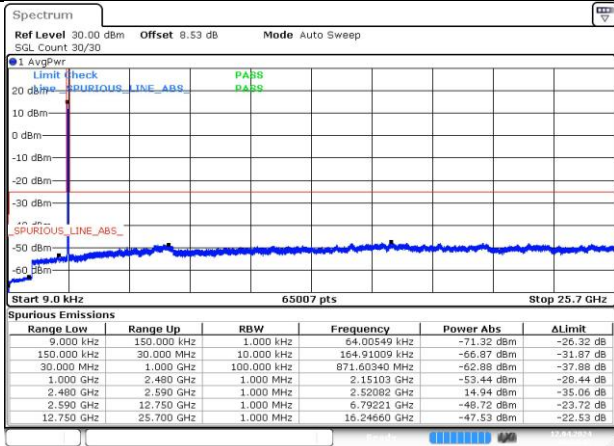
Date: 12 APR 2024 00:33:54

LCH\_DFT-QPSK / Edge\_1RB\_Left



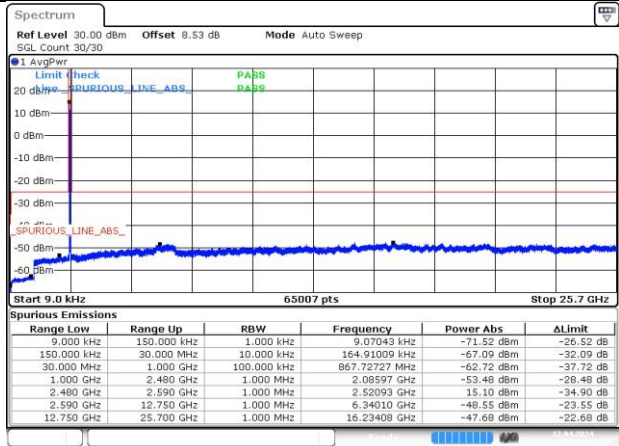
Date: 12 APR 2024 00:34:17

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



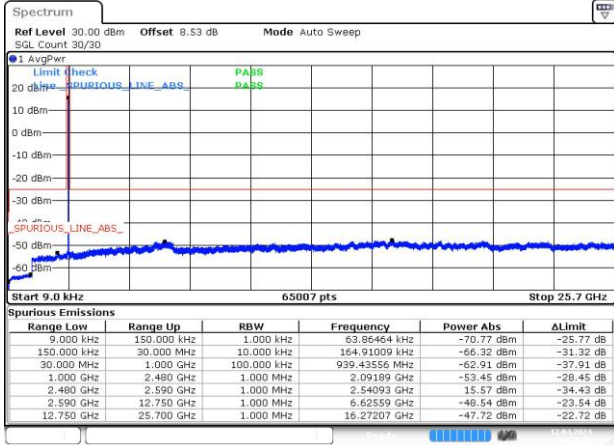
Date: 12 APR 2024 00:34:41

MCH\_DFT-QPSK / Edge\_1RB\_Left



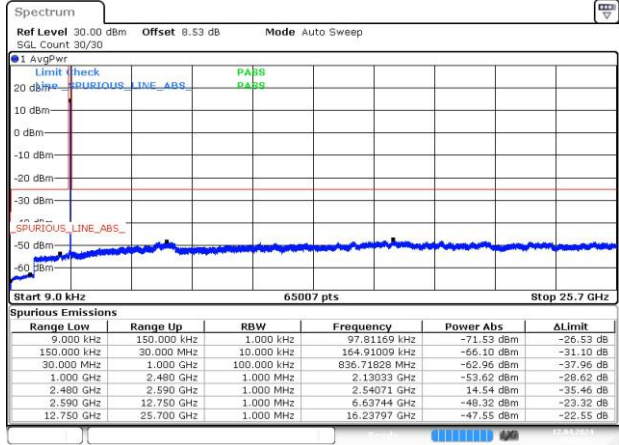
Date: 12 APR 2024 00:35:04

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:35:28

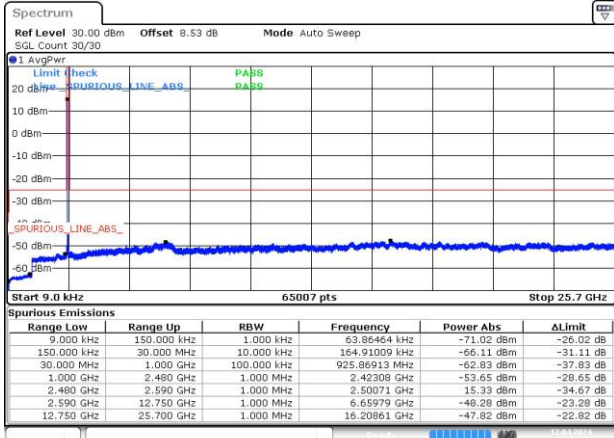
HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:35:51

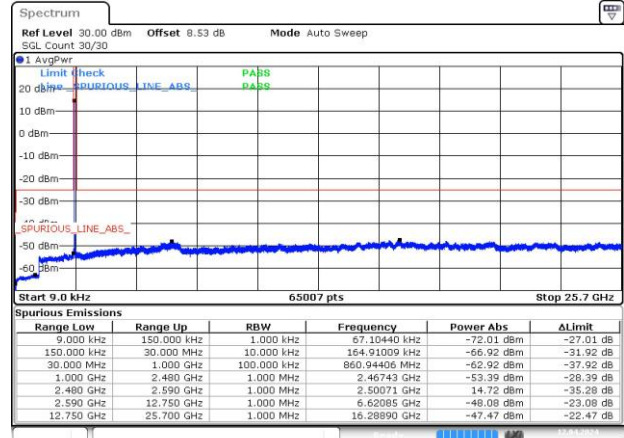
DC\_4A\_n7A / 15KHz / 35MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



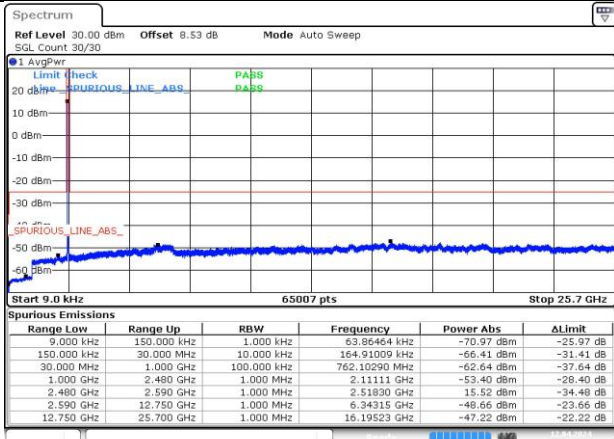
Date: 12 APR 2024 00:37:44

LCH\_DFT-QPSK / Edge\_1RB\_Left



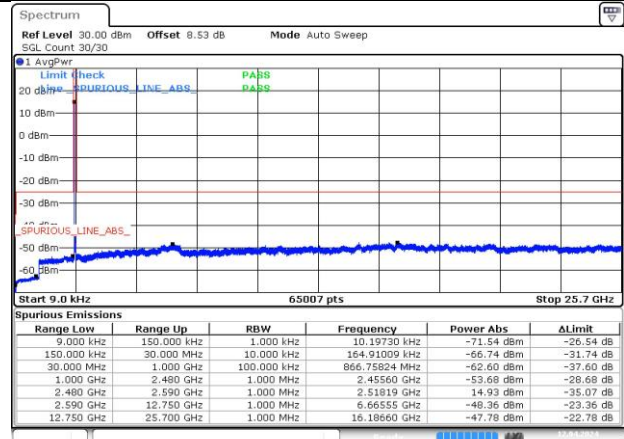
Date: 12 APR 2024 00:38:07

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



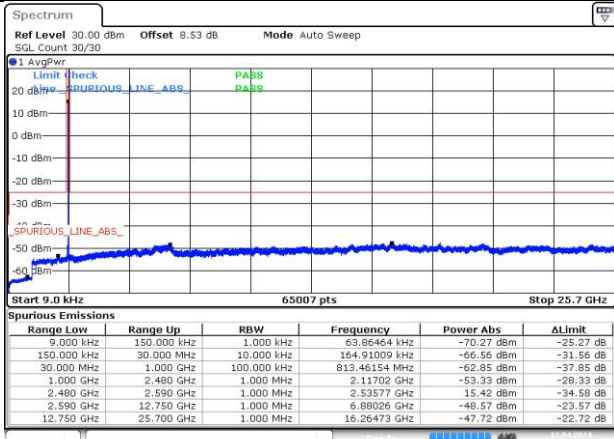
Date: 12 APR 2024 00:38:32

MCH\_DFT-QPSK / Edge\_1RB\_Left



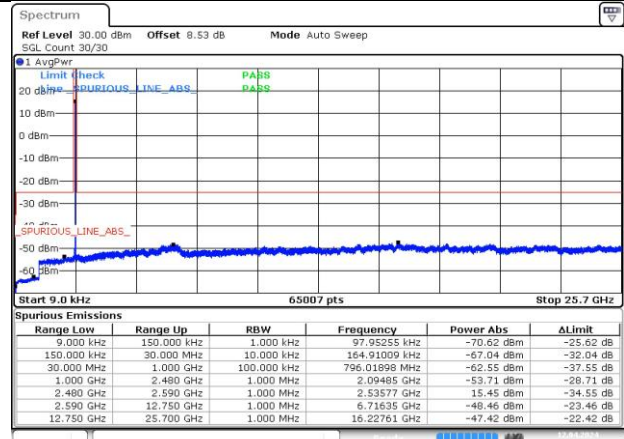
Date: 12 APR 2024 00:38:55

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:38:20

HCH\_DFT-QPSK / Edge\_1RB\_Left

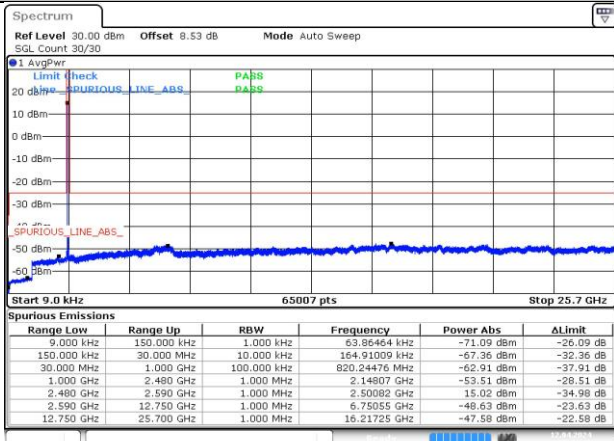


Date: 12 APR 2024 00:39:43



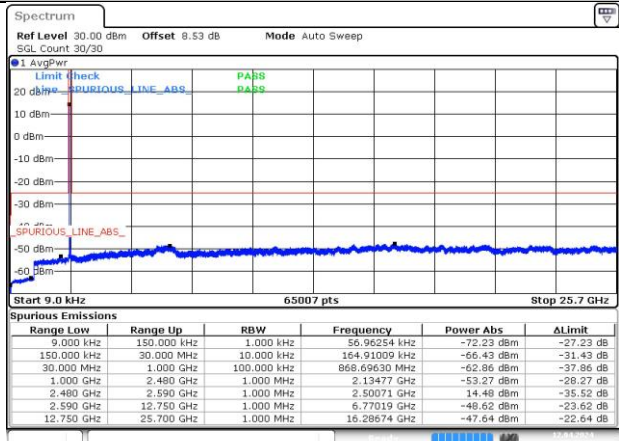
DC\_4A\_n7A / 15KHz / 40MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



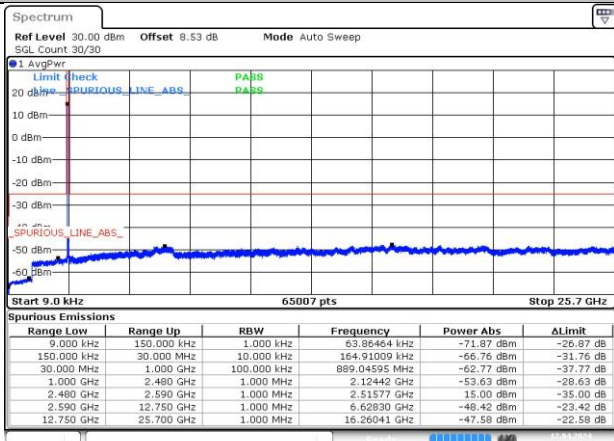
Date: 12 APR 2024 00:41:36

LCH\_DFT-QPSK / Edge\_1RB\_Left



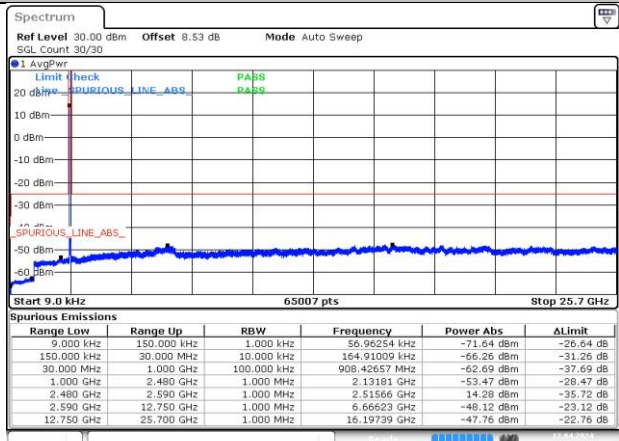
Date: 12 APR 2024 00:41:59

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



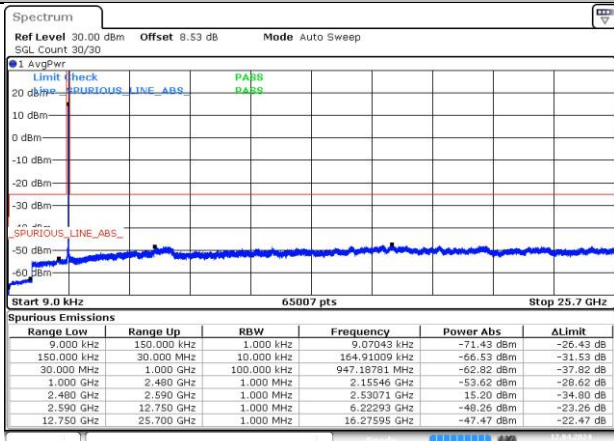
Date: 12 APR 2024 00:42:24

MCH\_DFT-QPSK / Edge\_1RB\_Left



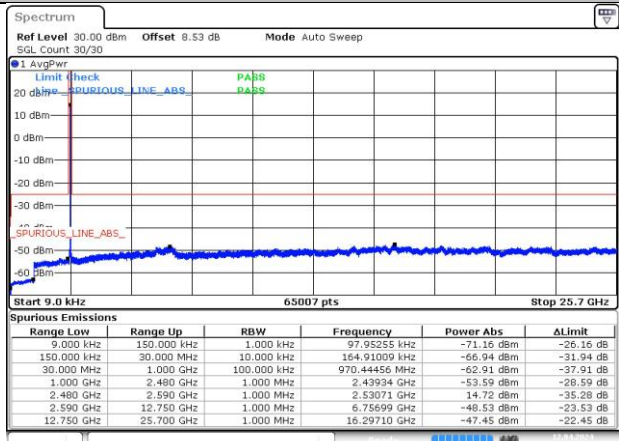
Date: 12 APR 2024 00:42:48

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left

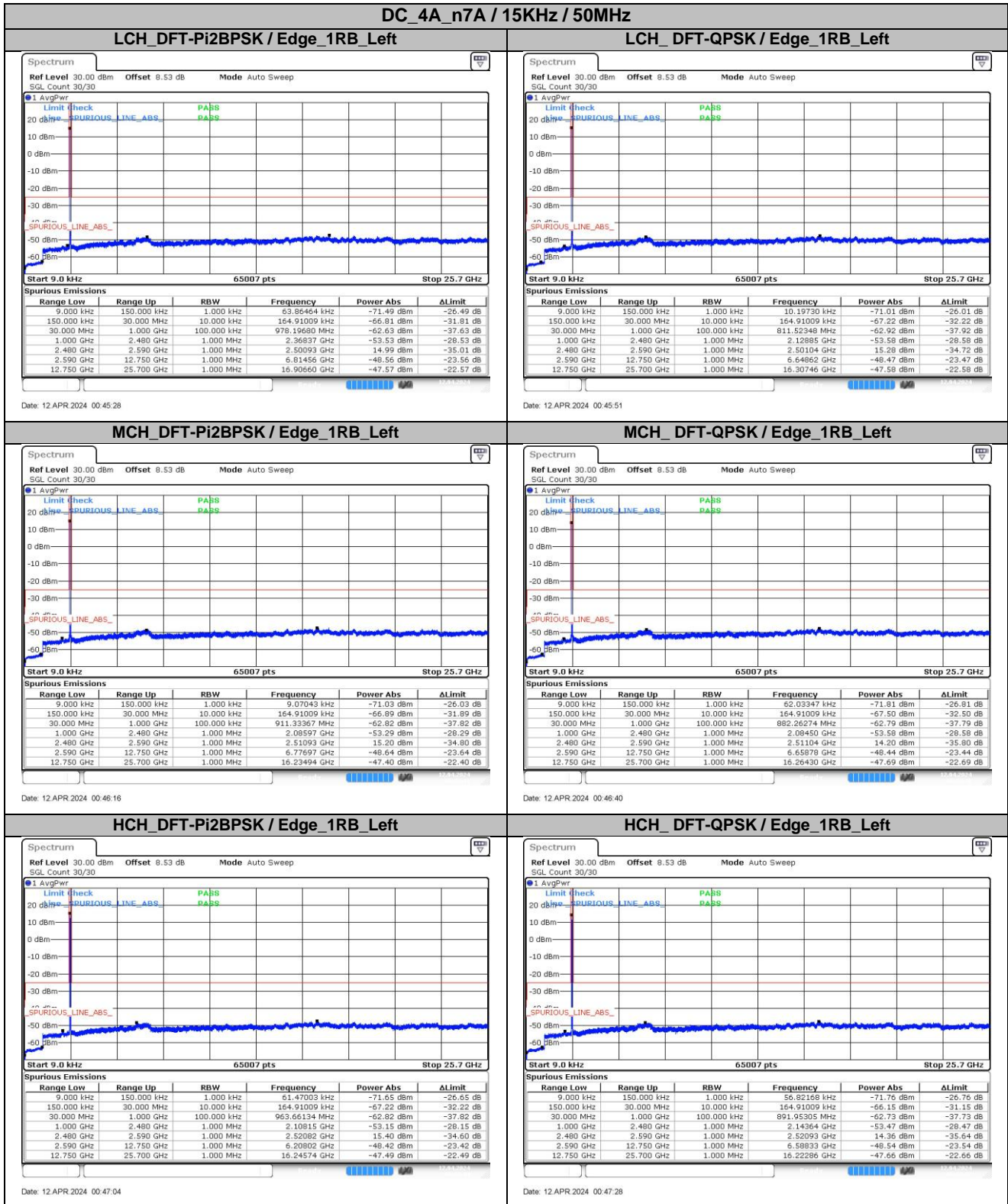


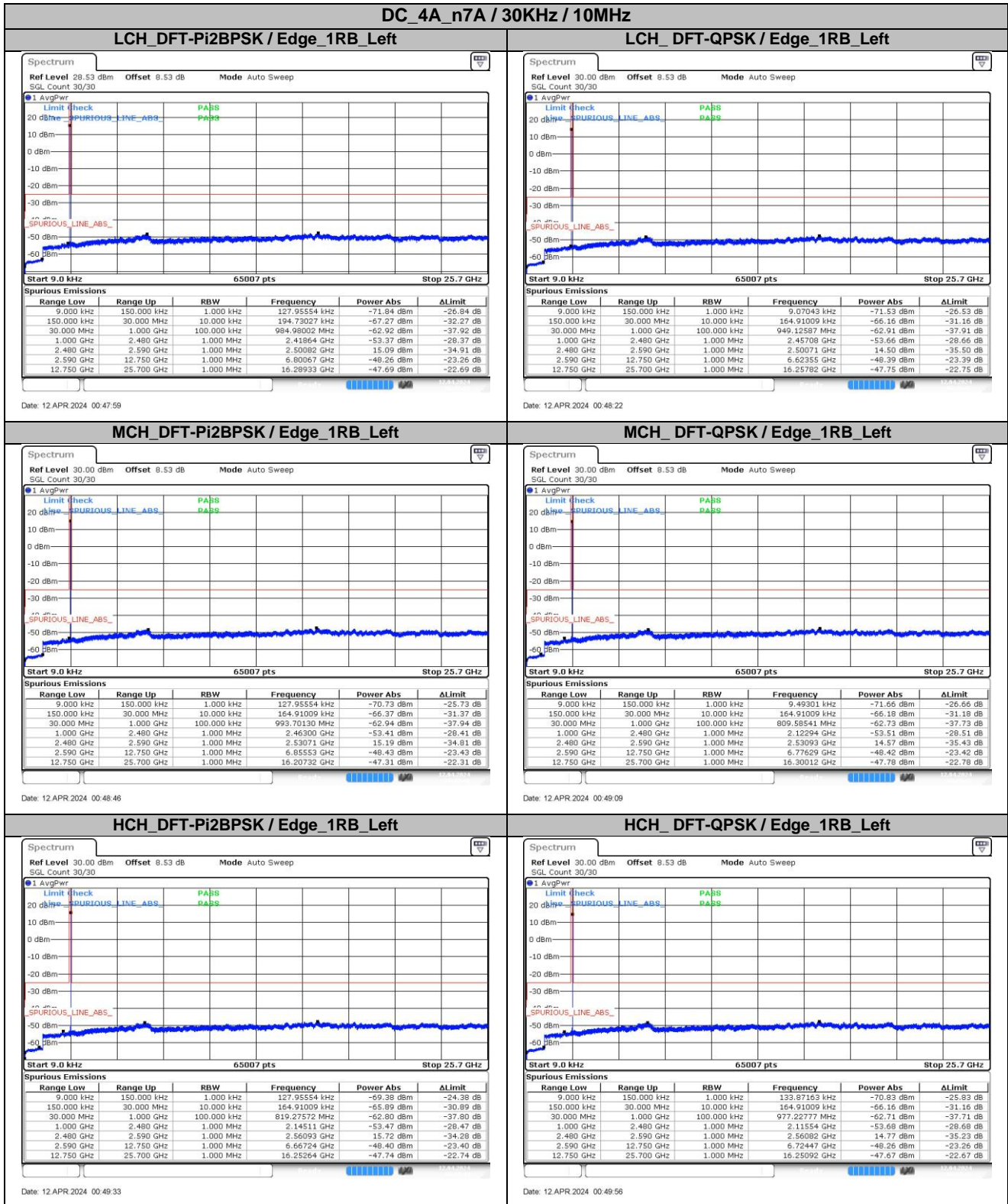
Date: 12 APR 2024 00:43:12

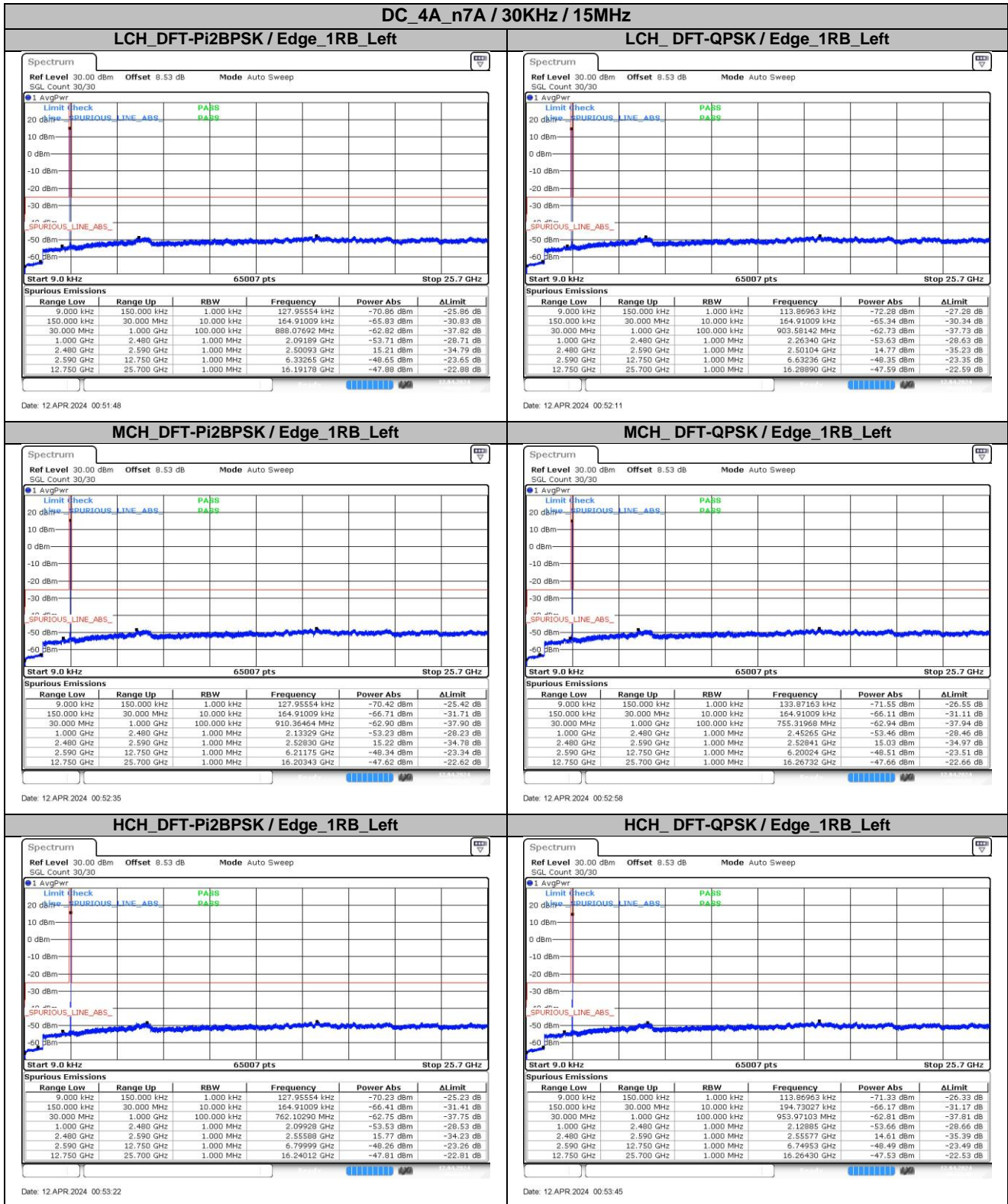
HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:43:35



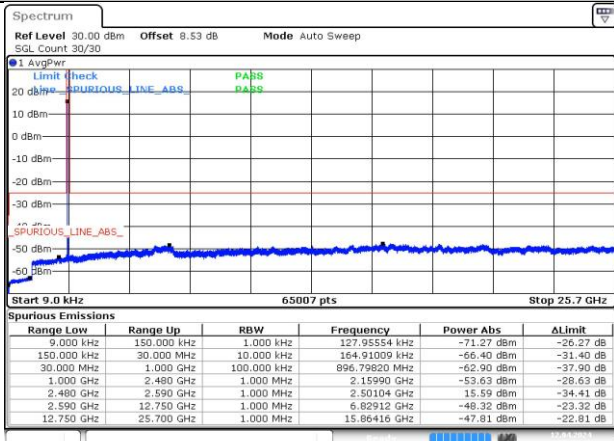






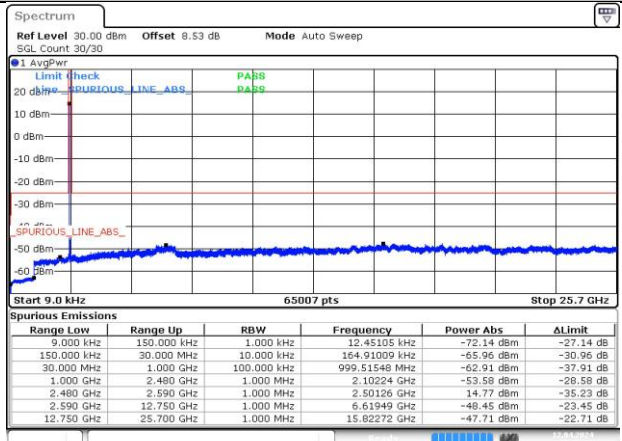
DC\_4A\_n7A / 30KHz / 20MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



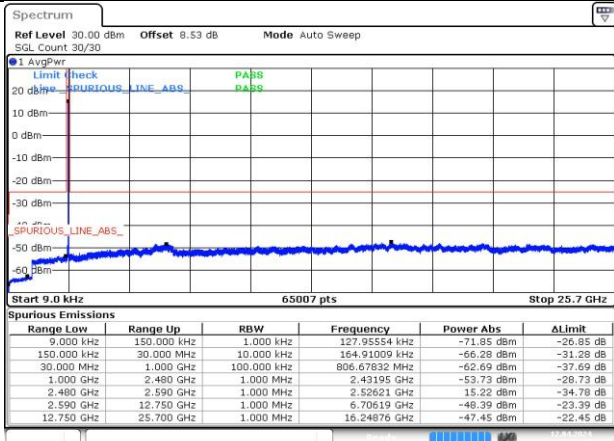
Date: 12 APR 2024 00:55:38

LCH\_DFT-QPSK / Edge\_1RB\_Left



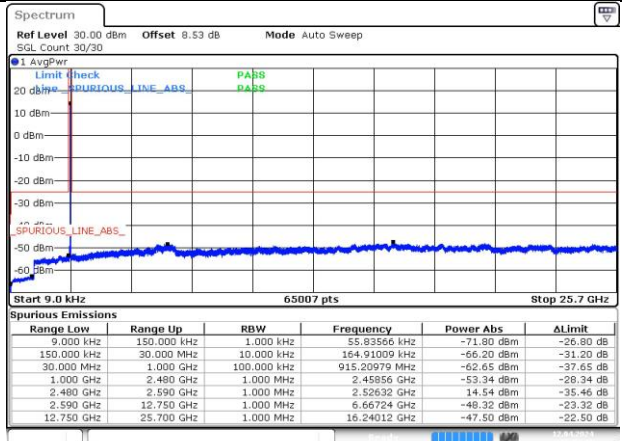
Date: 12 APR 2024 00:56:00

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



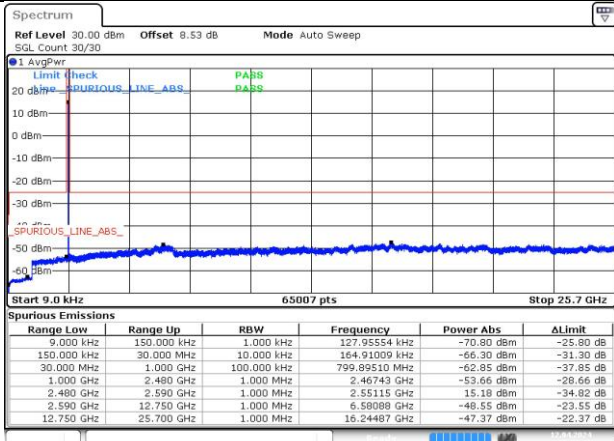
Date: 12 APR 2024 00:56:24

MCH\_DFT-QPSK / Edge\_1RB\_Left



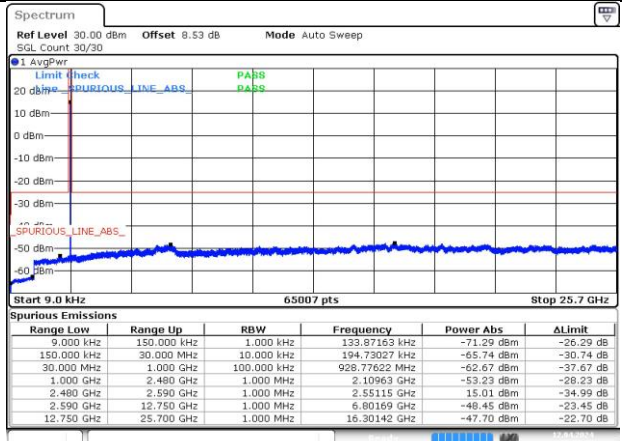
Date: 12 APR 2024 00:56:47

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:57:11

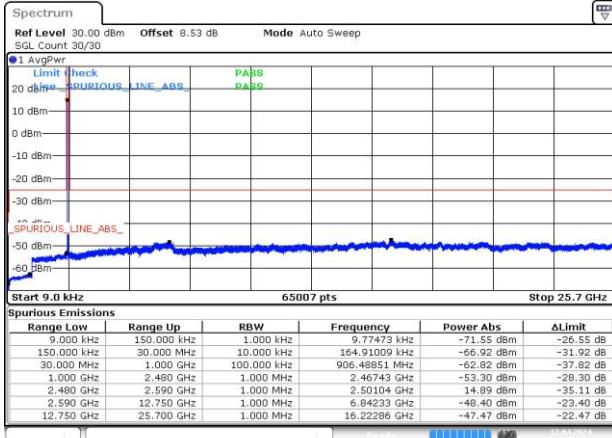
HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 12 APR 2024 00:57:34

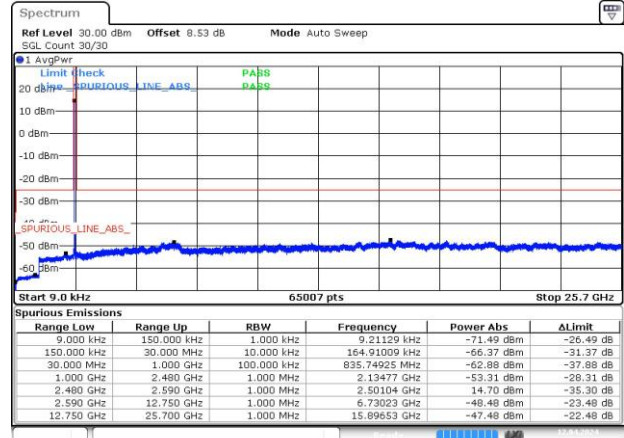
DC\_4A\_n7A / 30KHz / 25MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



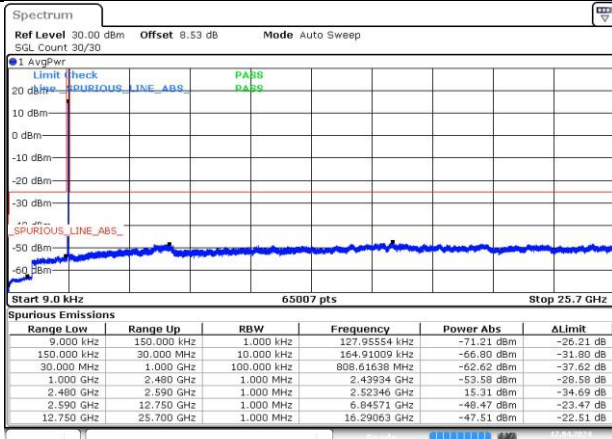
Date: 12 APR 2024 00:59:27

LCH\_DFT-QPSK / Edge\_1RB\_Left



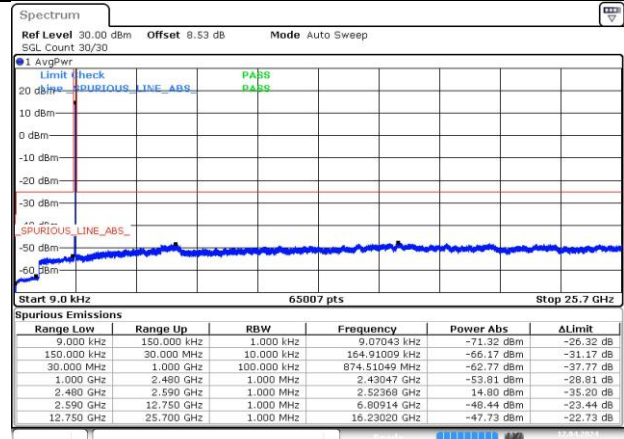
Date: 12 APR 2024 00:59:49

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



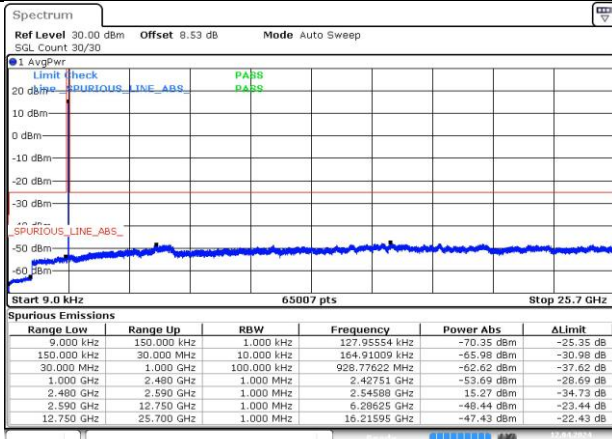
Date: 12 APR 2024 01:00:13

MCH\_DFT-QPSK / Edge\_1RB\_Left



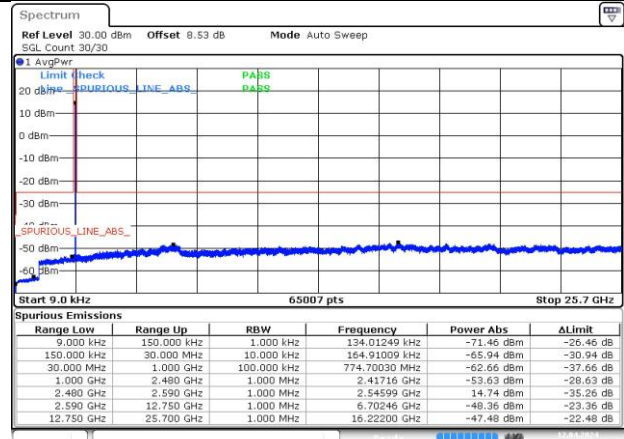
Date: 12 APR 2024 01:00:36

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12 APR 2024 01:01:00

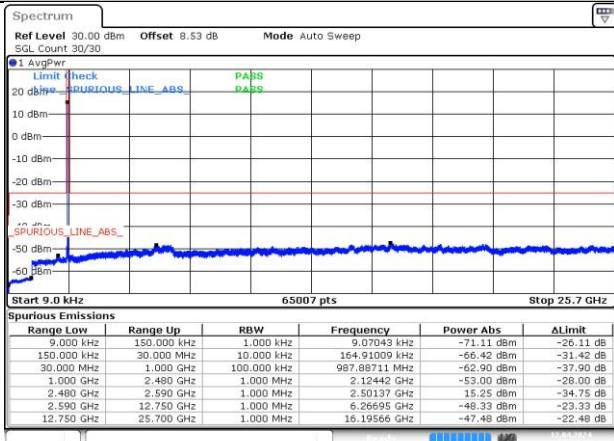
HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 12 APR 2024 01:01:23

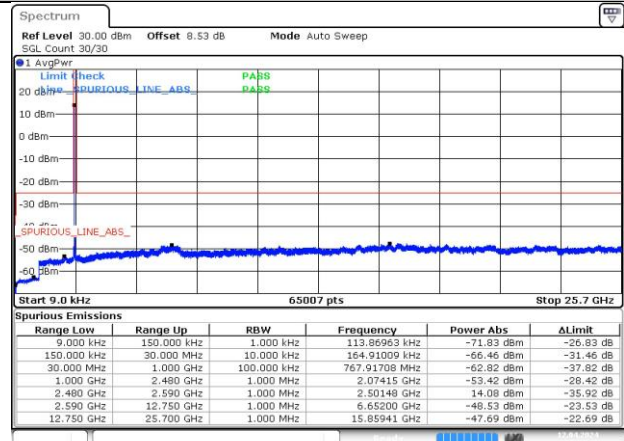
DC\_4A\_n7A / 30KHz / 30MHz

LCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



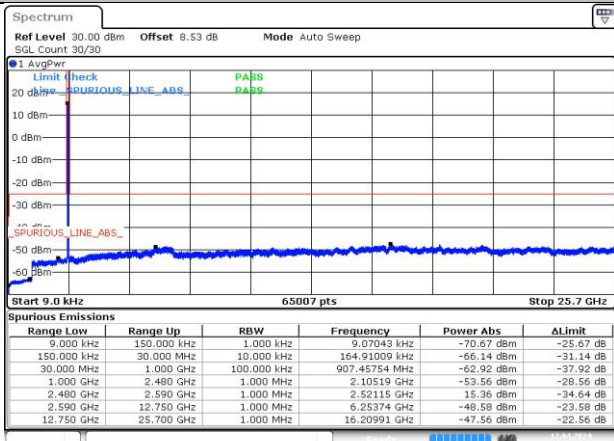
Date: 12 APR 2024 01:03:15

LCH\_DFT-QPSK / Edge\_1RB\_Left



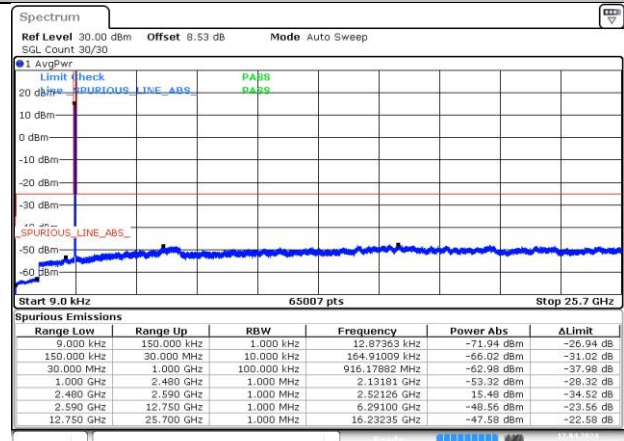
Date: 12 APR 2024 01:03:38

MCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



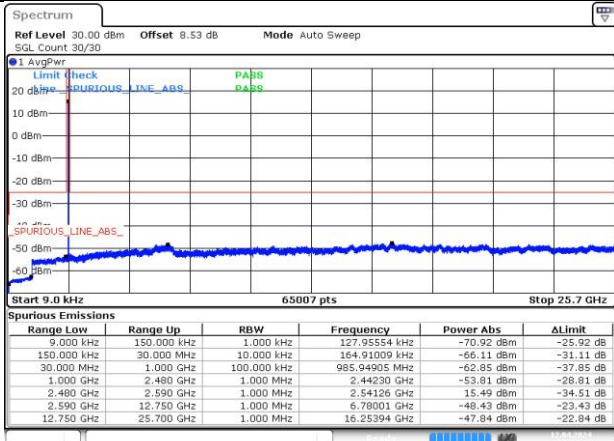
Date: 12 APR 2024 01:04:02

MCH\_DFT-QPSK / Edge\_1RB\_Left



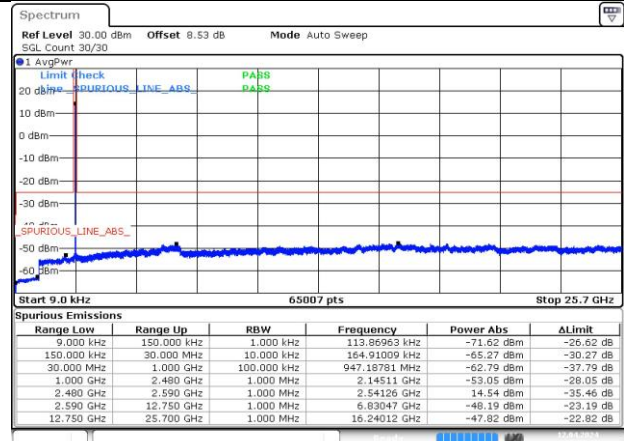
Date: 12 APR 2024 01:04:25

HCH\_DFT-Pi2BPSK / Edge\_1RB\_Left



Date: 12 APR 2024 01:04:49

HCH\_DFT-QPSK / Edge\_1RB\_Left



Date: 12 APR 2024 01:05:12