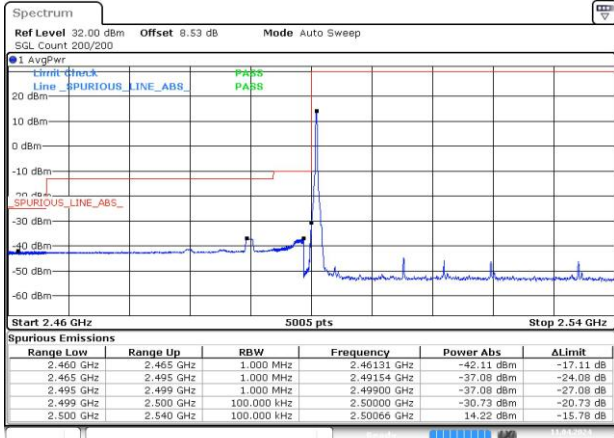


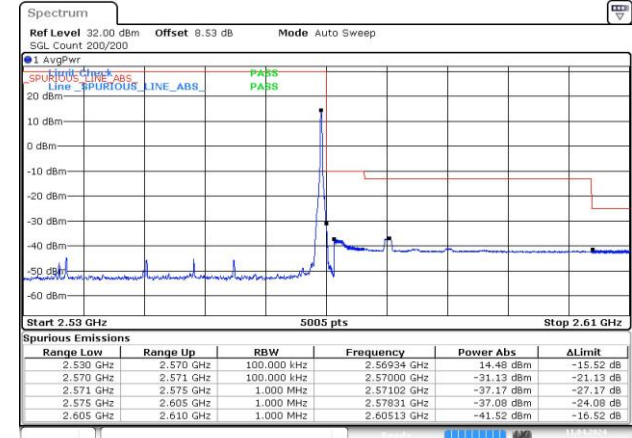
DC_4A_n7A / 15KHz / 35MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



Date: 11 APR 2024 23:26:47

HCH_DFT-Pi2BPSK / Edge_1RB_Right



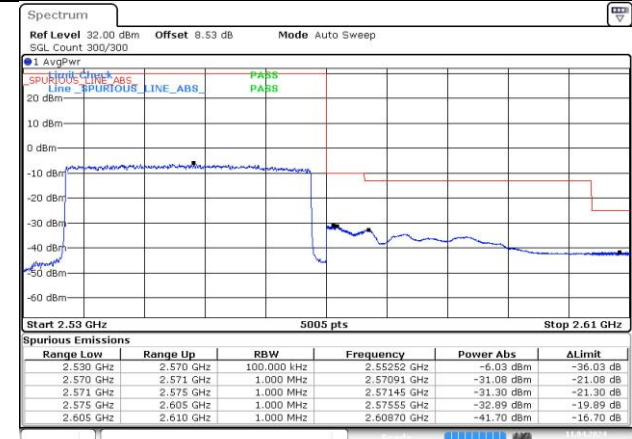
Date: 11 APR 2024 23:26:24

LCH_DFT-Pi2BPSK / Outer_Full



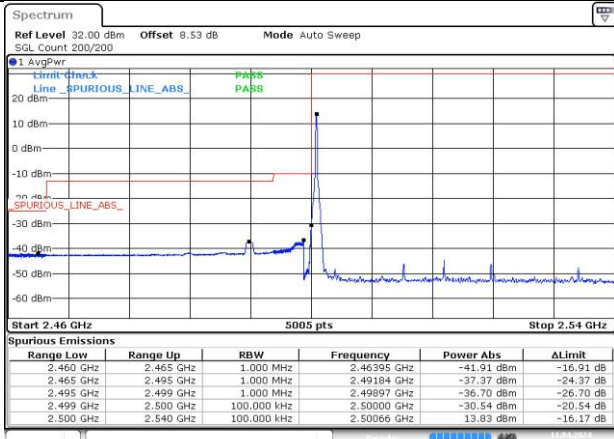
Date: 11 APR 2024 23:27:13

HCH_DFT-Pi2BPSK / Outer_Full



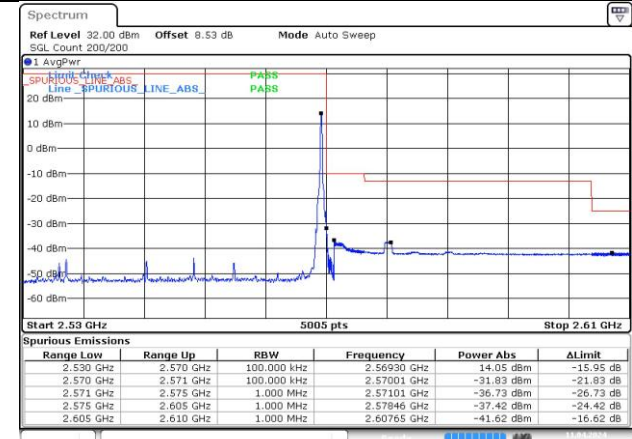
Date: 11 APR 2024 23:28:44

LCH_DFT-QPSK / Edge_1RB_Left

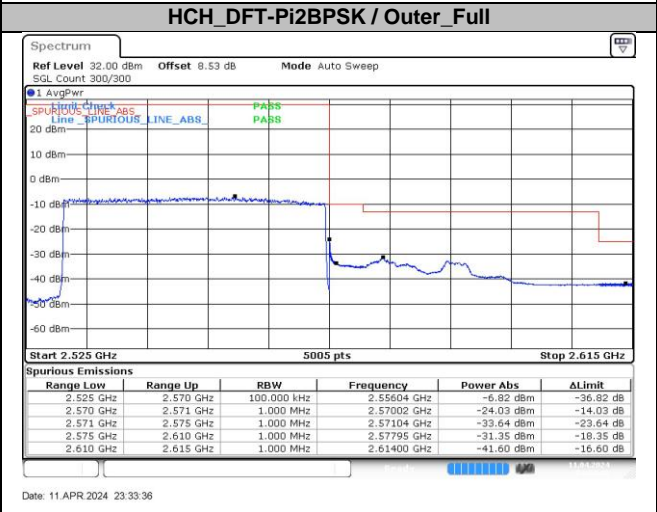
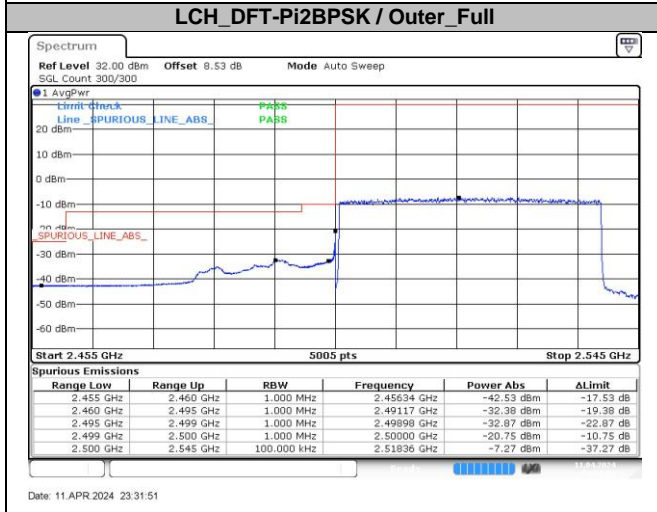
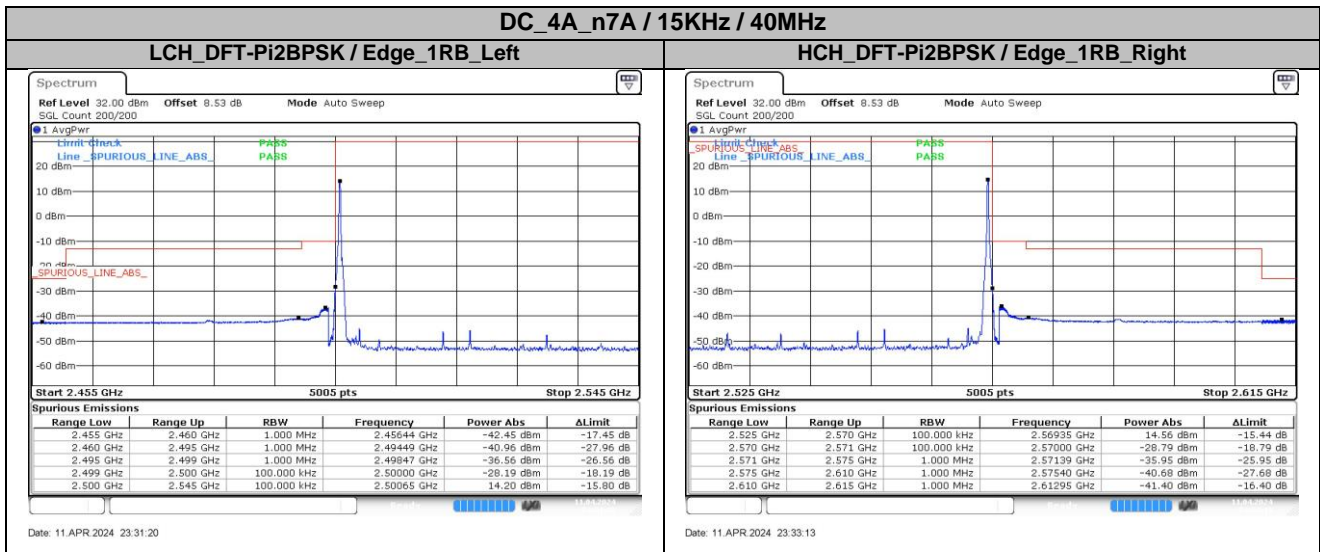
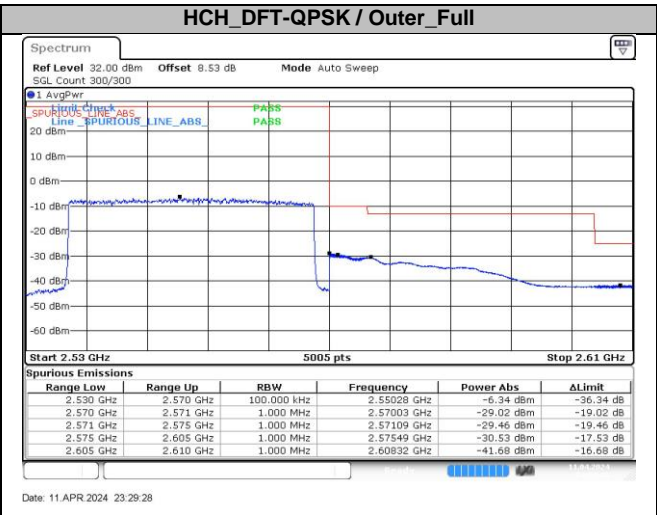
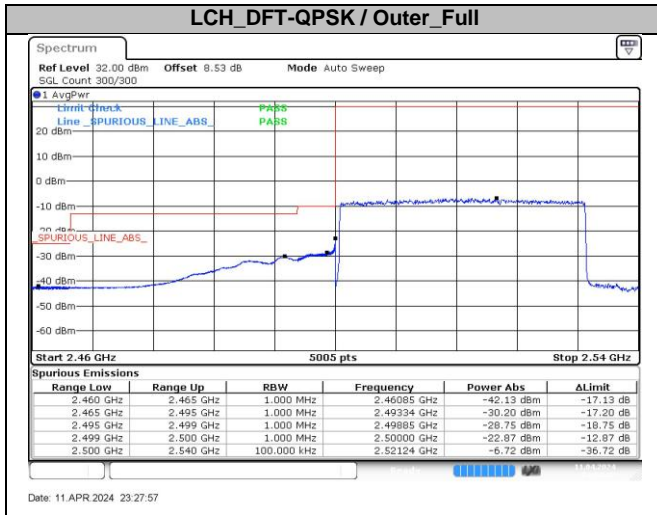


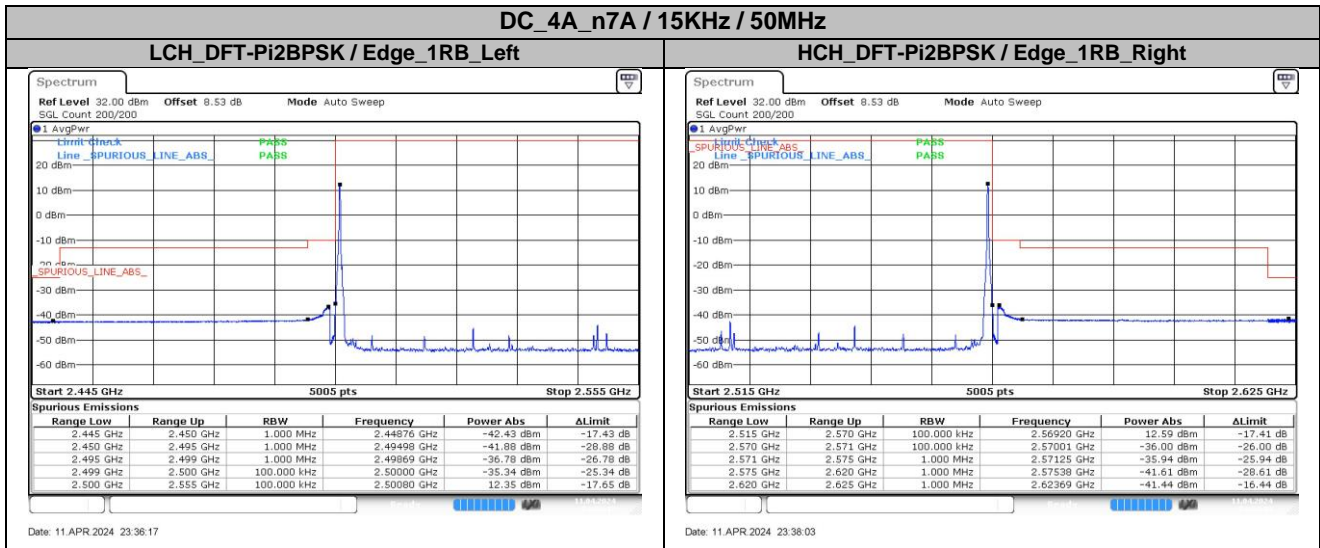
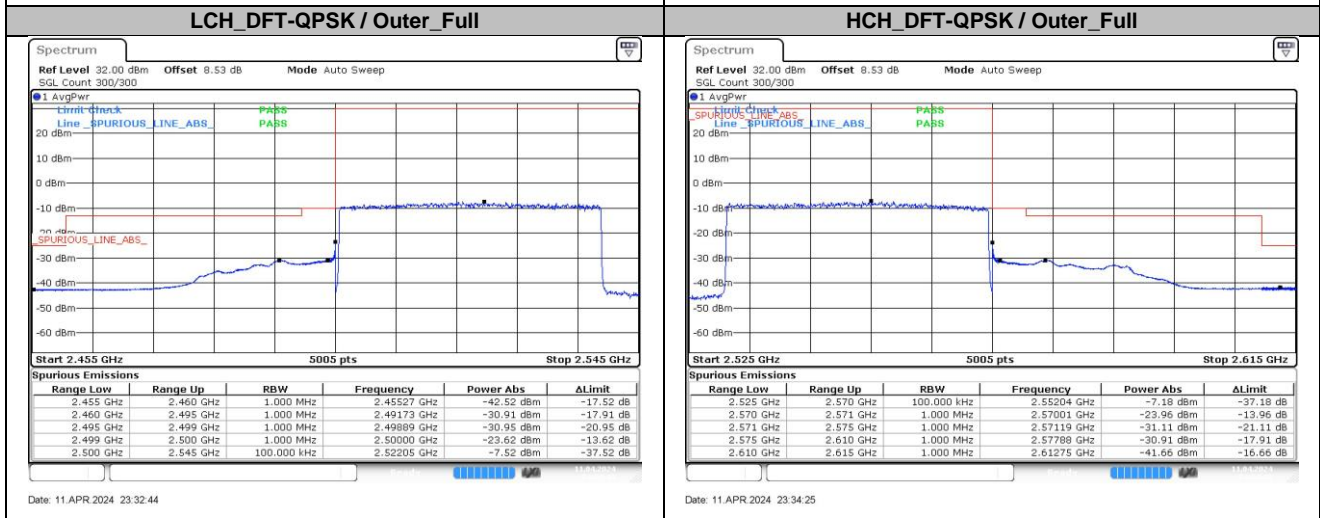
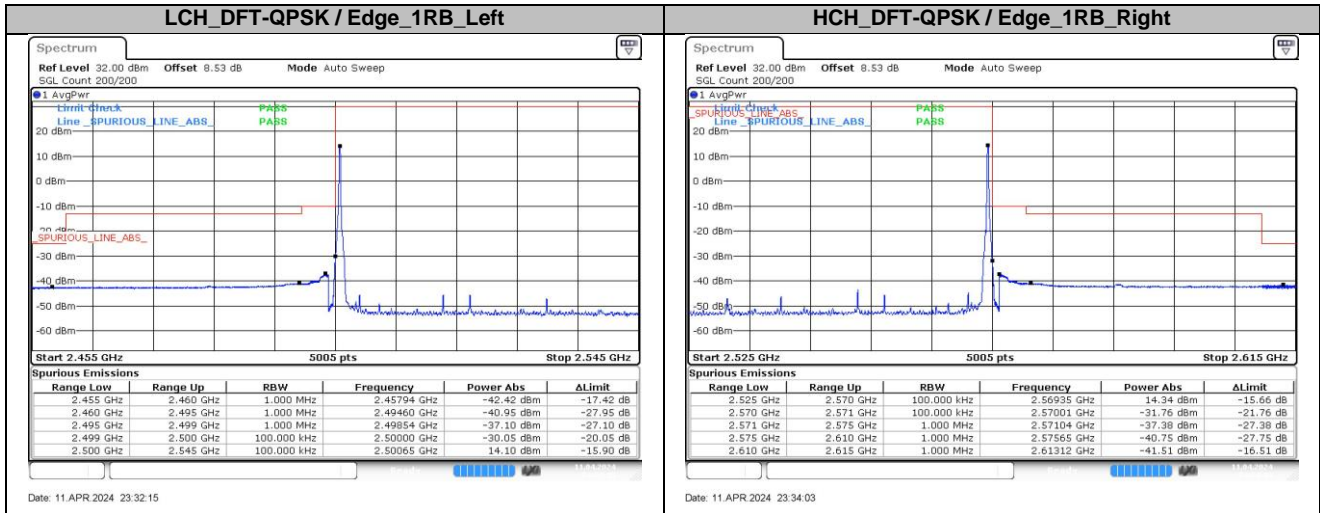
Date: 11 APR 2024 23:27:33

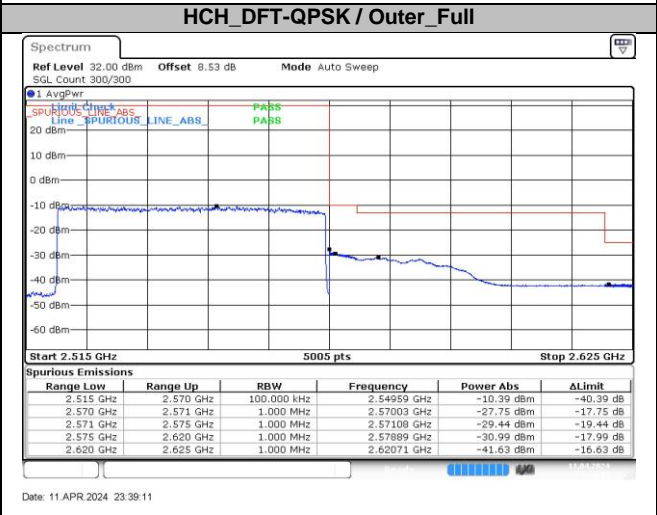
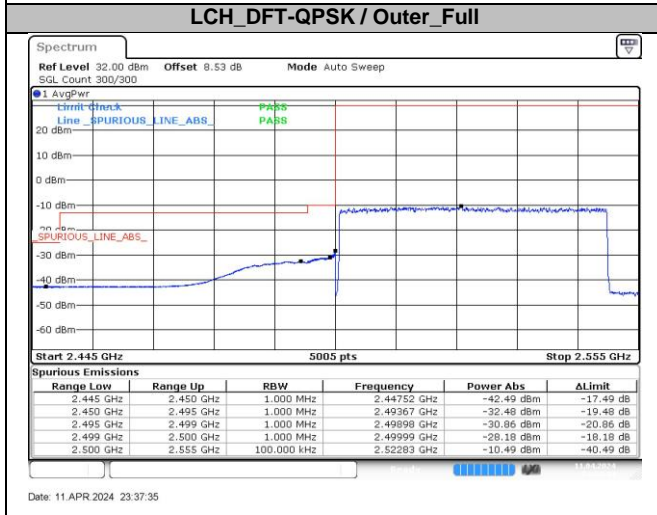
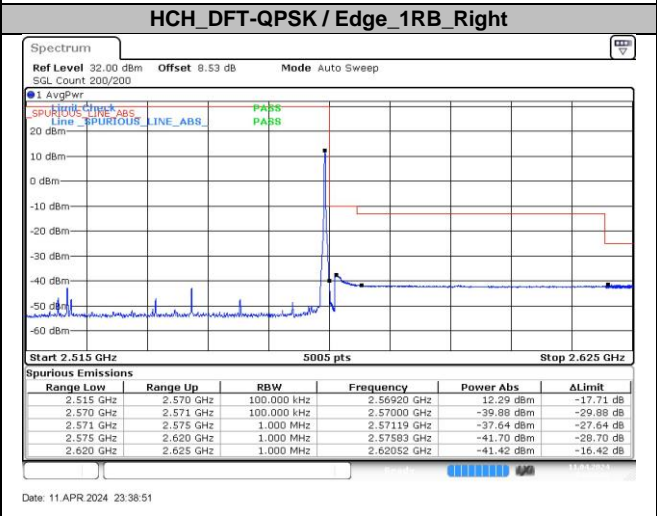
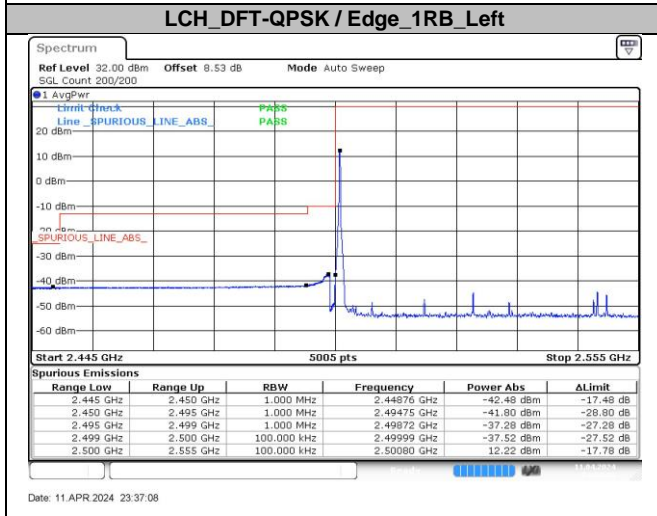
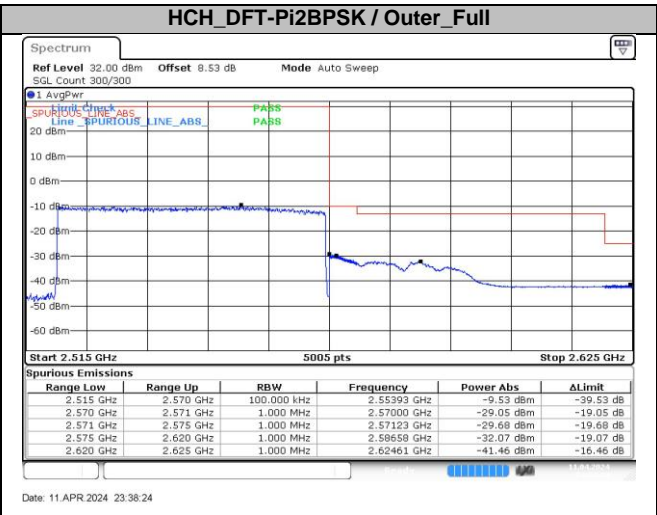
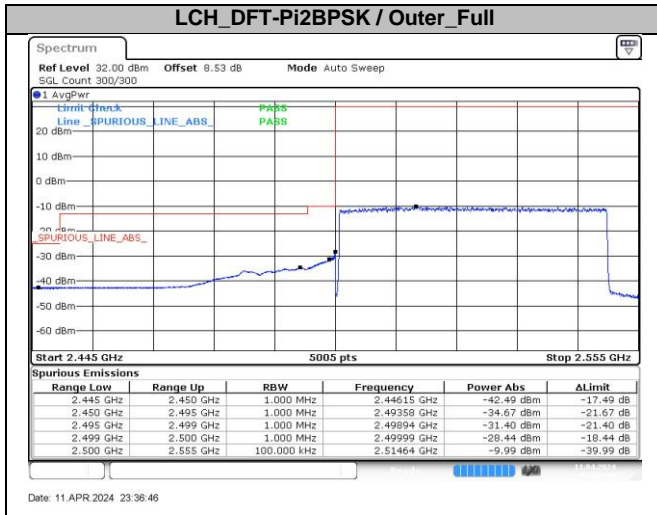
HCH_DFT-QPSK / Edge_1RB_Right



Date: 11 APR 2024 23:29:09

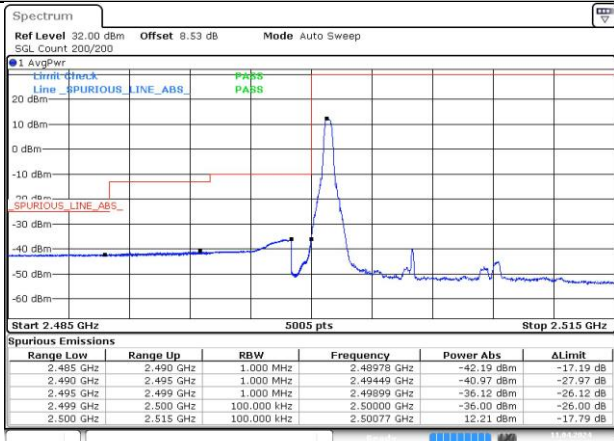






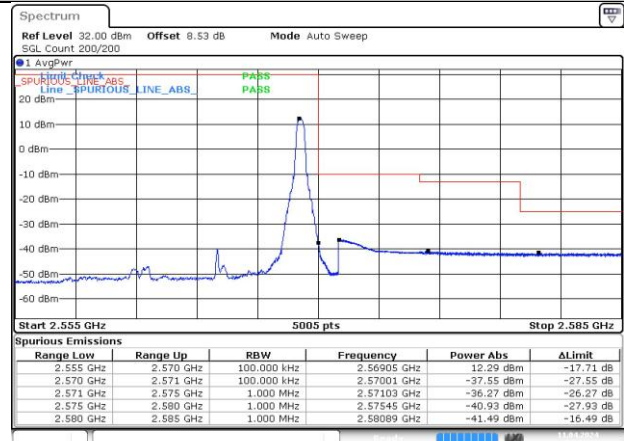
DC_4A_n7A / 30KHz / 10MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



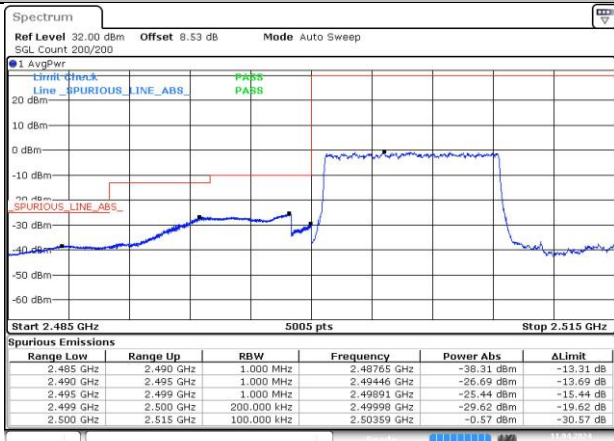
Date: 11 APR 2024 23:39:37

HCH_DFT-Pi2BPSK / Edge_1RB_Right



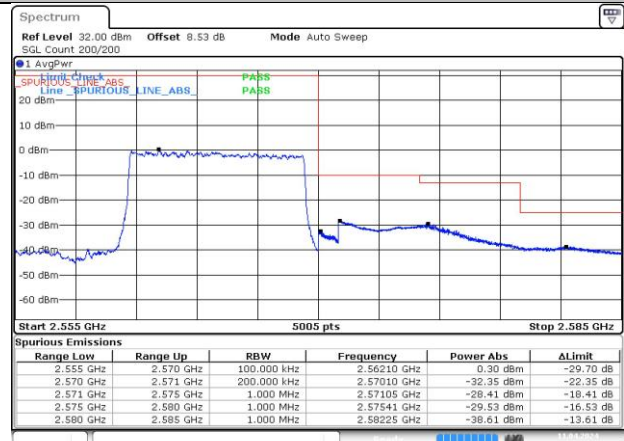
Date: 11 APR 2024 23:41:08

LCH_DFT-Pi2BPSK / Outer_Full



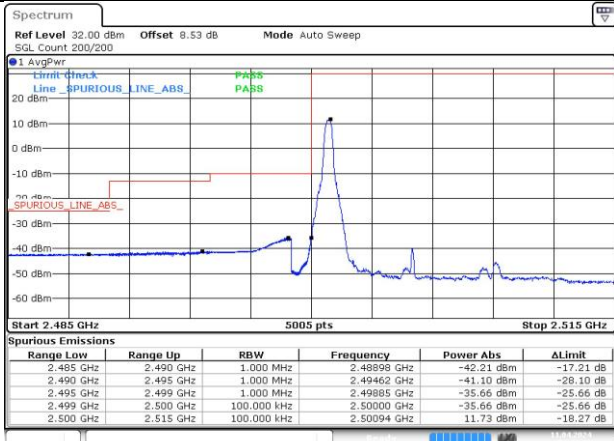
Date: 11 APR 2024 23:40:00

HCH_DFT-Pi2BPSK / Outer_Full



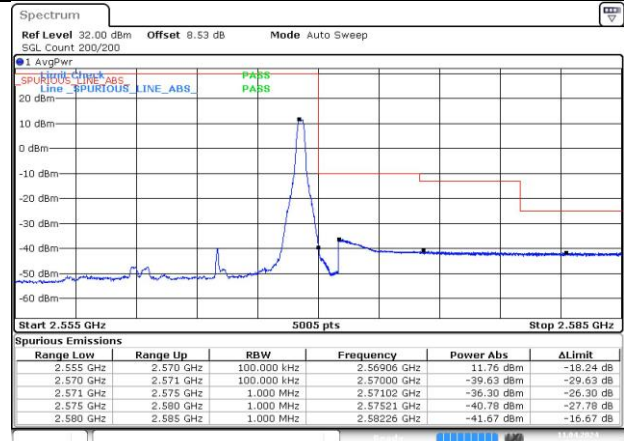
Date: 11 APR 2024 23:41:25

LCH_DFT-QPSK / Edge_1RB_Left

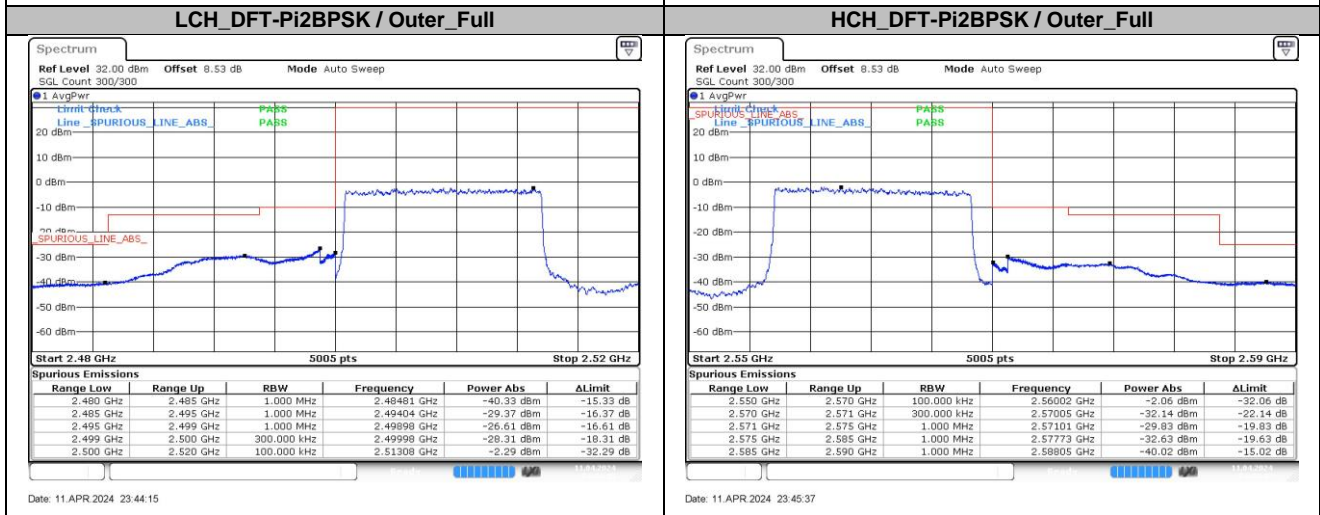
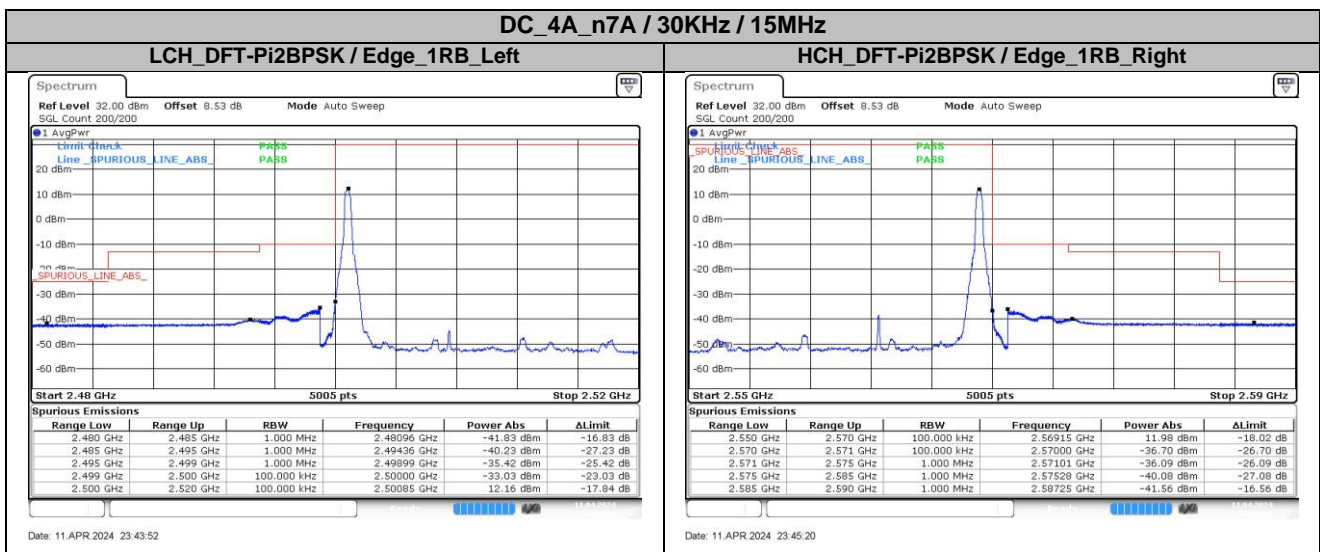
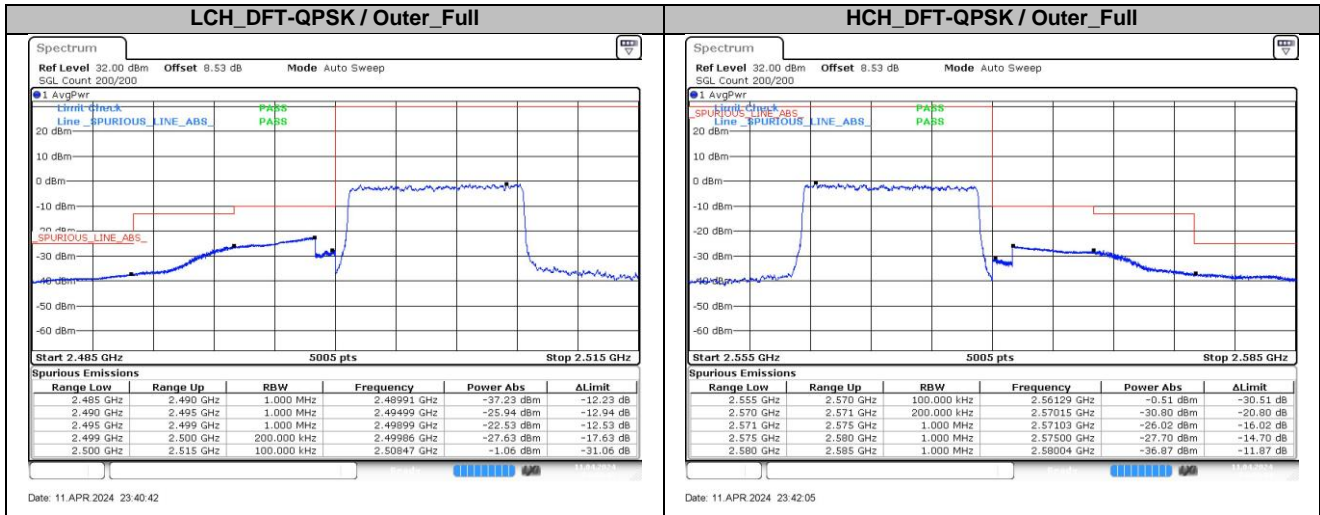


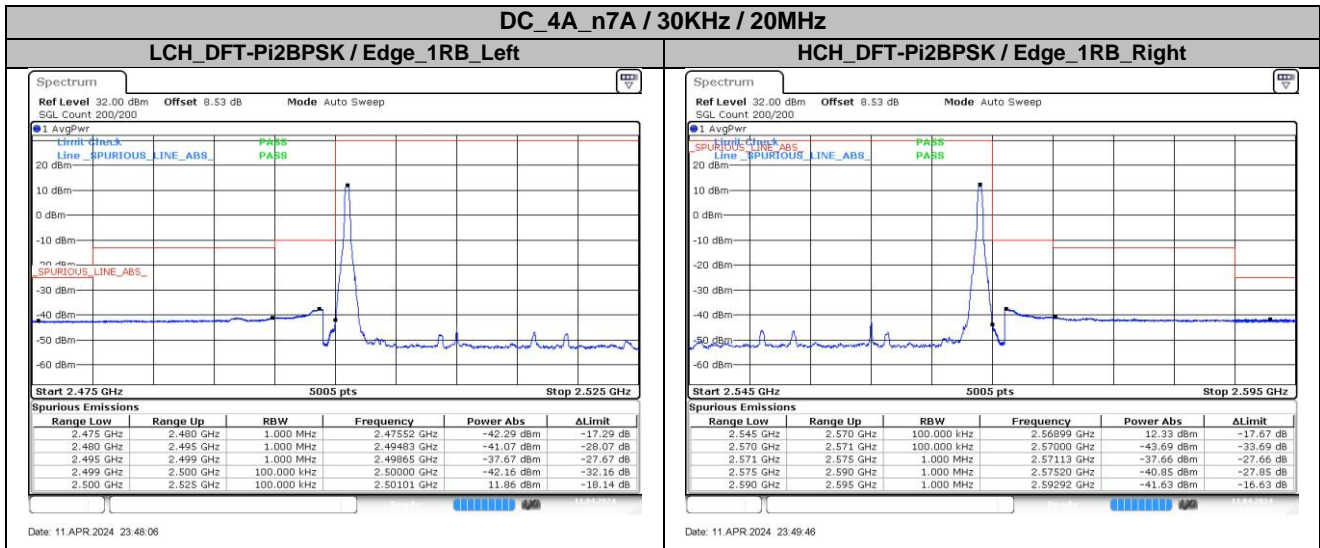
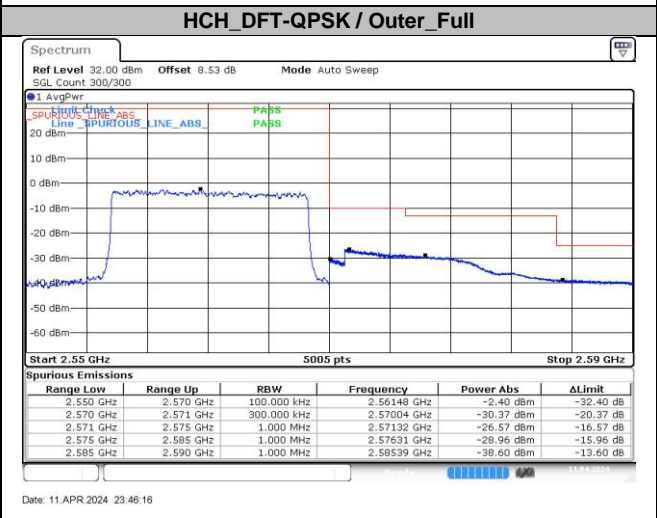
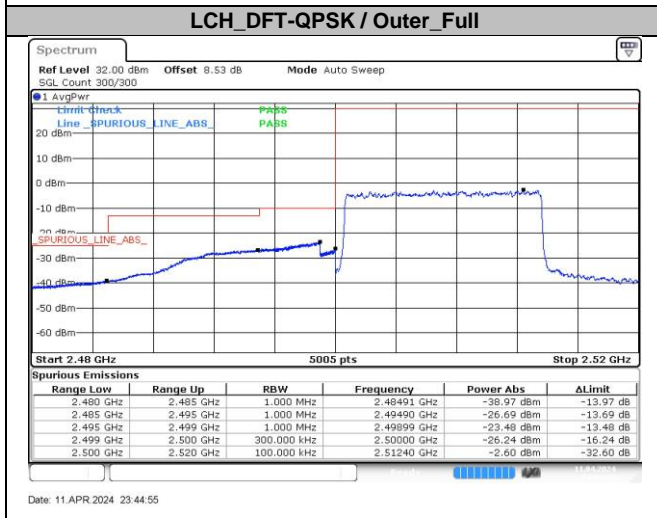
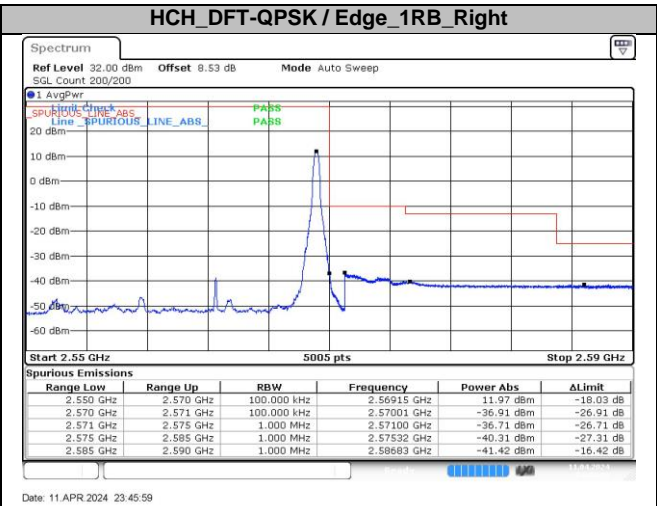
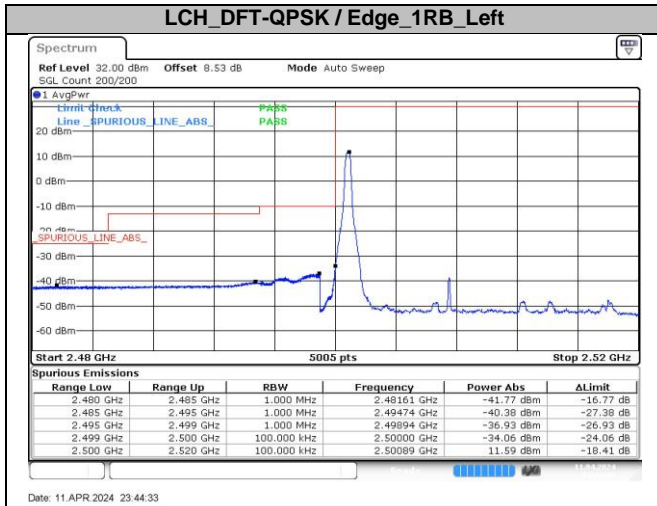
Date: 11 APR 2024 23:40:21

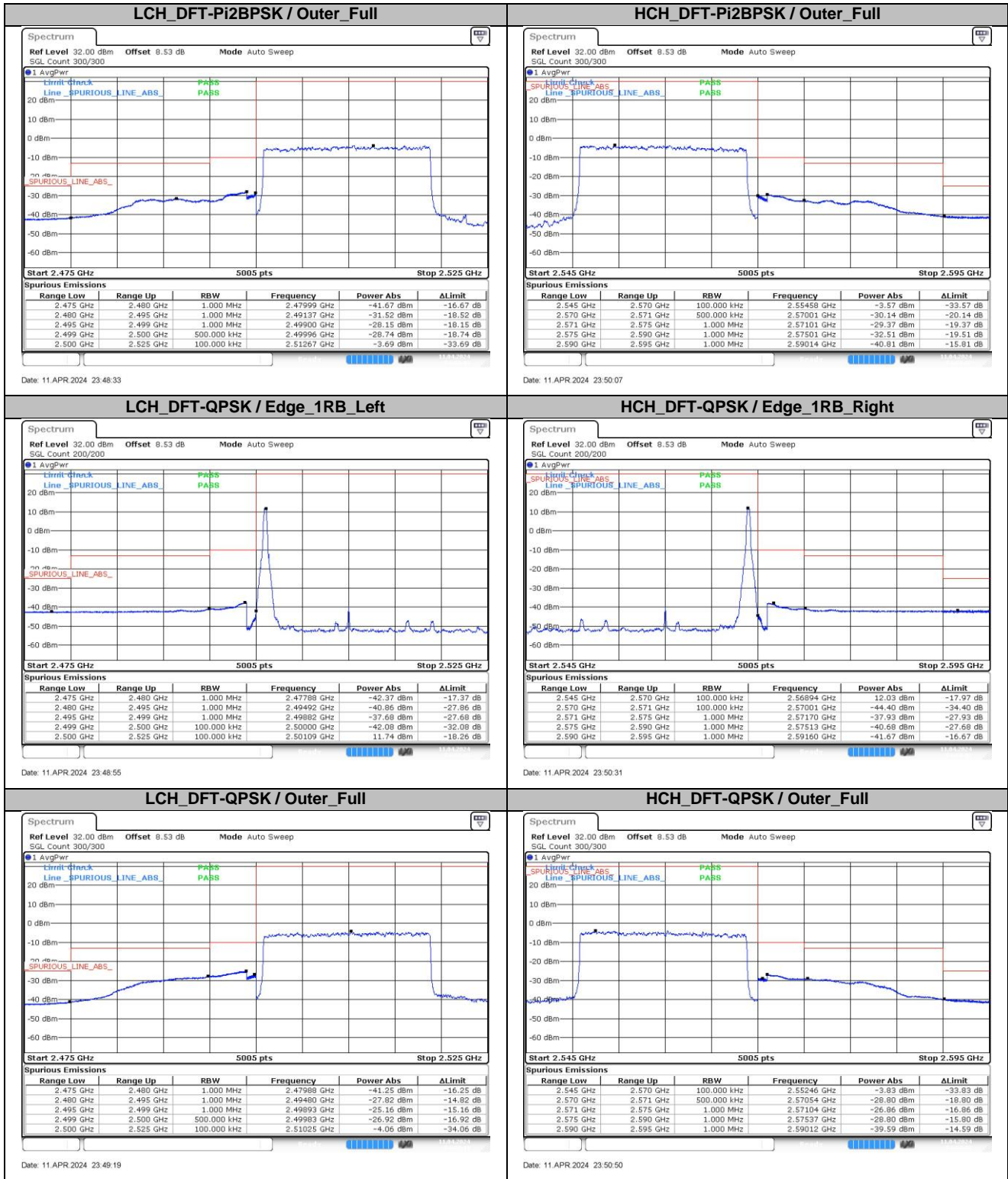
HCH_DFT-QPSK / Edge_1RB_Right



Date: 11 APR 2024 23:41:49

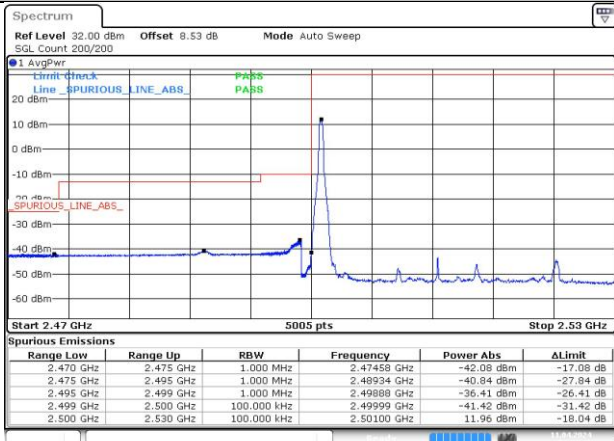






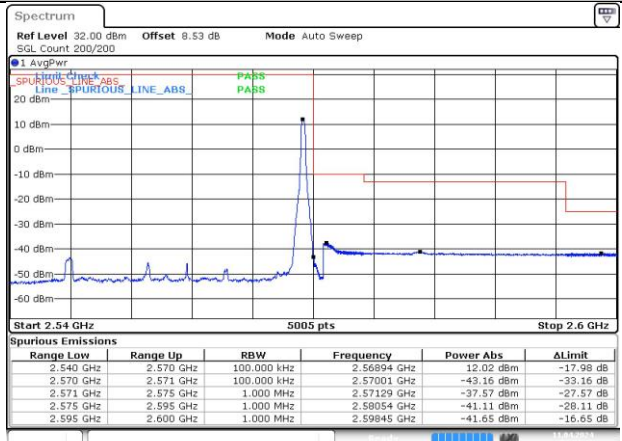
DC_4A_n7A / 30KHz / 25MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



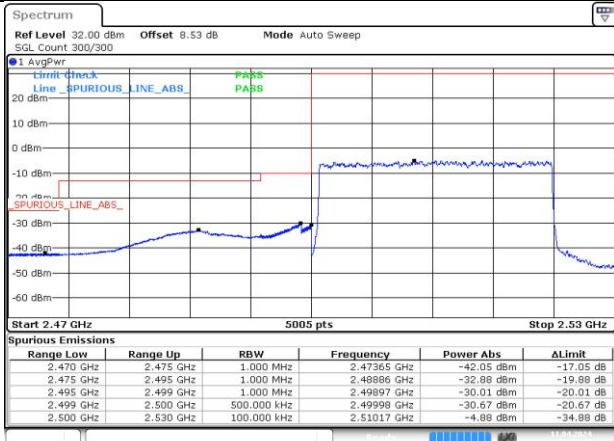
Date: 11 APR 2024 23:52:39

HCH_DFT-Pi2BPSK / Edge_1RB_Right



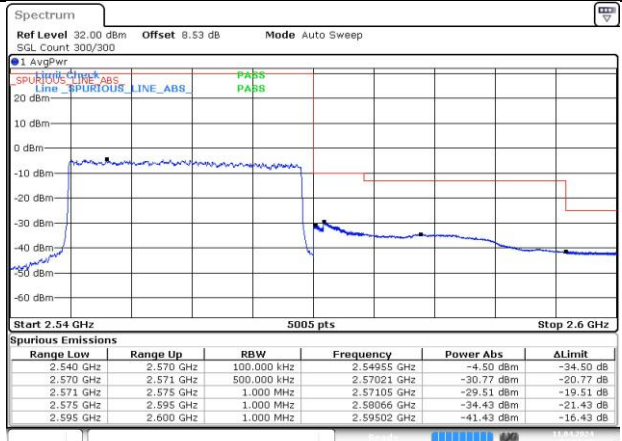
Date: 11 APR 2024 23:54:11

LCH_DFT-Pi2BPSK / Outer_Full



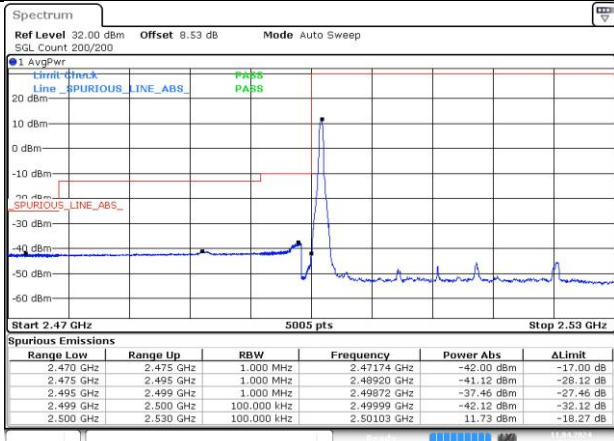
Date: 11 APR 2024 23:53:03

HCH_DFT-Pi2BPSK / Outer_Full



Date: 11 APR 2024 23:54:29

LCH_DFT-QPSK / Edge_1RB_Left

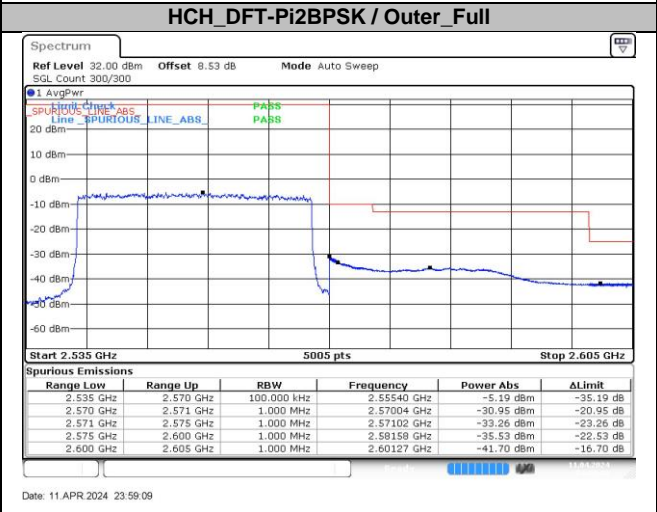
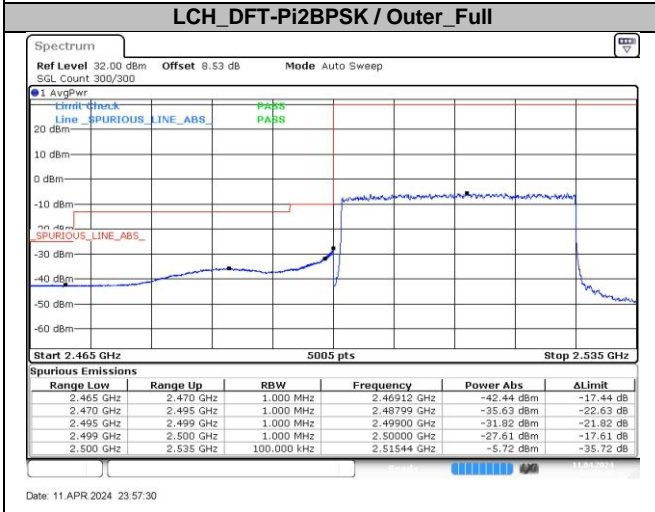
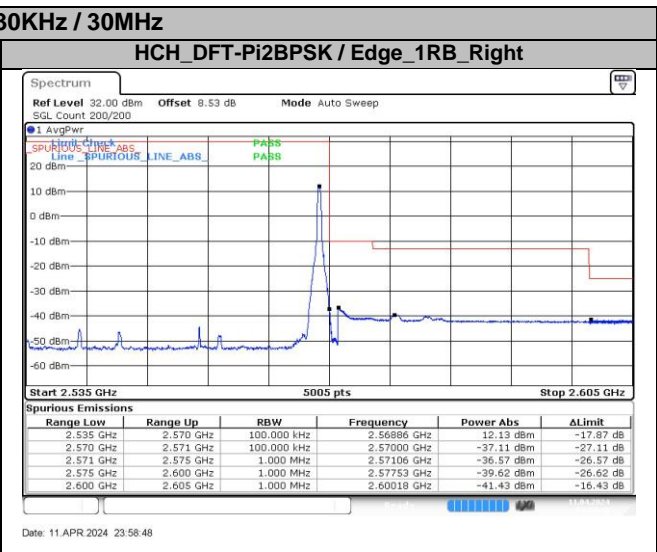
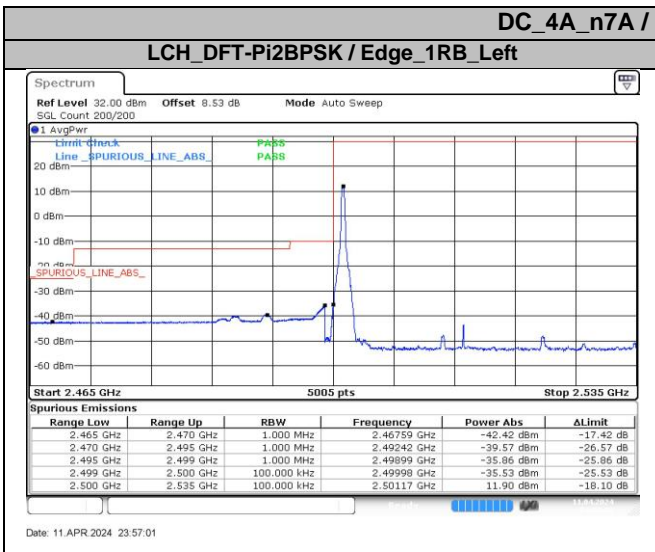
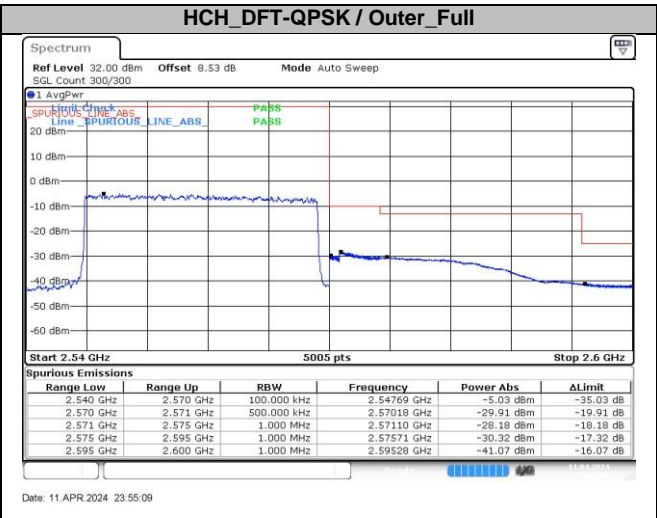
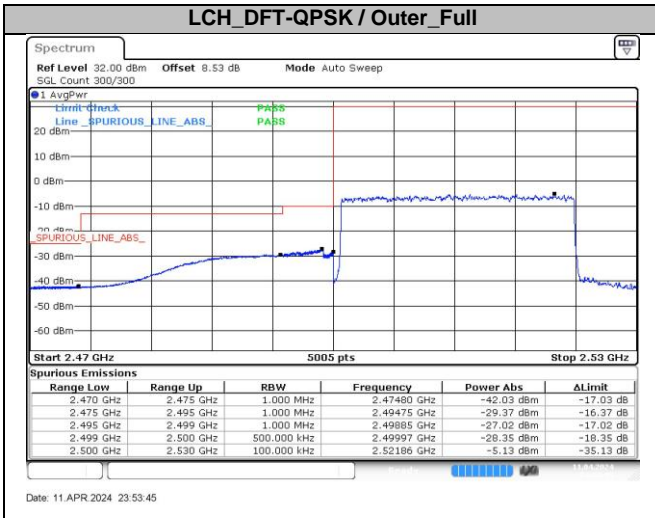


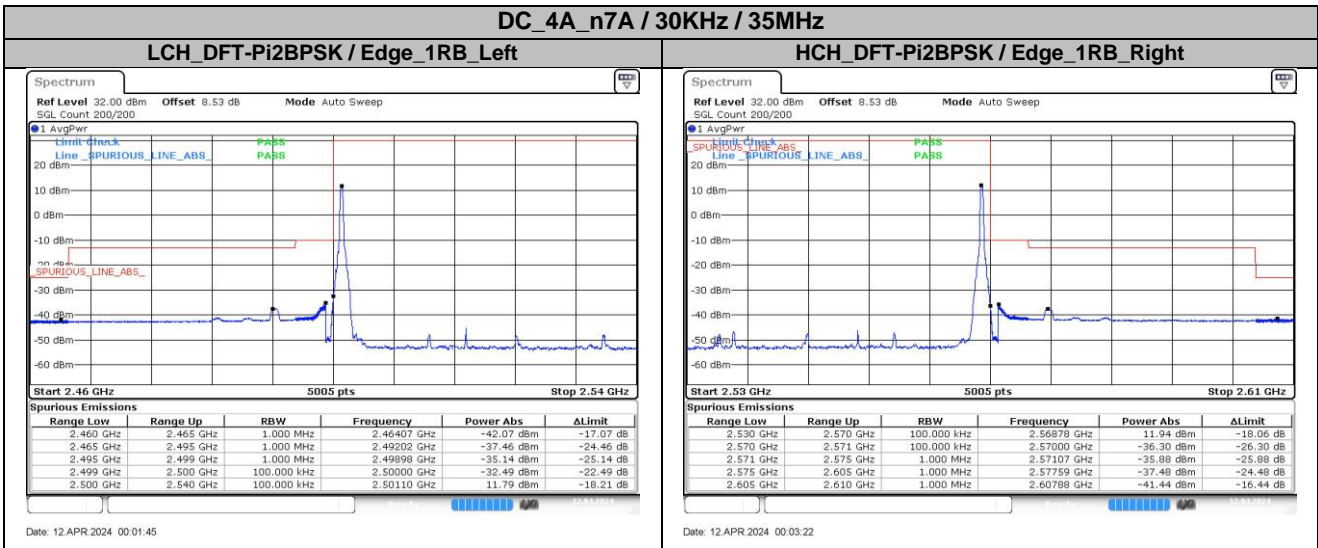
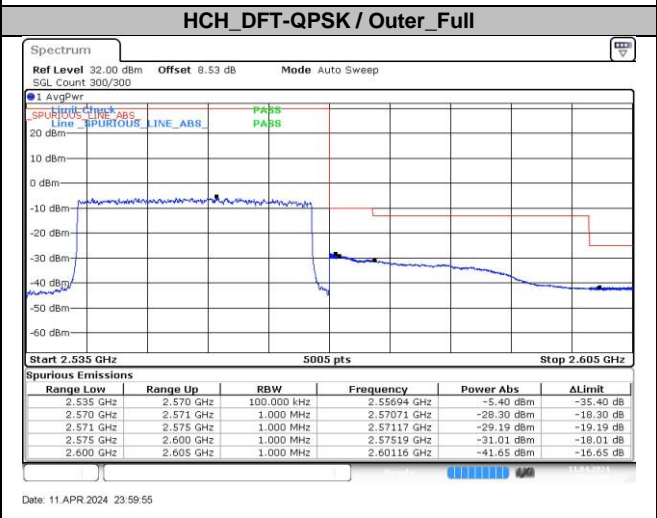
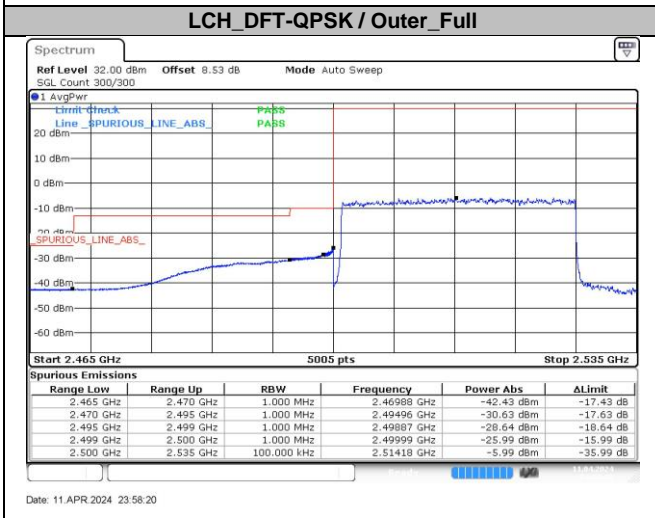
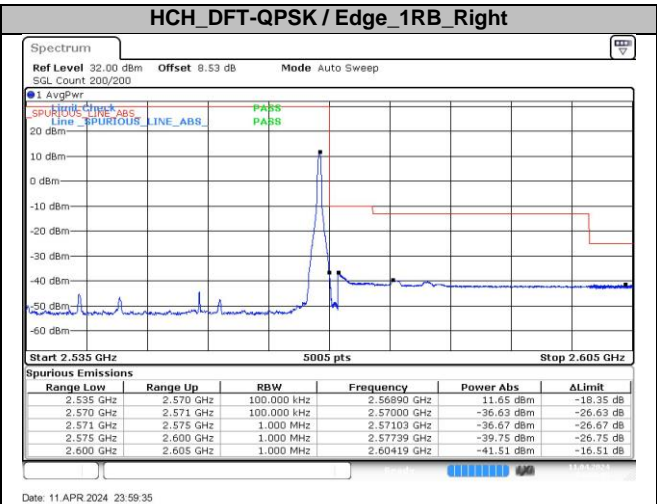
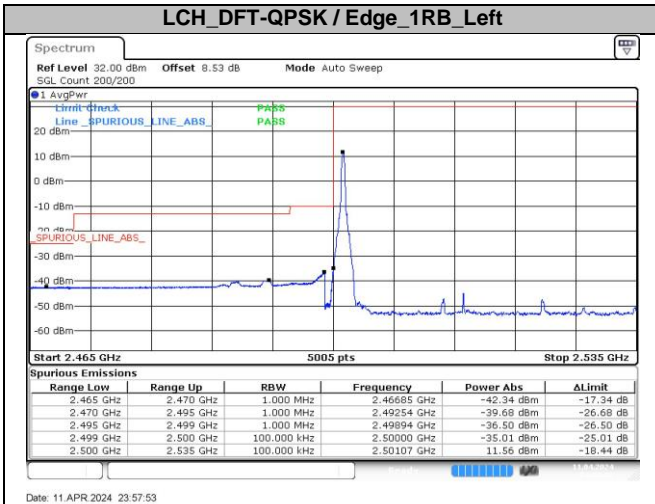
Date: 11 APR 2024 23:53:23

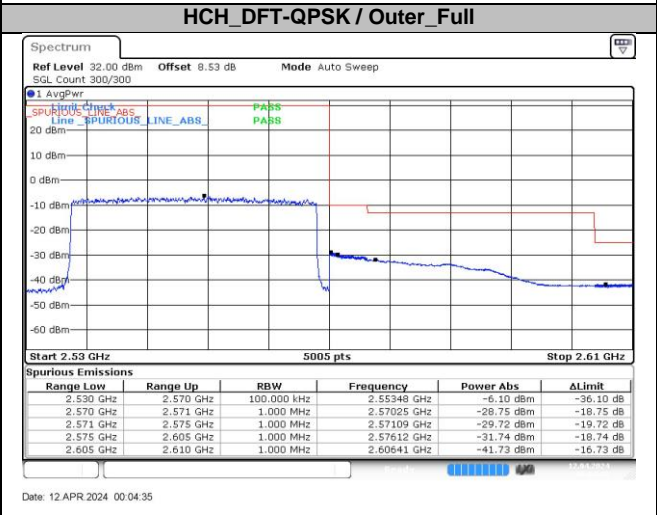
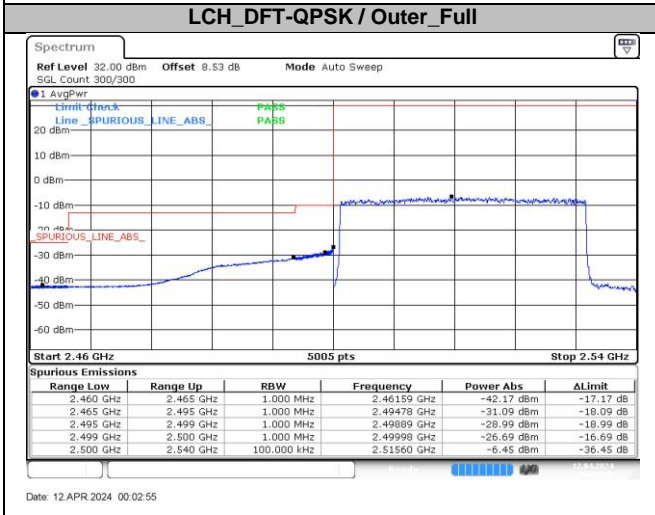
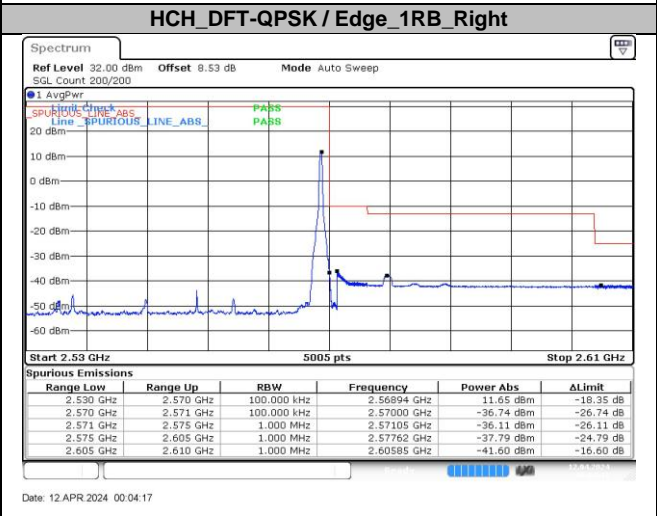
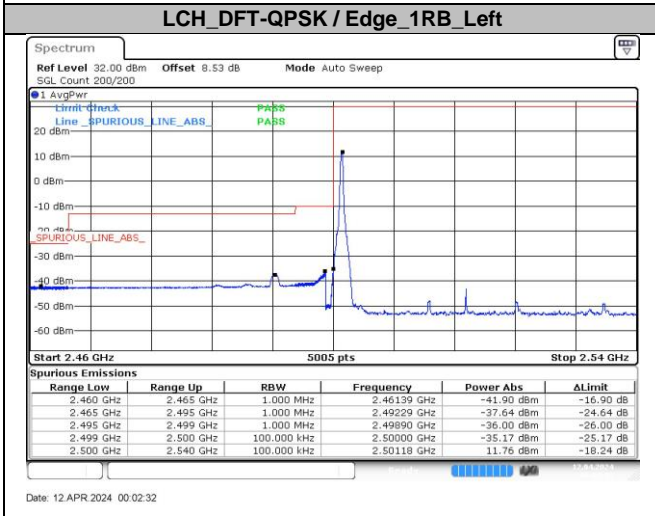
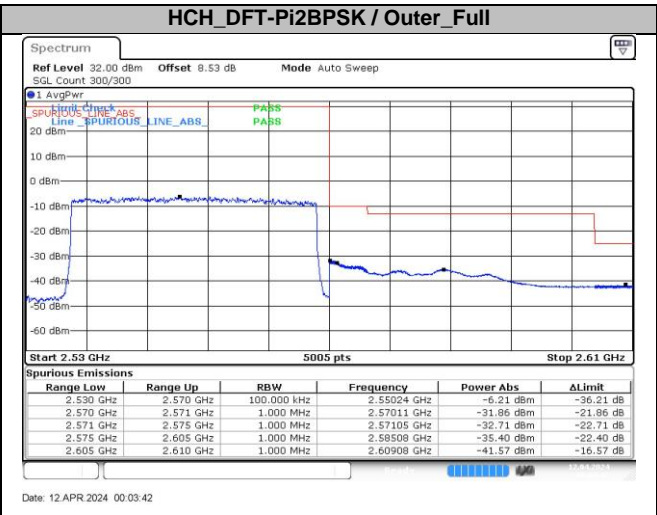
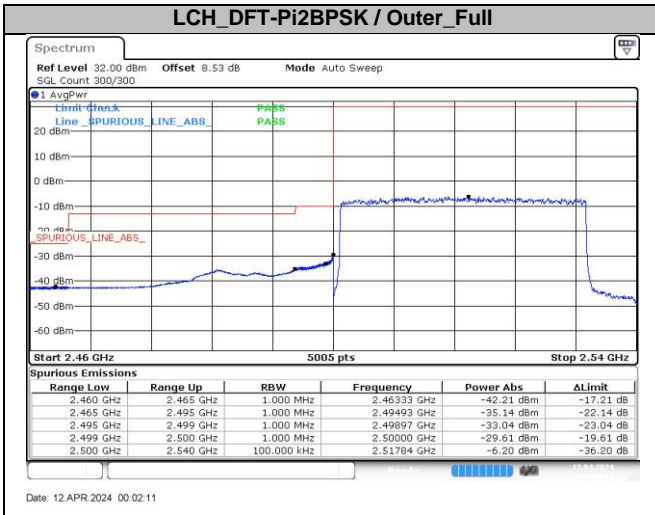
HCH_DFT-QPSK / Edge_1RB_Right



Date: 11 APR 2024 23:54:52

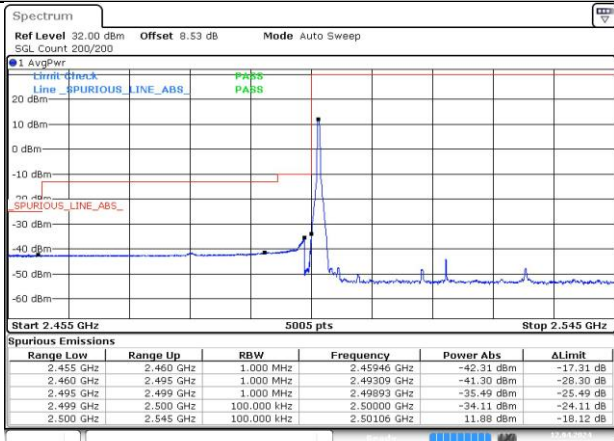






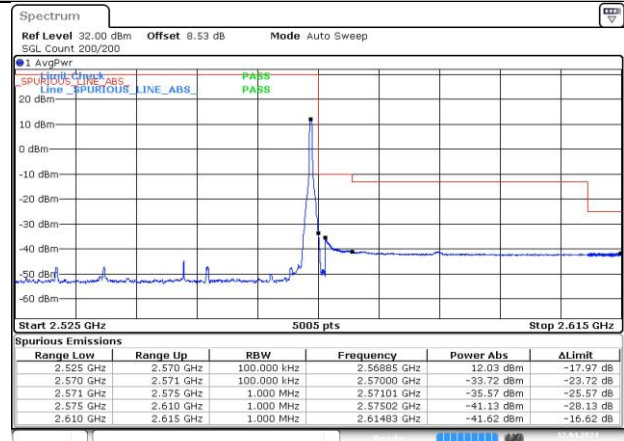
DC_4A_n7A / 30KHz / 40MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



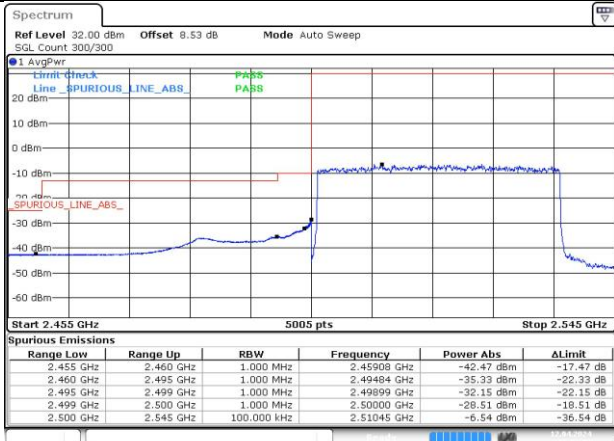
Date: 12 APR 2024 00:06:28

HCH_DFT-Pi2BPSK / Edge_1RB_Right



Date: 12 APR 2024 00:06:20

LCH_DFT-Pi2BPSK / Outer_Full



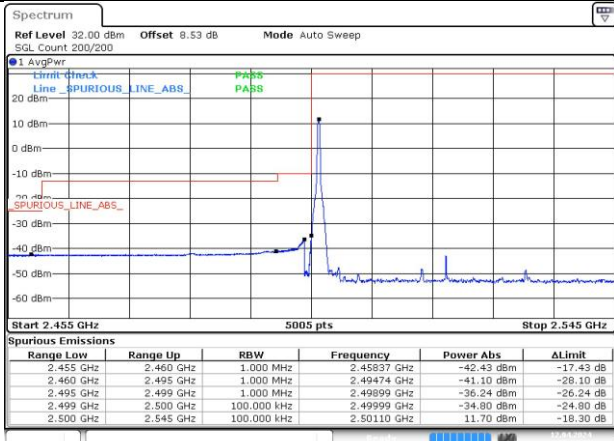
Date: 12 APR 2024 00:06:59

HCH_DFT-Pi2BPSK / Outer_Full



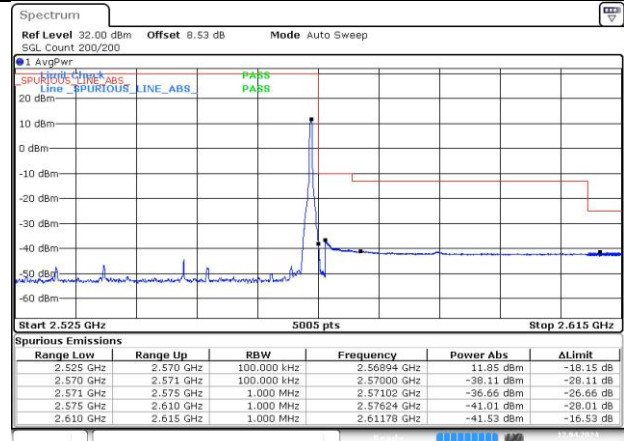
Date: 12 APR 2024 00:08:43

LCH_DFT-QPSK / Edge_1RB_Left



Date: 12 APR 2024 00:07:23

HCH_DFT-QPSK / Edge_1RB_Right



Date: 12 APR 2024 00:09:11

