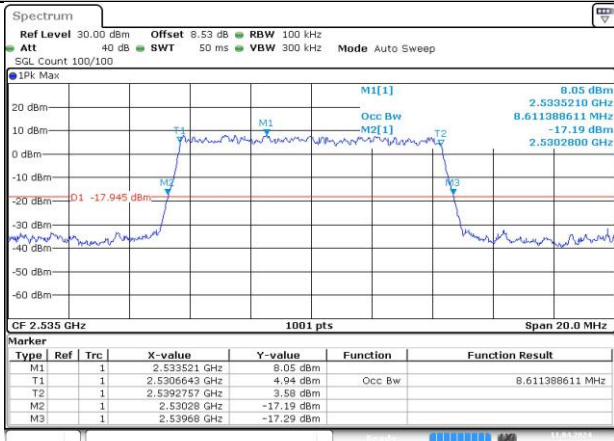
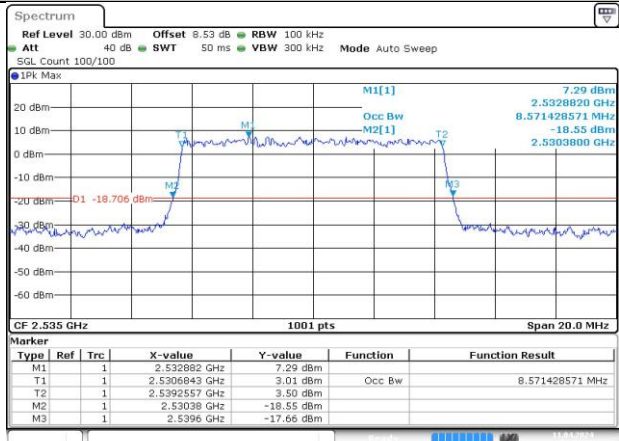


DC_4A_n7A / 30KHz / 10MHz

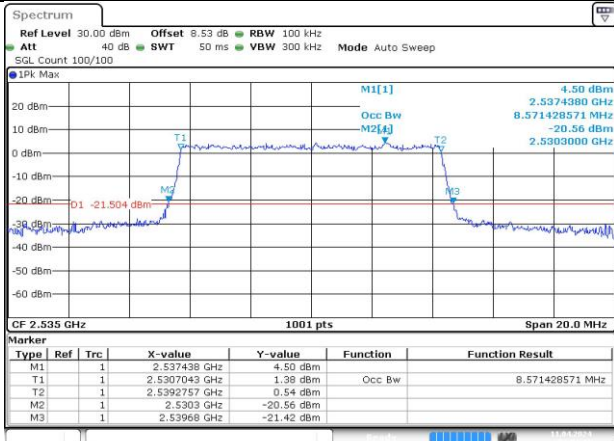
MCH_DFT-Pi2BPSK / Outer_Full



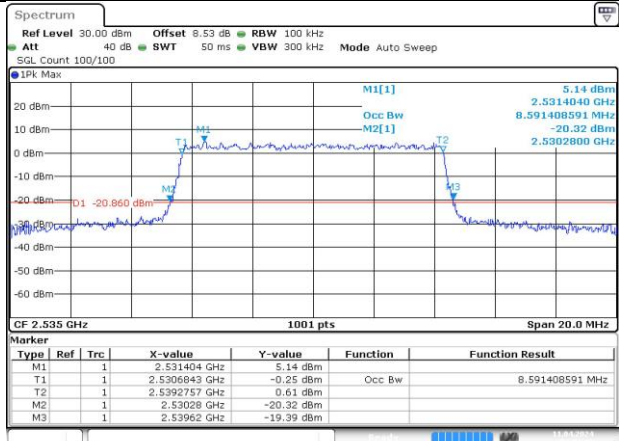
MCH_DFT-QPSK / Outer_Full



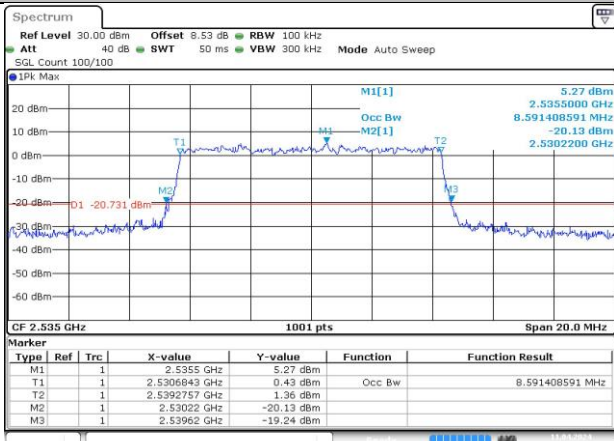
MCH_CP-QPSK / Outer_Full



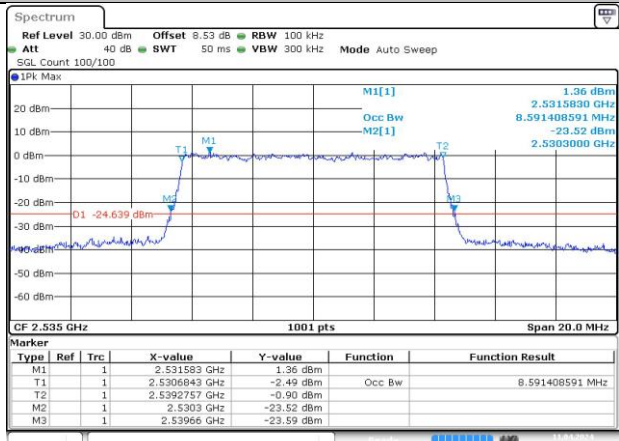
MCH_CP-16QAM / Outer_Full



MCH_CP-64QAM / Outer_Full

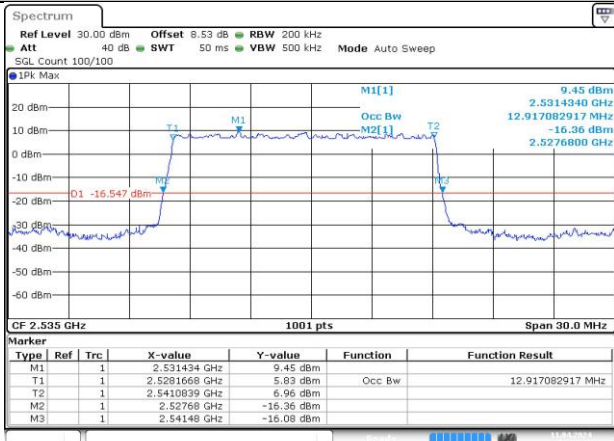


MCH_CP-256QAM / Outer_Full



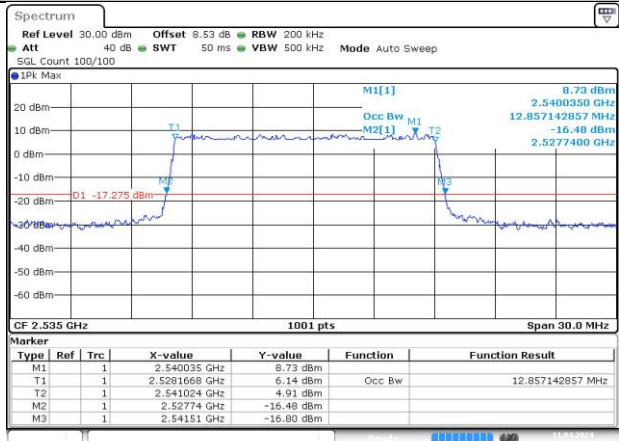
DC_4A_n7A / 30KHz / 15MHz

MCH_DFT-Pi2BPSK / Outer_Full



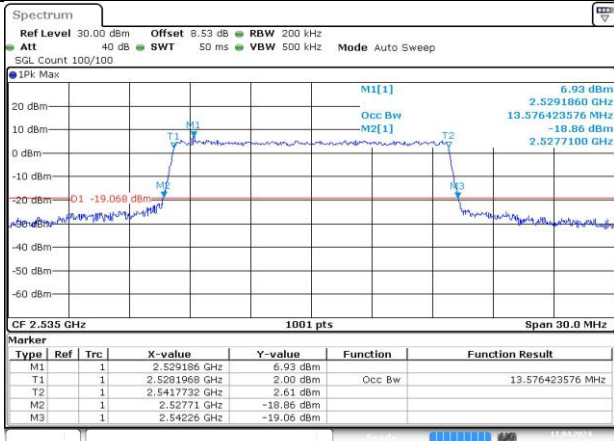
Date: 11.APR.2024 22:52:13

MCH_DFT-QPSK / Outer_Full



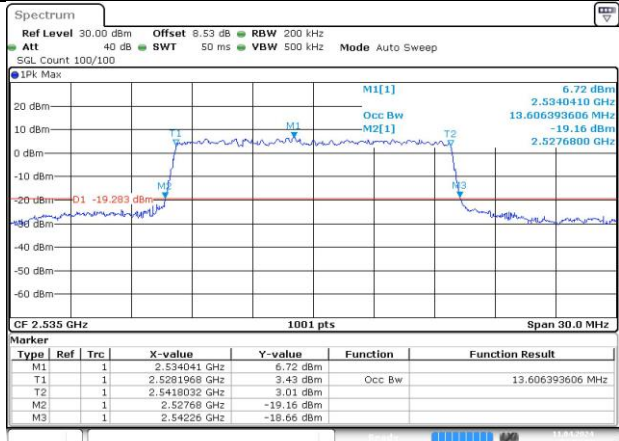
Date: 11.APR.2024 22:52:27

MCH_CP-QPSK / Outer_Full



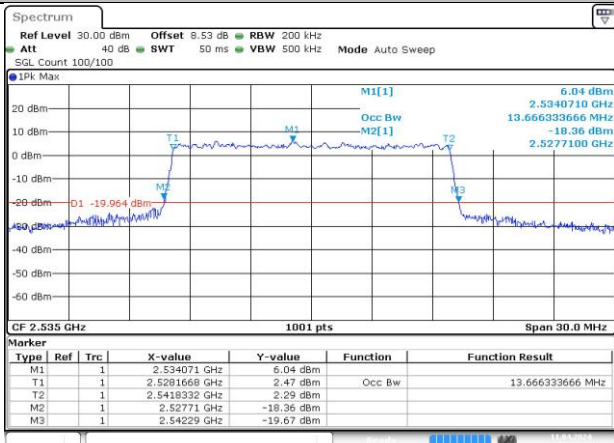
Date: 11.APR.2024 22:52:38

MCH_CP-16QAM / Outer_Full



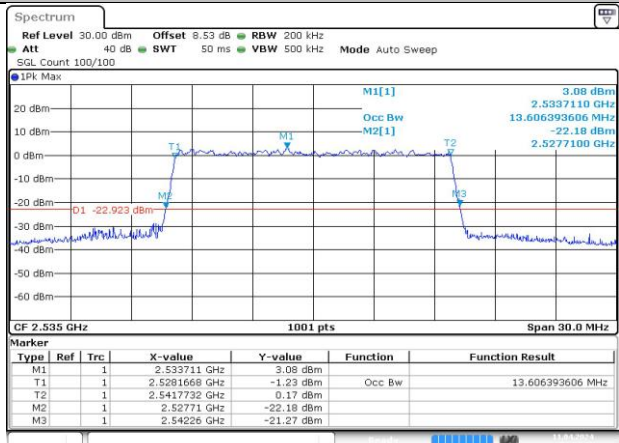
Date: 11.APR.2024 22:52:51

MCH_CP-64QAM / Outer_Full



Date: 11.APR.2024 22:53:04

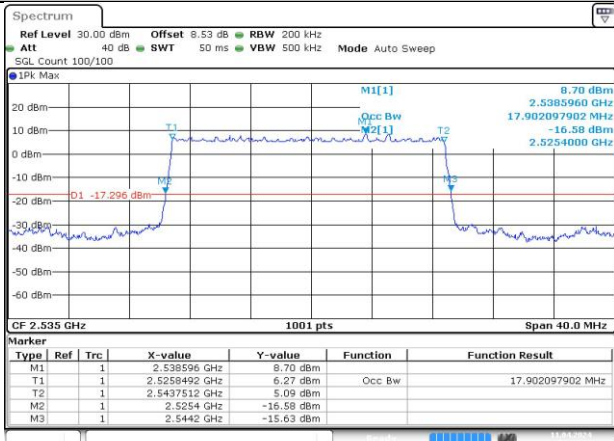
MCH_CP-256QAM / Outer_Full



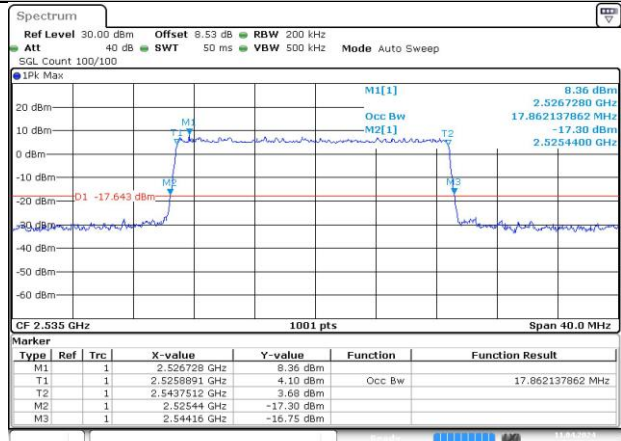
Date: 11.APR.2024 22:53:18

DC_4A_n7A / 30KHz / 20MHz

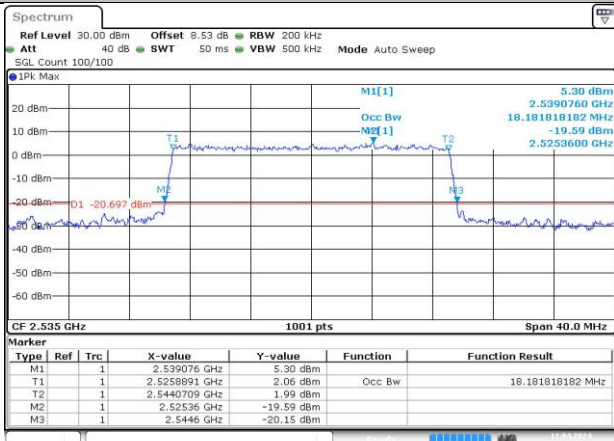
MCH_DFT-Pi2BPSK / Outer_Full



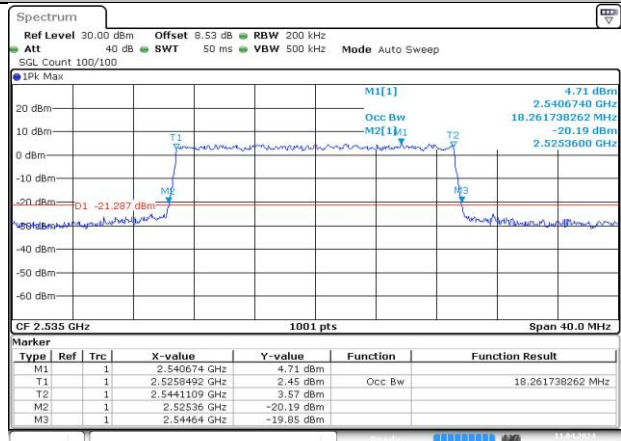
MCH_DFT-QPSK / Outer_Full



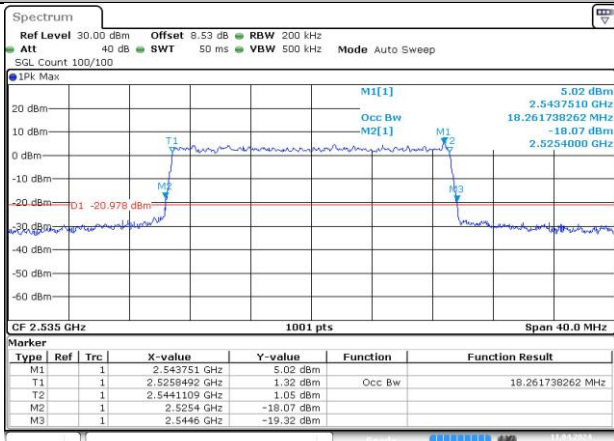
MCH_CP-QPSK / Outer_Full



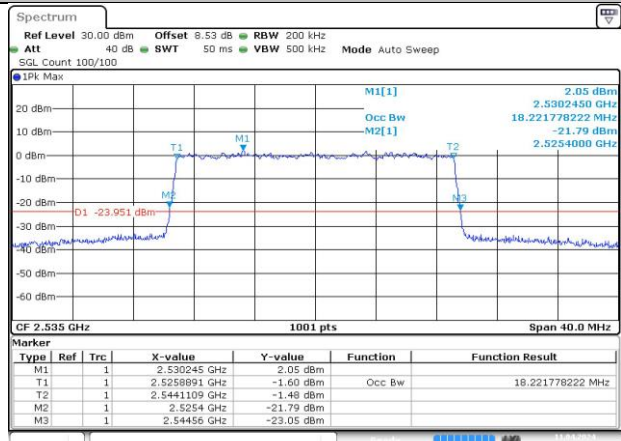
MCH_CP-16QAM / Outer_Full



MCH_CP-64QAM / Outer_Full

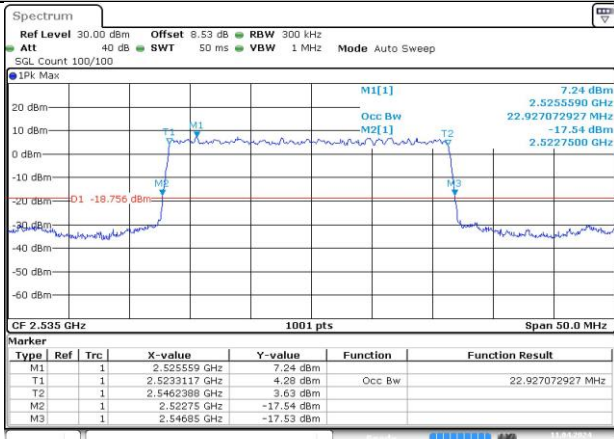


MCH_CP-256QAM / Outer_Full



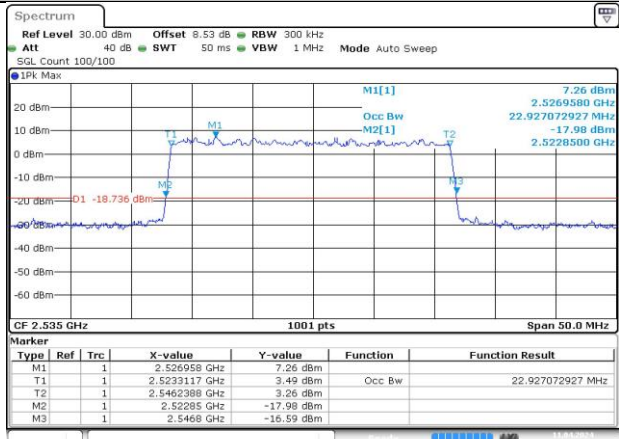
DC_4A_n7A / 30KHz / 25MHz

MCH_DFT-Pi2BPSK / Outer_Full



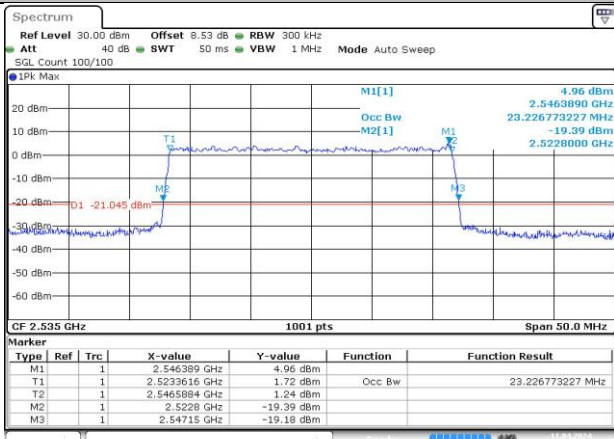
Date: 11 APR 2024 22:54:58

MCH_DFT-QPSK / Outer_Full



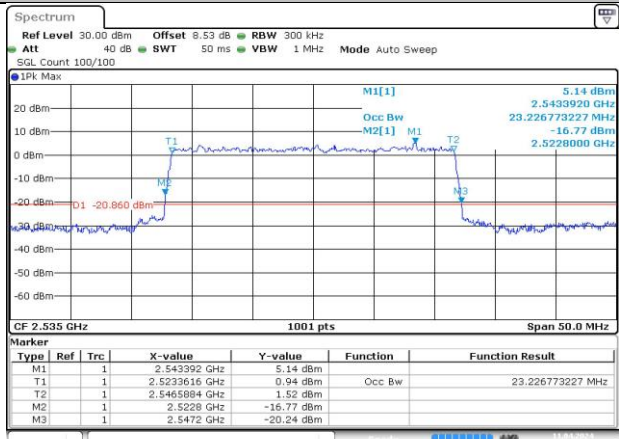
Date: 11 APR 2024 22:55:11

MCH_CP-QPSK / Outer_Full



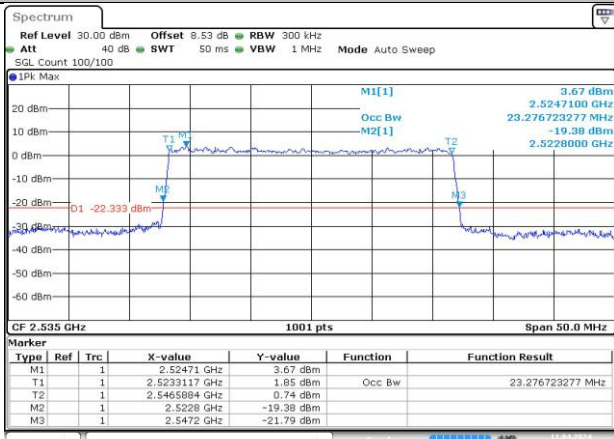
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MCH_CP-16QAM / Outer_Full



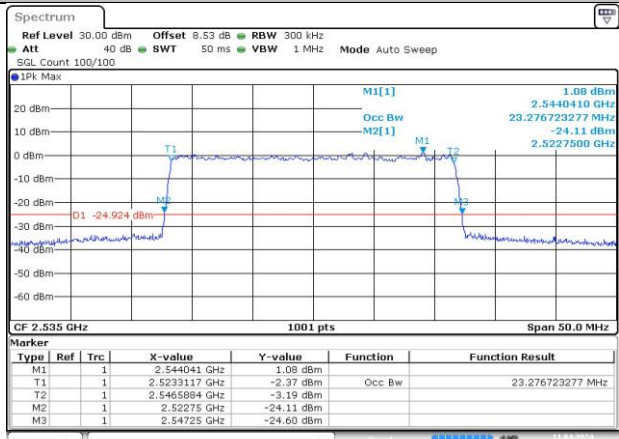
Date: 11 APR 2024 22:55:34

MCH_CP-64QAM / Outer_Full



Date: 11 APR 2024 22:55:47

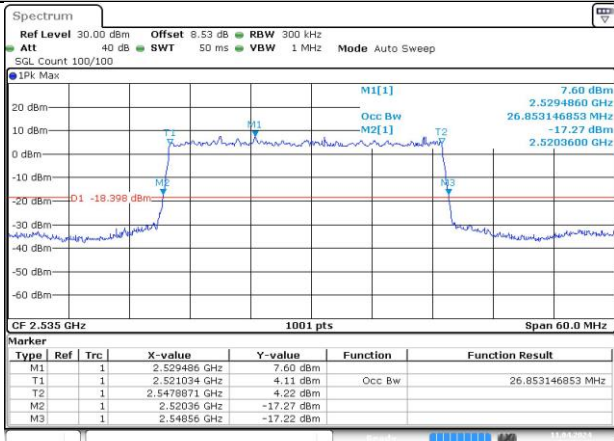
MCH_CP-256QAM / Outer_Full



Date: 11 APR 2024 22:56:00

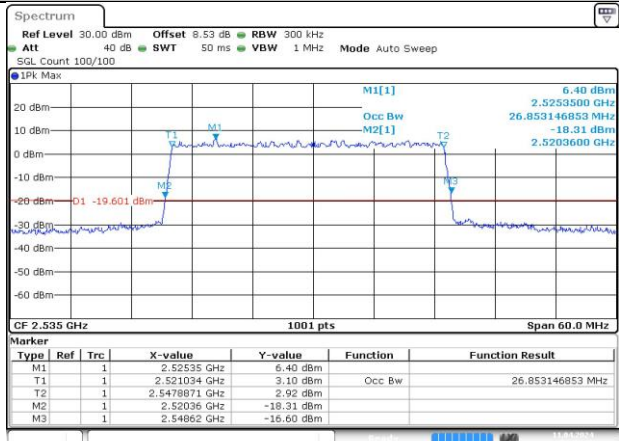
DC_4A_n7A / 30KHz / 30MHz

MCH_DFT-Pi2BPSK / Outer_Full



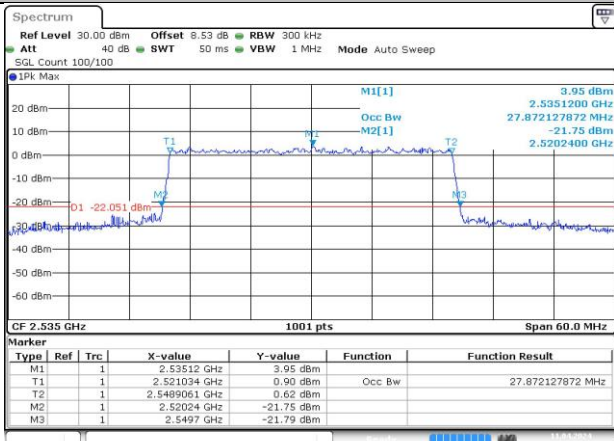
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MCH_DFT-QPSK / Outer_Full



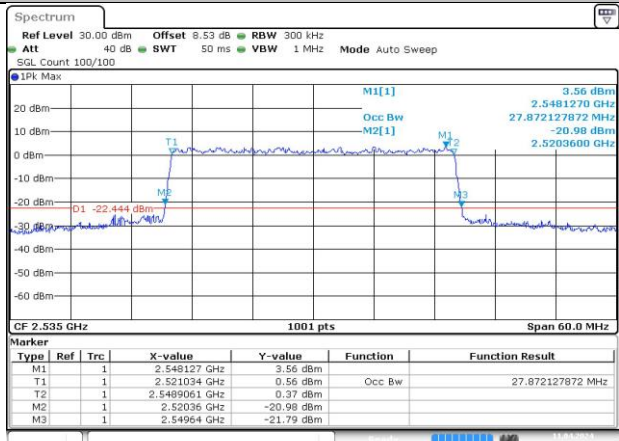
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MCH_CP-QPSK / Outer_Full



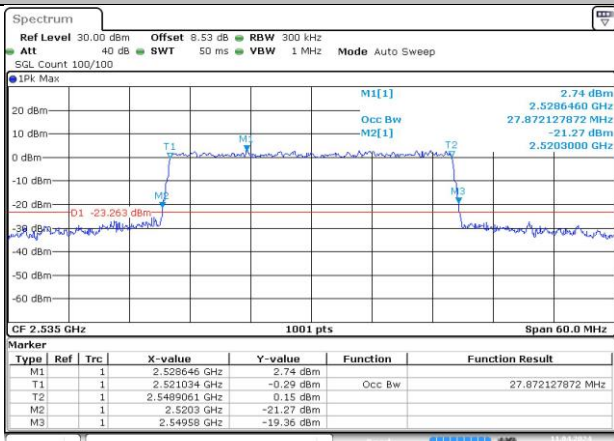
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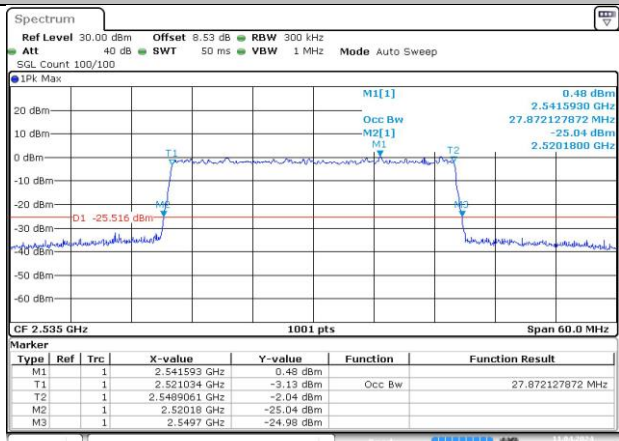
Date: 11.APR 2024 22:56:51

MCH_CP-64QAM / Outer_Full



Date: 11.APR 2024 22:57:04

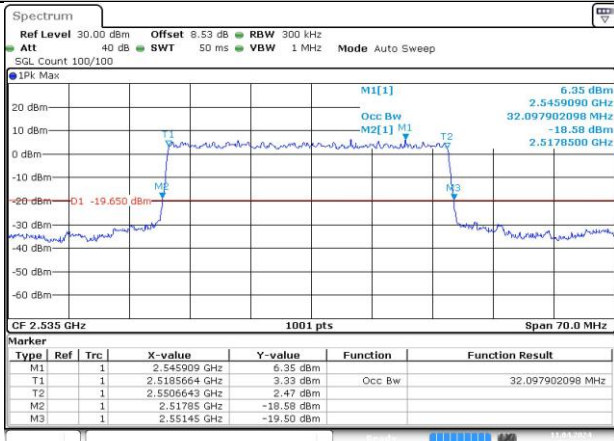
MCH_CP-256QAM / Outer_Full



Date: 11.APR 2024 22:57:16

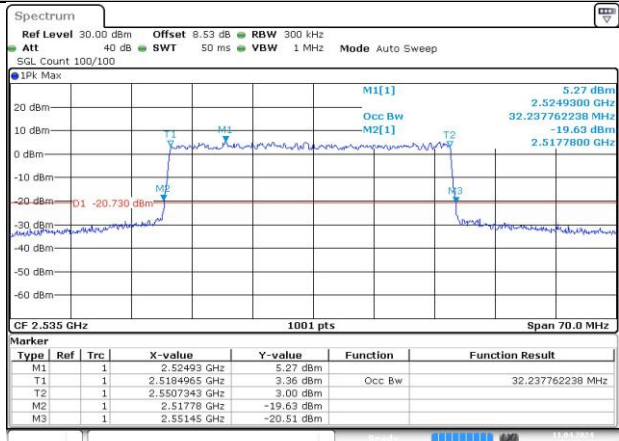
DC_4A_n7A / 30KHz / 35MHz

MCH_DFT-Pi2BPSK / Outer_Full



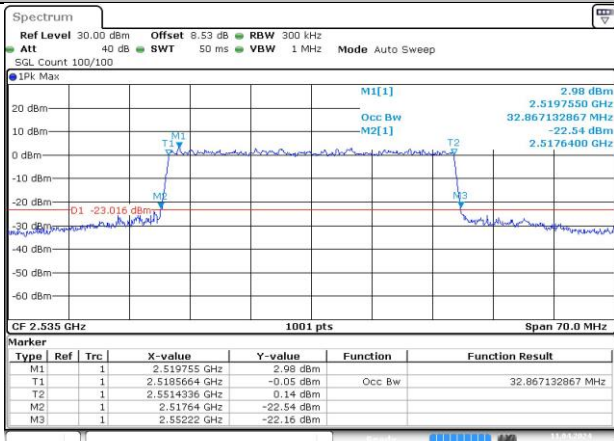
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MCH_DFT-QPSK / Outer_Full



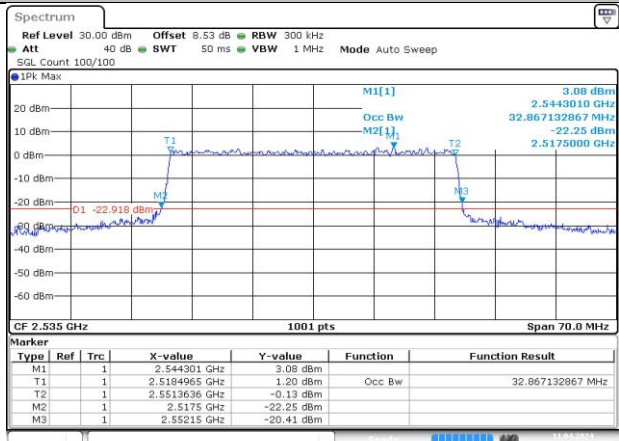
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MCH_CP-QPSK / Outer_Full



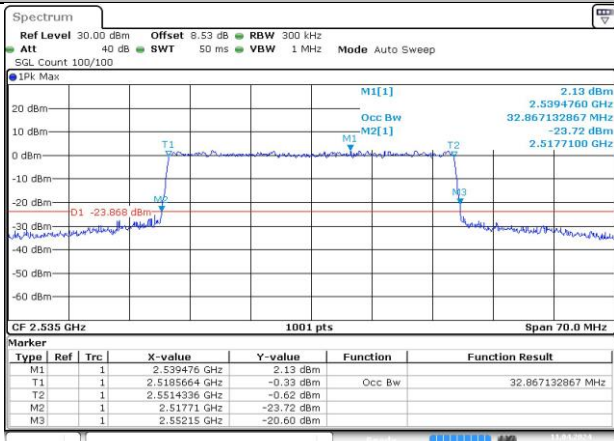
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MCH_CP-16QAM / Outer_Full



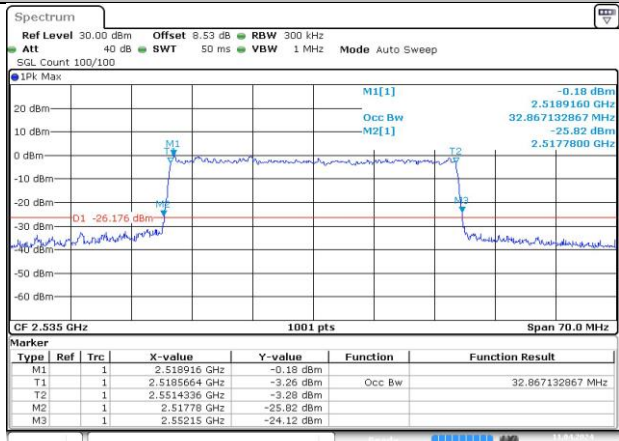
Date: 11.APR.2024 22:58:09

MCH_CP-64QAM / Outer_Full



Date: 11.APR.2024 22:58:21

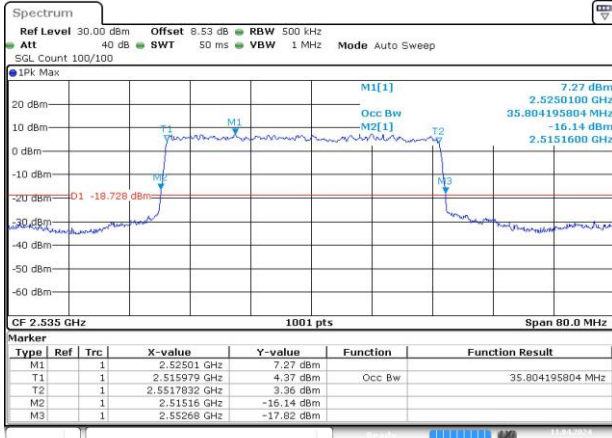
MCH_CP-256QAM / Outer_Full



Date: 11.APR.2024 22:58:34

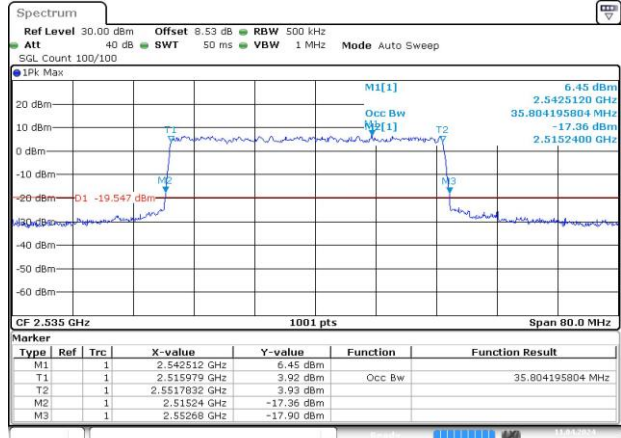
DC_4A_n7A / 30KHz / 40MHz

MCH_DFT-Pi2BPSK / Outer_Full



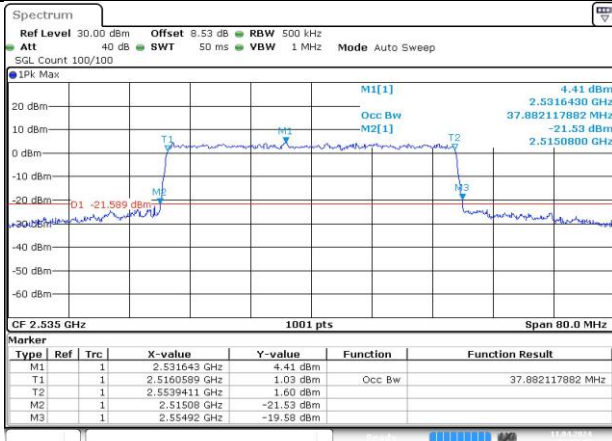
Date: 11.APR.2024 22:58:50

MCH_DFT-QPSK / Outer_Full



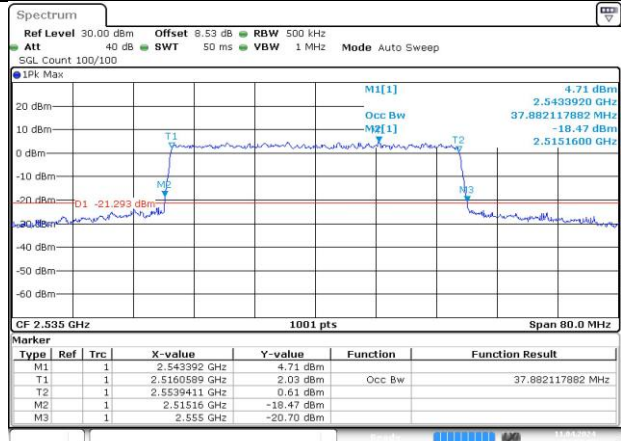
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MCH_CP-QPSK / Outer_Full



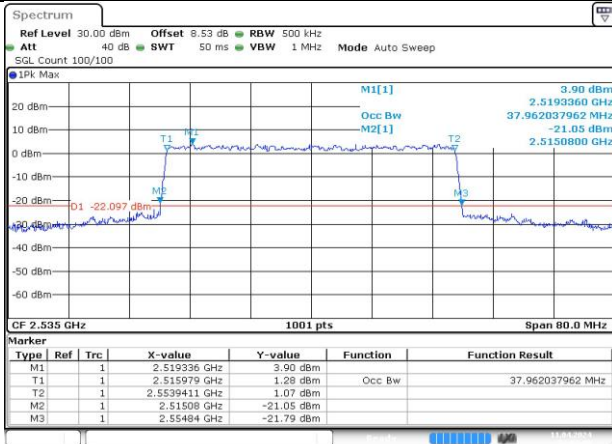
Date: 11.APR.2024 22:59:15

MCH_CP-16QAM / Outer_Full



Date: 11.APR.2024 22:59:27

MCH_CP-64QAM / Outer_Full

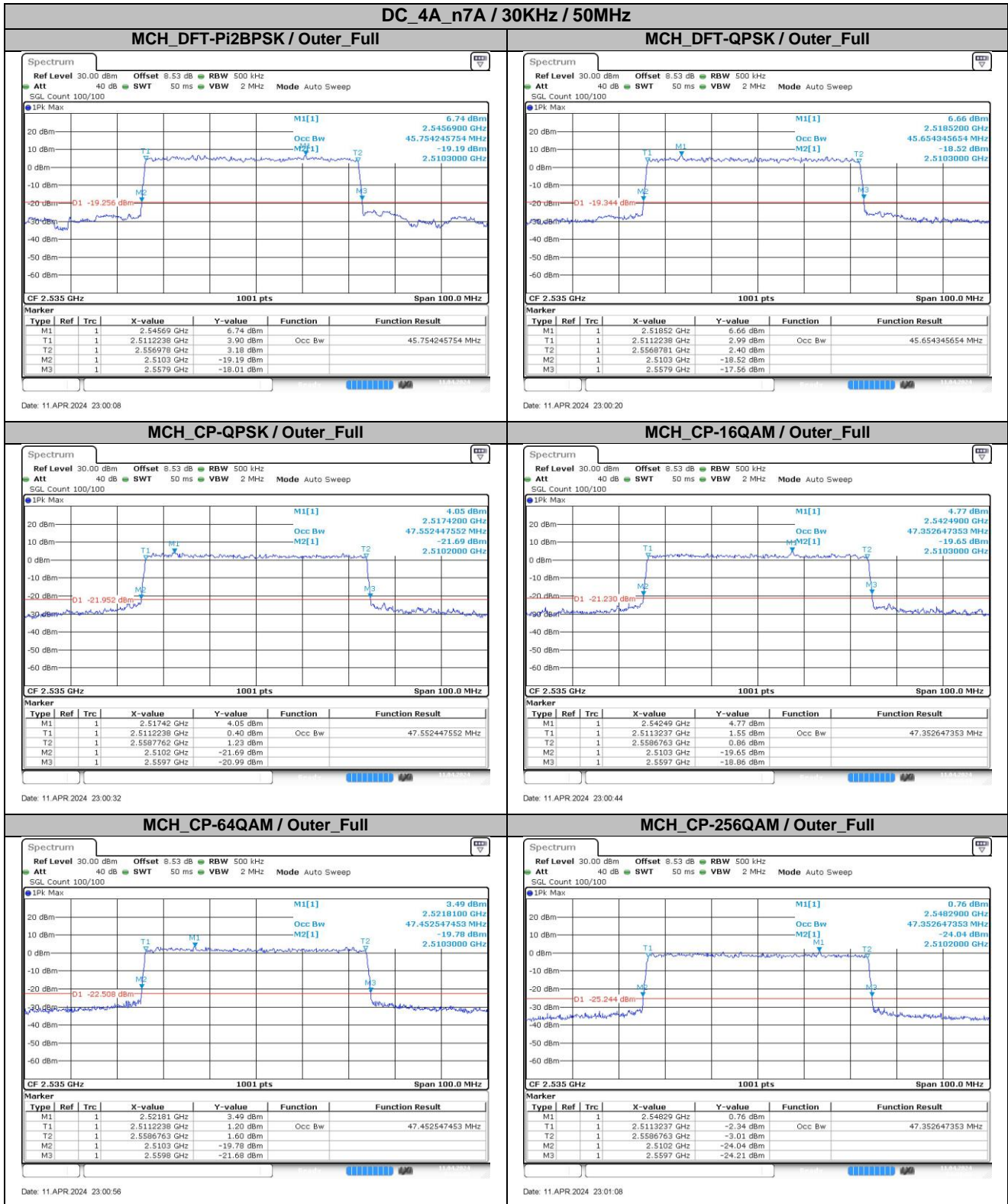


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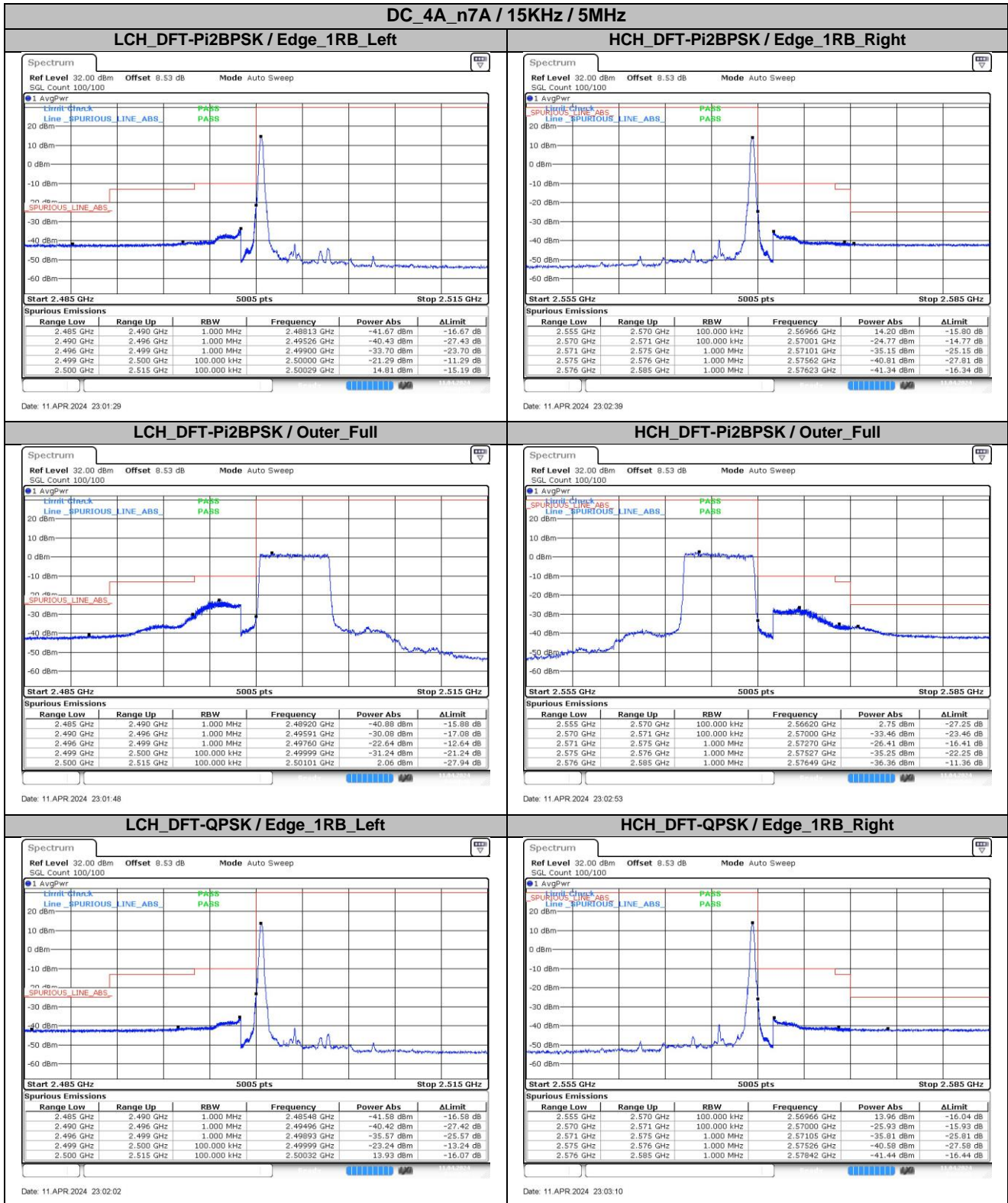


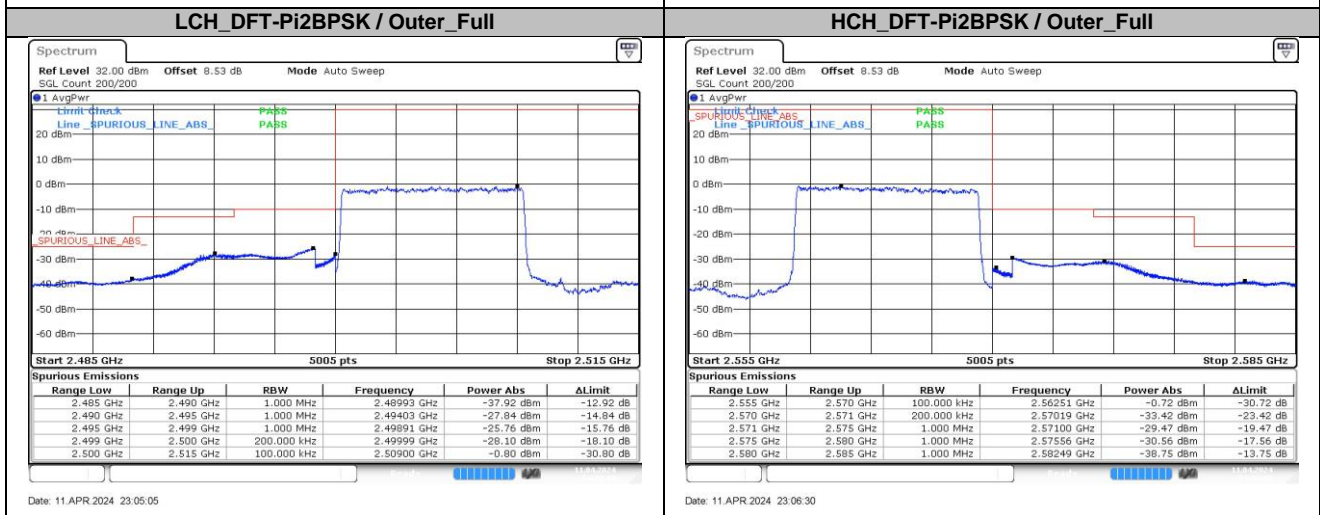
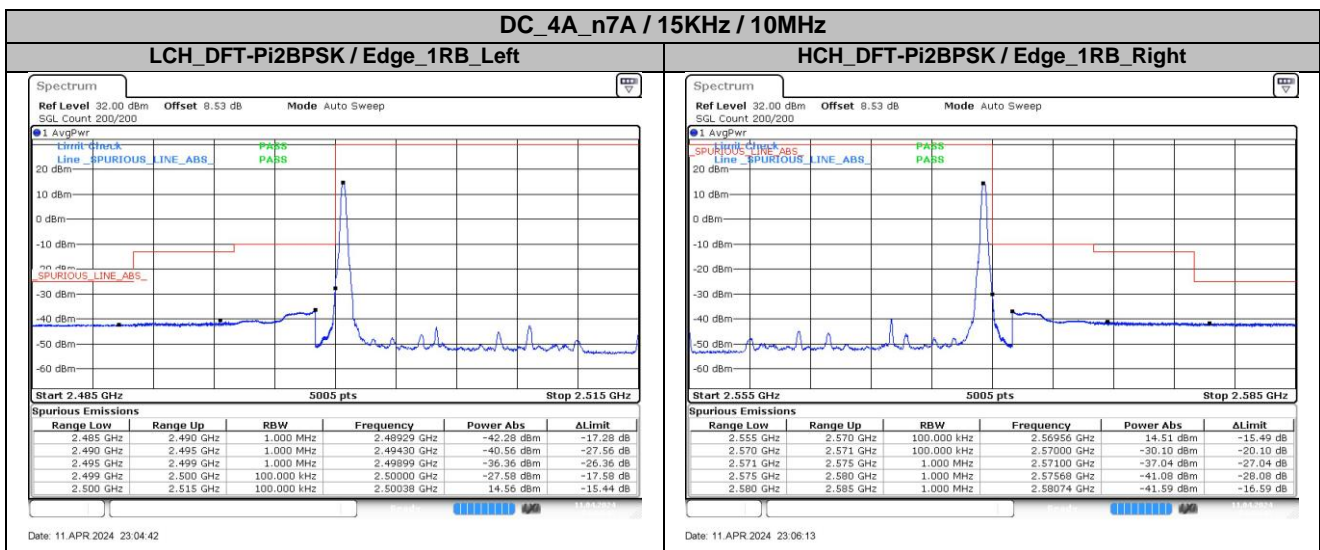
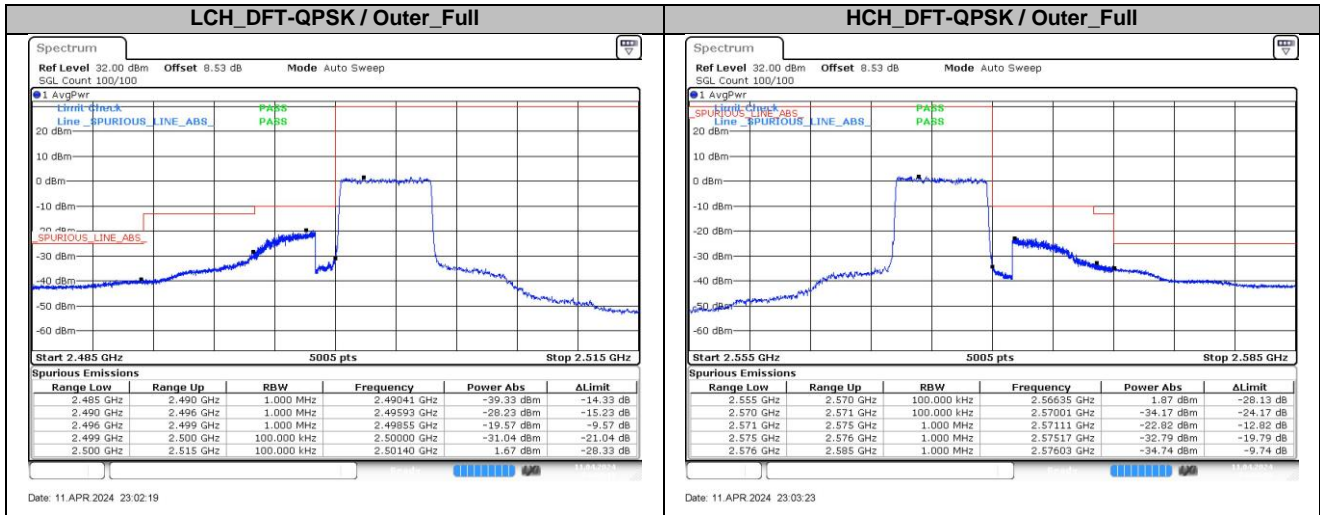
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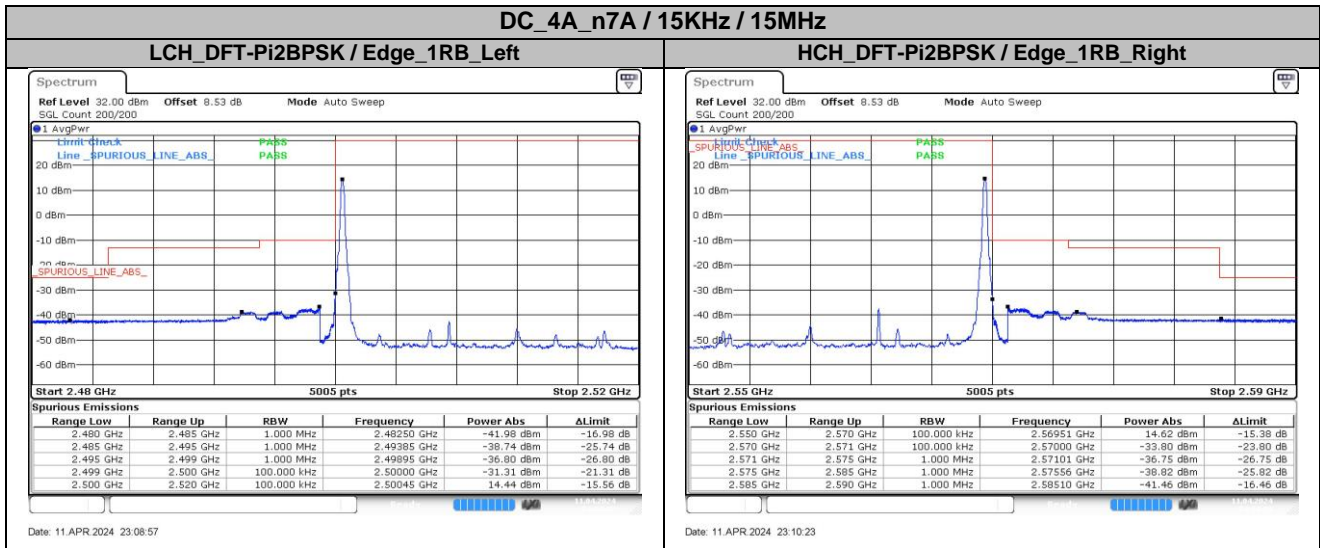
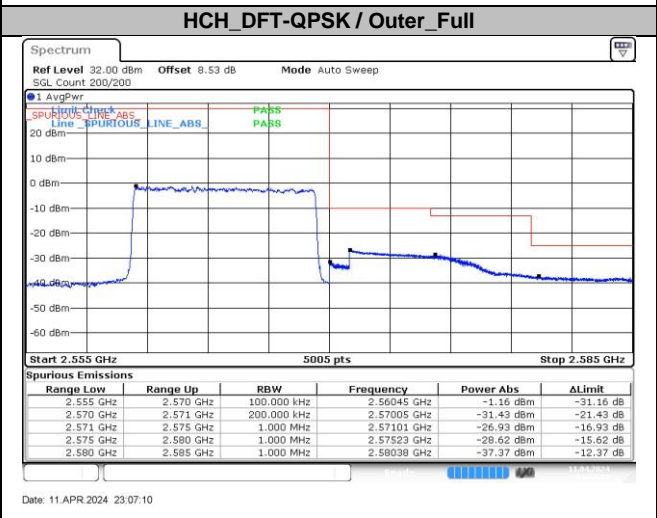
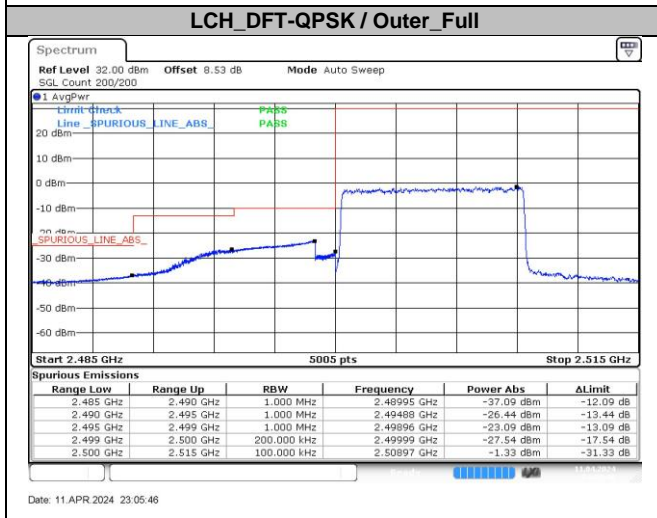
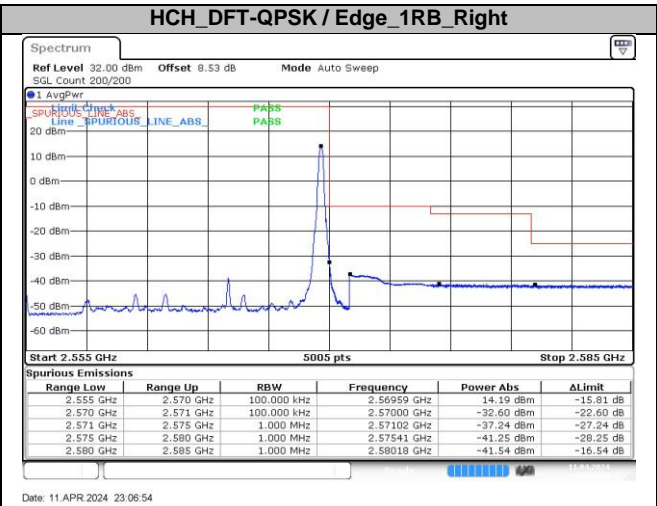
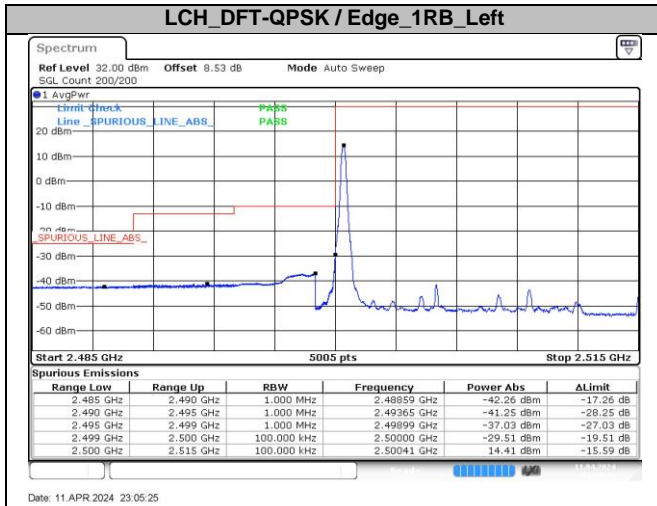


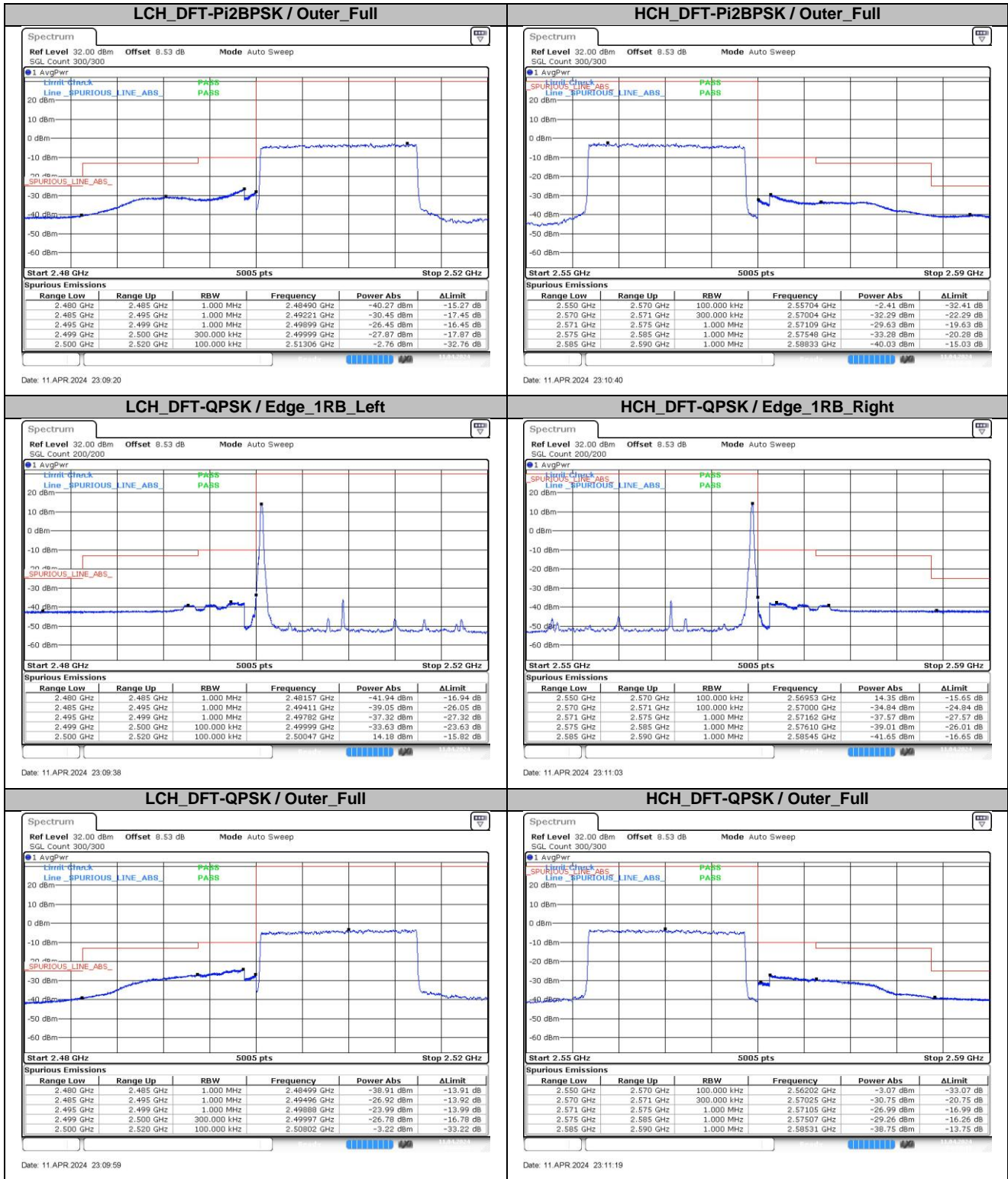
5. Conducted Band Edges

5.1. Test Plots



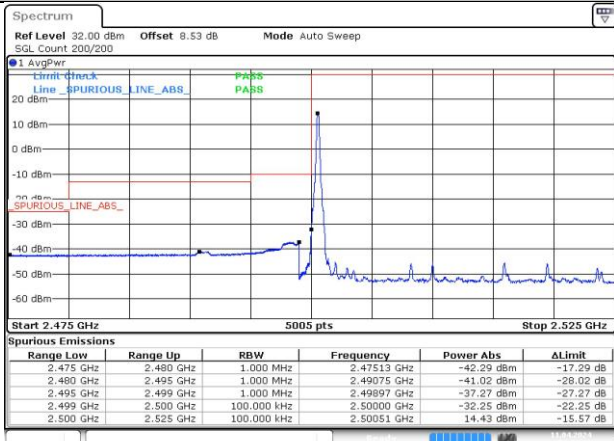






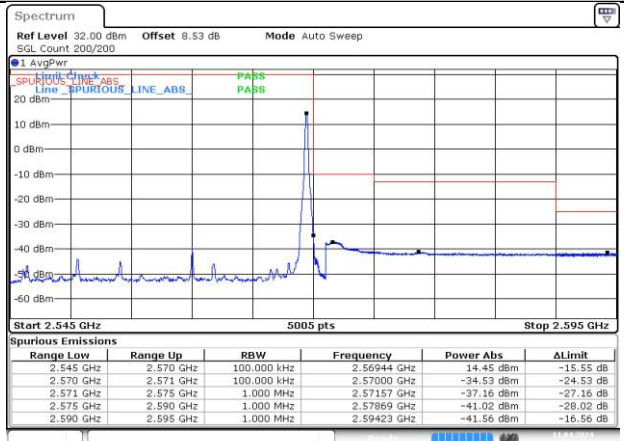
DC_4A_n7A / 15KHz / 20MHz

LCH_DFT-Pi2BPSK / Edge_1RB_Left



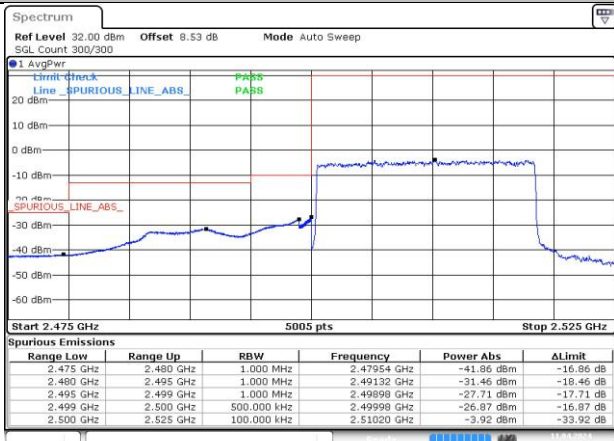
Date: 11 APR 2024 23:13:09

HCH_DFT-Pi2BPSK / Edge_1RB_Right



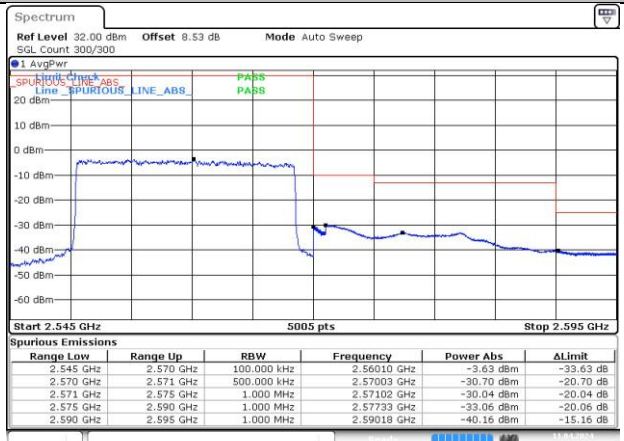
Date: 11 APR 2024 23:14:50

LCH_DFT-Pi2BPSK / Outer_Full



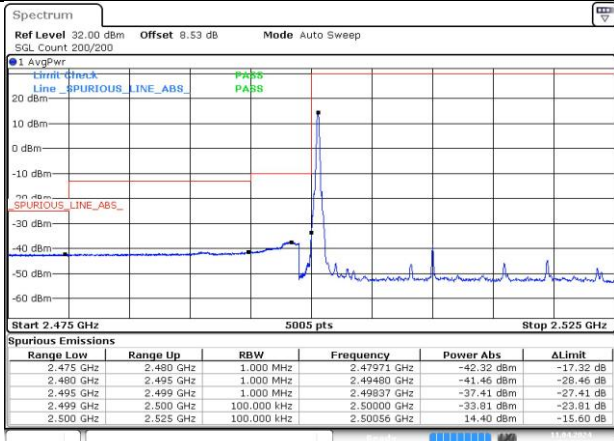
Date: 11 APR 2024 23:13:36

HCH_DFT-Pi2BPSK / Outer_Full



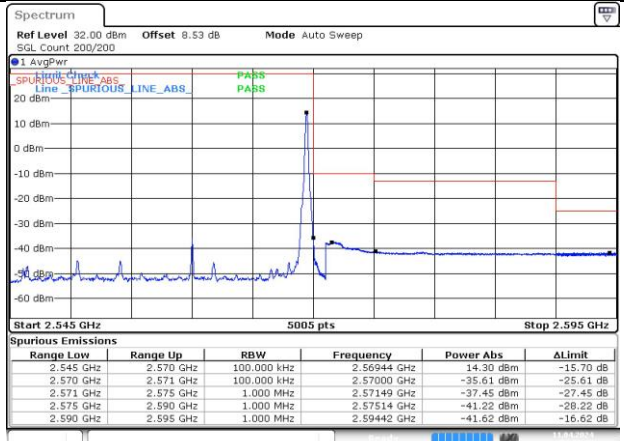
Date: 11 APR 2024 23:15:10

LCH_DFT-QPSK / Edge_1RB_Left



Date: 11 APR 2024 23:13:58

HCH_DFT-QPSK / Edge_1RB_Right



Date: 11 APR 2024 23:15:35

